

# XRT Timeline to be uploaded on 2014/10/02

Period: 2014/10/02 10:07:00 - 2014/10/07 09:19:00

\* \* \* \* \*

Normal mode

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## XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
10/02 10:20:00 - 10/02 20:59:54	Track ( -263.1, -145.3) <sup>Ⓢ 10/02 10:17:00</sup>	# OP start + 10min, track AR12178 (HOP272, 15-21hrs)
<b>PROG= 13 Inf.-time(s)</b>		
┌ Subr= 1 1-time(s) 2.0sec		
└ Seqn= 8 2-time(s) 2.0sec		
┌ Open/G-band	┌ Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└ Open/G-band	└ Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
┌ Subr= 2 2-time(s) 2.0sec		
└ Seqn= 24 1-time(s) 2.0sec		
┌ Open/Ti-poly	┌ Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└ Open/G-band	└ Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└ Seqn= 42 4-time(s) 2.0sec		
┌ Al-poly/Open	┌ thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└ thin-Be/Open	└ med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└ Open/thick-Al	└ Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└ Seqn= 62 8-time(s) 270.0sec		
┌ thin-Be/Open	┌ med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└ Al-poly/Open	└ thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└ thin-Be/Open	└ med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
└ Al-poly/Open	└ thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
└ thin-Be/Open	└ med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
└ Al-poly/Open	└ thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A3D: Synoptic Q95 2x2 - Al/mesh(5/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/2048) + Thi

Term	Pointing (x, y)	Comment
10/02 21:03:00 - 10/02 21:09:54	Fixed ( 0.0, 0.0)	synoptic, shifted +3hrs
10/03 06:03:00 - 10/03 06:09:54	Fixed ( 0.0, 0.0)	synoptic
10/03 18:03:00 - 10/03 18:09:54	Fixed ( 0.0, 0.0)	synoptic
10/04 05:32:00 - 10/04 05:38:54	Fixed ( 0.0, 0.0)	synoptic, shifted -31.0 min
<b>PROG= 03 1-time(s)</b>		
┌ Subr= 1 1-time(s) 12.0sec		
└ Seqn= 2 1-time(s) 4.0sec		
┌ Open/Al-mesh	┌ Open/Al-mesh close	Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Open/Al-mesh	└ Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Open/Al-mesh	└ Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Seqn= 5 1-time(s) 2.0sec		
┌ Open/Ti-poly	┌ Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└ Open/Ti-poly	└ Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└ Open/Ti-poly	└ Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└ Open/Ti-poly	└ Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
└ Open/Ti-poly	└ Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└ Seqn= 4 1-time(s) 4.0sec		
┌ Open/Ti-poly	┌ Open/Ti-poly close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Open/Ti-poly	└ Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Open/Ti-poly	└ Open/Ti-poly close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Seqn= 96 1-time(s) 2.0sec		
┌ thin-Be/Open	┌ thin-Be/Open close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ thin-Be/Open	└ thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ thin-Be/Open	└ thin-Be/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└ Seqn= 6 1-time(s) 2.0sec		
┌ Open/G-band	┌ Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
└ Open/G-band	└ Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A48: PFB observation for possible flare w/ Multifilter AR context images every 15min - 384x384

Term	Pointing (x, y)	Comment
10/02 21:13:00 - 10/02 22:59:54	Track ( -164.3, -147.6) <sup>Ⓢ 10/02 21:10:00</sup>	# track AR 12178
10/03 03:03:00 - 10/03 05:59:54	Track ( -110.4, -148.3) <sup>Ⓢ 10/03 03:00:00</sup>	# track AR 12178
10/03 06:13:00 - 10/03 17:59:54	Track ( -81.0, -148.6) <sup>Ⓢ 10/03 06:10:00</sup>	# track AR 12178 (HOP257)
10/03 18:13:00 - 10/03 22:59:54	Track ( 30.8, -148.5) <sup>Ⓢ 10/03 18:10:00</sup>	# track AR 12178
10/04 03:08:30 - 10/04 05:28:54	Track ( 113.0, -147.5) <sup>Ⓢ 10/04 03:00:00</sup>	# track AR 12178
10/04 05:42:00 - 10/04 09:12:00	Track ( 137.6, -147.0) <sup>Ⓢ 10/04 05:39:00</sup>	# track AR 12178 (HOP257)
<b>PROG= 09 Inf.-time(s)</b>		
┌ Subr= 1 1-time(s) 2.0sec		
└ Seqn= 24 1-time(s) 2.0sec		
┌ Open/Ti-poly	┌ Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└ Open/G-band	└ Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└ Seqn= 8 1-time(s) 2.0sec		
┌ Open/G-band	┌ Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└ Subr= 2 4-time(s) 2.0sec		
└ Seqn= 68 1-time(s) 2.0sec		

Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 37 15-time(s) 60.0sec</b>												
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	DPCM	3	1	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	DPCM	3	1	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	DPCM	3	2	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	DPCM	3	2	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	DPCM	3	3	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	DPCM	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1A47: AR Al/poly+Thin-Be-256x256-1min cad-AEC2-HOP267</b>												
Term	Pointing (x, y)						Comment					
10/02 23:03:00 - 10/03 02:59:54	Track ( -94.1, -136.0) @ 10/02 23:00:00						# HOP 267 (fan loop) @AR 12178					
10/03 23:03:00 - 10/04 02:46:30	Track ( 129.5, -134.7) @ 10/03 23:00:00						# HOP 267 (fan loop) @ AR 12178					
<b>PROG= 18 Inf.-time(s)</b>												
<b>Subr= 1 1-time(s) 30.0sec</b>												
<b>Seqn= 75 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
<b>Subr= 2 2-time(s) 2.0sec</b>												
<b>Seqn= 32 1-time(s) 2.0sec</b>												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
<b>Seqn= 50 30-time(s) 60.0sec</b>												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	256x256 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	256x256 (1064, 1048)	Q=95	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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<b>XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (4</b>												
Term	Pointing (x, y)						Comment					
10/02 10:20:00 - 10/02 20:59:54	Track ( -263.1, -145.3) @ 10/02 10:17:00						# OP start + 10min, track AR12178 (HOP272, 15-21hrs)					
10/02 21:13:00 - 10/02 22:59:54	Track ( -164.3, -147.6) @ 10/02 21:10:00						# track AR 12178					
10/02 23:03:00 - 10/03 02:59:54	Track ( -94.1, -136.0) @ 10/02 23:00:00						# HOP 267 (fan loop) @AR 12178					
10/03 03:03:00 - 10/03 05:59:54	Track ( -110.4, -148.3) @ 10/03 03:00:00						# track AR 12178					
10/03 06:13:00 - 10/03 17:59:54	Track ( -81.0, -148.6) @ 10/03 06:10:00						# track AR 12178 (HOP257)					
10/03 18:13:00 - 10/03 22:59:54	Track ( 30.8, -148.5) @ 10/03 18:10:00						# track AR 12178					
10/03 23:03:00 - 10/04 02:46:30	Track ( 129.5, -134.7) @ 10/03 23:00:00						# HOP 267 (fan loop) @ AR 12178					
10/04 03:08:30 - 10/04 05:28:54	Track ( 113.0, -147.5) @ 10/04 03:00:00						# track AR 12178					
10/04 05:42:00 - 10/04 09:12:00	Track ( 137.6, -147.0) @ 10/04 05:39:00						# track AR 12178 (HOP257)					
<b>PROG= 01 30-time(s)</b>												
<b>Subr= 1 30-time(s) 2.0sec</b>												
<b>Seqn= 26 1-time(s) 4.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 10 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 15 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Active Region Search**

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NOT USED

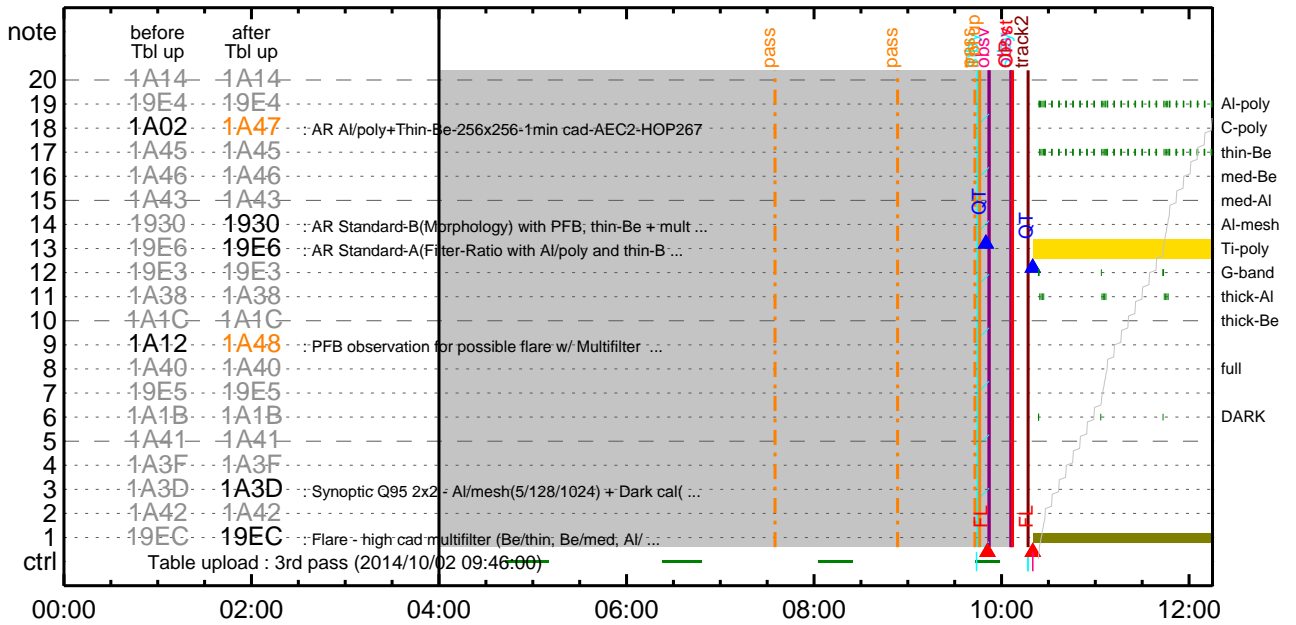
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**Flare Detection**

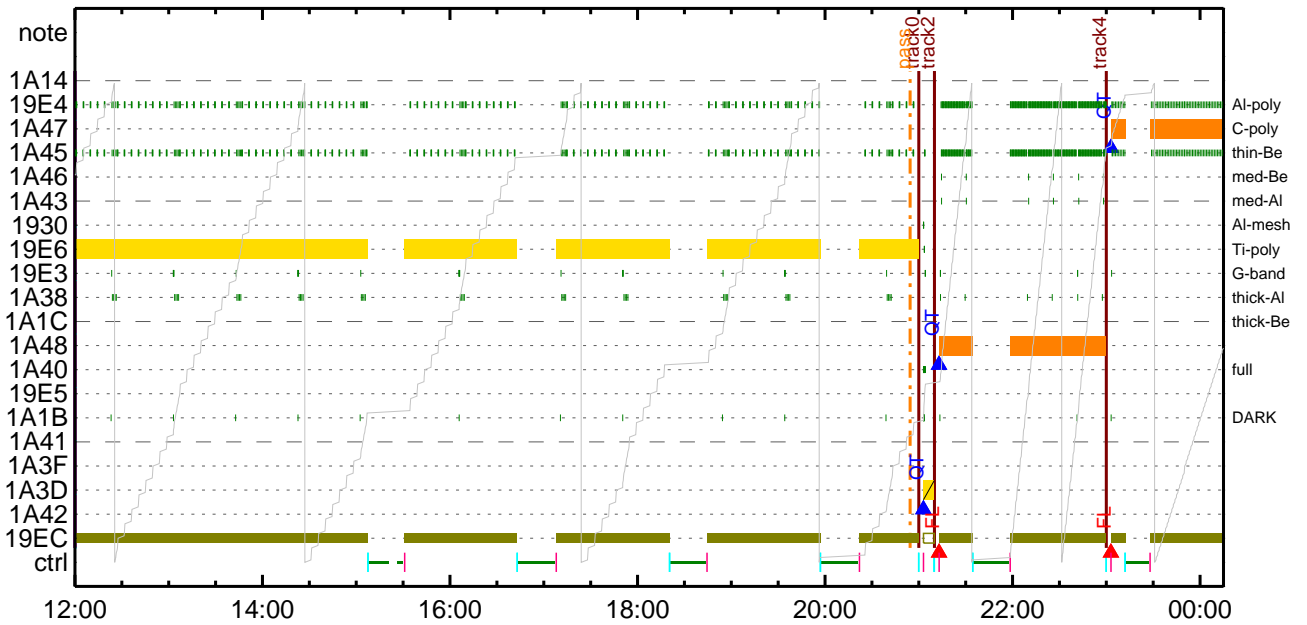
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<b>FLD Patrol</b>												
Term	Pointing (x, y)						Comment					
10/02 21:10:18 - 10/03 06:00:18	Track ( -164.3, -147.6) @ 10/02 21:10:00						# track AR 12178					
10/03 06:10:18 - 10/03 18:00:18	Track ( -81.0, -148.6) @ 10/03 06:10:00						# track AR 12178 (HOP257)					
10/03 18:10:18 - 10/04 05:29:18	Track ( 30.8, -148.5) @ 10/03 18:10:00						# track AR 12178					
10/04 05:39:18 - 10/07 09:19:00	Track ( 137.6, -147.0) @ 10/04 05:39:00						# track AR 12178 (HOP257)					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

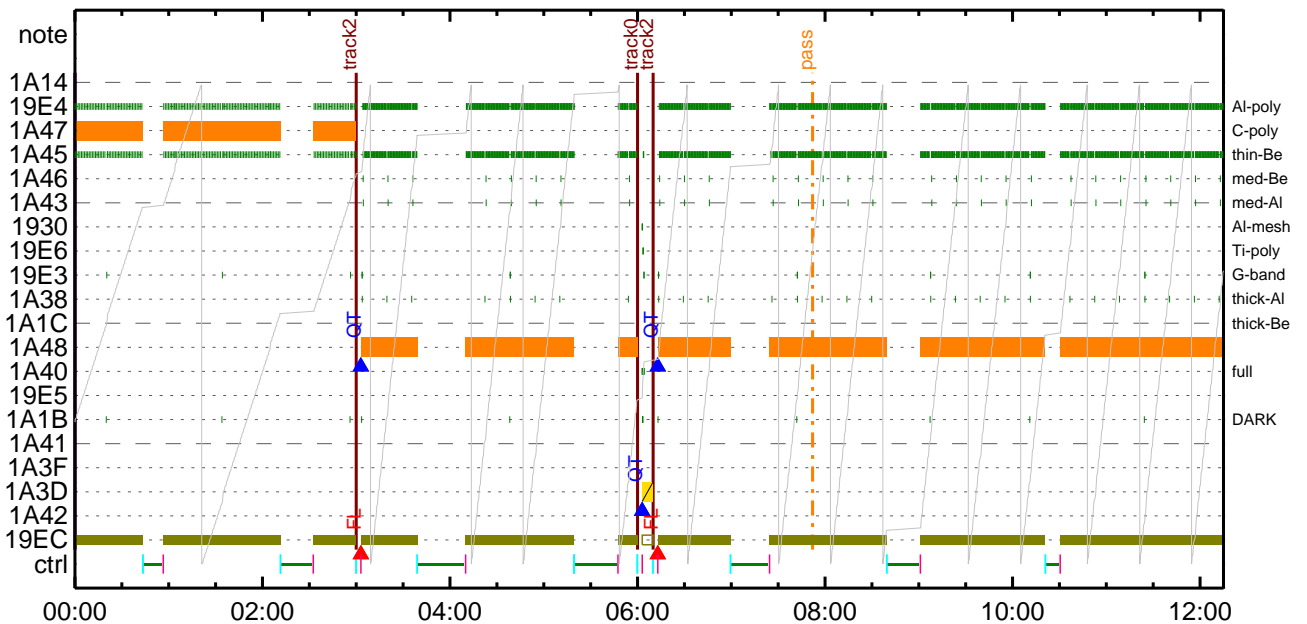
### CMDI #0609 2014/10/02



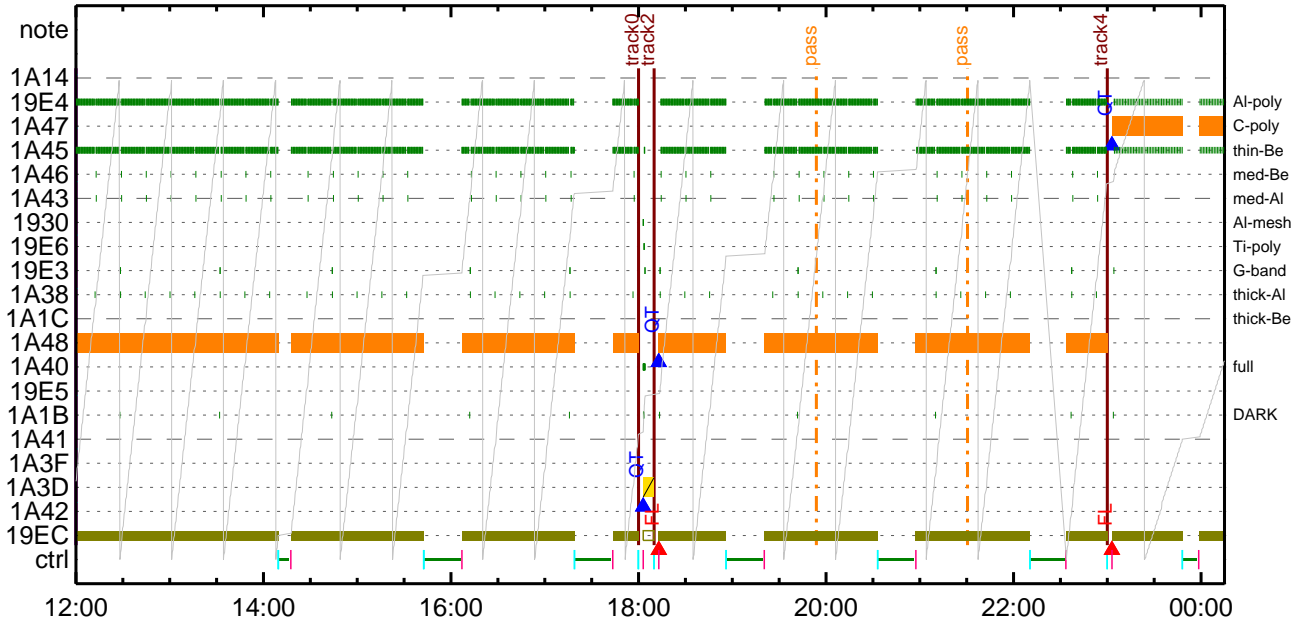
### CMDI #0609 2014/10/02



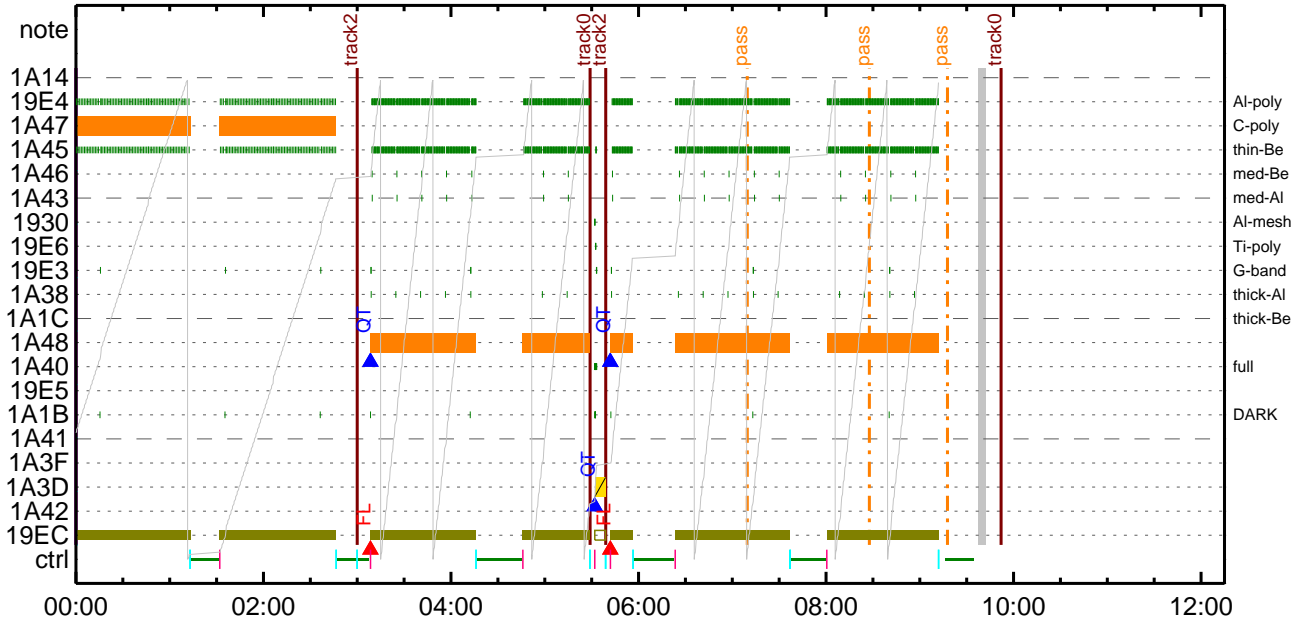
### CMDI #0609 2014/10/03



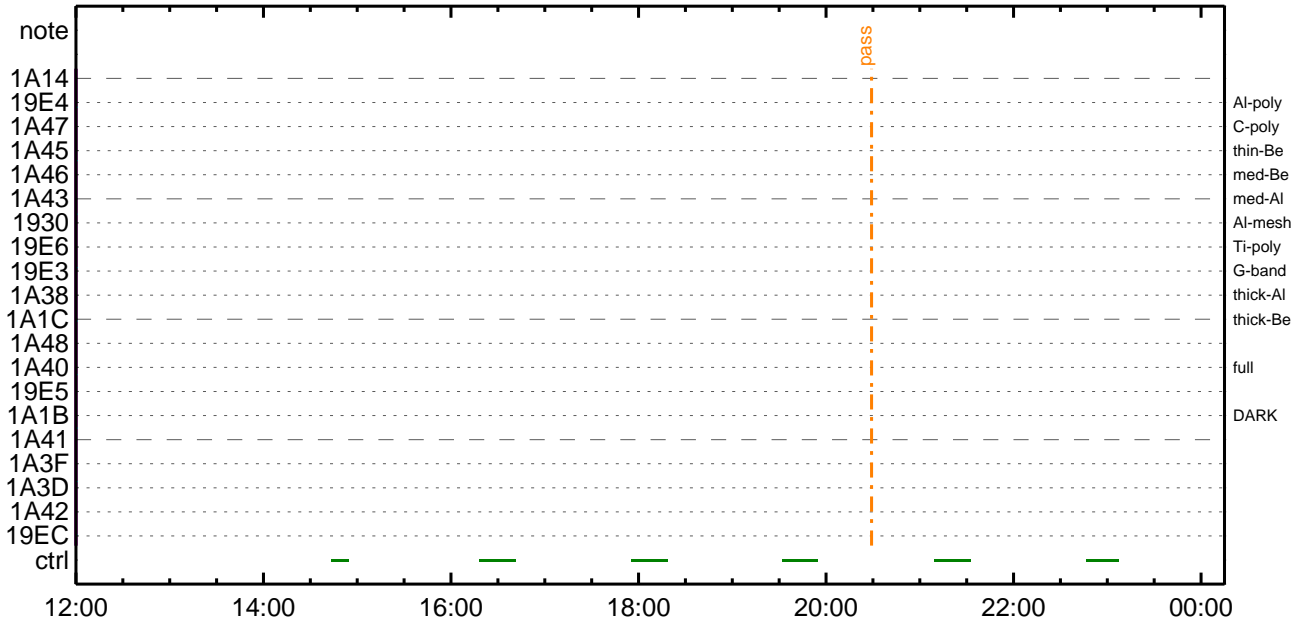
### CMDI #0609 2014/10/03



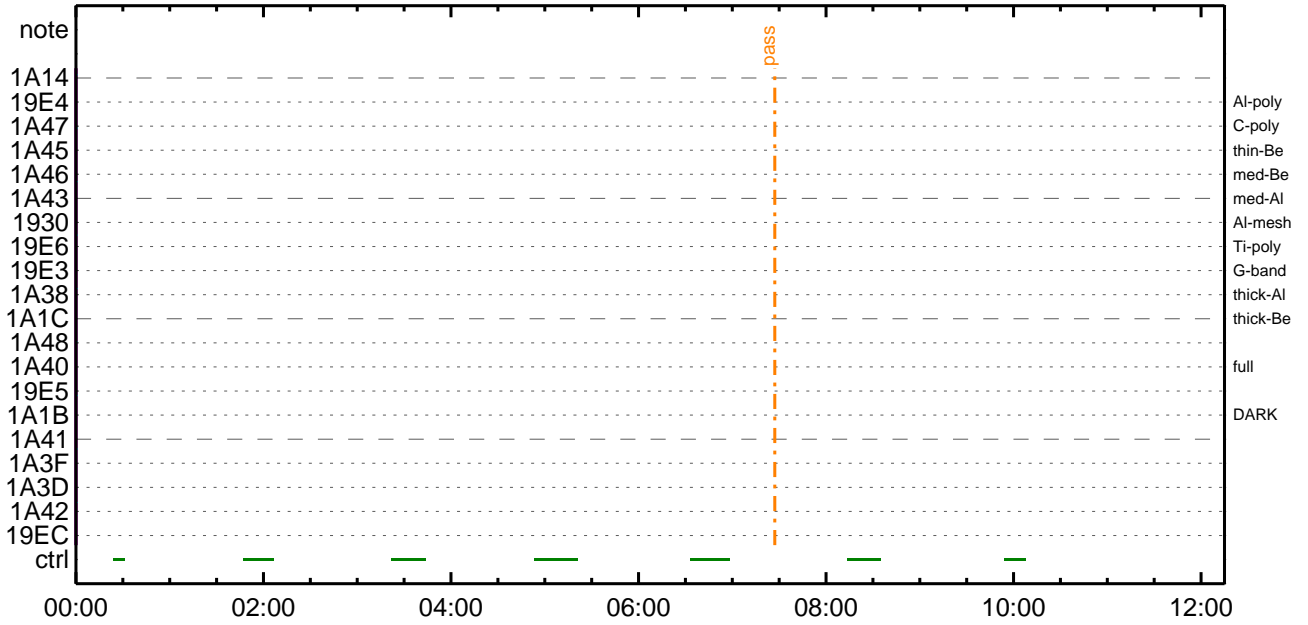
### CMDI #0609 2014/10/04



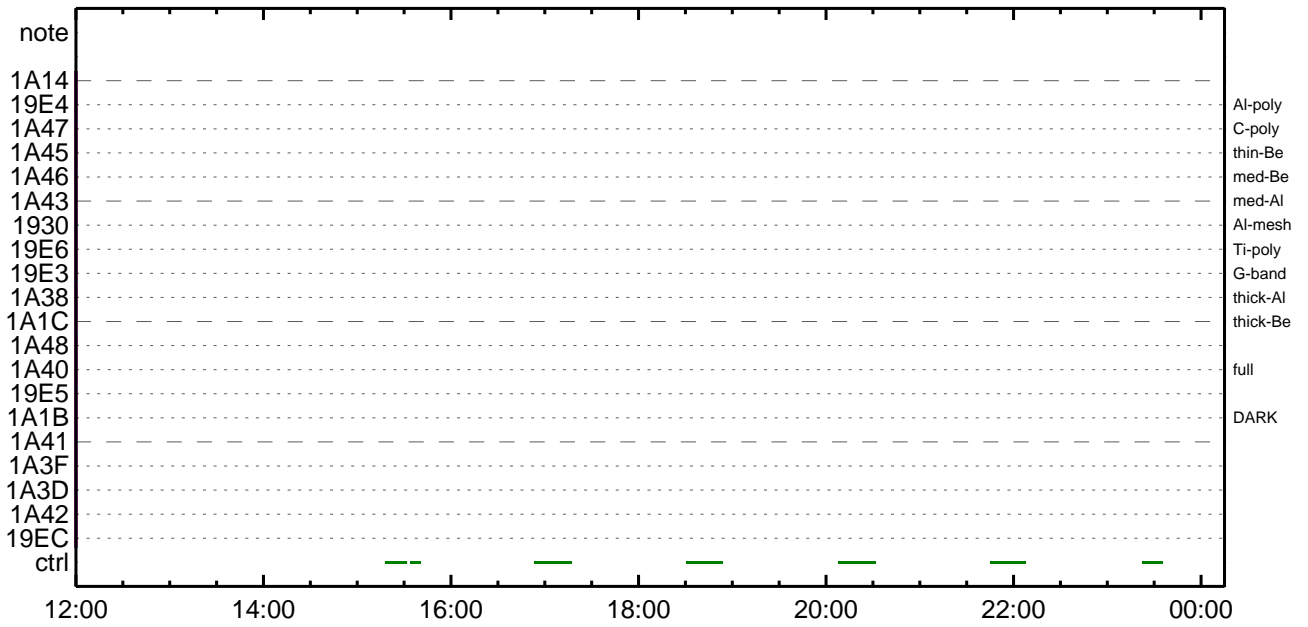
### CMDI #0609 2014/10/04



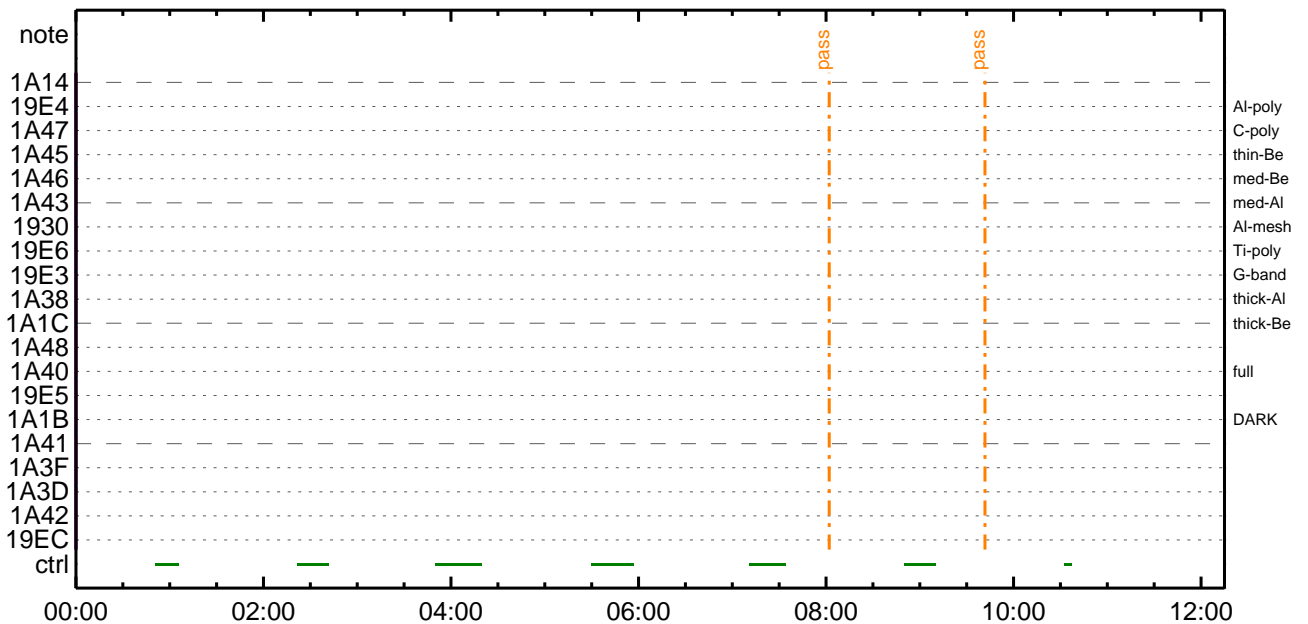
CMDI #0609 2014/10/05



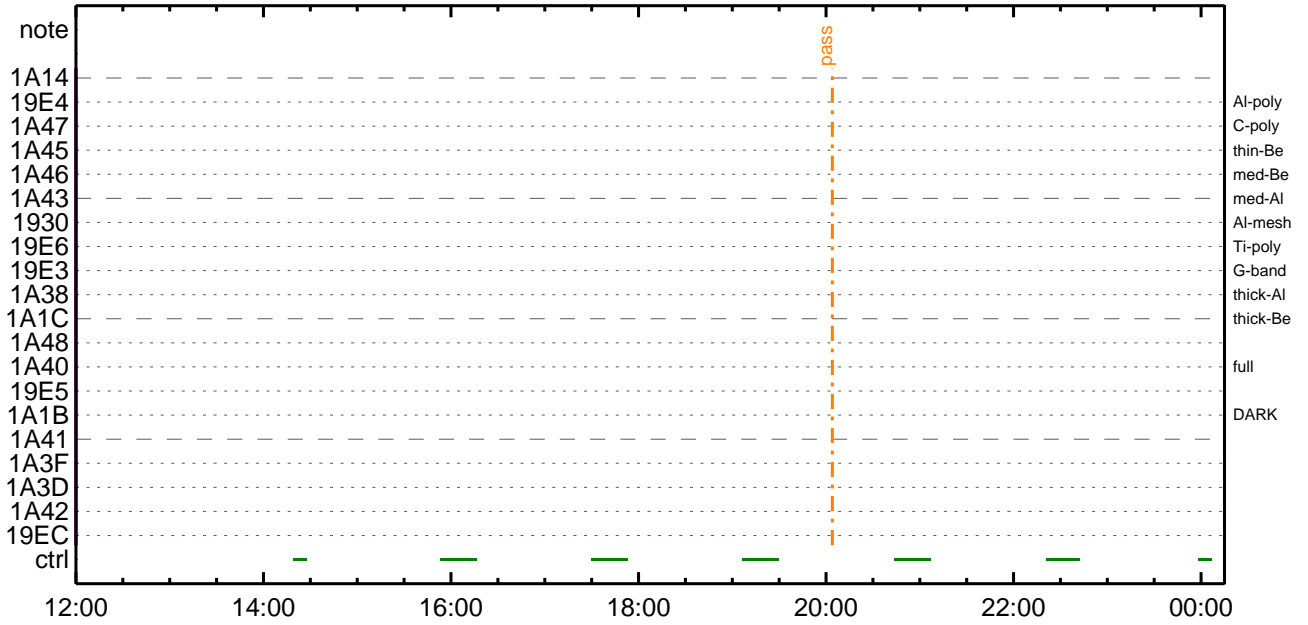
CMDI #0609 2014/10/05



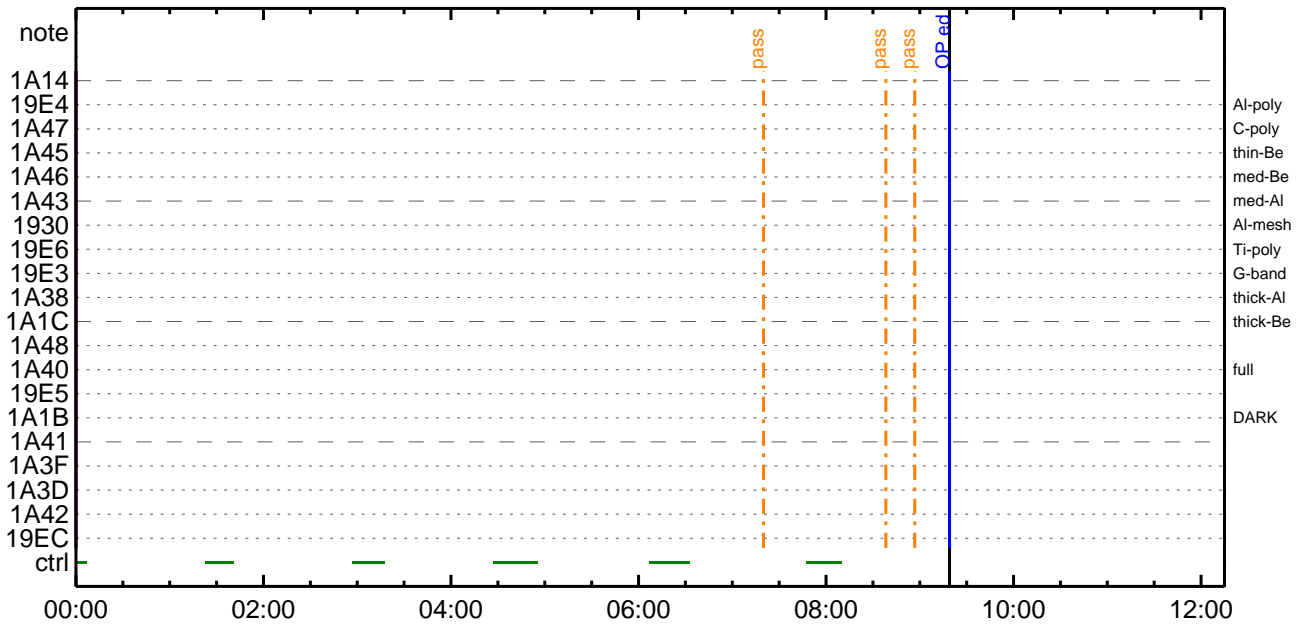
CMDI #0609 2014/10/06



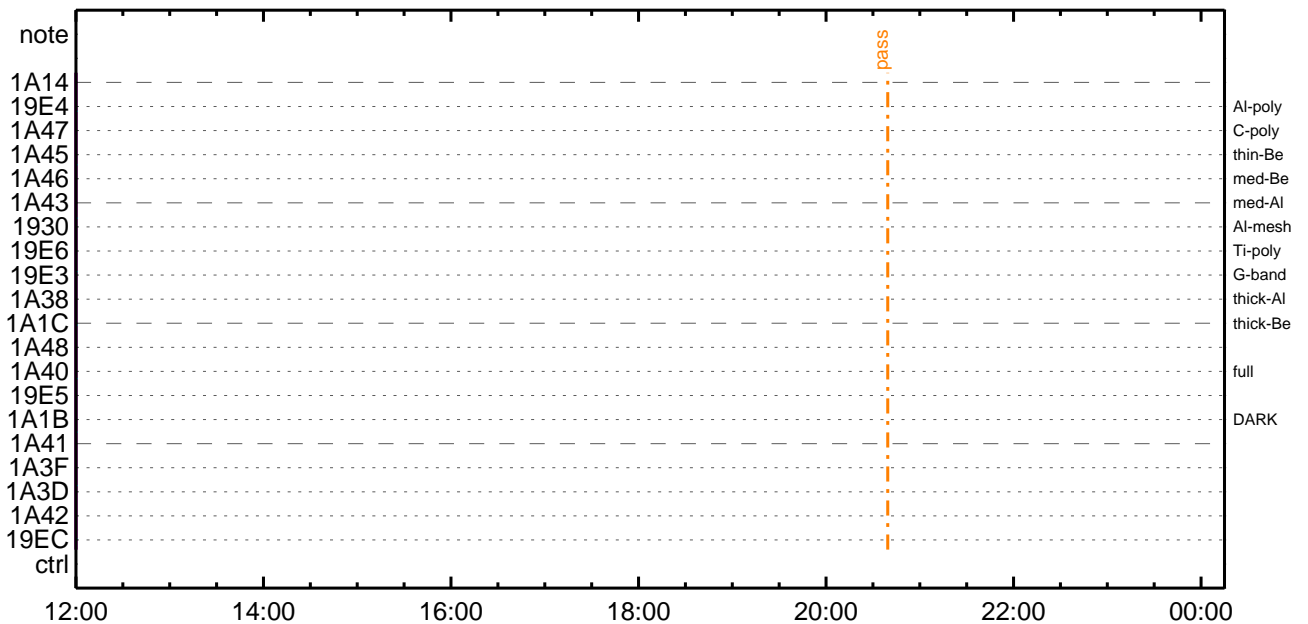
CMDI #0609 2014/10/06



CMDI #0609 2014/10/07



CMDI #0609 2014/10/07





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0096 C.                0302; SET0EDUMP0I00i0N01000|030E; E
0097 C.
0098 C. TI030Y0Y0E000AD0 (UT)
0099 +. TI 2014-10-02 10:02:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2014-10-02 10:02:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2014-10-02 10:02:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2014-10-02 10:06:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.                0E20I0E0i0N0I0Y0S0Y0Y-1a0U
0116 C.                00[HK1_TI_CMD_ENA/DIS]              EQ        ENA
0117 C.                00[HK1_TI_CMD_NUM]                EQ        4
0118 C.                00[HK1_NEXT_EXEC_PIM]              EQ        DHU
0119 C.                00[HK1_NEXT_EXEC_DC]              EQ        0xB3
0120 C.
0121 C. *****
0122 C. TI0I0E0Y0Y0Yx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; $ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                00[HK1_DMP_TOP_ADRS_1]              EQ        07
0129 C.                00[HK1_DMP_TOP_ADRS_0]              EQ        2B
0130 C.                00[HK1_DMP_BLOCK_NUM]              EQ        3
0131 C.                00[HK1_DMP_REPEAT_NUM]             EQ        0
0132 C.                00[HK1_DMA_DMP_PIM]               EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                00[HK1_PKT_FORM_NO]                EQ        7
0136 C.                00[HK1_PKT_GEN_TIME]               EQ        0.25 s
0137 C.                00[HK1_S_TLM_BIT_RATE]             EQ        32k
0138 C.                00[HK1_X_TLM_BIT_RATE]            EQ        4M
0139 C.                00[HK1_DMP_CHK_FLG]               EQ        EXEC
0140 C.
0141 C.                0Y0Y0Yx0I00003I0S
0142 C.                00[HK1_DMP_CHK_FLG]               EQ        NON
0143 C.
0144 C. RAM ID=TI_TBL0I0E1000e2I0K00003I0S
0145 C.
0146 C. DHU0a0;0Y0E;0Y0Y;0i;0Y0E;E000I0a01
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                00[HK1_PKT_FORM_NO]                EQ        2
0150 C.                00[HK1_PKT_GEN_TIME]               EQ        0.5S
0151 C.                00[HK1_S_TLM_BIT_RATE]             EQ        32K
0152 C.                00[HK1_X_TLM_BIT_RATE]            EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2014-10-02 10:06:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2014-10-02 10:06:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2014-10-02 10:06:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2014-10-02 10:06:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 0A0I0I000Y0E0E0A000eDCBC0x2e *****
0192 C. (0A0I0Y0Y0Y0Y0Y0Y0Y0Y0Y0Y0Y0E0E00004A0U000e)
0193 S. DC-BC dcbc-402:DCBC

```



```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```





(a) Spacecraft Operation Procedure (real-commands)

```
main-670 2014-10-02 12:51:57 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁ•µ°È»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈ¿µ•ííÉ;ÈÈ¿°ÇÓã•¿¿¼í¹ç¿Í;çÁ®, ù¿¹ãÈãÈãçÁ+¿®ã•¿Èãã¿ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-287:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2014-10-02 10:06:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 85 83 04 04)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0f 80 80 06 06)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 10 80 80 08 08)
0090 + DC 07-F0 MDP_XRT_FLD_ENA
0091 BC (d8)
0092 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0093 BC (c8)
0094 + DC 07-F0 MDP_XRT_AEC_RESET
0095 BC (d0)
```

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0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097     BC      (d5)
0098 + DC 07-F0 MDP_XRT_FLD_RESET
0099     BC      (da)
0100 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0101     BC      (c4 0e)
0102 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0103     BC      (c5 01)
0104   . C. ----- Success Verify ?              OK / NG ____
0105   . C.
0106   . C.
0107   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0108   . C.
0109 +. DC 07-F0 MDP_XRT_MODE_OBSV
0110     BC      (c2)
0111 +. TI 2014-10-02 10:06:02.0
0112     DC 07-F0 MDP_XRT_MODE_OBSV
0113     BC      (c2)
0114   . C. ----- Success Verify ?              OK / NG ____
0115   . C.
0116   . C. ***** XRT END *****
0117   . C.
0118   . C. ***** MDP `ûÃîáî»ö¼ŸáëËâä¹âèDCBC•x²è *****
0119   . C. (¼â°îŸÓŸÃŸÉŸÏŸÉŸâŸçŸèè¼¼¼¼¼»Ü¹âè)
0120   . S. DC-BC dcbc-402:DCBC
0121     (MDP_known_event)
0122   . C.
0123   . C.
0124   . C. ***** ŸDŸ¹•İ Daily±¿İŇâÉ'Ø¹âèDCBC•x²è *****
0125   . S. DC-BC dcbc-153:DCBC
0126     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127   . C.
0128   . C.
0129   . C. ;ãLOSŸÃŸŸŸÃŸ-¼Ã»Ü;ã
0130   . C.
0131   . C. ***** LOS *****
0132   . C.

```

\*\*\* OP Sequence for XRT \*\*\*

2014/10/02	10:16:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	10:16:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	10:16:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/10/02	10:17:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/10/02	10:17:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/10/02	10:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/10/02	10:17:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/10/02	10:17:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/10/02	10:17:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/02	10:19:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2014/10/02	10:19:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/10/02	10:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/02	15:07:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	15:07:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	15:07:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/02	15:07:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/02	15:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/02	15:30:00.0	XRT_Custom_430_OG [0x1ae]							
2014/10/02	15:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/02	16:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	16:43:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	16:43:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/02	16:43:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/02	16:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/02	17:07:00.0	XRT_Custom_430_OG [0x1ae]							
2014/10/02	17:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/02	18:20:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	18:20:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	18:20:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/02	18:20:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/02	18:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/02	18:43:30.0	XRT_Custom_430_OG [0x1ae]							
2014/10/02	18:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/02	19:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	19:57:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	19:57:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/02	19:57:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/02	20:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/02	20:21:00.0	XRT_Custom_430_OG [0x1ae]							
2014/10/02	20:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/02	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	20:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/02	20:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/10/02	21:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/10/02	21:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/10/02	21:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/10/02	21:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/10/02	21:02:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]							

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2014/10/02	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/02	21:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	21:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	21:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/10/02	21:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00	
2014/10/02	21:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/10/02	21:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/10/02	21:10:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/10/02	21:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/10/02	21:10:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/02	21:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2014/10/02	21:12:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/10/02	21:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/02	21:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	21:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	21:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/02	21:34:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/10/02	21:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/10/02	21:57:30.0	XRT_Custom_430_OG [0x1ae]					
2014/10/02	21:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/02	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/10/02	23:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04 00 00 00 00	
2014/10/02	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/10/02	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/10/02	23:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/10/02	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/10/02	23:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/02	23:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2014/10/02	23:02:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/10/02	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/02	23:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	23:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	23:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/02	23:12:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/10/02	23:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/10/02	23:27:00.0	XRT_Custom_430_OG [0x1ae]					
2014/10/02	23:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/03	00:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/03	00:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/03	00:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/03	00:43:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/10/03	00:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/10/03	00:55:30.0	XRT_Custom_430_OG [0x1ae]					
2014/10/03	00:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/03	02:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/03	02:11:32.0	XRT_CTRL_MANU_402_OG [0x192]					

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2014/10/03	02:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/10/03	02:11:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/10/03	02:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/10/03	02:31:30.0	XRT_Custom_430_OG [0x1ae]						
2014/10/03	02:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/10/03	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/10/03	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/10/03	02:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/10/03	03:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00		
2014/10/03	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/10/03	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/10/03	03:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/10/03	03:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/10/03	03:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/10/03	03:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2014/10/03	03:02:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2014/10/03	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/10/03	03:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/10/03	03:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/10/03	03:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/10/03	03:39:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/10/03	03:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/10/03	04:09:00.0	XRT_Custom_430_OG [0x1ae]						
2014/10/03	04:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/10/03	05:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/10/03	05:19:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/10/03	05:19:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/10/03	05:19:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/10/03	05:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/10/03	05:46:30.0	XRT_Custom_430_OG [0x1ae]						
2014/10/03	05:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/10/03	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/10/03	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/10/03	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/10/03	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/10/03	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/10/03	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/10/03	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/10/03	06:02:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2014/10/03	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/10/03	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/10/03	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/10/03	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/10/03	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00		
2014/10/03	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/10/03	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/10/03	06:10:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/10/03	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]						



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2014/10/03	06:10:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2014/10/03	06:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2014/10/03	06:12:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/10/03	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/10/03	06:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	06:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	06:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/10/03	06:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/10/03	07:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2014/10/03	07:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/03	07:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/10/03	08:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	08:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	08:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	08:39:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/10/03	08:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/10/03	09:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/10/03	09:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/03	10:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/10/03	10:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	10:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	10:21:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/10/03	10:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/10/03	10:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/03	10:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/03	14:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/10/03	14:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	14:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	14:09:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/10/03	14:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/10/03	14:16:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/03	14:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/03	15:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/10/03	15:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	15:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	15:42:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/10/03	15:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/10/03	16:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/03	16:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/03	17:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/10/03	17:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	17:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	17:19:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/10/03	17:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/10/03	17:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/03	17:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/03	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/10/03	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/03	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

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2014/10/03	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2014/10/03	18:00:18.0	XRT_FLD_DIS_422_OG [0x1a6] MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/10/03	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/10/03	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/10/03	18:02:58.0	XRT_QT_PROG_SET_428_OG [0x1ac] MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2014/10/03	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/03	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/10/03	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097] AOCU_NM	5	02-76	02	00	00	00	00
2014/10/03	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/10/03	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/10/03	18:10:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/10/03	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/10/03	18:10:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/03	18:12:56.0	XRT_QT_PROG_SET_401_OG [0x191] MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2014/10/03	18:12:58.0	XRT_FL_PROG_SET_438_OG [0x1b6] MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/10/03	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/03	18:56:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	18:56:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	18:56:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/03	18:56:06.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/03	18:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/03	19:19:30.0	XRT_Custom_430_OG [0x1ae]							
2014/10/03	19:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/03	20:33:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	20:33:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	20:33:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/03	20:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/03	20:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/03	20:56:30.0	XRT_Custom_430_OG [0x1ae]							
2014/10/03	20:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/03	22:10:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	22:10:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	22:10:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/03	22:10:36.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/03	22:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/03	22:32:30.0	XRT_Custom_430_OG [0x1ae]							
2014/10/03	22:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/03	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/10/03	23:00:00.0	AOCS_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	04	00	00	00	00
2014/10/03	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/10/03	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/10/03	23:00:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/10/03	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/10/03	23:00:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da				

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2014/10/03	23:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2014/10/03	23:02:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/10/03	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/03	23:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	23:48:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/03	23:48:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/03	23:48:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/03	23:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/03	23:57:30.0	XRT_Custom_430_OG [0x1ae]							
2014/10/03	23:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/04	01:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	01:13:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	01:13:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/04	01:13:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/04	01:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/04	01:31:00.0	XRT_Custom_430_OG [0x1ae]							
2014/10/04	01:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/04	02:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	02:46:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	02:46:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/04	02:46:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/04	02:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/04	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	02:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/10/04	03:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2014/10/04	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/10/04	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/10/04	03:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/10/04	03:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/10/04	03:00:26.0	XRT_FLD_RESET_443_OG [0x1bb]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/04	03:08:26.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2014/10/04	03:08:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/10/04	03:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/04	04:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	04:16:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	04:16:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/04	04:16:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/04	04:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/04	04:45:00.0	XRT_Custom_430_OG [0x1ae]							
2014/10/04	04:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/04	05:28:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	05:28:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	05:28:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/10/04	05:29:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/10/04	05:29:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/10/04	05:31:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/10/04	05:31:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				

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2014/10/04	05:31:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2014/10/04	05:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/04	05:38:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	05:38:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	05:38:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2014/10/04	05:39:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00 00 00 00			
2014/10/04	05:39:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/10/04	05:39:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/10/04	05:39:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/10/04	05:39:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/10/04	05:39:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/04	05:41:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2014/10/04	05:41:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/10/04	05:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/04	05:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	05:56:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	05:56:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/04	05:56:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/04	05:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/04	06:22:30.0	XRT_Custom_430_OG [0x1ae]							
2014/10/04	06:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/04	07:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	07:37:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	07:37:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/04	07:37:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/04	07:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/04	07:59:30.0	XRT_Custom_430_OG [0x1ae]							
2014/10/04	08:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/04	09:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	09:12:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/04	09:12:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/04	09:12:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/04	09:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/04	09:52:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00 00 00 00			