

XRT Timeline to be uploaded on 2015/03/03

Period: 2015/03/03 09:20:00 - 2015/03/07 10:39:00

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Normal mode

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XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
03/03 09:47:00 - 03/03 10:59:54	Track (251.9, 178.8) ^{Ⓢ 03/03 09:30:00}	# OP start + 10min, AR12293 obs.
03/03 21:03:00 - 03/03 23:59:54	Track (354.4, 174.6) ^{Ⓢ 03/03 21:00:00}	AR12293 obs.
03/04 06:13:00 - 03/04 10:29:54	Track (433.0, 170.2) ^{Ⓢ 03/04 06:10:00}	Cont.
03/04 18:13:00 - 03/04 23:59:54	Track (530.5, 163.1) ^{Ⓢ 03/04 18:10:00}	AR12293 obs.

PROG= 20 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 42 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 62 8-time(s) 270.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A47: AR Al/poly+Thin-Be-256x256-1min cad-AEC2-HOP267

Term	Pointing (x, y)	Comment
03/03 11:03:00 - 03/03 15:09:54	Track (-132.5, 387.0) ^{Ⓢ 03/03 11:00:00}	HOP267.
03/04 00:03:00 - 03/04 03:59:54	Track (-16.9, 388.1) ^{Ⓢ 03/04 00:00:00}	HOP267.
03/05 00:03:00 - 03/05 03:59:54	Track (195.8, 385.5) ^{Ⓢ 03/05 00:00:00}	HOP267.

PROG= 08 Inf.-time(s)

Subr= 1 1-time(s) 30.0sec													
└─ Seqn= 75 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 80 1-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
└─ Seqn= 69 30-time(s) 60.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	256x256 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	256x256 (1064, 1048)	Q=95	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A62: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(16/362/2048) + Thi

Term	Pointing (x, y)	Comment
03/03 15:13:00 - 03/03 15:19:54	Fixed (0.0, 0.0)	synoptic, shifted manually
03/04 06:03:00 - 03/04 06:09:54	Fixed (0.0, 0.0)	synoptic
03/04 18:03:00 - 03/04 18:09:54	Fixed (0.0, 0.0)	synoptic
03/05 06:21:00 - 03/05 06:27:54	Fixed (0.0, 0.0)	synoptic, shifted 18.0 min

PROG= 03 1-time(s)

Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 33 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 40 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 77 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

└─ Seqn= 6		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #1997: Dark - Open+Thick-Be - 8x8 - 384x384

Term	Pointing (x, y)	Comment
03/03 15:23:00 - 03/03 15:35:54	Fixed (936.0, 0.0)	HOP130 (1/15)
03/03 15:39:00 - 03/03 15:51:54	Fixed (468.0, 0.0)	(2/15)
03/03 15:55:00 - 03/03 16:07:54	Fixed (0.0, 0.0)	(3/15)
03/03 16:11:00 - 03/03 16:59:24	Fixed (-468.0, 0.0)	(4/15)
03/03 17:02:30 - 03/03 17:15:24	Fixed (-936.0, 0.0)	(5/15)
03/03 17:18:30 - 03/03 17:25:24	Fixed (815.0, 470.0)	(6/15)
03/03 17:28:30 - 03/03 17:41:24	Fixed (411.0, 847.0)	(7/15)
03/03 17:44:30 - 03/03 18:35:54	Fixed (0.0, 941.0)	(8/15)
03/03 18:39:00 - 03/03 18:51:54	Fixed (-411.0, 847.0)	(9/15)
03/03 18:55:00 - 03/03 19:01:54	Fixed (-815.0, 470.0)	(10/15)
03/03 19:05:00 - 03/03 19:11:54	Fixed (815.0, -470.0)	(11/15)
03/03 19:15:00 - 03/03 19:27:54	Fixed (411.0, -847.0)	(12/15)
03/03 19:31:00 - 03/03 20:12:54	Fixed (0.0, -941.0)	(13/15)
03/03 20:16:00 - 03/03 20:28:54	Fixed (-411.0, -847.0)	(14/15)
03/03 20:32:00 - 03/03 20:59:54	Fixed (-815.0, -471.0)	(15/15)

PROG= 14 1-time(s)

└─ Subr= 1		1-time(s)		2.0sec											
└─ Seqn= 30		1-time(s)		2.0sec											
Open/thick-Be	Open/thick-Be	close	Safe	Dark	63ms	Obs	8x8	384x384 (1024, 1024)		DPCM	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #19D7: PFB observation for possible flare -w/leak test, 60s cad

Term	Pointing (x, y)	Comment
03/04 04:03:00 - 03/04 05:59:54	Track (414.7, 171.3) @ 03/04 04:00:00	AR12293 obs.
03/05 04:32:00 - 03/05 06:17:54	Track (605.1, 156.2) @ 03/05 04:00:00	AR12293 obs.
03/05 06:31:00 - 03/05 07:22:00	Track (622.9, 154.3) @ 03/05 06:28:00	Cont.

PROG= 05 Inf.-time(s)

└─ Subr= 1		1-time(s)		2.0sec											
└─ Seqn= 97		2-time(s)		2.0sec											
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	3	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1064, 1048)		DPCM	0	3	2.0sec		
└─ Subr= 2		2-time(s)		2.0sec											
└─ Seqn= 51		30-time(s)		60.0sec											
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		DPCM	3	1	2.0sec		
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		DPCM	3	1	2.0sec		
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		DPCM	3	2	2.0sec		
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		DPCM	3	2	2.0sec		
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		DPCM	3	3	2.0sec		
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		DPCM	3	3	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #1A1B: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 33ms

Term	Pointing (x, y)	Comment
03/04 10:33:00 - 03/04 17:59:54	Fixed (-18.0, -960.0)	HOP81 at S-pole.

PROG= 13 Inf.-time(s)

└─ Subr= 1		1-time(s)		2.0sec											
└─ Seqn= 8		2-time(s)		2.0sec											
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec		
└─ Subr= 2		1-time(s)		2.0sec											
└─ Seqn= 48		1-time(s)		30.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec		
└─ Subr= 3		30-time(s)		2.0sec											
└─ Seqn= 57		1-time(s)		60.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Flare mode

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XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
03/03 09:47:00 - 03/03 10:59:54	Track (251.9, 178.8) @ 03/03 09:30:00	# OP start + 10min, AR12293 obs.
03/03 11:03:00 - 03/03 15:09:54	Track (-132.5, 387.0) @ 03/03 11:00:00	HOP267.
03/03 15:23:00 - 03/03 15:35:54	Fixed (936.0, 0.0)	HOP130 (1/15)
03/03 15:39:00 - 03/03 15:51:54	Fixed (468.0, 0.0)	(2/15)
03/03 15:55:00 - 03/03 16:07:54	Fixed (0.0, 0.0)	(3/15)
03/03 16:11:00 - 03/03 16:59:24	Fixed (-468.0, 0.0)	(4/15)
03/03 17:02:30 - 03/03 17:15:24	Fixed (-936.0, 0.0)	(5/15)
03/03 17:18:30 - 03/03 17:25:24	Fixed (815.0, 470.0)	(6/15)

03/03 17:28:30 - 03/03 17:41:24	Fixed (411.0, 847.0)	(7/15)
03/03 17:44:30 - 03/03 18:35:54	Fixed (0.0, 941.0)	(8/15)
03/03 18:39:00 - 03/03 18:51:54	Fixed (-411.0, 847.0)	(9/15)
03/03 18:55:00 - 03/03 19:01:54	Fixed (-815.0, 470.0)	(10/15)
03/03 19:05:00 - 03/03 19:11:54	Fixed (815.0, -470.0)	(11/15)
03/03 19:15:00 - 03/03 19:27:54	Fixed (411.0, -847.0)	(12/15)
03/03 19:31:00 - 03/03 20:12:54	Fixed (0.0, -941.0)	(13/15)
03/03 20:16:00 - 03/03 20:28:54	Fixed (-411.0, -847.0)	(14/15)
03/03 20:32:00 - 03/03 20:59:54	Fixed (-815.0, -471.0)	(15/15)
03/03 21:03:00 - 03/03 23:59:54	Track (354.4, 174.6) ^{Ⓢ 03/03 21:00:00}	AR12293 obs.
03/04 00:03:00 - 03/04 03:59:54	Track (-16.9, 388.1) ^{Ⓢ 03/04 00:00:00}	HOP267.
03/04 04:03:00 - 03/04 05:59:54	Track (414.7, 171.3) ^{Ⓢ 03/04 04:00:00}	AR12293 obs.
03/04 06:13:00 - 03/04 10:29:54	Track (433.0, 170.2) ^{Ⓢ 03/04 06:10:00}	Cont.
03/04 10:33:00 - 03/04 17:59:54	Fixed (-18.0, -960.0)	HOP81 at S-pole.
03/04 18:13:00 - 03/04 23:59:54	Track (530.5, 163.1) ^{Ⓢ 03/04 18:10:00}	AR12293 obs.
03/05 00:03:00 - 03/05 03:59:54	Track (195.8, 385.5) ^{Ⓢ 03/05 00:00:00}	HOP267.
03/05 04:32:00 - 03/05 06:17:54	Track (605.1, 156.2) ^{Ⓢ 03/05 04:00:00}	AR12293 obs.
03/05 06:31:00 - 03/05 07:22:00	Track (622.9, 154.3) ^{Ⓢ 03/05 06:28:00}	Cont.

PROG= 19 30-time(s)

Subr= 1 20-time(s) 2.0sec	
Seqn= 11 1-time(s) 2.0sec	Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec	thin-Be/Open med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
	med-Be/Open Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
	Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec	
Seqn= 10 1-time(s) 2.0sec	med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
	Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec	Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 15 1-time(s) 2.0sec	Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
	Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
	Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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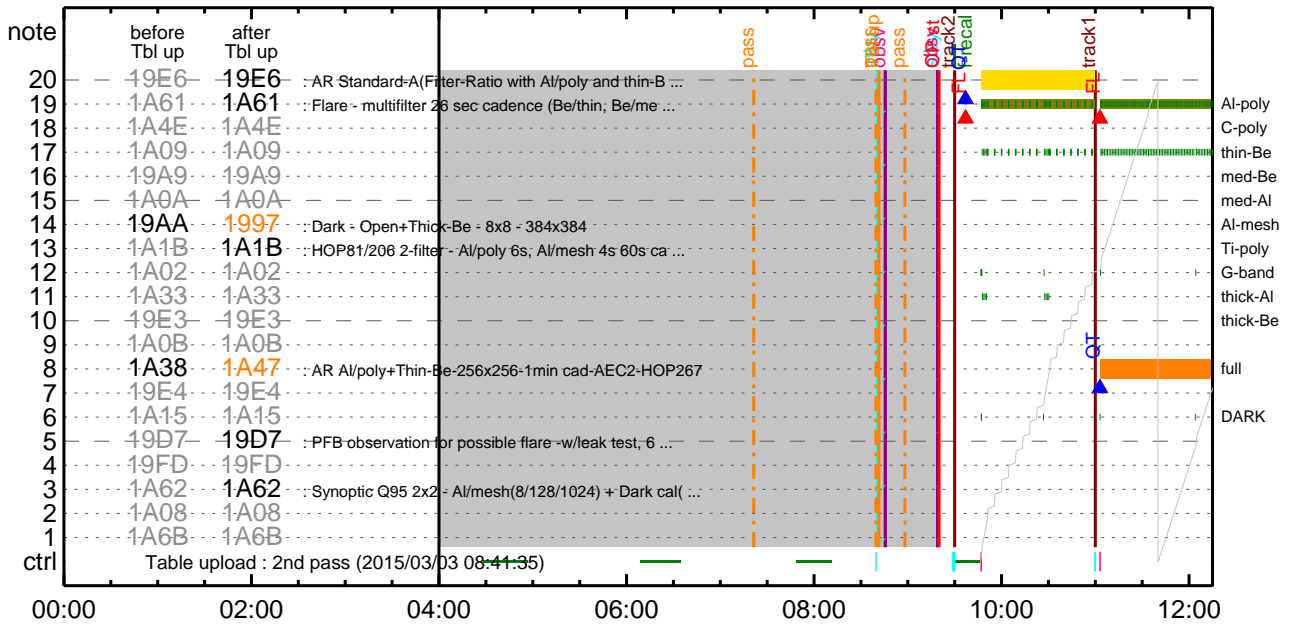
Flare Detection

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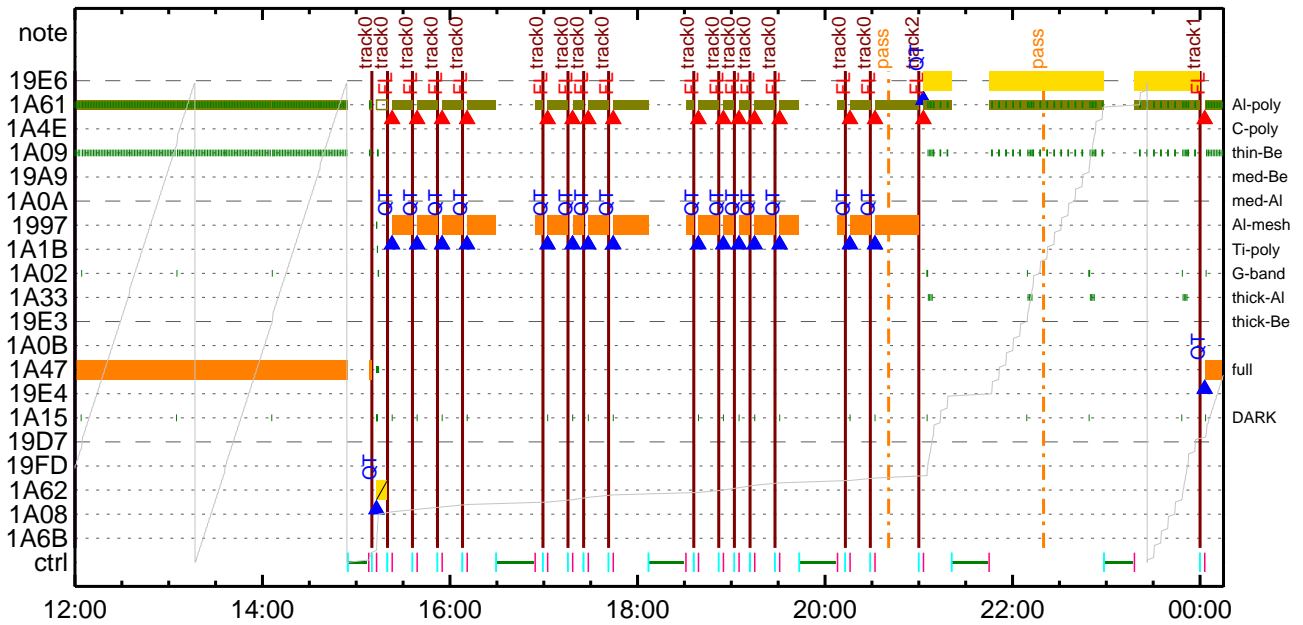
FLD Patrol

Term	Pointing (x, y)	Comment
03/03 15:20:18 - 03/04 06:00:18	Fixed (936.0, 0.0)	HOP130 (1/15)
03/04 06:10:18 - 03/04 18:00:18	Track (433.0, 170.2) ^{Ⓢ 03/04 06:10:00}	Cont.
03/04 18:10:18 - 03/05 06:18:18	Track (530.5, 163.1) ^{Ⓢ 03/04 18:10:00}	AR12293 obs.
03/05 06:28:18 - 03/07 10:39:00	Track (622.9, 154.3) ^{Ⓢ 03/05 06:28:00}	Cont.
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms Obs 8x8	Q=50 30sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

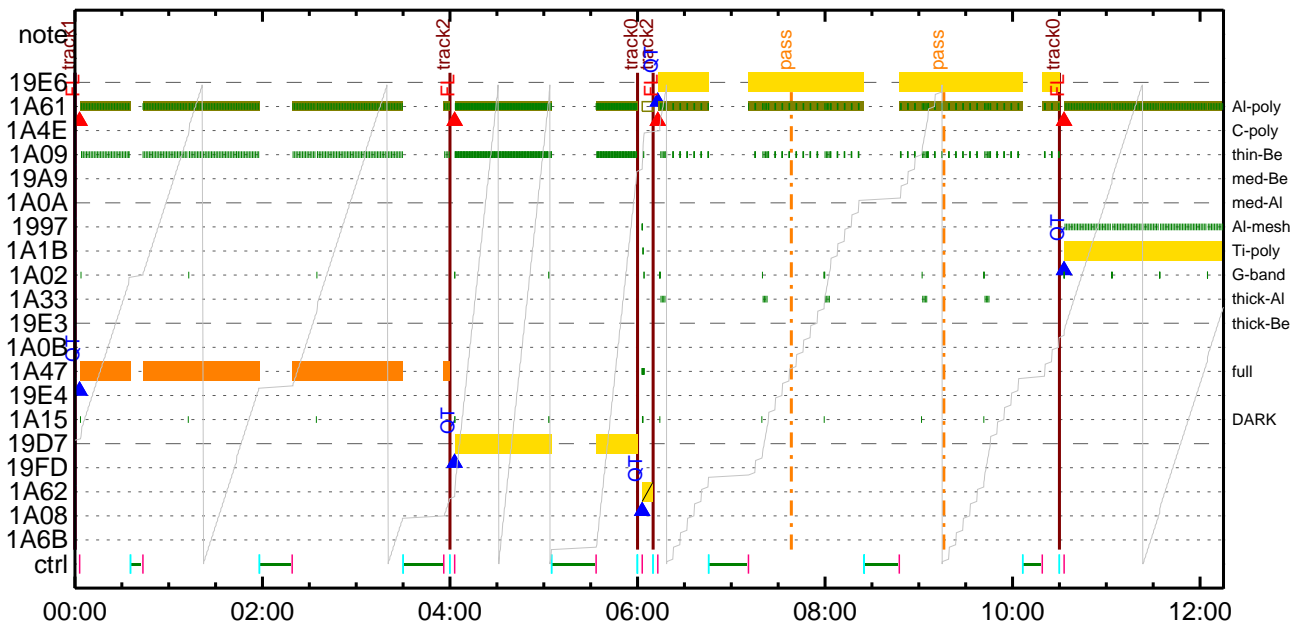
CMDI #0959 2015/03/03



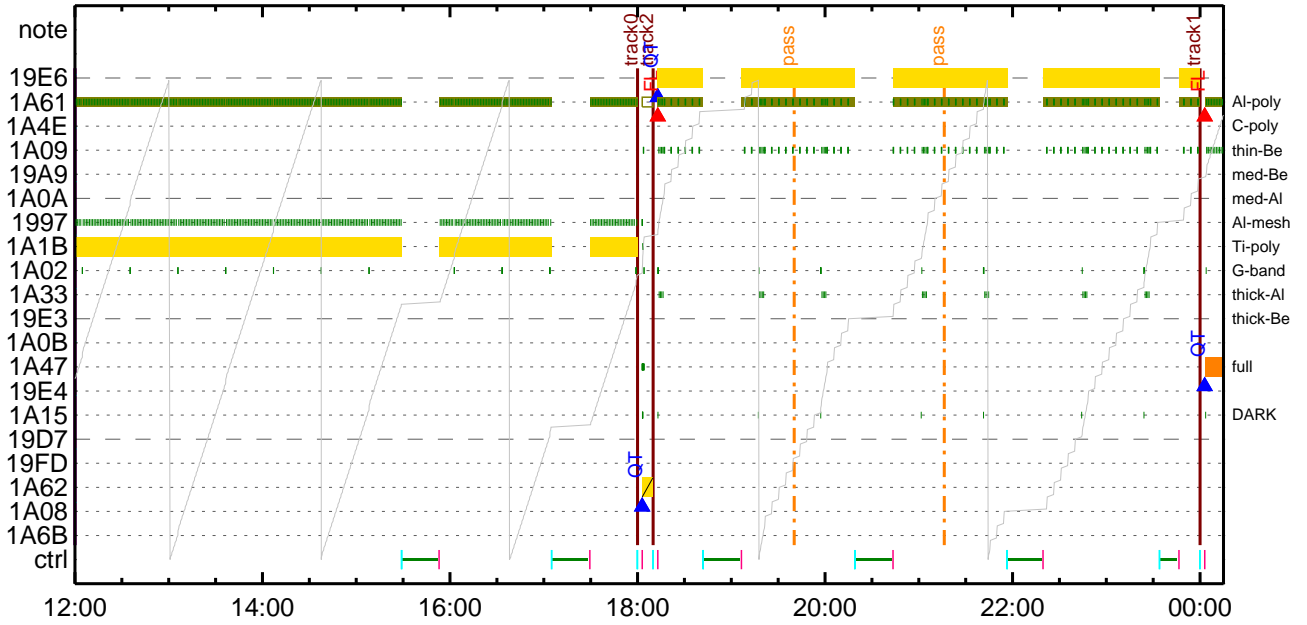
CMDI #0959 2015/03/03



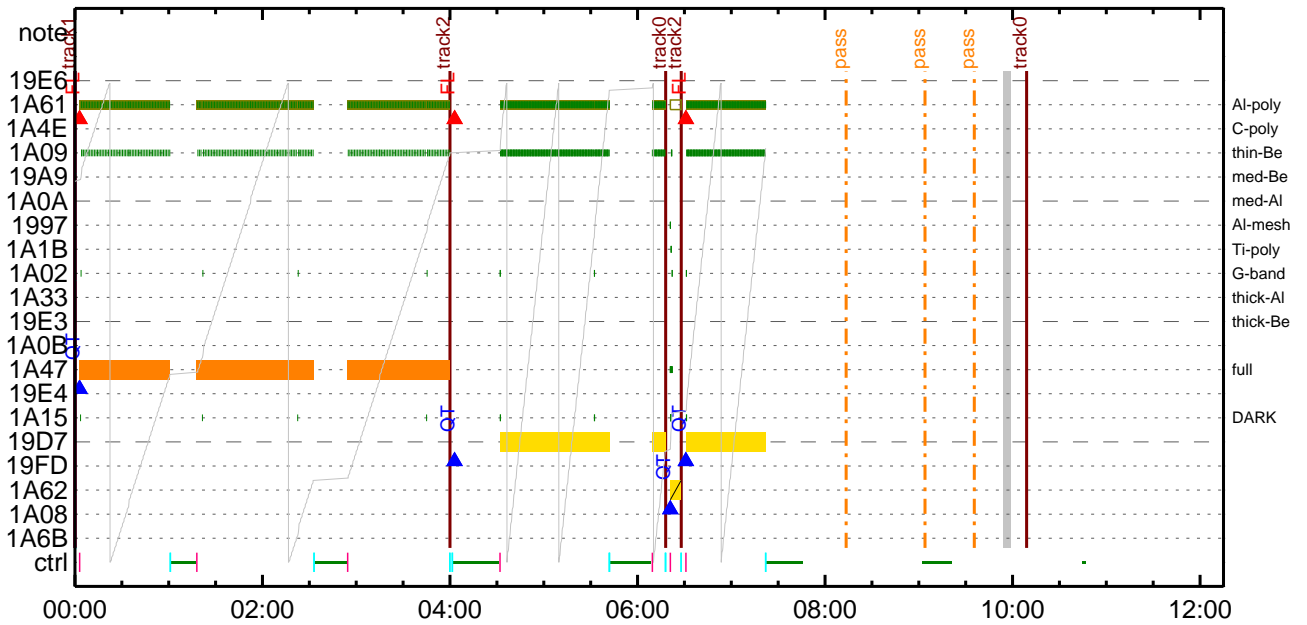
CMDI #0959 2015/03/04



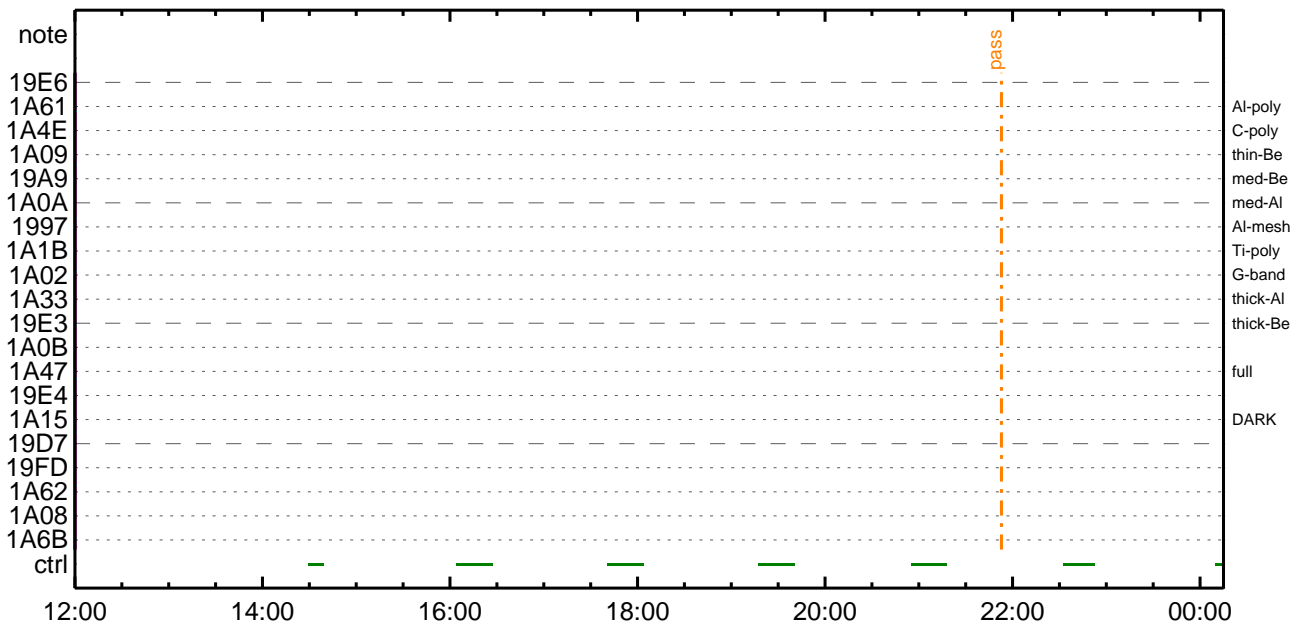
CMDI #0959 2015/03/04



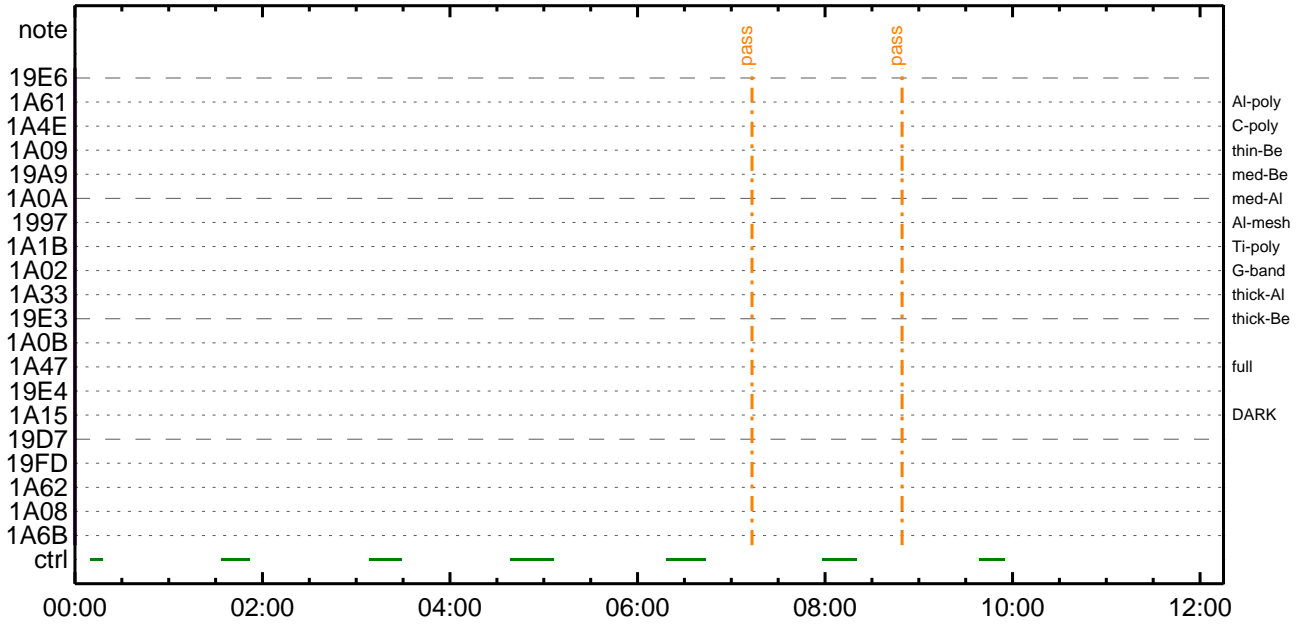
CMDI #0959 2015/03/05



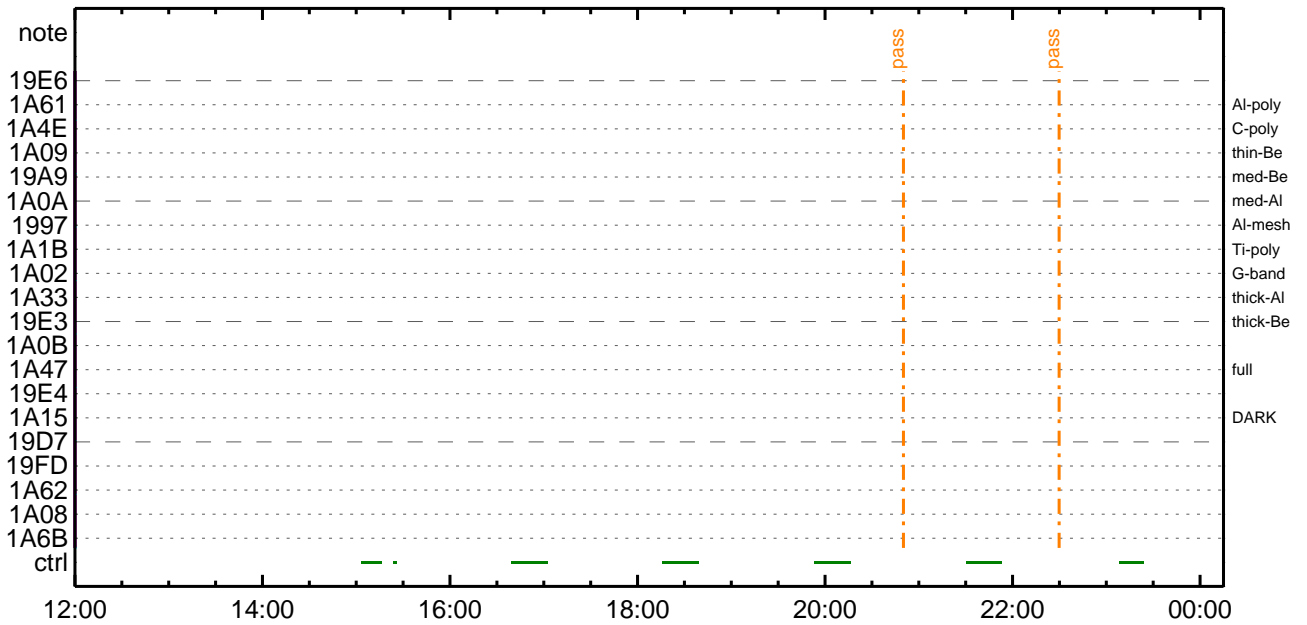
CMDI #0959 2015/03/05



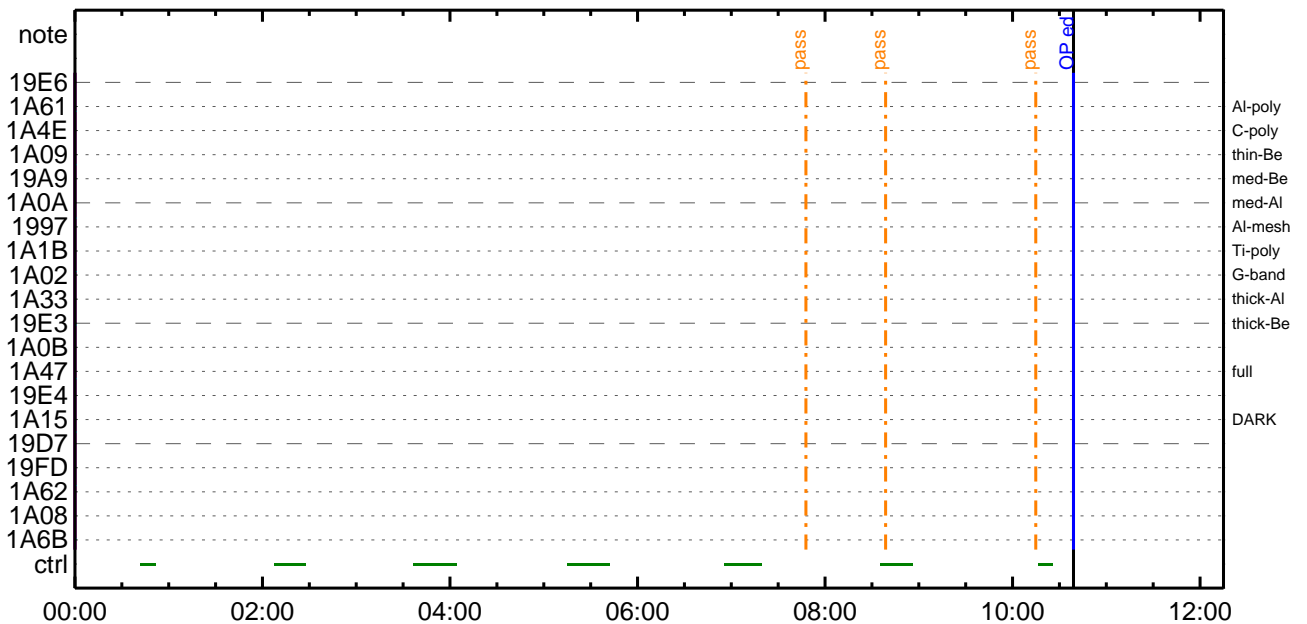
CMDI #0959 2015/03/06



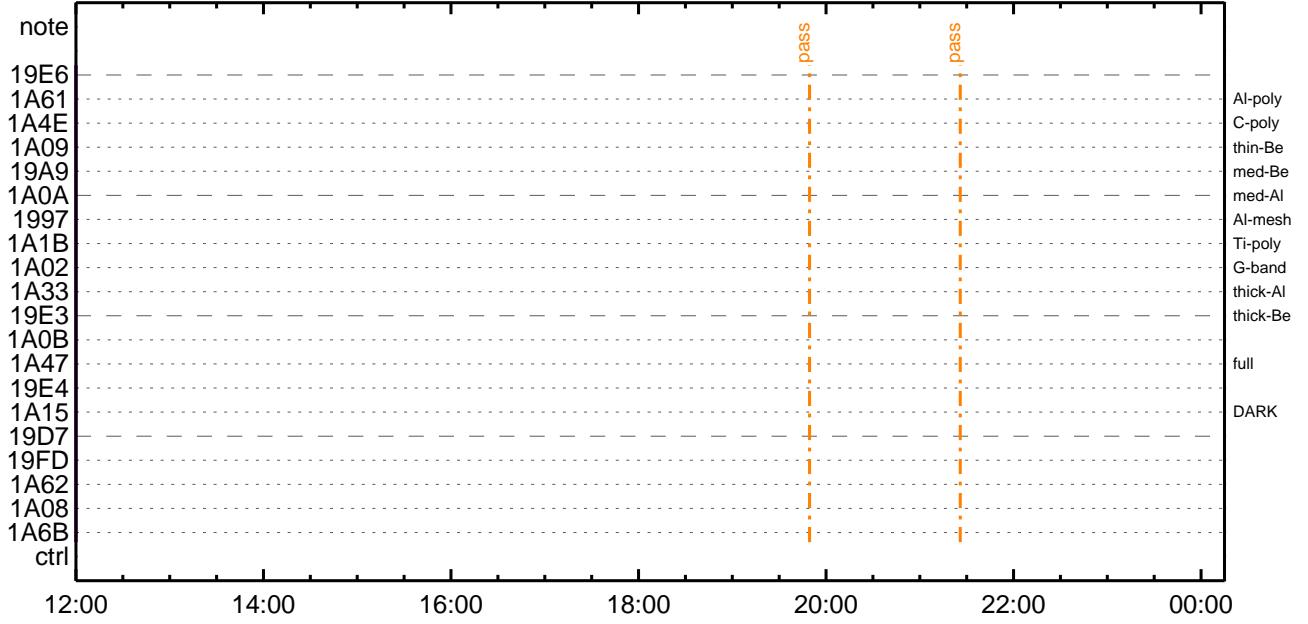
CMDI #0959 2015/03/06



CMDI #0959 2015/03/07



CMDI #0959 2015/03/07



(a) Spacecraft Operation Procedure (real-commands)

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main-977 2015-03-03 12:29:38 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YBYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿òÁó•µ°Ë»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÓó•ò¿¼í¹çòí;çÁ®, ùò¹òèòòòçÁ+¿®ó•òÈòòò³òÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-977:OP
0020 ()
0021 . S. OG og-977:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍî°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYóY×¼ªª î»òò³ íÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòí¼È¹ç•è²ÍOKòò³ íÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYóY×¼ªª î»òò³ íÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòí¼È¹ç•è²ÍOKòò³ íÇ§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYóY×¼ªª î»òò³ íÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPòí¼È¹ç•è²ÍOKòò³ íÇ§
0081 C.
0082 . C. ***** òÈ²¼òí¼Ä´¶ÍòòÈÈ-òòÁ+¿® (¼áµ-YÁYóY×¼½¿çòòÁÓÃæçªªóª°ò¼¼í¹çòçòá) *****
0083 C. DHUYâ;¼YÉ;È¼Y¼; Yí;¼YÉ;Èòòíáò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADò-Á+¿®NGòí¼È¹ç; ç°È²¼òí¼TI-CMDÁ+¿®òí¼Á¹Òò•òÈòòò³òÈ; f
```



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0096 C.                0303;çSET0EDUMP01Æ±°iYÑY¹0Ç¹00|0³0E;E
0097 C.
0098 . C. TIY³YF¥ÖYÉ00ÀDİç(UT)
0099 +. TI 2015-03-03 09:15:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2015-03-03 09:15:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2015-03-03 09:15:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2015-03-03 09:19:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. °E²¼0İÄè%îÍÑ0İYÁY§YÁY-¹àİÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 . C. YÄYÖY×¼³¹İ»00³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK00³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼,¥î;¼YÈ;È00İá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C. Execute, after the success of OP upload.
0157 +. TI 2015-03-03 09:19:00.0
0158 DC 07-F0 MDP_XRT_MODE_STBY
0159 BC (c3)
0160 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0161 C.
0162 C. ***** XRT END *****
0163 . C. Stop EIS observation and temporarily disable EIS mode changes
0164 C.
0165 C.
0166 C. ***** Start EIS operation (TI set) *****
0167 C. Execute, after the success of OP upload.
0168 C. Set EIS TI-commands
0169 +. TI 2015-03-03 09:19:30.0
0170 DC 07-FC EIS_MODE_MANU
0171 BC (21 02)
0172 +. TI 2015-03-03 09:19:40.0
0173 DC 07-FC EIS_MODE_CHG_DIS
0174 BC (22)
0175 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0176 C. ***** End EIS operation (TI set) *****
0177 C.
0178 C.
0179 C. *****
0180 C. SOT TI command set
0181 C. *****
0182 C. Execute, after the success of OP upload.
0183 +. TI 2015-03-03 09:19:16.0
0184 DC 07-F0 MDP_SOT_MODE_STBY
0185 BC (41)
0186 . C. -----
0187 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0188 C. -----
0189 C. ***** SOT END *****
0190 C.
0191 . C. ***** MDP `úÄİ0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C. (%â°İYÖYÄYÉYB¥YÁYÇYè0È¼00¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

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```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-978 2015-03-03 12:29:38 247 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Ä»Û;ã
0005 C.
0006 C. YÀYŞ;¼Y³YÞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁÍ;ËçµÄµ°Æ»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ËÈèµ•íÉ;ÈöÈ¼°ÇÖµ•µç¼l¹ççÍ;çÀ®, ùµ¹µöµçµçÁ+ç®µ•µËµµµ³µÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+çµ;ON
0016 C. *****
0017 C. ç" °ÆÀ, Í×ÈYµäLOSµçççÍ»p´Öµð¹ÍÍ, µ•; çÉÖÍ×µÈXÁÖONµí¹ÖµËµ¹µËµµµ³µÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY~¾ÖÄÖµççç; ç°È²¼µí°ÆÀ, ¼Èçµµð¼Ä¹Öµ¹µË;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°ÆÀ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëµ•µçµç¼l¹ççÍ; ç°È²¼µí°ÆÀ¹Öµµµ°; çDCBC-150µççÈµà;ç
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYËYÉÄÜÁÖ;ËÄ•Ä°²óÈð;Ë, áµí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, µ-¼«Æ°Ää»ßµ•µçç, á; ç°È²¼µµð¼Ä¹Öµ¹µË;ç
0055 C. YçYÓYÉYËYÉÄÜÁÖµççÄ•Ä°²óÈðµ-¼áµµ¼l¹ççÍ´°Í»µ¹µöµçµçÁÖµÀ;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°ÆÀ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëµ•µçµç¼l¹ççÍ; ç°È²¼µí°ÆÀ¹Öµµµ°; çDCBC-151µççÈµà;ç
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYËYÉÄÜÁÖ;ËÄ•Ä°²óÈð;Ë, áµí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Ää»ß;çXÁ+çµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Ää»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+çµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 5869705.3 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 C.
0139 C. ***** XRT START *****
0140 C.
0141 +. DC 07-F0 MDP_XRT_CTRL_MANU
0142 BC (c1)
0143 + DC 07-F0 MDP_XRT_MODE_STBY
0144 BC (c3)
0145 . C. ----- Success Verify ? OK / NG_____
0146 C.
0147 C. XRT Obs. Table Upload
0148 . S. RAM ram-291:MDP_OBS_X
0149 ( )
0150 C.
0151 +. DC 07-F0 MDP_DUMP_XRTTBL
0152 BC (84 07 00 00 00 3a d4)
0153 . C. ----- Comparison Check ? OK / ERR _____
0154 C.
0155 C.
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 01 b1 b1 04 04)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 02 b1 b1 08 08)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 03 b1 b1 08 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 04 b1 b1 06 06)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 05 85 83 06 06)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 06 85 83 06 06)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 07 85 83 08 08)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 08 85 83 04 04)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 09 80 80 20 20)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 0a 80 80 20 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 0b 80 80 08 20)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 0c 80 80 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 0f 80 80 06 06)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 10 80 80 08 08)
0184 + DC 07-F0 MDP_XRT_FLD_ENA
0185 BC (d8)
0186 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0187 BC (c8)
0188 + DC 07-F0 MDP_XRT_AEC_RESET
0189 BC (d0)
0190 + DC 07-F0 MDP_XRT_ARS_DIS
0191 BC (d5)
0192 + DC 07-F0 MDP_XRT_FLD_RESET
0193 BC (da)

```

0194 . C. ----- Success Verify ? OK / NG ____
0195 C.
0196 C.
0197 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0198 C.
0199 +. DC 07-F0 MDP_XRT_MODE_OBSV
0200 BC (c2)
0201 +. TI 2015-03-03 09:19:02.0
0202 DC 07-F0 MDP_XRT_MODE_OBSV
0203 BC (c2)
0204 . C. ----- Success Verify ? OK / NG ____
0205 C.
0206 C. ***** XRT END *****
0207 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0208 +. DC 07-FC EIS_MODE_CHG_ENA
0209 BC (20)
0210 . C. Verify EIS_MODE_CHG_FLG is ENA
0211 +. DC 07-FC EIS_MODE_MANU
0212 BC (21 02)
0213 . C. Verify EIS in MANUAL mode
0214 . C. Estimated OBSTBL upload time is 58s
0215 C. *****
0216 C. EIS START OBSTBL LOAD
0217 C. *****
0218 . S. RAM ram-820:EIS_OBSTBL
0219 (
0220 +. DC 07-FC EIS_DUMP_OBSTBL
0221 BC (07 07 07 00 00 70 00)
0222 C.
0223 C. Execute, after the success of OBSTBL upload.
0224 C. Set EIS TI-commands
0225 +. TI 2015-03-03 09:19:50.0
0226 DC 07-FC EIS_MODE_CHG_ENA
0227 BC (20)
0228 . C. [] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0229 C. *****
0230 C. EIS END OBSTBL LOAD
0231 C. *****
0232 C.
0233 . C. ***** MDP `úÃîñî»ö%ÝñËÂðñ¹ñêDCBC•x²è *****
0234 C. (%á°îÝÓÝÃÝËÝËÝËÝáÝçÝèñË%¼ñ¼Ã»Ûñ¹ñè)
0235 . S. DC-BC dcbc-402:DCBC
0236 (MDP_known_event)
0237 C.
0238 C.
0239 . C. ***** ÝDÝ¹•Ï Daily±¿ÎññË´Øñ¹ñèDCBC•x²è *****
0240 . S. DC-BC dcbc-153:DCBC
0241 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0242 C.
0243 C.
0244 . C. ;ãLOSÝÁÝ§ÝÃÝ¹¼Ã»Û;ã
0245 C.
0246 . C. ***** LOS *****
0247 C.

(a) Spacecraft Operation Procedure (real-commands)

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main-979 2015-03-03 12:29:38 85 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Ä»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÏYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëø¿ðÄø•µºË»ÍxÁÇøíYçYÁYxYí;¼YÉ;ËÈèµ•ííË;ËøÈ¼ºÇÔø•ø¿¼í¹çøí;çÄ®, ùø¹øÈøÈøÇÄ+¿®ø•øÈøøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-266:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁYxYí;¼YÉøíÄºøÈSTS_CHKøðOFFøÈø¹øÈ
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKøðONøÈø¹øÈ
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 . C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2015-03-03 09:19:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 . C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 . C. ***** MDP `ÛÄíøí»ø¼YøÈÄø¹øÈDCBC•x²è *****
0072 C. (¼áºíYÓYÁYÈYßYÈYáYçYÈøÈ¼øø¼Ä»Ûø¹øÈ)
0073 . S. DC-BC dcbc-402:DCBC
0074 (MDP_known_event)
0075 C.
0076 C.
0077 . C. ***** YDY!·Ï Daily±¿íÑøÈ`Øø¹øÈDCBC•x²è *****
0078 . S. DC-BC dcbc-153:DCBC
0079 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080 C.
0081 C.
0082 . C. ;ãLOSÁY$YÁY~¼Ä»Û;ã
0083 C.
0084 . C. ***** LOS *****
0085 C.

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Mar 03, 15 12:29

XRT_OGLIST_0959.chk

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*** OP Sequence for XRT ***

2015/03/03	09:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	09:29:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	09:29:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	09:29:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/03	09:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	09:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	09:29:58.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2015/03/03	09:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/03/03	09:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/03	09:33:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/03/03	09:34:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/03	09:34:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/03	09:34:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	09:34:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	09:34:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	09:36:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2015/03/03	09:36:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/03	09:46:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/03	09:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/03/03	11:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/03/03	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/03	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/03	11:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	11:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	11:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2015/03/03	11:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/03	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	14:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	14:54:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	14:54:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	14:54:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/03	14:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/03	15:07:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/03	15:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	15:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	15:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	15:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/03/03	15:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/03/03	15:10:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/03/03	15:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/03/03	15:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	15:12:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/03/03	15:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

2015/03/03	15:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/03	15:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	15:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	15:20:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/03/03	15:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 ac cd
2015/03/03	15:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/03	15:20:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/03	15:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/03	15:20:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/03	15:22:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/03	15:22:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/03/03	15:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/03/03	15:35:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/03	15:35:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	15:35:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	15:36:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/03/03	15:36:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 d6 67
2015/03/03	15:36:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/03	15:36:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/03	15:36:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/03	15:36:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/03	15:38:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/03	15:38:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/03/03	15:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/03/03	15:51:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/03	15:51:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	15:51:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	15:52:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/03/03	15:52:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00
2015/03/03	15:52:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/03	15:52:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/03	15:52:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/03	15:52:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/03	15:54:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/03	15:54:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/03/03	15:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/03/03	16:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/03	16:07:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	16:07:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	16:08:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/03/03	16:08:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 29 99
2015/03/03	16:08:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/03	16:08:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/03	16:08:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/03	16:08:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/03	16:10:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/03			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e

2015/03/03	16:10:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/03	16:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	16:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	16:29:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	16:29:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	16:29:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/03	16:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/03	16:53:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/03	16:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	16:59:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	16:59:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	16:59:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/03/03	16:59:30.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	00	00	53	33
2015/03/03	16:59:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/03	16:59:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/03	16:59:52.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	16:59:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	16:59:56.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	17:02:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/03/03	17:02:28.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/03	17:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	17:15:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	17:15:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	17:15:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/03/03	17:15:30.0	AOCS_Ore-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2015/03/03	17:15:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/03	17:15:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/03	17:15:52.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	17:15:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	17:15:56.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	17:18:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/03/03	17:18:28.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/03	17:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	17:25:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	17:25:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	17:25:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/03/03	17:25:30.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2015/03/03	17:25:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/03	17:25:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/03	17:25:52.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	17:25:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	17:25:56.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	17:28:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/03/03	17:28:28.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/03	17:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	17:41:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	17:41:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2015/03/03	17:41:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/03/03	17:41:30.0	AOCS_OrE-point_Start_10_OG [0x0a0] AOCU_NM	5	02-76	00 ac 5b 00 00
2015/03/03	17:41:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/03	17:41:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/03	17:41:52.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/03	17:41:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/03	17:41:56.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/03	17:44:26.0	XRT_QT_PROG_SET_427_OG [0x1ab] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/03/03	17:44:28.0	XRT_FL_PROG_SET_401_OG [0x191] MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/03/03	17:44:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/03	18:07:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	18:07:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	18:07:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/03	18:07:06.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/03/03	18:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/03/03	18:30:00.0	XRT_Custom_430_OG [0x1ae]			
2015/03/03	18:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/03	18:35:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	18:35:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	18:35:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/03/03	18:36:00.0	AOCS_OrE-point_Start_11_OG [0x0a1] AOCU_NM	5	02-76	00 b4 b5 24 8b
2015/03/03	18:36:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/03	18:36:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/03	18:36:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/03	18:36:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/03	18:36:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/03	18:38:56.0	XRT_QT_PROG_SET_427_OG [0x1ab] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/03/03	18:38:58.0	XRT_FL_PROG_SET_401_OG [0x191] MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/03/03	18:39:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/03	18:51:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	18:51:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	18:51:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/03/03	18:52:00.0	AOCS_OrE-point_Start_12_OG [0x0a2] AOCU_NM	5	02-76	00 d6 36 48 72
2015/03/03	18:52:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/03	18:52:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2015/03/03	18:52:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2015/03/03	18:52:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2015/03/03	18:52:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da
2015/03/03	18:54:56.0	XRT_QT_PROG_SET_427_OG [0x1ab] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2015/03/03	18:54:58.0	XRT_FL_PROG_SET_401_OG [0x191] MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2015/03/03	18:55:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/03/03	19:01:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	19:01:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/03/03	19:01:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2015/03/03	19:02:00.0	AOCS_OrE-point_Start_13_OG [0x0a3] AOCU_NM	5	02-76	00 29 ca b7 8e
2015/03/03	19:02:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2015/03/03	19:02:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8

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2015/03/03	19:02:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	19:02:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	19:02:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	19:04:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/03/03	19:04:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/03	19:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	19:11:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	19:11:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	19:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/03/03	19:12:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 4b 4b db 75				
2015/03/03	19:12:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/03	19:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/03	19:12:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	19:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	19:12:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	19:14:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/03/03	19:14:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/03	19:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	19:27:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	19:27:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	19:27:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/03/03	19:28:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 53 a5 00 00				
2015/03/03	19:28:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/03	19:28:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/03	19:28:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	19:28:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	19:28:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	19:30:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2015/03/03	19:30:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/03	19:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	19:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	19:43:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	19:43:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	19:43:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/03	19:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/03	20:07:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/03	20:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	20:12:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	20:12:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	20:12:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/03/03	20:13:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00 4b 4b 24 8b				
2015/03/03	20:13:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/03	20:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/03	20:13:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	20:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	20:13:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	20:15:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				

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2015/03/03	20:15:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/03	20:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	20:28:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	20:28:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	20:28:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2015/03/03	20:29:00.0	AOCS_OrE-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	29 db 48 72			
2015/03/03	20:29:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/03	20:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/03	20:29:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	20:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	20:29:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	20:31:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2015/03/03	20:31:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/03	20:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	20:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	20:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2015/03/03	21:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00 00 00 00			
2015/03/03	21:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/03	21:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/03	21:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/03	21:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/03	21:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	21:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2015/03/03	21:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/03	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	21:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	21:21:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	21:21:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	21:21:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/03	21:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/03	21:44:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/03	21:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	22:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	22:58:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	22:58:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/03	22:58:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/03	23:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/03	23:17:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/03	23:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/03	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/03	23:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2015/03/04	00:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	00 00 00 00			
2015/03/04	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/04	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/04	00:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/04	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							

2015/03/04	00:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/03/04	00:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/04	00:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2015/03/04	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2015/03/04	00:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/04	00:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	00:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	00:35:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/04	00:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/03/04	00:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/03/04	00:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0	
2015/03/04	01:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/04	01:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	01:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	01:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/04	02:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/03/04	02:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/03/04	02:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0	
2015/03/04	03:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/04	03:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	03:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	03:30:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/04	03:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/03/04	03:55:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/03/04	03:56:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0	
2015/03/04	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/04	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	03:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	04:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/03/04	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02	00 00 00 00
2015/03/04	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/03/04	04:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/03/04	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/03/04	04:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/03/04	04:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/04	04:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2015/03/04	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2015/03/04	05:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/04	05:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	05:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	05:05:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/04	05:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/03/04	05:32:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/03/04	05:33:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0	
2015/03/04	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/04	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/04	06:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
			AOCU_NM	5	02-76	00	00 00 00 00

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2015/03/04	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/03/04	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/03/04	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/04	06:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/03/04	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/03/04	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/03/04	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/04	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/04	06:10:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/04	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/04	06:10:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	06:12:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2015/03/04	06:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/04	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	06:45:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	06:45:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	06:45:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	06:45:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/04	06:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/04	07:10:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/04	07:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	08:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	08:25:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	08:25:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	08:25:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/04	08:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/04	08:46:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/04	08:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	10:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	10:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	10:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	10:06:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/04	10:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/04	10:18:00.5	XRT_Custom_430_OG [0x1ae]							
2015/03/04	10:19:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	10:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	10:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/03/04	10:30:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00 55 58 01 99				
2015/03/04	10:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/04	10:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/04	10:30:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/04	10:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/04	10:30:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	10:32:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				

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2015/03/04	10:32:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2015/03/04	10:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	15:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	15:29:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	15:29:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	15:29:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/04	15:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/04	15:52:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/04	15:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	17:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	17:05:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	17:05:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	17:05:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/04	17:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/04	17:28:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/04	17:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00			
2015/03/04	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00	00 00			
2015/03/04	18:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/03/04	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/03/04	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/04	18:02:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2015/03/04	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00			
2015/03/04	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00	00 00			
2015/03/04	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/04	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/04	18:10:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/04	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/04	18:10:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	18:12:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2015/03/04	18:12:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/04	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	18:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	18:42:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	18:42:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	18:42:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/04	18:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/04	19:05:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/04	19:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	20:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	20:19:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	20:19:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	20:19:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2015/03/04	20:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/04	20:42:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/04	20:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	21:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	21:56:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	21:56:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	21:56:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/04	21:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/04	22:18:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/04	22:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	23:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	23:34:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	23:34:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/04	23:34:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/04	23:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/04	23:45:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/04	23:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/04	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/04	23:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/03/05	00:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/03/05	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/05	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/03/05	00:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/03/05	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/03/05	00:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/05	00:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2015/03/05	00:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2015/03/05	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/05	01:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/05	01:01:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/05	01:01:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/05	01:01:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/05	01:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/05	01:17:00.0	XRT_Custom_430_OG [0x1ae]							
2015/03/05	01:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/05	02:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/05	02:33:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/05	02:33:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/03/05	02:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/03/05	02:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/03/05	02:53:30.0	XRT_Custom_430_OG [0x1ae]							
2015/03/05	02:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/03/05	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/05	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/03/05	03:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/03/05	04:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/03/05	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/03/05	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				

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2015/03/05	04:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/03/05	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/03/05	04:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/05	04:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/05	04:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/05	04:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/05	04:01:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/03/05	04:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2015/03/05	04:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2015/03/05	04:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/03/05	04:31:00.0	XRT_Custom_430_OG [0x1ae]					
2015/03/05	04:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/05	05:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/05	05:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/05	05:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/05	05:42:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/03/05	05:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/03/05	06:08:30.0	XRT_Custom_430_OG [0x1ae]					
2015/03/05	06:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/05	06:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/05	06:17:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/05	06:17:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2015/03/05	06:18:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00	00
2015/03/05	06:18:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2015/03/05	06:20:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2015/03/05	06:20:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/03/05	06:20:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2015/03/05	06:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/05	06:27:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/05	06:27:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/05	06:27:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2015/03/05	06:28:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00	00
2015/03/05	06:28:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/03/05	06:28:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/03/05	06:28:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/03/05	06:28:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/03/05	06:28:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/05	06:30:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2015/03/05	06:30:58.0	XRT_FL_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2015/03/05	06:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/03/05	07:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/05	07:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/03/05	07:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/03/05	07:22:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/03/05	07:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/03/05	10:09:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00	00