

XRT Timeline to be uploaded on 2015/10/15

Period: 2015/10/15 10:45:00 - 2015/10/20 10:10:00

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Normal mode

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XOB #1AC4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (3ms)

Term	Pointing (x, y)	Comment
10/15 10:58:00 - 10/15 12:59:54	Track (-782.6, -205.7) @ 10/15 10:55:00	# OP start + 10min/AR12434 tracking
10/16 18:15:00 - 10/17 05:48:24	Track (-586.5, -226.1) @ 10/16 18:12:00	AR12434 tracking
10/17 06:01:30 - 10/17 09:32:00	Track (-497.5, -231.9) @ 10/17 05:58:30	AR12434 tracking

PROG= 19 Inf.-time(s)

Subr= 2	1-time(s)	2.0sec													
Seqn= 56 1-time(s) 2.0sec			Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
			Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 4-time(s) 2.0sec			Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
			Al-poly/Open	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62 15-time(s) 120.0sec			thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #1AE2: AR (Filter-Ratio with Al/poly and thin-Be), 128x128 at 1064, 1048 with G-band (3ms/3ms leak) 30s cad - AEC3- HOP268

Term	Pointing (x, y)	Comment
10/15 13:03:00 - 10/15 18:40:30	Track (-794.0, -246.0) @ 10/15 13:00:00	HOP268 AR12434 with DST and IRIS
10/16 13:03:00 - 10/16 17:38:30	Track (-656.5, -262.7) @ 10/16 13:00:00	HOP268 (AR12434)

PROG= 01 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec													
Seqn= 56 1-time(s) 2.0sec			Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
			Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec			Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	128x128 (1064, 1048)	Q=95	3	0	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	128x128 (1064, 1048)	Q=95	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #1ADE: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(16/362/1443) + TH

Term	Pointing (x, y)	Comment
10/15 19:08:00 - 10/15 19:14:54	Fixed (0.0, 0.0)	synoptic, shifted 60min
10/16 06:19:00 - 10/16 06:25:54	Fixed (0.0, 0.0)	synoptic, shifted 16.0 min
10/16 18:05:00 - 10/16 18:11:54	Fixed (0.0, 0.0)	synoptic, shifted 2.0 min
10/17 05:51:30 - 10/17 05:58:24	Fixed (0.0, 0.0)	synoptic, shifted -11.5 min

PROG= 05 1-time(s)

Subr= 1	1-time(s)	2.0sec													
Seqn= 5 1-time(s) 2.0sec			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 49 1-time(s) 2.0sec			Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 39 1-time(s) 2.0sec			thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec			Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
			Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

XOB #1AE5: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
10/15 19:18:00 - 10/15 20:17:30	Track (-736.7, -211.8) @ 10/15 19:15:00	AR12434 tracking
10/16 05:03:00 - 10/16 05:40:00	Track (-677.0, -218.3) @ 10/16 05:00:00	AR12434 tracking
PROG= 13 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
Subr= 2 5-time(s) 2.0sec		
Seqn= 75 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
Seqn= 23 2-time(s) 60.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 2.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 2.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AC2: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,1ms) - Leak (2x2,1ms) - 120s cad

Term	Pointing (x, y)	Comment
10/15 20:45:00 - 10/15 20:56:54	Fixed (-929.0, 206.0)	HOP79 1/21
10/15 21:00:00 - 10/15 21:11:54	Fixed (-762.0, 163.0)	HOP79 2/21
10/15 21:15:00 - 10/15 21:26:54	Fixed (-584.0, 144.0)	HOP79 3/21
10/15 21:30:00 - 10/15 21:55:00	Fixed (-400.0, 133.0)	HOP79 4/21
10/15 22:21:00 - 10/15 22:32:54	Fixed (-210.0, 126.0)	HOP79 5/21
10/15 22:36:00 - 10/15 22:47:54	Fixed (-16.0, 124.0)	HOP79 6/21
10/15 22:51:00 - 10/15 23:02:54	Fixed (178.0, 126.0)	HOP79 7/21
10/15 23:06:00 - 10/15 23:32:00	Fixed (368.0, 133.0)	HOP79 8/21
10/15 23:48:30 - 10/16 00:00:24	Fixed (552.0, 144.0)	HOP79 9/21
10/16 00:03:30 - 10/16 00:14:54	Fixed (730.0, 163.0)	HOP79 10/21
10/16 00:18:00 - 10/16 01:00:00	Fixed (897.0, 206.0)	HOP79 11/21
10/16 01:19:30 - 10/16 01:31:24	Fixed (-911.0, -26.0)	HOP79 12/21
10/16 01:34:30 - 10/16 01:46:24	Fixed (-712.0, -26.0)	HOP79 13/21
10/16 01:49:30 - 10/16 02:01:24	Fixed (-512.0, -26.0)	HOP79 14/21
10/16 02:04:30 - 10/16 02:31:00	Fixed (-313.0, -26.0)	HOP79 15/21
10/16 02:55:30 - 10/16 03:07:24	Fixed (-114.0, -26.0)	HOP79 16/21
10/16 03:10:30 - 10/16 03:22:24	Fixed (85.0, -26.0)	HOP79 17/21
10/16 03:25:30 - 10/16 03:37:24	Fixed (284.0, -26.0)	HOP79 18/21
10/16 03:40:30 - 10/16 03:59:30	Fixed (483.0, -26.0)	HOP79 19/21
10/16 04:33:00 - 10/16 04:44:54	Fixed (682.0, -26.0)	HOP79 20/21
10/16 04:48:00 - 10/16 04:59:54	Fixed (881.0, -26.0)	HOP79 21/21
PROG= 09 Inf.-time(s)		
Subr= 1 45-time(s) 120.0sec		
Seqn= 29 1-time(s) 4.0sec		
thin-Be/Open	med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024)	Q=98 2 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 26 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1AD0: AR Abnormal-B(Morphology), HOP245 Be-thin w/ 24s cad and Be-med PFB, multifilter context w/ 20 min cad, G-band test w/ 60 min cad, 192x

Term	Pointing (x, y)	Comment
10/16 06:29:00 - 10/16 12:59:54	Track (-637.4, -226.2) @ 10/16 06:26:00	HOP245 (AR12434)
PROG= 06 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 81 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 3ms Obs 1x1 192x256 (1064, 1048)	Q=95 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 3ms Obs 1x1 192x256 (1064, 1048)	Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 192x256 (1064, 1048)	Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Dark 250ms Obs 1x1 192x256 (1064, 1048)	Q=98 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Dark 250ms Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
Subr= 2 3-time(s) 2.0sec		
Seqn= 66 3-time(s) 2.0sec		
Open/thick-Al	Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
med-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
med-Al/Open	Open/thick-Al close Safe Norm 2.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
Seqn= 88 50-time(s) 2.0sec		

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	192x256 (1064, 1048)	Q=95	3	0	8.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	8.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	8.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
10/15 10:58:00 - 10/15 12:59:54	Track (-782.6, -205.7) ^{Ⓜ 10/15 10:55:00}	# OP start + 10min/AR12434 tracking
10/15 13:03:00 - 10/15 18:40:30	Track (-794.0, -246.0) ^{Ⓜ 10/15 13:00:00}	HOP268 AR12434 with DST and IRIS
10/15 19:18:00 - 10/15 20:17:30	Track (-736.7, -211.8) ^{Ⓜ 10/15 19:15:00}	AR12434 tracking
10/15 20:45:00 - 10/15 20:56:54	Fixed (-929.0, 206.0)	HOP79 1/21
10/15 21:00:00 - 10/15 21:11:54	Fixed (-762.0, 163.0)	HOP79 2/21
10/15 21:15:00 - 10/15 21:26:54	Fixed (-584.0, 144.0)	HOP79 3/21
10/15 21:30:00 - 10/15 21:55:00	Fixed (-400.0, 133.0)	HOP79 4/21
10/15 22:21:00 - 10/15 22:32:54	Fixed (-210.0, 126.0)	HOP79 5/21
10/15 22:36:00 - 10/15 22:47:54	Fixed (-16.0, 124.0)	HOP79 6/21
10/15 22:51:00 - 10/15 23:02:54	Fixed (178.0, 126.0)	HOP79 7/21
10/15 23:06:00 - 10/15 23:32:00	Fixed (368.0, 133.0)	HOP79 8/21
10/15 23:48:30 - 10/16 00:00:24	Fixed (552.0, 144.0)	HOP79 9/21
10/16 00:03:30 - 10/16 00:14:54	Fixed (730.0, 163.0)	HOP79 10/21
10/16 00:18:00 - 10/16 01:00:00	Fixed (897.0, 206.0)	HOP79 11/21
10/16 01:19:30 - 10/16 01:31:24	Fixed (-911.0, -26.0)	HOP79 12/21
10/16 01:34:30 - 10/16 01:46:24	Fixed (-712.0, -26.0)	HOP79 13/21
10/16 01:49:30 - 10/16 02:01:24	Fixed (-512.0, -26.0)	HOP79 14/21
10/16 02:04:30 - 10/16 02:31:00	Fixed (-313.0, -26.0)	HOP79 15/21
10/16 02:55:30 - 10/16 03:07:24	Fixed (-114.0, -26.0)	HOP79 16/21
10/16 03:10:30 - 10/16 03:22:24	Fixed (85.0, -26.0)	HOP79 17/21
10/16 03:25:30 - 10/16 03:37:24	Fixed (284.0, -26.0)	HOP79 18/21
10/16 03:40:30 - 10/16 03:59:30	Fixed (483.0, -26.0)	HOP79 19/21
10/16 04:33:00 - 10/16 04:44:54	Fixed (682.0, -26.0)	HOP79 20/21
10/16 04:48:00 - 10/16 04:59:54	Fixed (881.0, -26.0)	HOP79 21/21
10/16 05:03:00 - 10/16 05:40:00	Track (-677.0, -218.3) ^{Ⓜ 10/16 05:00:00}	AR12434 tracking
10/16 06:29:00 - 10/16 12:59:54	Track (-637.4, -226.2) ^{Ⓜ 10/16 06:26:00}	HOP245 (AR12434)
10/16 13:03:00 - 10/16 17:38:30	Track (-656.5, -262.7) ^{Ⓜ 10/16 13:00:00}	HOP268 (AR12434)
10/16 18:15:00 - 10/17 05:48:24	Track (-586.5, -226.1) ^{Ⓜ 10/16 18:12:00}	AR12434 tracking
10/17 06:01:30 - 10/17 09:32:00	Track (-497.5, -231.9) ^{Ⓜ 10/17 05:58:30}	AR12434 tracking

PROG= 07 30-time(s)

Subr= 1 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 84 1-time(s) 2.0sec	Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

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Active Region Search

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NOT USED

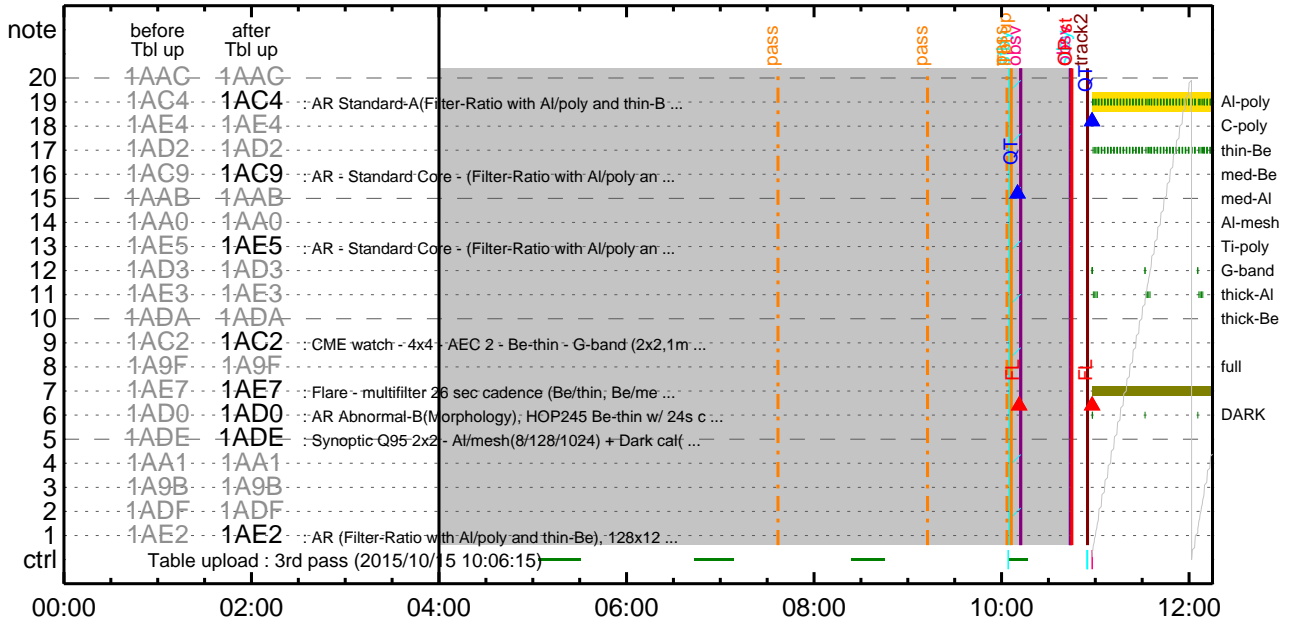
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Flare Detection

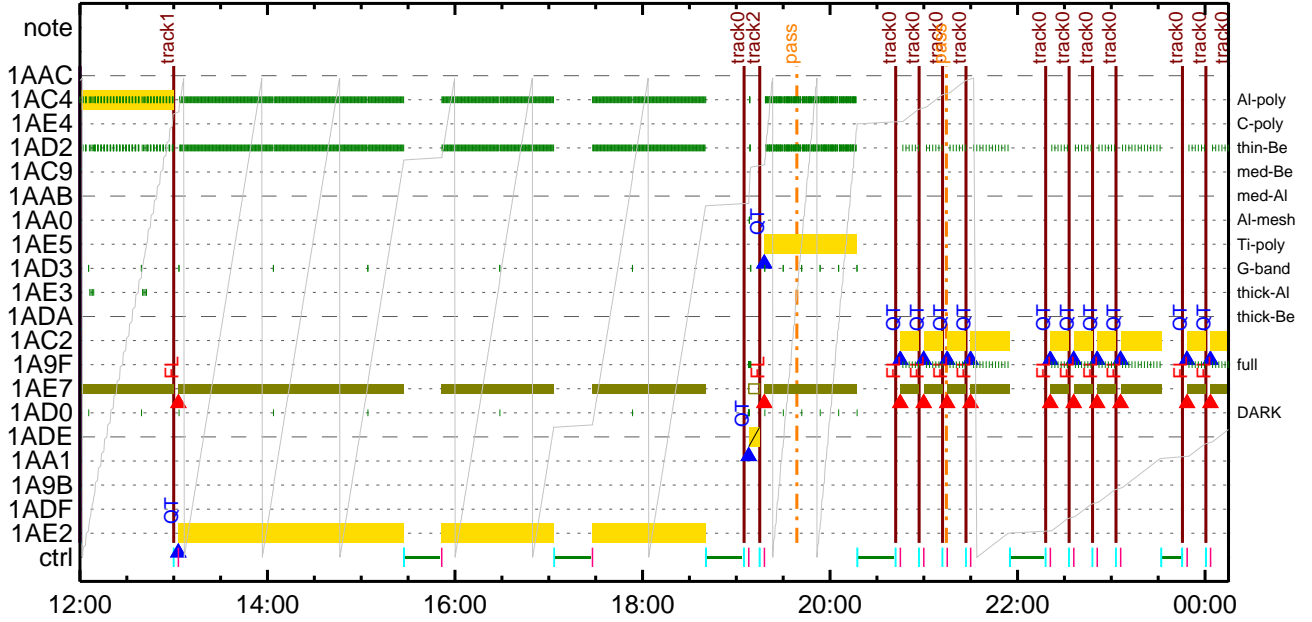
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FLD Patrol												
Term	Pointing (x, y)		Comment									
10/15 19:15:18 - 10/16 06:16:18	Track (-736.7, -211.8) ^{Ⓜ 10/15 19:15:00}		AR12434 tracking									
10/16 06:26:18 - 10/16 18:02:18	Track (-637.4, -226.2) ^{Ⓜ 10/16 06:26:00}		HOP245 (AR12434)									
10/16 18:12:18 - 10/17 05:48:48	Track (-586.5, -226.1) ^{Ⓜ 10/16 18:12:00}		AR12434 tracking									
10/17 05:58:48 - 10/20 10:10:00	Track (-497.5, -231.9) ^{Ⓜ 10/17 05:58:30}		AR12434 tracking									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8			Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

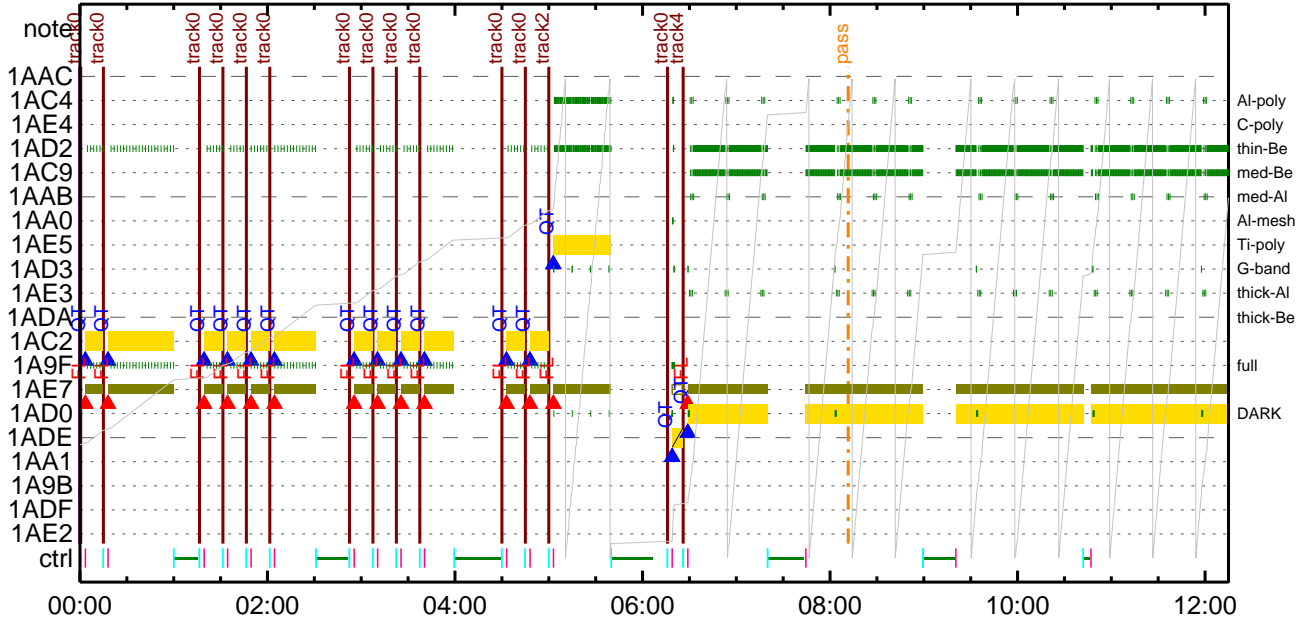
CMDI #0459 2015/10/15



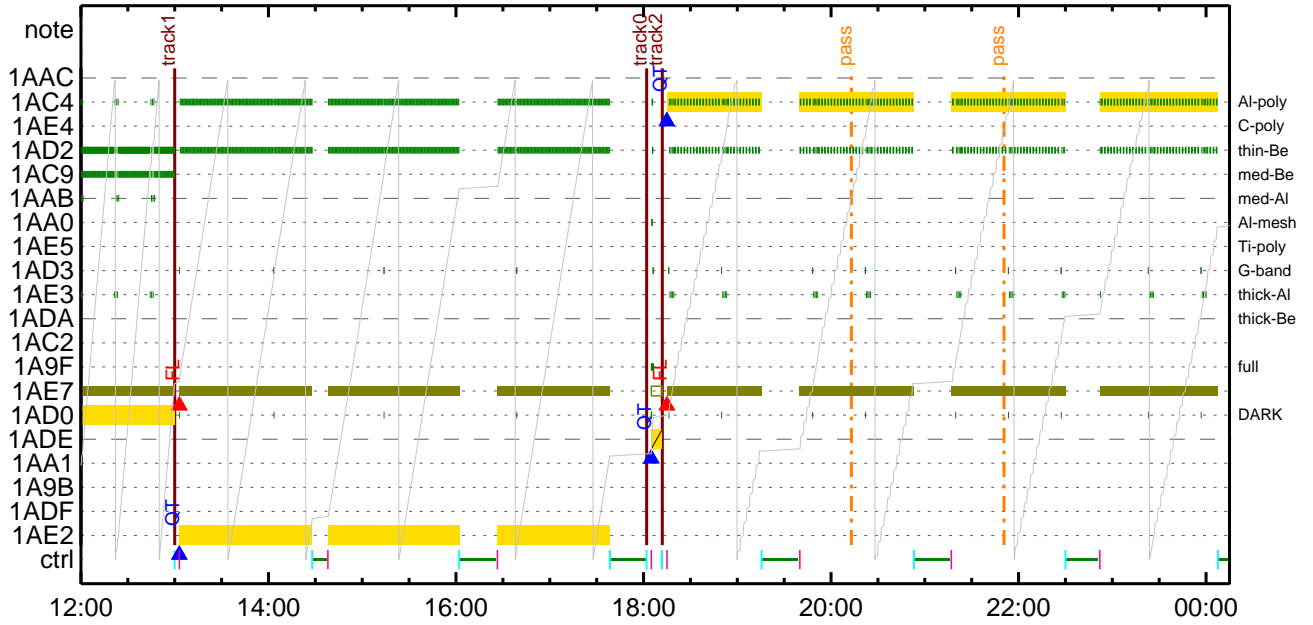
CMDI #0459 2015/10/15



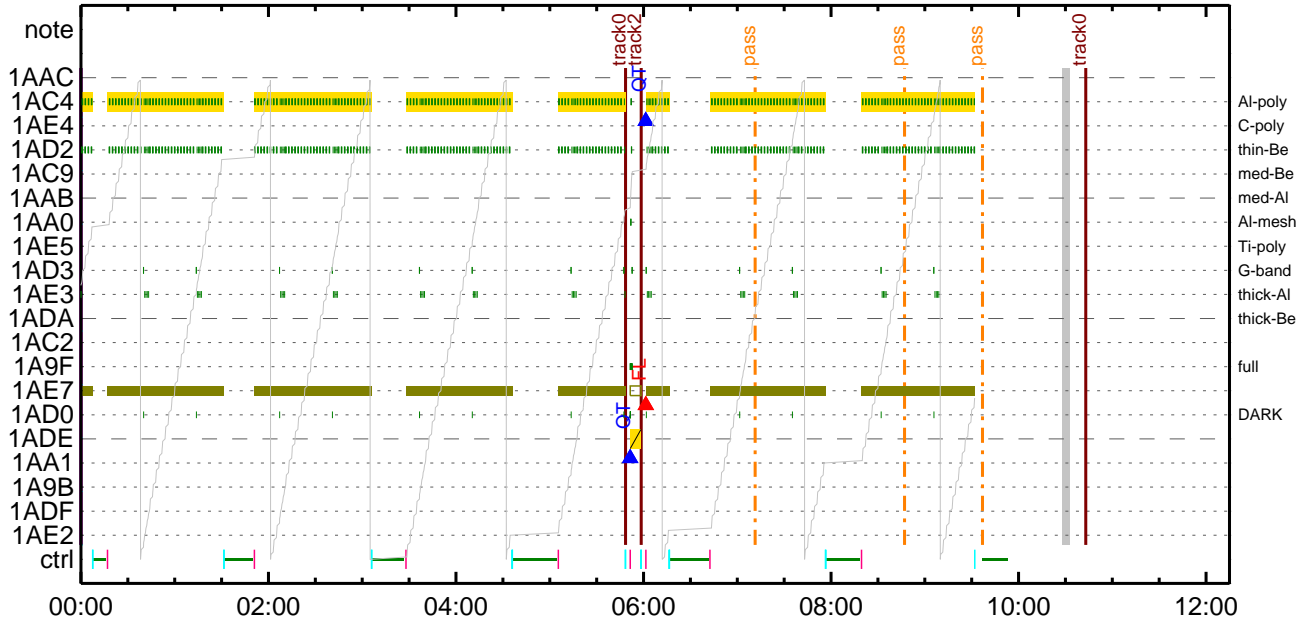
CMDI #0459 2015/10/16



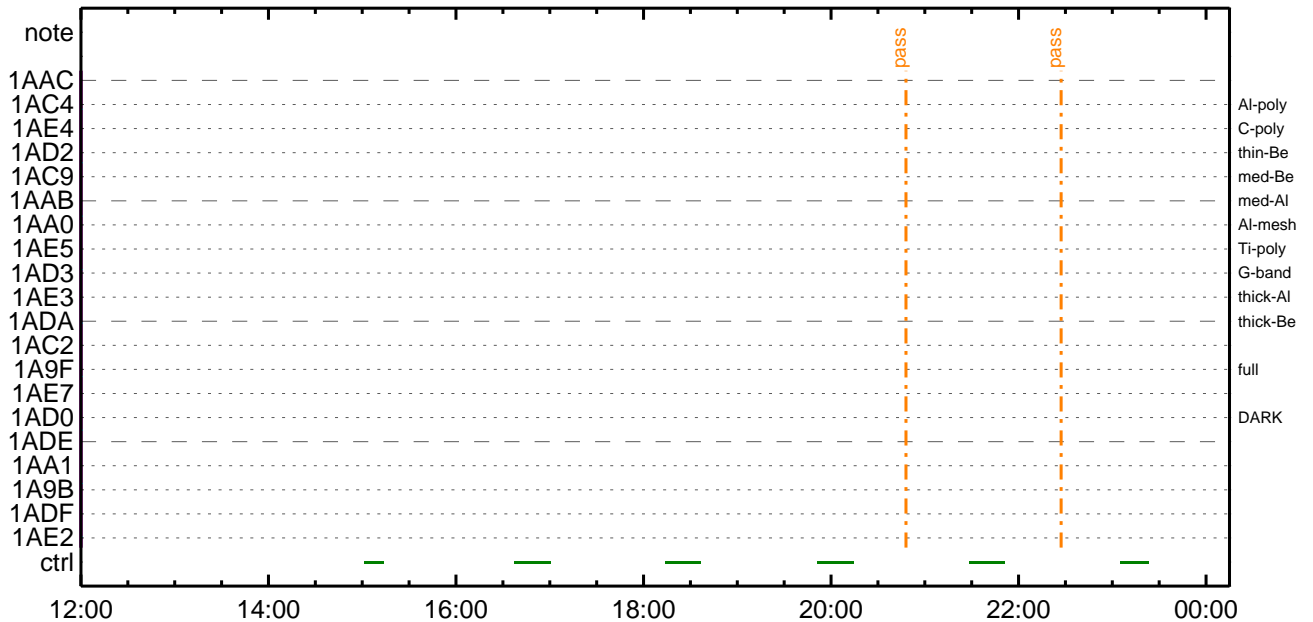
CMDI #0459 2015/10/16



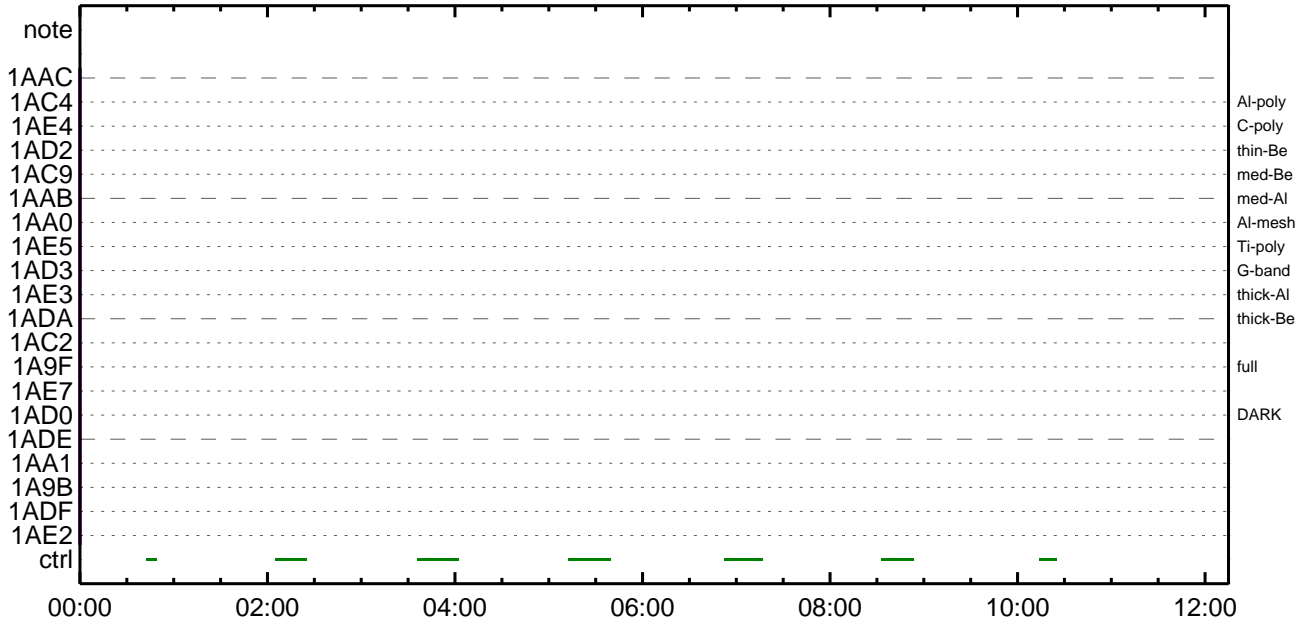
CMDI #0459 2015/10/17



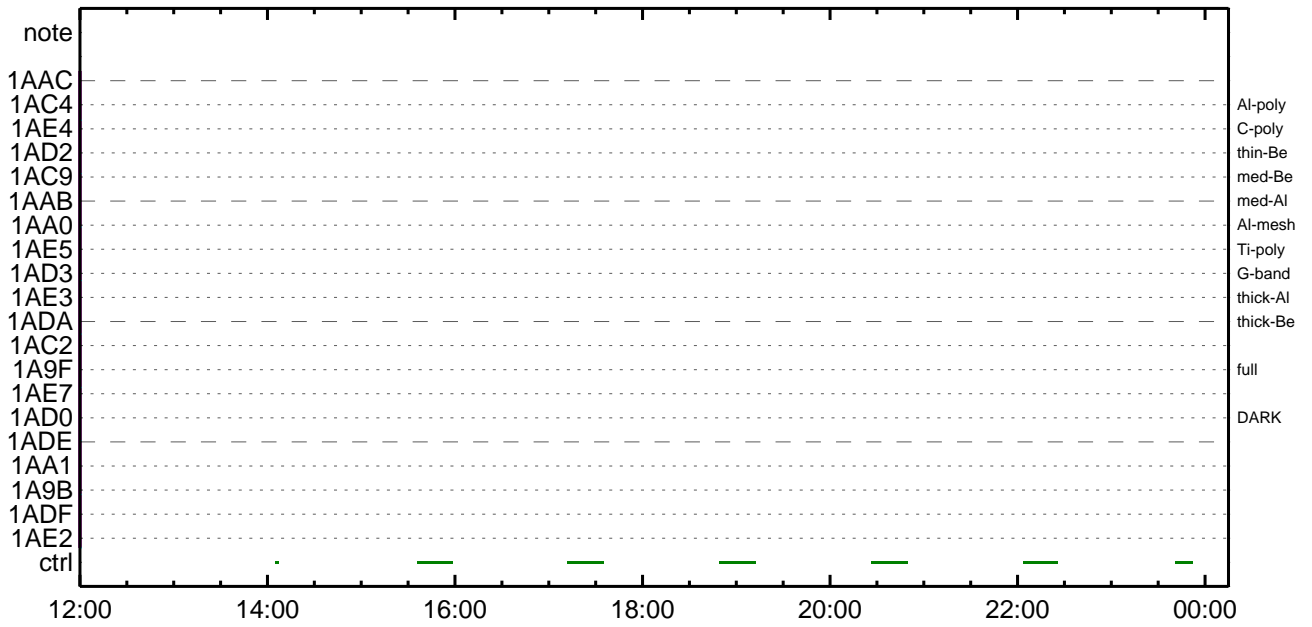
CMDI #0459 2015/10/17



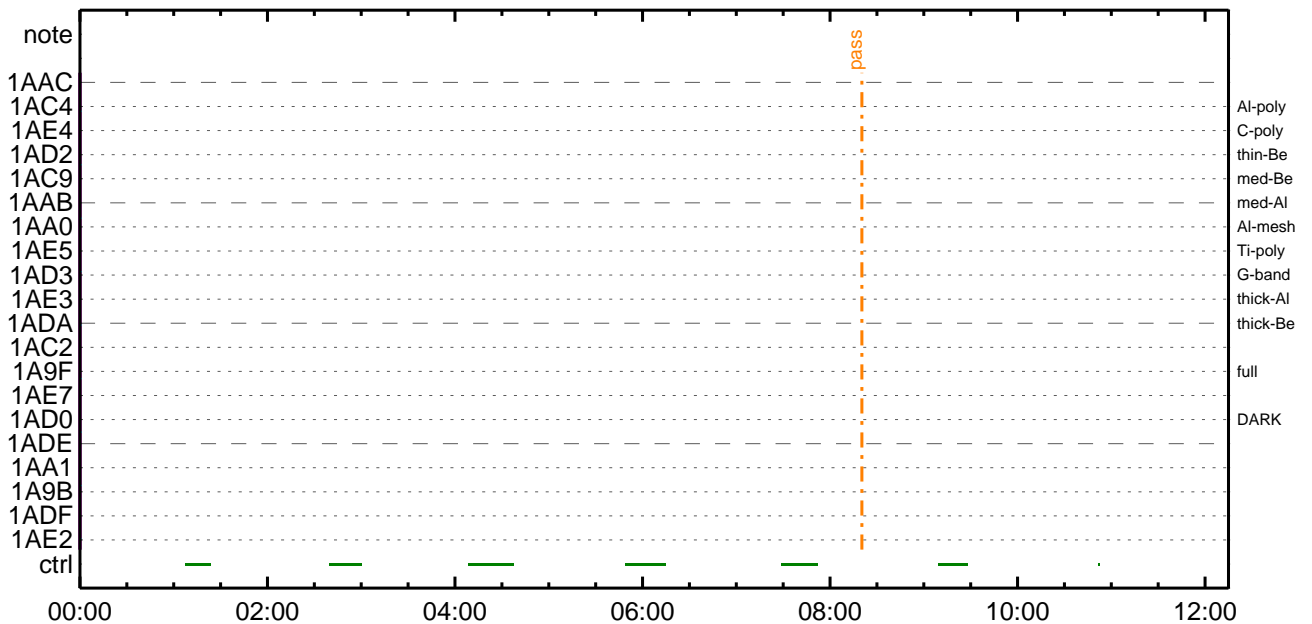
CMDI #0459 2015/10/18



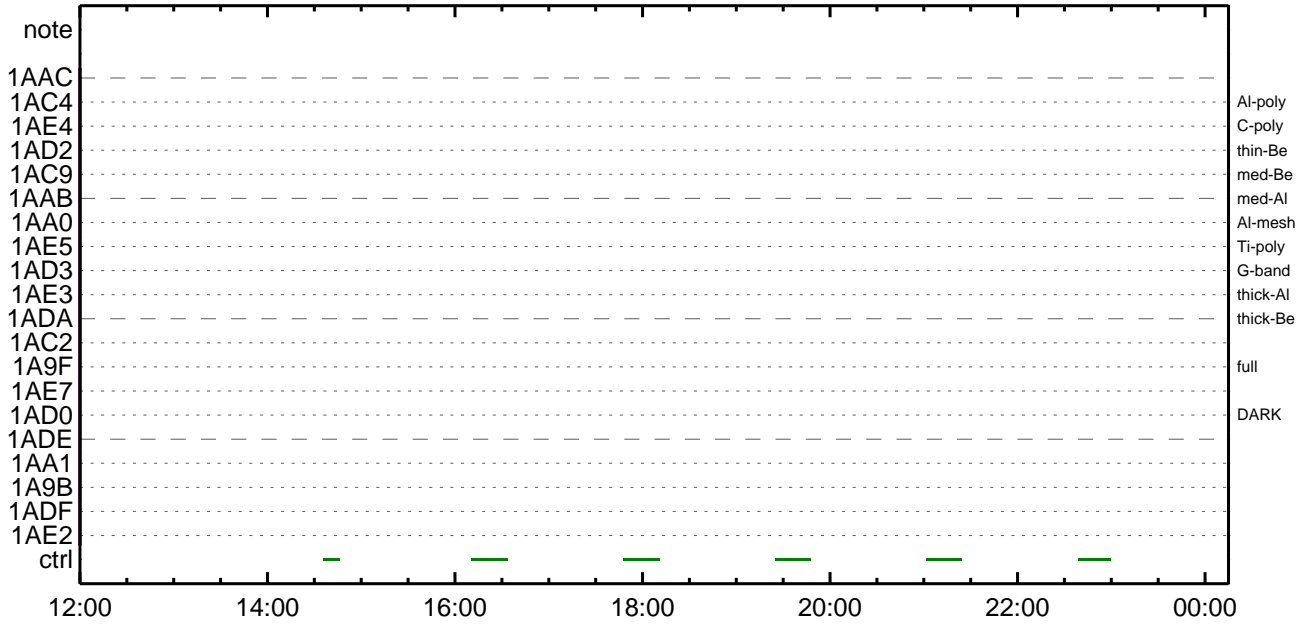
CMDI #0459 2015/10/18



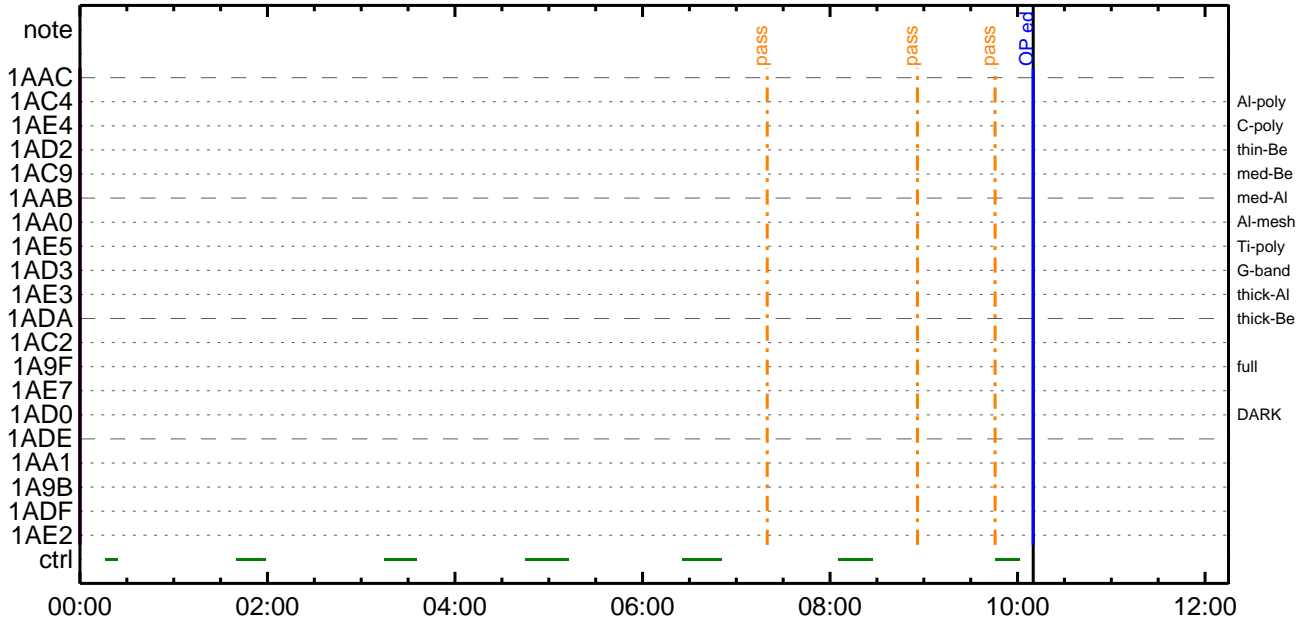
CMDI #0459 2015/10/19



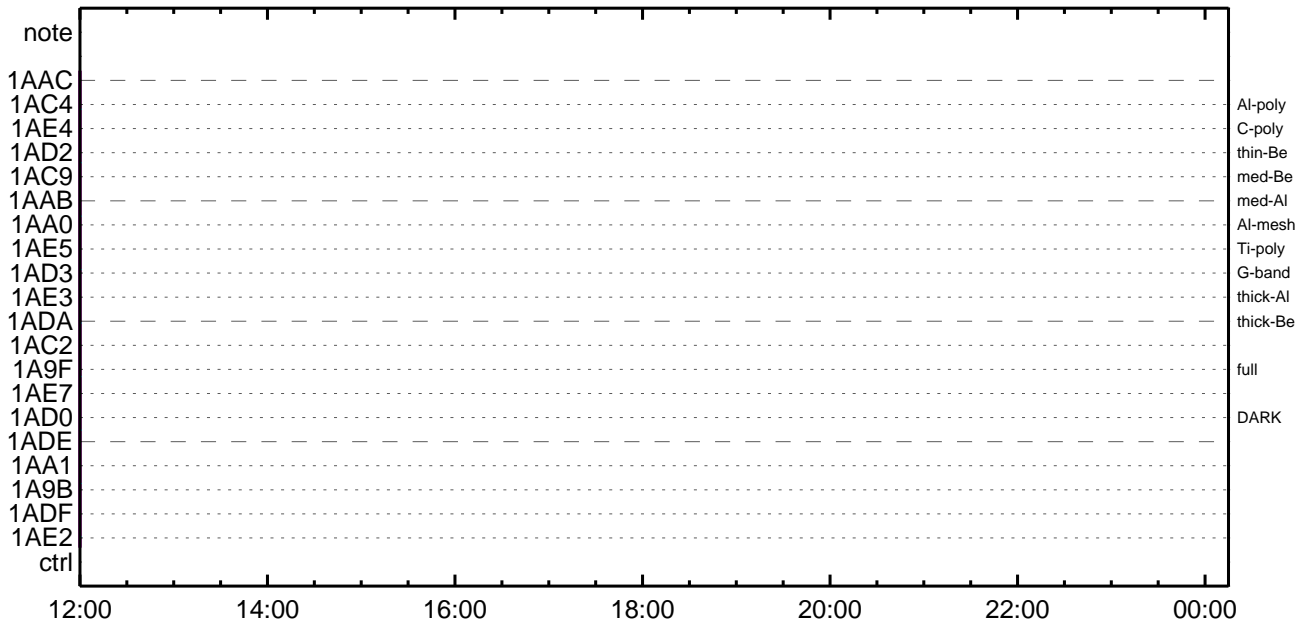
CMDI #0459 2015/10/19



CMDI #0459 2015/10/20



CMDI #0459 2015/10/20




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-431:OP
0104 ( )
0105 S. OG og-431:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPf°eYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»oð³îç§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî½ª¹ç•ë²îOKoð³îç§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªªî»oð³îç§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî½ª¹ç•ë²îOKoð³îç§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªªî»oð³îç§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî½ª¹ç•ë²îOKoð³îç§
0165 C.
0166 C. ***** oÊ²¼oî½ª¹ç•ë²îOKoð³îç§ *****
0167 C. DHUYã;4YE;Ê½Y½;Yi;4YE;Êoðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADã-Á÷ç@NGuî½ª¹ç;ç°Ê²¼oî½ª¹ç•ë²îOKoð³îç§ã
0180 C. oð³îç§;çSETãEDUMPAî½ª¹ç•ë²îOKoð³îç§ã
0181 C.
0182 C. TIY³YpYóYÊoðîãî½ª¹ç(UT)
0183 +. TI 2015-10-15 10:40:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2015-10-15 10:40:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2015-10-15 10:40:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-433 2015-10-15 13:27:08 152 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YFÏÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Ëø¿ðÄø•µ°È»ÍxÀÇøíYçYÄY×Yí;¼YÉ;ËËè%µ•ííË;ËøÈ¼°ÇÓø•ø¿¼l¹çøí;çÄ®, ùø¹øðøðøÇÄ+¿®ø•øËøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-268:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-284:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2015-10-15 10:44:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F0 MDP_XRT_CTRL_MANU
0068 BC (c1)
0069 + DC 07-F0 MDP_XRT_MODE_STBY
0070 BC (c3)
0071 . C. ----- Success Verify ? OK / NG____
0072 C.
0073 C. XRT Obs. Table Upload
0074 . S. RAM ram-291:MDP_OBS_X
0075 ( )
0076 C.
0077 +. DC 07-F0 MDP_DUMP_XRTTBL
0078 BC (84 07 00 00 00 3a d4)
0079 . C. ----- Comparison Check ? OK / ERR ____
0080 C.
0081 C.
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 01 b1 b1 04 04)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 02 b1 b1 08 08)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 03 b1 b1 08 08)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 04 b1 b1 06 06)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 05 85 83 06 06)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 06 85 83 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 07 85 83 08 08)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 85 83 02 02)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 80 80 20 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 80 80 20 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0b 80 80 08 20)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 0c 85 83 03 04)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0f 80 80 06 06)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 10 80 80 08 08)
0110 + DC 07-F0 MDP_XRT_FLD_ENA
0111 BC (d8)
0112 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0113 BC (c8)
0114 + DC 07-F0 MDP_XRT_AEC_RESET
0115 BC (d0)
0116 + DC 07-F0 MDP_XRT_ARS_DIS
0117 BC (d5)
0118 + DC 07-F0 MDP_XRT_FLD_RESET
0119 BC (da)
0120 + DC 07-F0 MDP_XRT_QT_PROG_SET
0121 BC (c4 10)
0122 + DC 07-F0 MDP_XRT_FL_PROG_SET
0123 BC (c5 07)
0124 . C. ----- Success Verify ? OK / NG ____
0125 C.
0126 C.
0127 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0128 C.
0129 + DC 07-F0 MDP_XRT_MODE_OBSV
0130 BC (c2)
0131 + TI 2015-10-15 10:44:02.0
0132 DC 07-F0 MDP_XRT_MODE_OBSV
0133 BC (c2)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C. ***** XRT END *****
0137 C.
0138 . C. ***** MDP `úÃîñî»ö¼ÝñÊÃÐñ¹ñèDCBC•x²è *****
0139 C. (%á°î¥Ó¥Ã¥Ê¥Þ¥Ë¥á¥ç¥èñÊ¼ññ¼Ã»Ûñ¹ñè)
0140 . S. DC-BC dcbc-402:DCBC
0141 (MDP_known_event)
0142 C.
0143 C.
0144 . C. ***** ¥Ð¥¹•İ Daily±¿İññÊ´Øñ¹ñèDCBC•x²è *****
0145 . S. DC-BC dcbc-153:DCBC
0146 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0147 C.
0148 C.
0149 . C. ;ãLOS¥Á¥§¥Ã¥¬¼Ã»Û;ã
0150 C.
0151 . C. ***** LOS *****
0152 C.
```

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*** OP Sequence for XRT ***

2015/10/15	10:54:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	10:54:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	10:54:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/10/15	10:55:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/10/15	10:55:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/15	10:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/15	10:55:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/15	10:55:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/15	10:55:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/15	10:57:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2015/10/15	10:57:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/10/15	10:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/15	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	12:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/10/15	13:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/10/15	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/15	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/15	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/15	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/15	13:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/15	13:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2015/10/15	13:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/10/15	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/15	15:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	15:27:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	15:27:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/15	15:27:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/15	15:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/15	15:50:30.0	XRT_Custom_430_OG [0x1ae]							
2015/10/15	15:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/15	17:03:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	17:03:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	17:03:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/15	17:03:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/15	17:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/15	17:27:00.0	XRT_Custom_430_OG [0x1ae]							
2015/10/15	17:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/15	18:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	18:40:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	18:40:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/15	18:40:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/15	18:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/15	19:04:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	19:04:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/15	19:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/10/15	19:05:00.0	AOCS_Ore-point_Start_3_OG [0x099]							

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2015/10/15	19:05:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2015/10/15	19:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2015/10/15	19:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2015/10/15	19:07:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]		2	07-F0	c4	05			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	05			
2015/10/15	19:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2015/10/15	19:14:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2015/10/15	19:14:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2015/10/15	19:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2015/10/15	19:15:00.0	AOCS_Ore-point_Start_1_OG [0x097]		5	02-76	02	00	00	00	00
		AOCU_NM		5	02-76	02	00	00	00	00
2015/10/15	19:15:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2015/10/15	19:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2015/10/15	19:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2015/10/15	19:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2015/10/15	19:15:26.0	XRT_FLD_RESET_433_OG [0x1b1]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2015/10/15	19:17:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]		2	07-F0	c4	0d			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0d			
2015/10/15	19:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	07			
2015/10/15	19:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2015/10/15	20:17:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2015/10/15	20:17:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2015/10/15	20:17:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2015/10/15	20:17:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2015/10/15	20:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2015/10/15	20:41:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2015/10/15	20:41:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2015/10/15	20:41:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2015/10/15	20:42:00.0	AOCS_ORe-point_Start_4_OG [0x09a]		5	02-76	00	ed	b3	52	97
		AOCU_NM		5	02-76	00	ed	b3	52	97
2015/10/15	20:42:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2015/10/15	20:42:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2015/10/15	20:42:22.0	XRT_AEC_RESET_441_OG [0x1b9]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2015/10/15	20:42:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2015/10/15	20:42:26.0	XRT_FLD_RESET_447_OG [0x1bf]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2015/10/15	20:44:56.0	XRT_QT_PROG_SET_407_OG [0x197]		2	07-F0	c4	09			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	09			
2015/10/15	20:44:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	07			
2015/10/15	20:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2015/10/15	20:56:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2015/10/15	20:56:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2015/10/15	20:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2015/10/15	20:57:00.0	AOCS_ORe-point_Start_5_OG [0x09b]		5	02-76	00	f1	82	43	be
		AOCU_NM		5	02-76	00	f1	82	43	be
2015/10/15	20:57:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2015/10/15	20:57:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2015/10/15	20:57:22.5	XRT_AEC_RESET_441_OG [0x1b9]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2015/10/15	20:57:24.5	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2015/10/15	20:57:26.5	XRT_FLD_RESET_447_OG [0x1bf]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2015/10/15	20:59:56.5	XRT_QT_PROG_SET_407_OG [0x197]		2	07-F0	c4	09			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	09			
2015/10/15	20:59:58.5	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	07			
2015/10/15	21:00:00.5	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				

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2015/10/15	21:11:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	21:11:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	21:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/10/15	21:12:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 f3 34 33 e7	
2015/10/15	21:12:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/15	21:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/15	21:12:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/15	21:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/15	21:12:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/15	21:14:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2015/10/15	21:14:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/10/15	21:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/15	21:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	21:26:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	21:26:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/10/15	21:27:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 f4 32 23 8d	
2015/10/15	21:27:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/15	21:27:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/15	21:27:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/15	21:27:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/15	21:27:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/15	21:29:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2015/10/15	21:29:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/10/15	21:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/15	21:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	21:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	21:55:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/15	21:55:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/10/15	21:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/10/15	22:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	22:17:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	22:17:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/10/15	22:18:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 f4 cd 12 a7	
2015/10/15	22:18:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/15	22:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/15	22:18:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/15	22:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/15	22:18:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/15	22:20:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2015/10/15	22:20:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/10/15	22:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/15	22:32:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	22:32:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	22:32:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/10/15	22:33:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 f4 fe 01 68	
2015/10/15	22:33:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/15	22:33:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	

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2015/10/15	22:33:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/15	22:33:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/15	22:33:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/15	22:35:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/15	22:35:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2015/10/15	22:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/10/15	22:47:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/15	22:47:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	22:47:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	22:48:00.0	AOCS_Or-e-point_Start_10_OG [0x0a0]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/10/15	22:48:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	f4 cd f0 32
2015/10/15	22:48:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/15	22:48:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/15	22:48:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/15	22:48:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/15	22:50:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/15	22:50:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2015/10/15	22:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/10/15	23:02:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/15	23:02:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	23:02:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	23:03:00.0	AOCS_Or-e-point_Start_11_OG [0x0a1]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/10/15	23:03:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	f4 32 df 4c
2015/10/15	23:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/15	23:03:22.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/15	23:03:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/15	23:03:26.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/15	23:05:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/15	23:05:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2015/10/15	23:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/10/15	23:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/15	23:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	23:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	23:32:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/15	23:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/10/15	23:45:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/10/15	23:45:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	23:45:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/15	23:45:30.0	AOCS_Or-e-point_Start_12_OG [0x0a2]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2015/10/15	23:45:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	f3 34 ce f2
2015/10/15	23:45:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/15	23:45:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/15	23:45:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/15	23:45:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/15	23:48:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/15	23:48:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07

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2015/10/15	23:48:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/16	00:00:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/16	00:00:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/16	00:00:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/10/16	00:00:30.0	AOCS_Ore-point_Start_13_OG [0x0a3]						
		AOCU_NM	5	02-76	00 f1 82 bf 1b			
2015/10/16	00:00:48.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/10/16	00:00:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/10/16	00:00:52.0	XRT_AEC_RESET_441_OG [0x1b9]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/10/16	00:00:54.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/16	00:00:56.0	XRT_FLD_RESET_447_OG [0x1bf]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/16	00:03:26.0	XRT_QT_PROG_SET_407_OG [0x197]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2015/10/16	00:03:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2015/10/16	00:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/16	00:14:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/16	00:14:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/16	00:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/10/16	00:15:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]						
		AOCU_NM	5	02-76	00 ed b3 b0 42			
2015/10/16	00:15:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/10/16	00:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/10/16	00:15:22.0	XRT_AEC_RESET_441_OG [0x1b9]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/10/16	00:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/16	00:15:26.0	XRT_FLD_RESET_447_OG [0x1bf]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/16	00:17:56.0	XRT_QT_PROG_SET_407_OG [0x197]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2015/10/16	00:17:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2015/10/16	00:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/16	01:00:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/16	01:00:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/16	01:00:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/16	01:00:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/10/16	01:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/10/16	01:16:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/16	01:16:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/16	01:16:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/10/16	01:16:30.0	AOCS_Ore-point_Start_15_OG [0x0a5]						
		AOCU_NM	5	02-76	00 02 4d 50 fd			
2015/10/16	01:16:48.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/10/16	01:16:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/10/16	01:16:52.0	XRT_AEC_RESET_441_OG [0x1b9]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/10/16	01:16:54.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/16	01:16:56.0	XRT_FLD_RESET_447_OG [0x1bf]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/16	01:19:26.0	XRT_QT_PROG_SET_407_OG [0x197]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2015/10/16	01:19:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2015/10/16	01:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/16	01:31:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/16	01:31:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/16	01:31:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2015/10/16	01:31:30.0	AOCS_Ore-point_Start_16_OG [0x0a6]						
		AOCU_NM	5	02-76	00 02 4d 3f 4b			
2015/10/16	01:31:48.0	XRT_FLD_ENA_411_OG [0x19b]						

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2015/10/16	01:31:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/10/16	01:31:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/10/16	01:31:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/10/16	01:31:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/10/16	01:34:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2015/10/16	01:34:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2015/10/16	01:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/10/16	01:46:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/16	01:46:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/16	01:46:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2015/10/16	01:46:30.0	AOCS_OrE-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00	02	4d 2d 81
2015/10/16	01:46:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/10/16	01:46:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/10/16	01:46:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/10/16	01:46:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/10/16	01:46:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/10/16	01:49:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2015/10/16	01:49:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2015/10/16	01:49:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/10/16	02:01:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/16	02:01:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/16	02:01:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2015/10/16	02:01:30.0	AOCS_OrE-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	02	4d 1b ce
2015/10/16	02:01:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/10/16	02:01:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/10/16	02:01:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/10/16	02:01:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/10/16	02:01:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/10/16	02:04:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	
2015/10/16	02:04:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07	
2015/10/16	02:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2015/10/16	02:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/16	02:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/16	02:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/10/16	02:31:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2015/10/16	02:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2015/10/16	02:52:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/16	02:52:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2015/10/16	02:52:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2015/10/16	02:52:30.5	AOCS_OrE-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00	02	4d 0a 24
2015/10/16	02:52:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2015/10/16	02:52:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2015/10/16	02:52:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2015/10/16	02:52:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2015/10/16	02:52:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da		
2015/10/16	02:55:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09	

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2015/10/16	02:55:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2015/10/16	02:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/16	03:07:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/16	03:07:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/16	03:07:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/10/16	03:07:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00 02 4d f8 73	
2015/10/16	03:07:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/16	03:07:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/16	03:07:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/16	03:07:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/16	03:07:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/16	03:10:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2015/10/16	03:10:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/10/16	03:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/16	03:22:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/16	03:22:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/16	03:22:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/10/16	03:22:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 02 4d e6 c1	
2015/10/16	03:22:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/16	03:22:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/16	03:22:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/16	03:22:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/16	03:22:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/16	03:25:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2015/10/16	03:25:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/10/16	03:25:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/16	03:37:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/16	03:37:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/16	03:37:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/10/16	03:37:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 02 4d d5 0f	
2015/10/16	03:37:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2015/10/16	03:37:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2015/10/16	03:37:52.0	XRT_AEC_RESET_441_OG [0x1b9]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2015/10/16	03:37:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2015/10/16	03:37:56.0	XRT_FLD_RESET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/16	03:40:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2015/10/16	03:40:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07	
2015/10/16	03:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2015/10/16	03:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/16	03:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/16	03:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2015/10/16	03:59:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2015/10/16	04:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2015/10/16	04:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/16	04:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2015/10/16	04:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2015/10/16	04:30:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]					

2015/10/16	04:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	02	4d	c3	65
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/16	04:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/16	04:30:22.0	XRT_AEC_RESET_441_OG [0x1b9]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/16	04:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/16	04:30:26.0	XRT_FLD_RESET_447_OG [0x1bf]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	04:32:56.0	XRT_QT_PROG_SET_407_OG [0x197]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2015/10/16	04:32:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/10/16	04:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	04:44:54.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	04:44:56.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	04:44:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/10/16	04:45:00.0	AOCS_Orе-point_Start_24_OG [0x0ae]								
			AOCU_NM	5	02-76	00	02	4d	b1	b3
2015/10/16	04:45:18.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/16	04:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/16	04:45:22.0	XRT_AEC_RESET_441_OG [0x1b9]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/16	04:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/16	04:45:26.0	XRT_FLD_RESET_447_OG [0x1bf]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	04:47:56.0	XRT_QT_PROG_SET_407_OG [0x197]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2015/10/16	04:47:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/10/16	04:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	04:59:54.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	04:59:56.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	04:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2015/10/16	05:00:00.0	AOCS_Orе-point_Start_1_OG [0x097]								
			AOCU_NM	5	02-76	02	00	00	00	00
2015/10/16	05:00:18.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/16	05:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/16	05:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/16	05:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/16	05:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	05:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2015/10/16	05:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2015/10/16	05:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	05:40:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	05:40:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	05:40:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	05:40:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/16	05:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/16	06:15:54.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	06:15:56.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	06:15:58.0	XRT_FOCUS_POSITION_403_OG [0x193]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2015/10/16	06:16:00.0	AOCS_Orе-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	00	00	00	00	00
2015/10/16	06:16:18.0	XRT_FLD_DIS_406_OG [0x196]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/10/16	06:18:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/10/16	06:18:56.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/16	06:18:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2015/10/16	06:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				

2015/10/16	06:25:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	06:25:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	06:25:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/10/16	06:26:00.0	AOCS_OrE-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2015/10/16	06:26:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/16	06:26:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/16	06:26:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/16	06:26:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/16	06:26:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	06:28:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2015/10/16	06:28:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/10/16	06:29:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	07:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	07:20:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	07:20:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	07:20:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/16	07:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/16	07:43:30.0	XRT_Custom_430_OG [0x1ae]							
2015/10/16	07:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	08:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	08:59:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	08:59:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	08:59:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/16	09:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/16	09:19:30.0	XRT_Custom_430_OG [0x1ae]							
2015/10/16	09:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	10:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	10:42:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	10:42:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	10:42:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/16	10:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/16	10:46:00.0	XRT_Custom_430_OG [0x1ae]							
2015/10/16	10:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	12:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/10/16	13:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2015/10/16	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/16	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/16	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/16	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/16	13:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	13:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2015/10/16	13:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/10/16	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	14:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	14:28:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	14:28:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2015/10/16	14:28:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/16	14:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/16	14:37:00.0	XRT_Custom_430_OG [0x1ae]							
2015/10/16	14:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	16:02:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	16:02:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	16:02:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	16:02:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/16	16:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/16	16:25:30.0	XRT_Custom_430_OG [0x1ae]							
2015/10/16	16:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	17:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	17:38:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	17:38:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	17:38:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/16	17:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/16	18:01:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	18:01:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	18:01:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2015/10/16	18:02:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2015/10/16	18:02:18.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2015/10/16	18:04:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2015/10/16	18:04:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/16	18:04:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2015/10/16	18:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	18:11:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	18:11:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	18:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2015/10/16	18:12:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2015/10/16	18:12:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2015/10/16	18:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2015/10/16	18:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2015/10/16	18:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2015/10/16	18:12:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	18:14:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2015/10/16	18:14:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2015/10/16	18:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	19:15:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	19:15:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	19:15:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	19:15:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2015/10/16	19:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2015/10/16	19:39:00.0	XRT_Custom_430_OG [0x1ae]							
2015/10/16	19:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2015/10/16	20:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	20:53:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2015/10/16	20:53:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2015/10/16	20:53:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2015/10/16	20:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/16	21:16:00.0	XRT_Custom_430_OG [0x1ae]			
2015/10/16	21:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/16	22:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/16	22:30:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/16	22:30:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/16	22:30:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/16	22:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/16	22:51:00.0	XRT_Custom_430_OG [0x1ae]			
2015/10/16	22:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/17	00:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	00:07:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	00:07:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/17	00:07:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/17	00:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/17	00:16:00.0	XRT_Custom_430_OG [0x1ae]			
2015/10/17	00:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/17	01:31:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	01:31:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	01:31:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/17	01:31:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/17	01:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/17	01:50:00.0	XRT_Custom_430_OG [0x1ae]			
2015/10/17	01:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/17	03:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	03:06:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	03:06:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/17	03:06:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/17	03:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/17	03:27:00.0	XRT_Custom_430_OG [0x1ae]			
2015/10/17	03:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/17	04:36:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	04:36:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	04:36:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2015/10/17	04:36:06.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2015/10/17	04:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2015/10/17	05:04:30.0	XRT_Custom_430_OG [0x1ae]			
2015/10/17	05:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/17	05:48:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	05:48:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	05:48:28.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2015/10/17	05:48:30.0	AOCS_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2015/10/17	05:48:48.0	XRT_FLD_DIS_406_OG [0x196]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2015/10/17	05:51:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2015/10/17	05:51:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2015/10/17	05:51:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2015/10/17	05:51:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2015/10/17	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	05:58:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2015/10/17	05:58:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			

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2015/10/17	05:58:30.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			AOCU_NM	5	02-76	02	00	00	00
2015/10/17	05:58:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2015/10/17	05:58:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2015/10/17	05:58:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2015/10/17	05:58:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2015/10/17	05:58:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/17	06:01:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2015/10/17	06:01:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2015/10/17	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/17	06:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/17	06:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/17	06:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/17	06:16:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/10/17	06:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/10/17	06:41:30.0	XRT_Custom_430_OG [0x1ae]							
2015/10/17	06:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/17	07:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/17	07:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/17	07:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2015/10/17	07:56:36.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2015/10/17	07:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2015/10/17	08:18:30.0	XRT_Custom_430_OG [0x1ae]							
2015/10/17	08:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2015/10/17	09:32:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2015/10/17	10:43:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00