# XRT Timeline to be uploaded on 2016/10/04

Period: 2016/10/04 09:45:00 - 2016/10/08 10:59:00

* * * * * **Normal mode** * * * * *

**XOB #1B18: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 10/04 09:58:00 - 10/04 11:59:54 | Track ( -95.0, -33.3) @ 10/04 09:55:00 | # OP start + 10min, HOP 323 continued from previous plan. |

**PROG= 19 Inf.-time(s)**

**Subr= 1 1-time(s) 2.0sec**

**Seqn= 56 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 3ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 3ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |

**Subr= 2 5-time(s) 2.0sec**

**Seqn= 75 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 2 | 0 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 2 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |

**Seqn= 50 4-time(s) 90.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 0 | 25.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 1 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 1 | 25.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 2 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 2 | 2.0sec |

**XOB #1B02: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 10/04 12:03:00 - 10/04 17:57:30 | Track ( -876.7, -327.4) @ 10/04 12:00:00 | # AR at NE limb with IRIS/SST. |
| 10/04 18:34:00 - 10/05 06:05:54 | Track ( -859.8, -333.6) @ 10/04 18:31:00 | # AR at SE limb. |
| 10/05 12:03:00 - 10/05 17:59:54 | Track ( -798.3, -349.3) @ 10/05 12:00:00 | # AR at NE limb with IRIS/SST. |
| 10/05 18:13:00 - 10/06 05:31:30 | Track ( -771.1, -354.5) @ 10/05 18:10:00 | # AR at SE limb. |

**PROG= 11 Inf.-time(s)**

**Subr= 1 1-time(s) 2.0sec**

**Seqn= 56 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 3ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 3ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |

**Subr= 2 5-time(s) 2.0sec**

**Seqn= 75 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 2 | 0 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 2 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |

**Seqn= 79 4-time(s) 120.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 0 | 34.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 1 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 1 | 34.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 2 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 2 | 2.0sec |

**XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Th**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 10/04 18:24:00 - 10/04 18:30:54 | Fixed ( 0.0, 0.0) | synoptic, shifted 21.0 min |
| 10/05 18:03:00 - 10/05 18:09:54 | Fixed ( 0.0, 0.0) | synoptic |
| 10/06 06:11:00 - 10/06 06:17:54 | Fixed ( 0.0, 0.0) | synoptic, shifted 8.0 min |

**PROG= 14 1-time(s)**

**Subr= 1 1-time(s) 2.0sec**

**Seqn= 5 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 8x8 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 2048x512 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 512x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |

**Seqn= 1 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 24ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 250ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 2.83s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**Seqn= 99 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 44ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 4.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

| | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **Seqn= 67 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 177ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 2.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 11.3s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Seqn= 54 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 3ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 3ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

**XOB #1B15: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(512/4096), Med-Al**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 10/05 06:09:00 - 10/05 06:15:54 | Fixed ( 0.0, 0.0) | synoptic, shifted 6.0 min |

| | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **PROG= 18 1-time(s)** | | | | | | | | | | | | | |
| **Subr= 1 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| **Seqn= 5 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 8x8 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 2048x512 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 512x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| **Seqn= 1 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 24ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 250ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 2.83s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Seqn= 99 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 44ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 4.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Seqn= 67 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 177ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 2.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 11.3s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Seqn= 54 1-time(s) 4.0sec** | | | | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 3ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 3ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Subr= 2 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| **Seqn= 46 2-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/thick-Be | Open/thick-Be | close | Safe | Norm | 64.0s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| **Seqn= 58 2-time(s) 2.0sec** | | | | | | | | | | | | | |
| Al-poly/Ti-poly | Al-poly/thick-Al | close | Safe | Norm | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Al-poly/Ti-poly | Al-poly/thick-Al | close | Safe | Norm | 4.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| **Seqn= 71 2-time(s) 2.0sec** | | | | | | | | | | | | | |
| med-Al/Open | med-Al/Open | close | Safe | Norm | 4.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| med-Al/Open | med-Al/Open | close | Safe | Norm | 32.0s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

**XOB #1B55: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (2x2,1ms) - Leak (2x2,1ms) - 180s cad (G-band/Leak first)**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 10/05 06:19:00 - 10/05 11:59:54 | Track ( -112.3, -108.0) @ 10/05 06:16:00 | # HOP 323 (7:30-12:00 officially). |

| | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **PROG= 17 Inf.-time(s)** | | | | | | | | | | | | | |
| **Subr= 1 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| **Seqn= 26 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 1ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Subr= 2 20-time(s) 180.0sec** | | | | | | | | | | | | | |
| **Seqn= 8 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.41s | Obs | 4x4 | 2048x2048 (1024, 1024) | DPCM | 2 | 0 | 2.0sec |
| **Seqn= 6 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 125ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 3 | 0 | 2.0sec |
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 1.00s | Obs | 4x4 | 2048x2048 (1024, 1024) | DPCM | 2 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

**XOB #1AF6: CME watch - 4x4 - AEC 2/3 - Be-thin (Be-Med) - Long/Short Pairs - 180s cad**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 10/06 06:21:00 - 10/06 10:33:00 | Track ( -106.9, -73.0) @ 10/06 06:18:00 | # HOP 323. |

| | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **PROG= 02 Inf.-time(s)** | | | | | | | | | | | | | |
| **Subr= 1 20-time(s) 180.0sec** | | | | | | | | | | | | | |
| **Seqn= 81 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.41s | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 2 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 3 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

* * * * * **Flare mode** * * * * *

**XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 10/04 09:58:00 - 10/04 11:59:54 | Track ( -95.0, -33.3) @ 10/04 09:55:00 | # OP start + 10min, HOP 323 continued from previous plan. |
| 10/04 12:03:00 - 10/04 17:57:30 | Track ( -876.7, -327.4) @ 10/04 12:00:00 | # AR at NE limb with IRIS/SST. |
| 10/04 18:34:00 - 10/05 06:05:54 | Track ( -859.8, -333.6) @ 10/04 18:31:00 | # AR at SE limb. |
| 10/05 06:19:00 - 10/05 11:59:54 | Track ( -112.3, -108.0) @ 10/05 06:16:00 | # HOP 323 (7:30-12:00 officially). |
| 10/05 12:03:00 - 10/05 17:59:54 | Track ( -798.3, -349.3) @ 10/05 12:00:00 | # AR at NE limb with IRIS/SST. |
| 10/05 18:13:00 - 10/06 05:31:30 | Track ( -771.1, -354.5) @ 10/05 18:10:00 | # AR at SE limb. |
| 10/06 06:21:00 - 10/06 10:33:00 | Track ( -106.9, -73.0) @ 10/06 06:18:00 | # HOP 323. |

**PROG= 07 30-time(s)**

**Subr= 1 20-time(s) 2.0sec**

**Seqn= 11 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 125ms | Obs | 2x2 | 512x512 (1024, 1024) | Q=95 | 2 | 0 | 2.0sec |

**Seqn=100 1-time(s) 10.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 125ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 2 | 0 | 2.0sec |
| med-Be/Open | Open/thick-Al | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Be | close | Safe | Norm | 1.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |

**Subr= 2 1-time(s) 2.0sec**

**Seqn= 10 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| med-Al/Open | med-Al/thick-Al | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Norm | 2.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |

**Seqn= 11 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 125ms | Obs | 2x2 | 512x512 (1024, 1024) | Q=95 | 2 | 0 | 2.0sec |

**Seqn= 84 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 3ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 3ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Dark | 1.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Dark | 1.00s | Obs | 2x2 | 512x512 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |

## * * * * * Active Region Search * * * * *

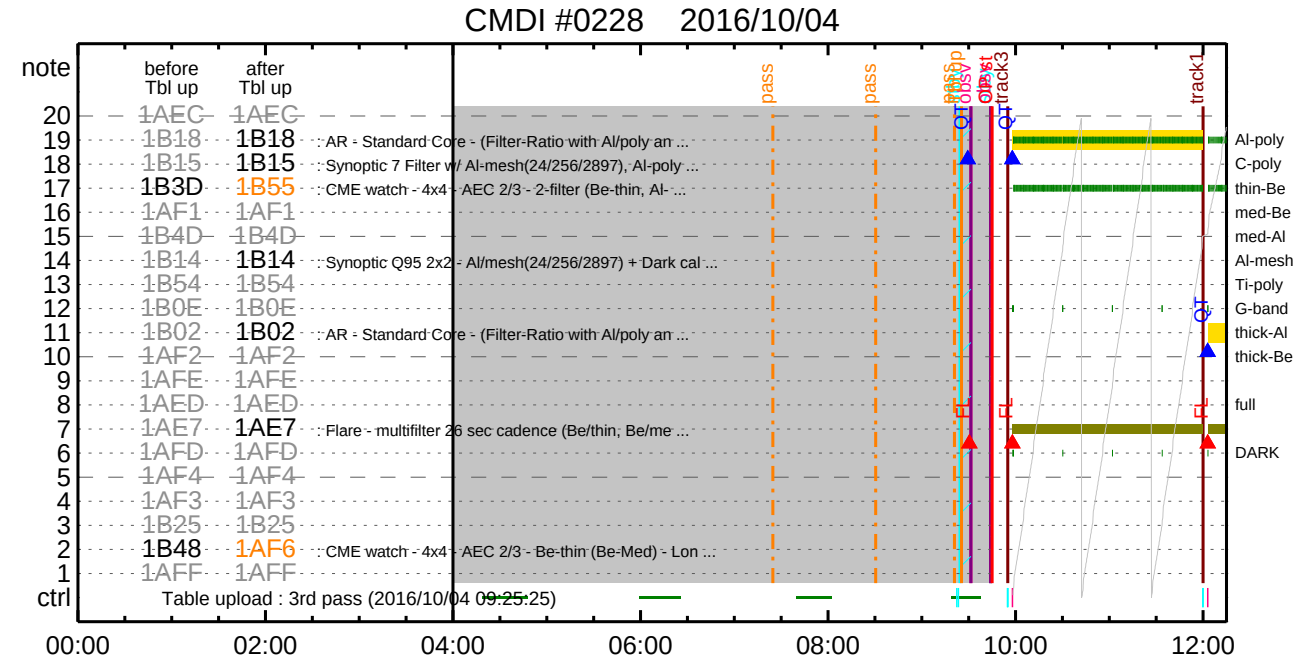
NOT USED

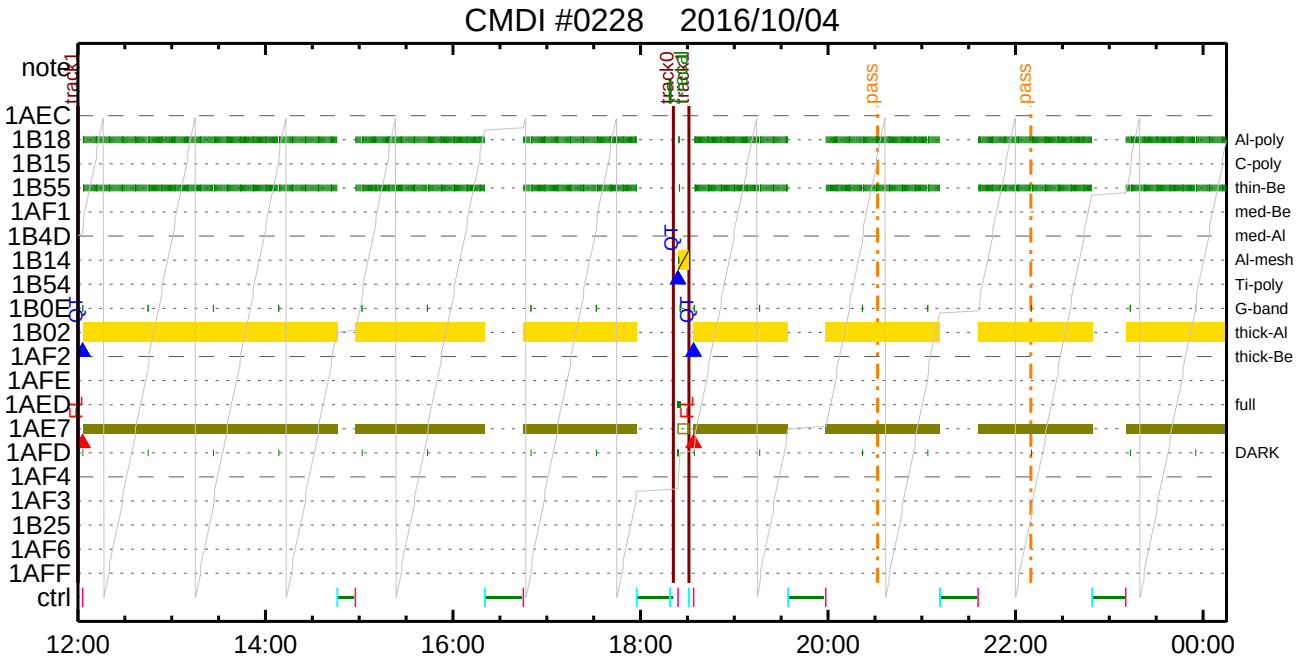
## * * * * * Flare Detection * * * * *

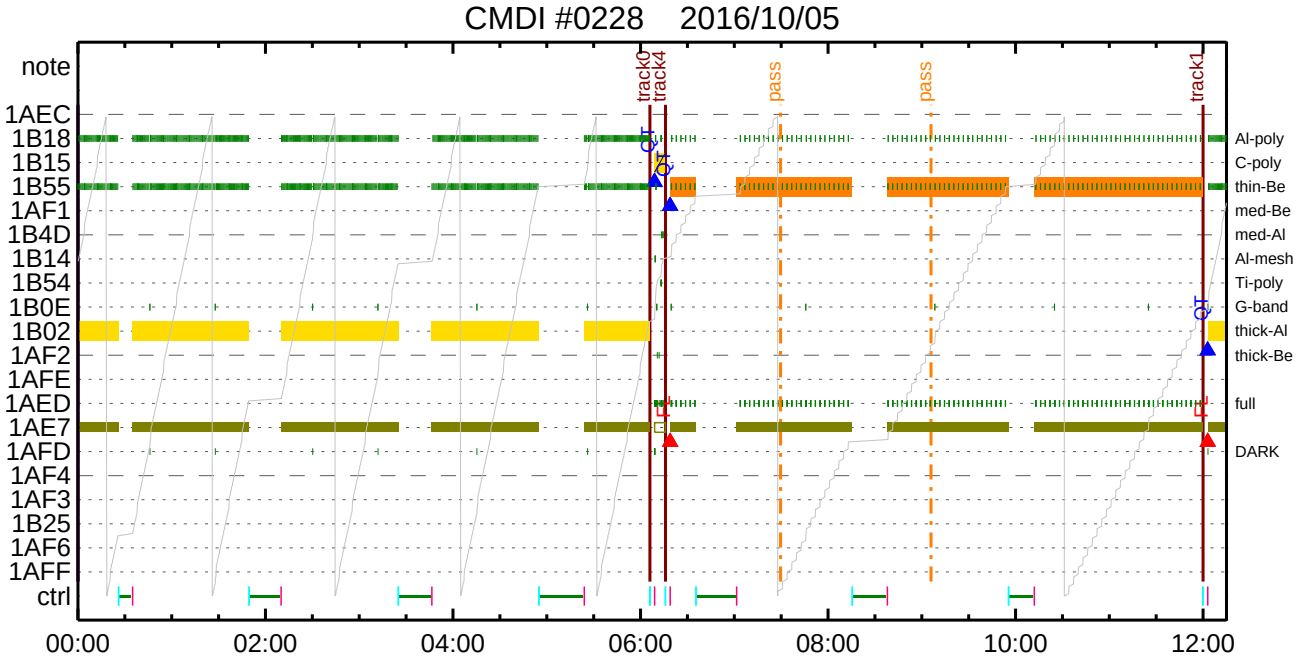
**FLD Patrol**

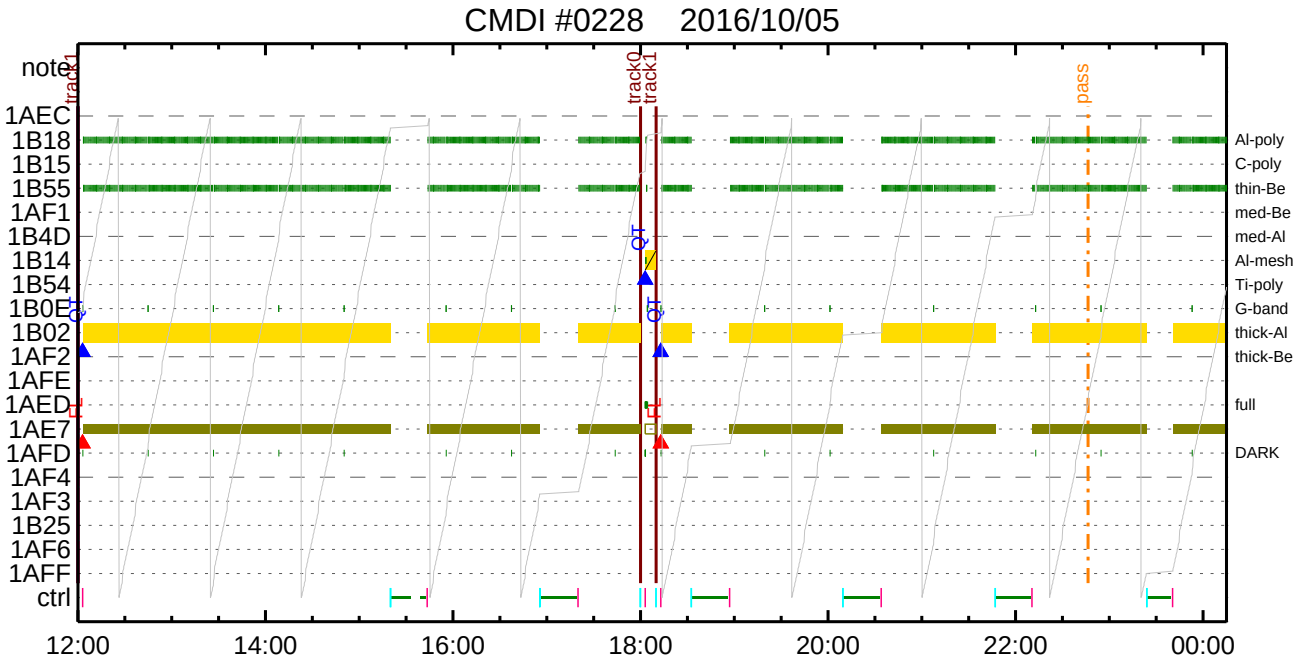
| Term | Pointing (x, y) | Comment |
|---|---|---|
| 10/04 09:55:18 - 10/04 18:23:18 | Track ( -95.0, -33.3) @ 10/04 09:55:00 | # OP start + 10min, HOP 323 continued from previous plan. |
| 10/04 18:31:18 - 10/05 06:06:18 | Track ( -859.8, -333.6) @ 10/04 18:31:00 | # AR at SE limb. |
| 10/05 06:16:18 - 10/05 18:00:18 | Track ( -112.3, -108.0) @ 10/05 06:16:00 | # HOP 323 (7:30-12:00 officially). |
| 10/05 18:10:18 - 10/06 06:08:18 | Track ( -771.1, -354.5) @ 10/05 18:10:00 | # AR at SE limb. |
| 10/06 06:18:18 - 10/08 10:59:00 | Track ( -106.9, -73.0) @ 10/06 06:18:00 | # HOP 323. |

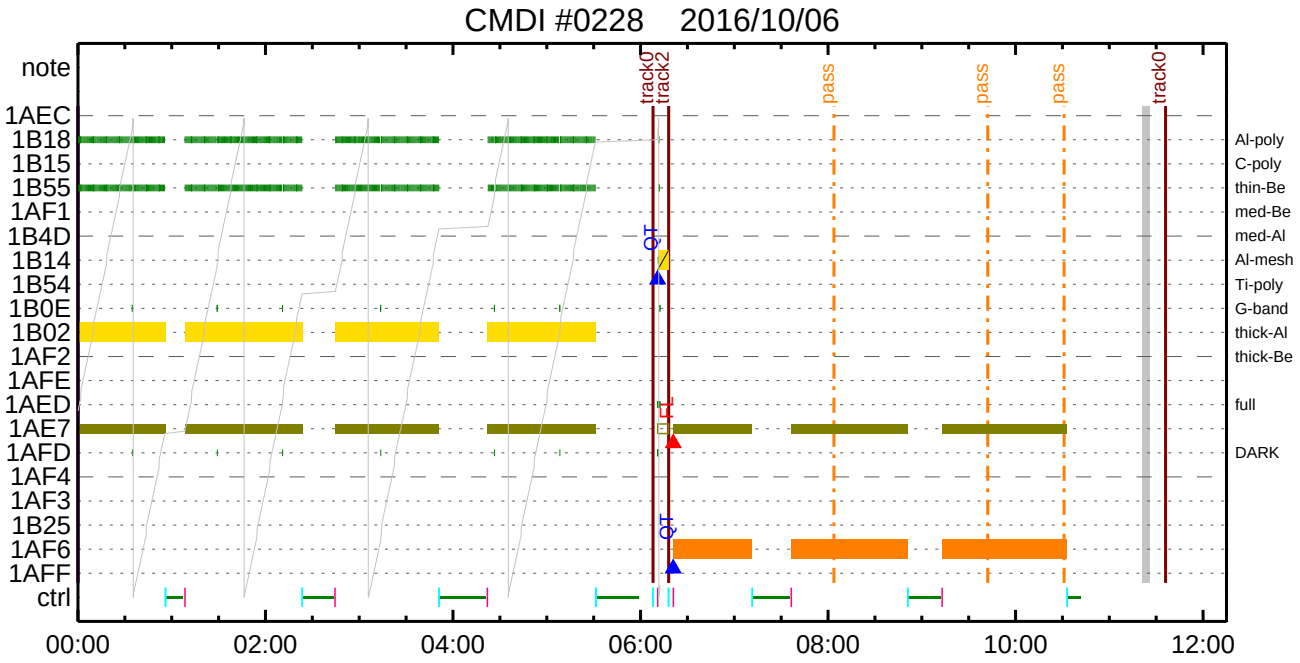
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Ti-poly | Open/thick-Al | close | Safe | Norm | 8ms | Obs | 8x8 | | Q=50 | | | 30sec |

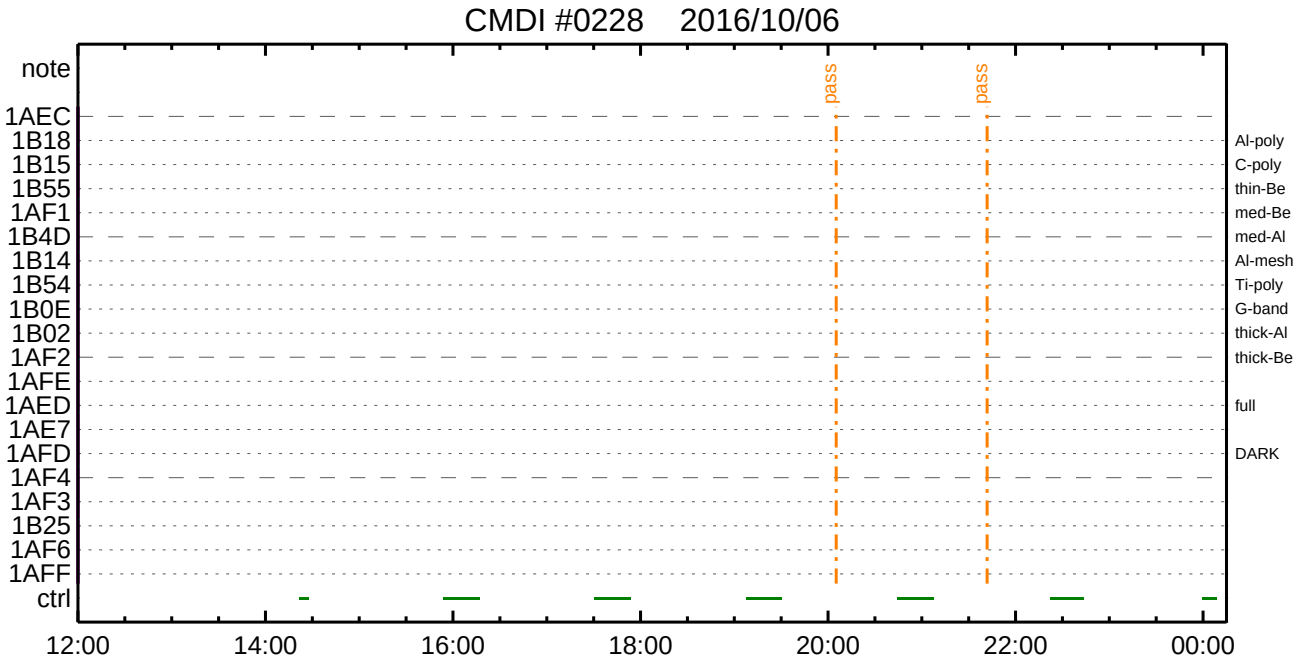

CMDI #0228 2016/10/04
note
before Tbl up
after Tbl up
1AEC 1AEC
1B18 1B18 : AR - Standard Core - (Filter-Ratio with Al/poly an ...
1B15 1B15 : Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly ...
1B3D 1B55 : CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al- ...
1AF1 1AF1
1B4D 1B4D
1B14 1B14 : Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal ...
1B54 1B54
1B0E 1B0E
1B02 1B02 : AR - Standard Core - (Filter-Ratio with Al/poly an ...
1AF2 1AF2
1AFE 1AFE
1AED 1AED
1AE7 1AE7 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
1AFD 1AFD
1AF4 1AF4
1AF3 1AF3
1B25 1B25
1B48 1AF6 : CME watch - 4x4 - AEC 2/3 - Be-thin (Be-Med) - Lon ...
1AFF 1AFF
ctrl
Table upload : 3rd pass (2016/10/04 09:25:25)
pass
pass
track3
track1
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

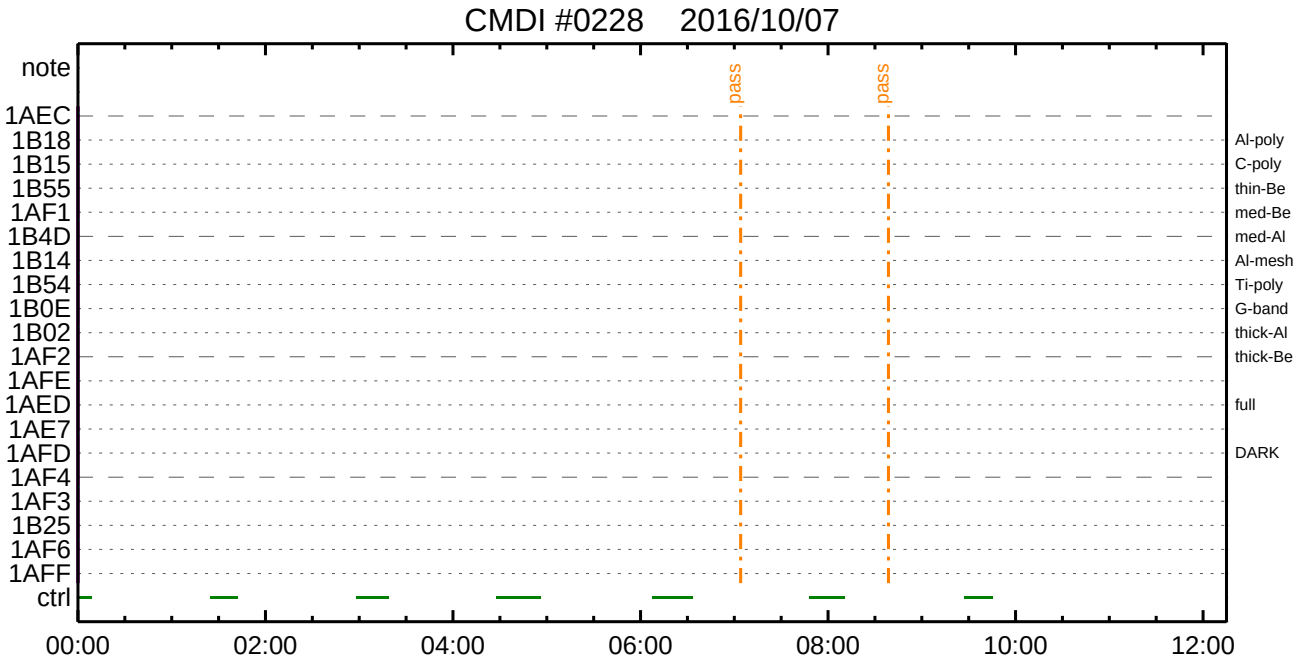

CMDI #0228 2016/10/04
note
track1
track0
track4
pass
pass
1AEC
1B18
1B15
1B55
1AF1
1B4D
1B14
1B54
1B0E
1B02
1AF2
1AFE
1AED
1AE7
1AFD
1AF4
1AF3
1B25
1AF6
1AFF
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

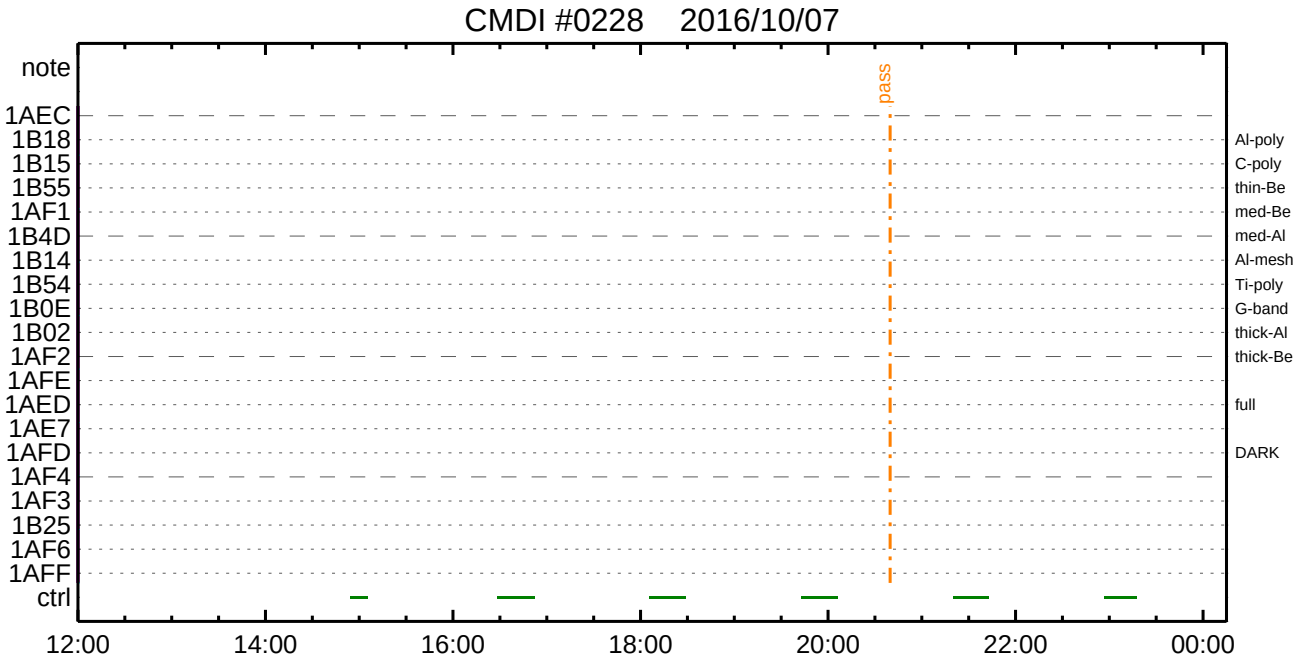

CMDI #0228 2016/10/05
note
track0
track4
pass
pass
track1
1AEC
1B18
1B15
1B55
1AF1
1B4D
1B14
1B54
1B0E
1B02
1AF2
1AFE
1AED
1AE7
1AFD
1AF4
1AF3
1B25
1AF6
1AFF
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

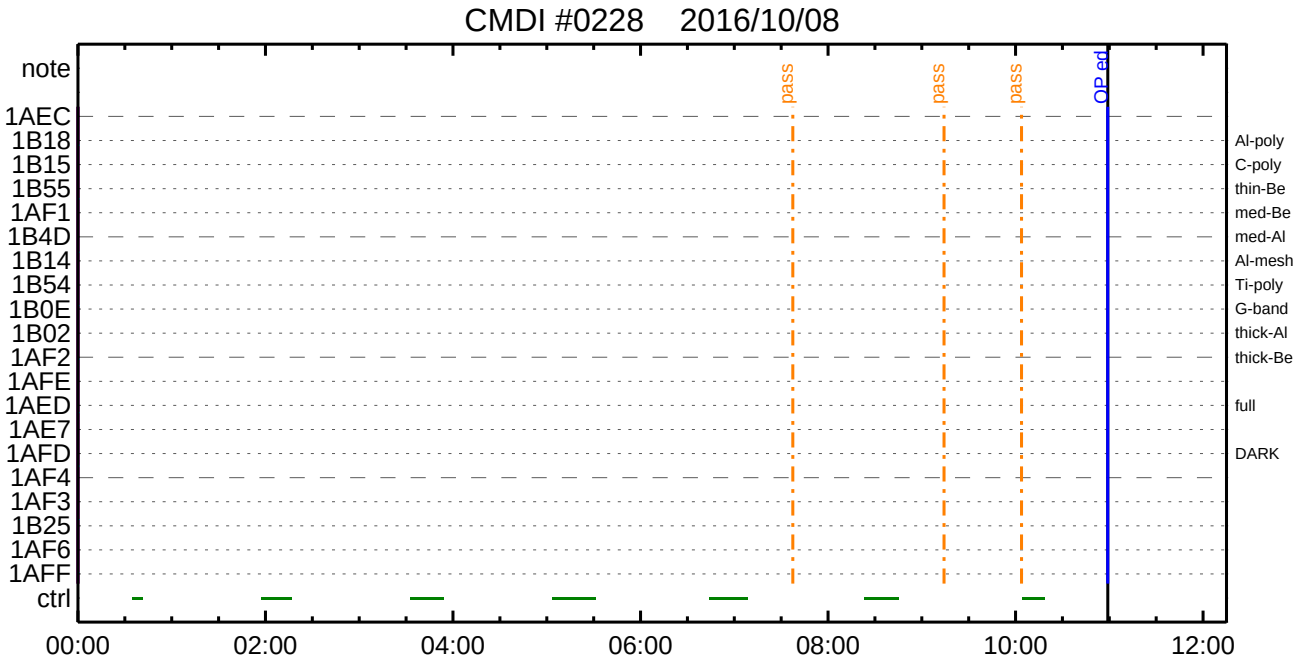

CMDI #0228 2016/10/05
note
1AEC
1B18
1B15
1B55
1AF1
1B4D
1B14
1B54
1B0E
1B02
1AF2
1AFE
1AED
1AE7
1AFD
1AF4
1AF3
1B25
1AF6
1AFF
ctrl
track1
track0
track1
pass
OT
FT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

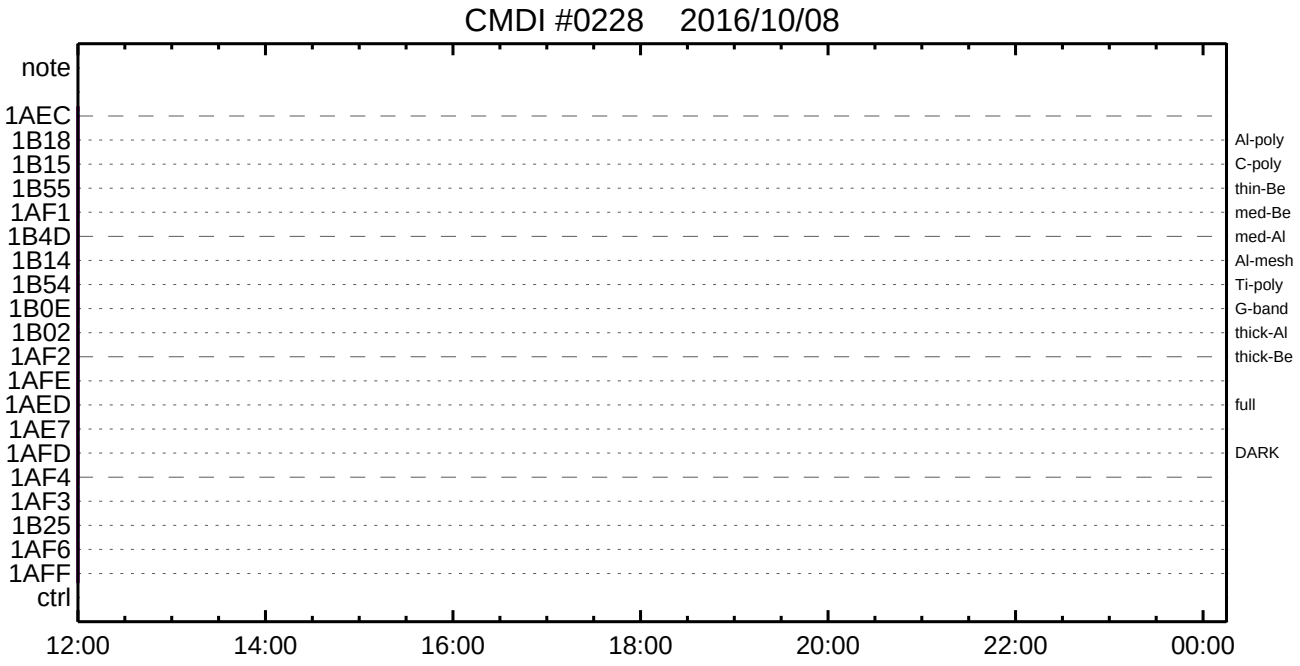

CMDI #0228 2016/10/06
note
1AEC
1B18
1B15
1B55
1AF1
1B4D
1B14
1B54
1B0E
1B02
1AF2
1AFE
1AED
1AE7
1AFD
1AF4
1AF3
1B25
1AF6
1AFF
ctrl
track0
track2
pass
pass
pass
track0
OT
FT
OT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0228 2016/10/06
note
1AEC
1B18
1B15
1B55
1AF1
1B4D
1B14
1B54
1B0E
1B02
1AF2
1AFE
1AED
1AE7
1AFD
1AF4
1AF3
1B25
1AF6
1AFF
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0228 2016/10/07
note
1AEC
1B18
1B15
1B55
1AF1
1B4D
1B14
1B54
1B0E
1B02
1AF2
1AFE
1AED
1AE7
1AFD
1AF4
1AF3
1B25
1AF6
1AFF
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0228 2016/10/07
note
1AEC
1B18
1B15
1B55
1AF1
1B4D
1B14
1B54
1B0E
1B02
1AF2
1AFE
1AED
1AE7
1AFD
1AF4
1AF3
1B25
1AF6
1AFF
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0228 2016/10/08
note
1AEC
1B18
1B15
1B55
1AF1
1B4D
1B14
1B54
1B0E
1B02
1AF2
1AFE
1AED
1AE7
1AFD
1AF4
1AF3
1B25
1AF6
1AFF
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0228 2016/10/08
note
1AEC
1B18
1B15
1B55
1AF1
1B4D
1B14
1B54
1B0E
1B02
1AF2
1AFE
1AED
1AE7
1AFD
1AF4
1AF3
1B25
1AF6
1AFF
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-154 2016-10-04 14:21:19 194 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-154:OP
0020     ()
0021   . S. OG    og-154:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Î¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099  +. TI 2016-10-04 09:40:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103  +. TI 2016-10-04 09:40:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107  +. TI 2016-10-04 09:40:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111  +. TI 2016-10-04 09:44:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ***************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ***************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133  +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹Ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147  +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154   . C. Stop EIS observation and temporarily disable EIS mode changes
0155     C.
0156     C.
0157     C. ********** Start EIS operation (TI set) **********
0158     C. Execute, after the success of OP upload.
0159     C. Set EIS TI-commands
0160  +. TI 2016-10-04 09:44:30.0
0161     DC 07-FC EIS_MODE_MANU
0162     BC       (21 02)
0163  +. TI 2016-10-04 09:44:40.0
0164     DC 07-FC EIS_MODE_CHG_DIS
0165     BC       (22)
0166   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0167     C. *********** End EIS operation (TI set) ***********
0168     C.
0169     C.
0170     C.
0171     C. ************ XRT START ***********
0172     C. Execute, after the success of OP upload.
0173  +. TI 2016-10-04 09:44:00.0
0174     DC 07-F0 MDP_XRT_MODE_STBY
0175     BC       (c3)
0176   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0177     C.
0178     C. ************ XRT END ***********
0179     C.
0180   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0181     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0182   . S. DC-BC dcbc-402:DCBC
0183     (MDP_known_event)
0184     C.
0185     C.
0186   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0187   . S. DC-BC dcbc-153:DCBC
0188     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189     C.
0190     C.
0191   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0192     C.
0193   . C. ********** LOS **********
```

0194 C.

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(a) Spacecraft Operation Procedure (real-commands)

main-155 2016-10-04 14:21:19 178 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007    +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011    +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019    +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021    +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023    +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029    . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031    . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036    . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037    +. DC 01-29 DHU_S/X_VC4_OFF
0038    +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040    +  DC 06-B3 DR_REP_START
0041    +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046    . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047    +. DC 06-B3 DR_REP_START
0048    +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054    . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸ å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057    . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062    . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063    +. DC 01-29 DHU_S/X_VC4_OFF
0064    +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066    +  DC 06-B3 DR_REP_START
0067    +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072    . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073    +. DC 06-B3 DR_REP_START
0074    +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079    . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083    . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084    +. DC 06-B4 DR_REP_STOP
0085    +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090    . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091    +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093    +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 5866418.6 +- 1.0 (s) [ ]
0136     C.
0137   . C.
0138   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_CHG_ENA
0140     BC       (20)
0141   . C. Verify EIS_MODE_CHG_FLG is ENA
0142   +. DC 07-FC EIS_MODE_MANU
0143     BC       (21 02)
0144   . C. Verify EIS in MANUAL mode
0145   . C. Estimated OBSTBL upload time is 1m2s
0146     C. ********************************
0147     C. EIS START OBSTBL LOAD
0148     C. ********************************
0149   . S. RAM   ram-820:EIS_OBSTBL
0150     ()
0151   +. DC 07-FC EIS_DUMP_OBSTBL
0152     BC       (07 07 07 00 00 70 00)
0153     C.
0154     C. Execute, after the success of OBSTBL upload.
0155     C. Set EIS TI-commands
0156   +. TI 2016-10-04 09:44:50.0
0157     DC 07-FC EIS_MODE_CHG_ENA
0158     BC       (20)
0159   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0160     C. ********************************
0161     C. EIS END OBSTBL LOAD
0162     C. ********************************
0163     C.
0164   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0165     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0166   . S. DC-BC dcbc-402:DCBC
0167     (MDP_known_event)
0168     C.
0169     C.
0170   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0171   . S. DC-BC dcbc-153:DCBC
0172     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173     C.
0174     C.
0175   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0176     C.
0177   . C. ********** LOS **********
0178     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-156 2016-10-04 14:21:19 92 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +. DC 07-F0 MDP_XRT_CTRL_MANU
0020     BC       (c1)
0021   +  DC 07-F0 MDP_XRT_MODE_STBY
0022     BC       (c3)
0023   . C. ------------ Success Verify ?              OK / NG____
0024     C.
0025     C. XRT Obs. Table Upload
0026   . S. RAM   ram-291:MDP_OBS_X
0027     ()
0028     C.
0029   +. DC 07-F0 MDP_DUMP_XRTTBL
0030     BC       (84 07 00 00 00 3a d4)
0031   . C. ------------ Comparison Check ?                        OK / ERR ____
0032     C.
0033     C.
0034   +. DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 01 b1 b1 04 04)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 02 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 03 b1 b1 08 08)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 04 b1 b1 06 06)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 06 85 83 06 06)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 07 80 80 20 20)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 08 80 80 20 08)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 09 80 80 08 20)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 0f 80 80 06 06)
0052   +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 10 80 80 08 08)
0054   +  DC 07-F0 MDP_XRT_FLD_DIS
0055     BC       (d9)
0056   +  DC 07-F0 MDP_XRT_FLRCTRL_DIS
0057     BC       (c9)
0058   +  DC 07-F0 MDP_XRT_ARS_DIS
0059     BC       (d5)
0060   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0061     BC       (c4 13)
0062   +. DC 07-F0 MDP_XRT_FL_PROG_SET
0063     BC       (c5 07)
0064   . C. ---------- Success Verify ?           OK / NG ____
0065     C.
0066     C.
0067   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0068     C.
0069   +. DC 07-F0 MDP_XRT_MODE_OBSV
0070     BC       (c2)
0071   +. TI 2016-10-04 09:44:02.0
0072     DC 07-F0 MDP_XRT_MODE_OBSV
0073     BC       (c2)
0074   . C. ---------- Success Verify ?           OK / NG ____
0075     C.
0076     C. ************ XRT END ************
0077     C.
0078   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0079     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0080   . S. DC-BC dcbc-402:DCBC
0081     (MDP_known_event)
0082     C.
0083     C.
0084   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0085   . S. DC-BC dcbc-153:DCBC
0086     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0087     C.
0088     C.
0089   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0090     C.
0091   . C. *********** LOS ***********
0092     C.
```

```
*** OP Sequence for XRT ***

2016/10/04  09:54:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  09:54:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  09:54:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/10/04  09:55:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 00 00 00 00
2016/10/04  09:55:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/10/04  09:55:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/10/04  09:55:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/10/04  09:55:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/04  09:55:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/04  09:57:56.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2016/10/04  09:57:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/10/04  09:58:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/04  11:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  11:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  11:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/10/04  12:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 01 00 00 00 00
2016/10/04  12:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/10/04  12:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/10/04  12:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/10/04  12:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/04  12:00:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/04  12:02:56.0  XRT_QT_PROG_SET_449_OG [0x1c1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2016/10/04  12:02:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/10/04  12:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/04  14:46:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  14:46:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  14:46:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/04  14:46:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/04  14:49:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/04  14:56:30.0  XRT_Custom_430_OG [0x1ae]
2016/10/04  14:57:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/04  16:20:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  16:20:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  16:20:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/04  16:20:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/04  16:23:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/04  16:44:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/04  16:45:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/04  17:57:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  17:57:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  17:57:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/04  17:57:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/04  18:00:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/04  18:18:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  18:18:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  18:18:58.0  XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]
                                XRT_FOCUS_RECAL      2 07-F8 78 00
2016/10/04  18:21:00.0  AOCS_ORe-point_Start_3_OG [0x099]
```

```
                                    AOCU_NM              5 02-76 00 00 00 00 00
2016/10/04  18:22:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/10/04  18:23:18.0  XRT_FLD_DIS_431_OG [0x1af]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2016/10/04  18:23:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/10/04  18:23:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/04  18:23:58.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2016/10/04  18:24:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/04  18:30:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  18:30:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  18:30:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/10/04  18:31:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 01 00 00 00 00
2016/10/04  18:31:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2016/10/04  18:31:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/10/04  18:31:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2016/10/04  18:31:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/04  18:31:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/04  18:33:56.0  XRT_QT_PROG_SET_449_OG [0x1c1]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2016/10/04  18:33:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/10/04  18:34:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/04  19:34:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  19:34:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  19:34:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/04  19:34:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/04  19:37:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/04  19:57:30.0  XRT_Custom_430_OG [0x1ae]
2016/10/04  19:58:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/04  21:11:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  21:11:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  21:11:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/04  21:11:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/04  21:14:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/04  21:35:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/04  21:36:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/04  22:49:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  22:49:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/04  22:49:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/04  22:49:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/04  22:52:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/04  23:09:30.0  XRT_Custom_430_OG [0x1ae]
2016/10/04  23:10:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  00:26:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  00:26:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  00:26:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  00:26:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  00:29:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  00:34:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  00:35:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  01:49:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  01:49:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
```

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2016/10/05  01:49:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  01:49:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  01:52:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  02:09:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  02:10:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  03:25:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  03:25:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  03:25:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  03:25:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  03:28:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  03:45:30.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  03:46:30.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  04:55:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  04:55:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  04:55:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  04:55:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  04:58:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  05:23:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  05:24:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  06:05:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  06:05:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  06:05:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/10/05  06:06:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2016/10/05  06:06:18.0  XRT_FLD_DIS_406_OG [0x196]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2016/10/05  06:08:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/10/05  06:08:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/05  06:08:58.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2016/10/05  06:09:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  06:15:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  06:15:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  06:15:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/10/05  06:16:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 04 00 00 00 00
2016/10/05  06:16:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/10/05  06:16:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/10/05  06:16:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/10/05  06:16:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/05  06:16:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  06:18:56.0  XRT_QT_PROG_SET_435_OG [0x1b3]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2016/10/05  06:18:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/10/05  06:19:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  06:35:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  06:35:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  06:35:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  06:35:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  06:38:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  07:00:30.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  07:01:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  08:15:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  08:15:32.0  XRT_CTRL_MANU_402_OG [0x192]
```

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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  08:15:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  08:15:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  08:18:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  08:37:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  08:38:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  09:55:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  09:55:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  09:55:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  09:55:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  09:58:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  10:11:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  10:12:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  11:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  11:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  11:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/10/05  12:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 01 00 00 00 00
2016/10/05  12:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2016/10/05  12:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/10/05  12:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2016/10/05  12:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/05  12:00:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  12:02:56.0  XRT_QT_PROG_SET_449_OG [0x1c1]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2016/10/05  12:02:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/10/05  12:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  15:20:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  15:20:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  15:20:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  15:20:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  15:23:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  15:42:30.5  XRT_Custom_430_OG [0x1ae]
2016/10/05  15:43:30.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  16:55:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  16:55:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  16:55:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  16:55:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  16:58:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  17:19:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  17:20:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  17:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  17:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  17:59:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/10/05  18:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2016/10/05  18:00:18.0  XRT_FLD_DIS_406_OG [0x196]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2016/10/05  18:02:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/10/05  18:02:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/05  18:02:58.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2016/10/05  18:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  18:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
```

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2016/10/05  18:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  18:09:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/10/05  18:10:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 01 00 00 00 00
2016/10/05  18:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/10/05  18:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/10/05  18:10:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/10/05  18:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/05  18:10:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  18:12:56.0  XRT_QT_PROG_SET_449_OG [0x1c1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2016/10/05  18:12:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/10/05  18:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  18:32:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  18:32:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  18:32:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  18:32:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  18:35:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  18:56:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  18:57:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  20:09:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  20:09:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  20:09:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  20:09:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  20:12:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  20:33:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  20:34:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  21:47:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  21:47:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  21:47:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  21:47:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  21:50:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  22:09:31.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  22:10:31.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/05  23:24:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  23:24:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/05  23:24:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/05  23:24:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/05  23:27:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/05  23:39:30.0  XRT_Custom_430_OG [0x1ae]
2016/10/05  23:40:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/06  00:56:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  00:56:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  00:56:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/06  00:56:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/06  00:59:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/06  01:07:30.0  XRT_Custom_430_OG [0x1ae]
2016/10/06  01:08:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/06  02:23:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  02:23:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  02:23:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
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2016/10/06  02:23:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/06  02:26:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/06  02:43:30.0  XRT_Custom_430_OG [0x1ae]
2016/10/06  02:44:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/06  03:51:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  03:51:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  03:51:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/06  03:51:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/06  03:54:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/06  04:21:00.0  XRT_Custom_430_OG [0x1ae]
2016/10/06  04:22:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/06  05:31:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  05:31:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  05:31:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/06  05:31:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/06  05:34:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/06  06:07:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  06:07:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  06:07:58.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2016/10/06  06:08:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2016/10/06  06:08:18.0  XRT_FLD_DIS_406_OG [0x196]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2016/10/06  06:10:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2016/10/06  06:10:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/06  06:10:58.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2016/10/06  06:11:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/06  06:17:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  06:17:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  06:17:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2016/10/06  06:18:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 02 00 00 00 00
2016/10/06  06:18:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2016/10/06  06:18:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2016/10/06  06:18:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2016/10/06  06:18:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2016/10/06  06:18:26.0  XRT_FLD_RESET_433_OG [0x1b1]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/06  06:20:56.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2016/10/06  06:20:58.0  XRT_FL_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 07
2016/10/06  06:21:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/06  07:11:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  07:11:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  07:11:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/06  07:11:36.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/06  07:14:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/06  07:35:30.0  XRT_Custom_430_OG [0x1ae]
2016/10/06  07:36:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/06  08:51:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  08:51:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  08:51:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/06  08:51:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
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2016/10/06  08:54:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/06  09:12:01.5  XRT_Custom_430_OG [0x1ae]
2016/10/06  09:13:01.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2016/10/06  10:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  10:33:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2016/10/06  10:33:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2016/10/06  10:33:06.0  XRT_PREFLR_STRT_414_OG [0x19e]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2016/10/06  10:36:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2016/10/06  11:36:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
```