

XRT Timeline to be uploaded on 2016/10/18

Period: 2016/10/18 10:22:00 - 2016/10/22 11:05:00

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Normal mode

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XOB #1AF1: CCD Monitor During Bakeout - G-band 3ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(512ms), Al/Poly(1443ms) - w leak image-3ms

Term	Pointing (x, y)	Comment
10/19 12:18:00 - 10/19 12:24:54	Fixed (-528.4, -528.4)	4 Quadrant observation for post CCD-BO #1
PROG= 16 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 86 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 3ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open	med-Be/Open close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1AF2: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
10/19 12:28:00 - 10/19 12:34:54	Fixed (528.4, -528.4)	4 Quadrant observation for post CCD-BO #2
PROG= 10 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 15 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 3ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open	med-Be/Open close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1AF3: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
10/19 12:38:00 - 10/19 12:44:54	Fixed (528.4, 528.4)	4 Quadrant observation for post CCD-BO #3
PROG= 04 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 35 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 3ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 3ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 55 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open	med-Be/Open close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 54 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1AF4: CCD Monitor During Bakeout - G-Band 3ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-3 ms

Term	Pointing (x, y)	Comment
10/19 12:48:00 - 10/19 12:59:54	Fixed (-528.4, 528.4)	4 Quadrant observation for post CCD-BO #4
PROG= 05 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 3 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 3ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		

Seqn= 55	2-time(s)	2.0sec										
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3	2-time(s)	2.0sec										
Seqn= 54	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B25: AR - Standard Core - (Filter-Ratio with thin-Be and Med-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Med-Be context, with

Term	Pointing (x, y)	Comment
10/19 13:03:00 - 10/19 14:19:54	Track (507.0, -3.7) @ 10/19 13:00:00	track AR 12602
10/19 14:23:00 - 10/19 17:59:54	Track (514.9, -0.2) @ 10/19 14:20:00	Coordinated observations with IRIS/DST

PROG= 03	Inf.-time(s)											
Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 48	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 97	4-time(s)	30.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + Ti

Term	Pointing (x, y)	Comment
10/19 18:03:00 - 10/19 18:09:54	Fixed (0.0, 0.0)	synoptic
10/20 06:16:30 - 10/20 06:23:24	Fixed (0.0, 0.0)	synoptic, shifted 13.5 min

PROG= 14	1-time(s)											
Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
10/19 18:13:00 - 10/20 06:13:24	Track (547.4, -1.0) @ 10/19 18:10:00	track AR 12602 and support HOP 306

PROG= 01	Inf.-time(s)											
Subr= 1	1-time(s)	2.0sec										
Seqn= 56	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96 4-time(s) 60.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	15.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B2C: HOP308 - Thin-Be AEC 2/3 with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with G-band (3ms/3ms VLS=CLS), 10 cad

Term	Pointing (x, y)	Comment
10/20 06:26:30 - 10/20 08:57:00	Track (637.2, 6.3) @ 10/20 06:23:30	track AR 12602
PROG= 09 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 56 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 12-time(s) 2.0sec		
Seqn= 41 15-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
Seqn= 96 4-time(s) 30.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)

Term	Pointing (x, y)	Comment
10/19 13:03:00 - 10/19 14:19:54	Track (507.0, -3.7) @ 10/19 13:00:00	track AR 12602
10/19 14:23:00 - 10/19 17:59:54	Track (514.9, -0.2) @ 10/19 14:20:00	Coordinated observations with IRIS/DST
10/19 18:13:00 - 10/20 06:13:24	Track (547.4, -1.0) @ 10/19 18:10:00	track AR 12602 and support HOP 306
10/20 06:26:30 - 10/20 08:57:00	Track (637.2, 6.3) @ 10/20 06:23:30	track AR 12602
PROG= 07 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 84 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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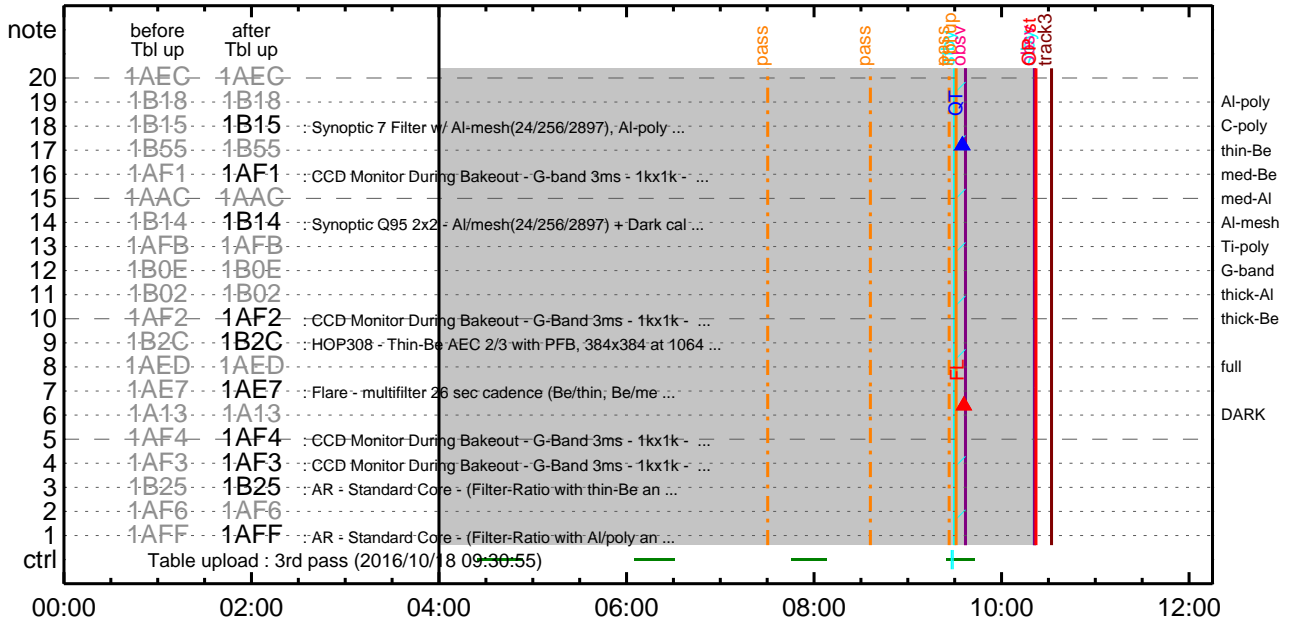
Flare Detection

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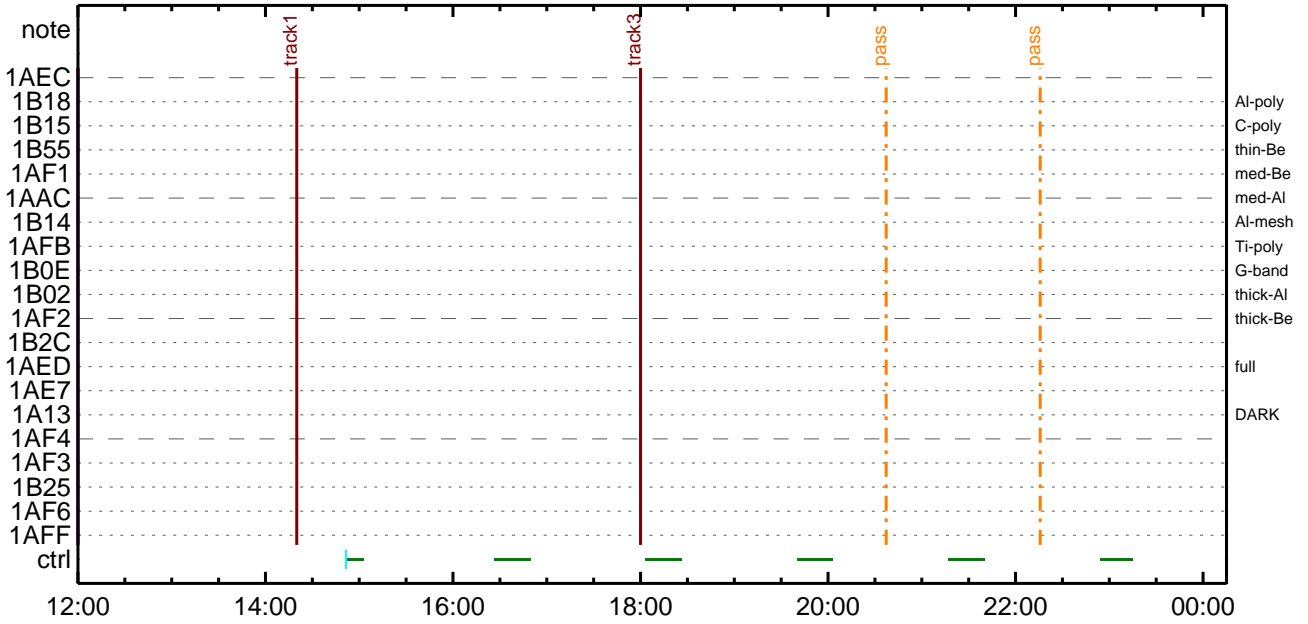
FLD Patrol

Term	Pointing (x, y)	Comment
10/19 13:00:18 - 10/19 18:00:18	Track (507.0, -3.7) @ 10/19 13:00:00	track AR 12602
10/19 18:10:18 - 10/20 06:13:48	Track (547.4, -1.0) @ 10/19 18:10:00	track AR 12602 and support HOP 306
10/20 06:23:48 - 10/22 11:05:00	Track (637.2, 6.3) @ 10/20 06:23:30	track AR 12602
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 80sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

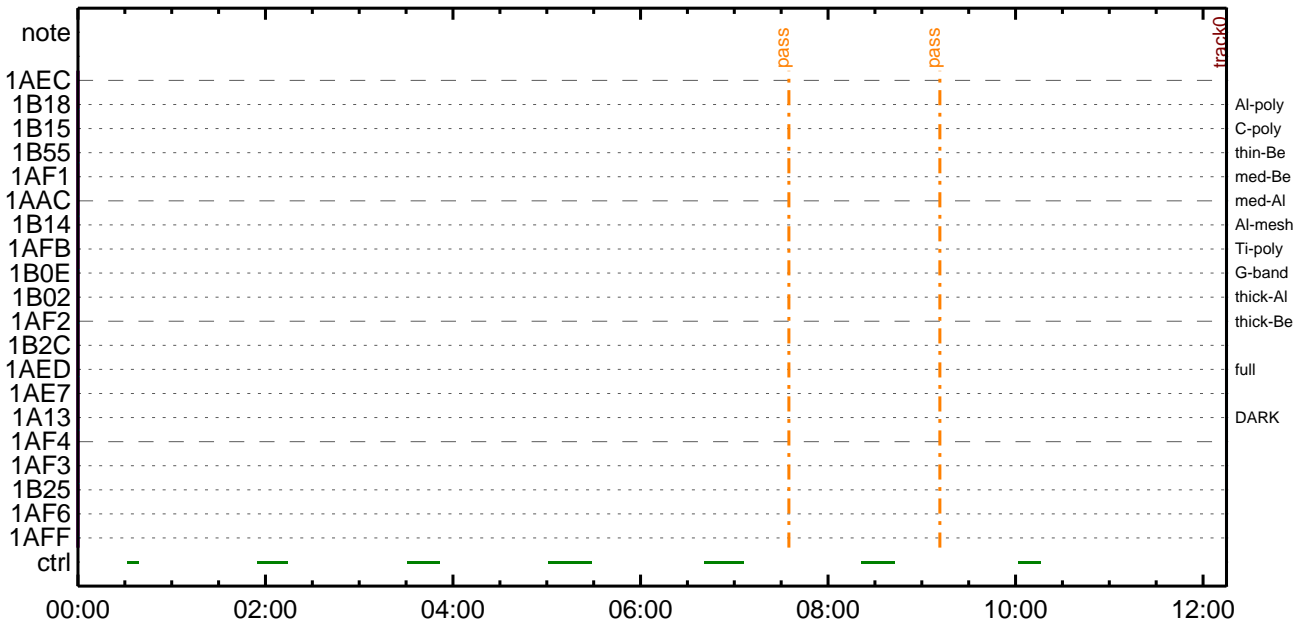
CMDI #0254 2016/10/18



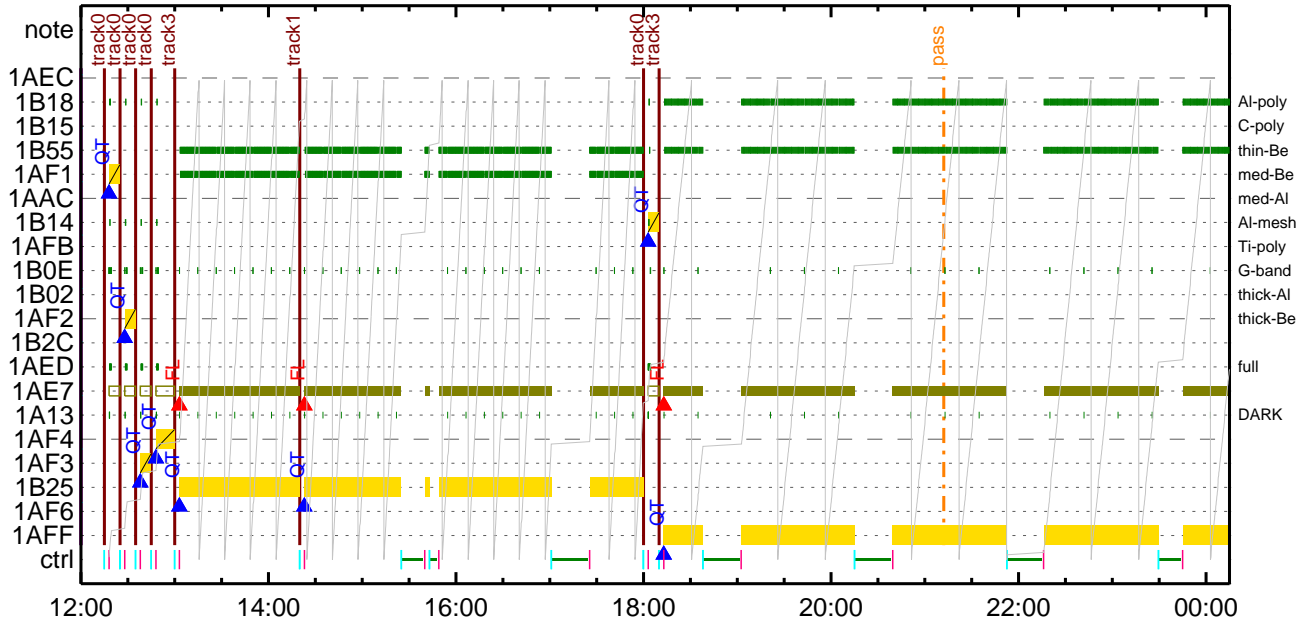
CMDI #0254 2016/10/18



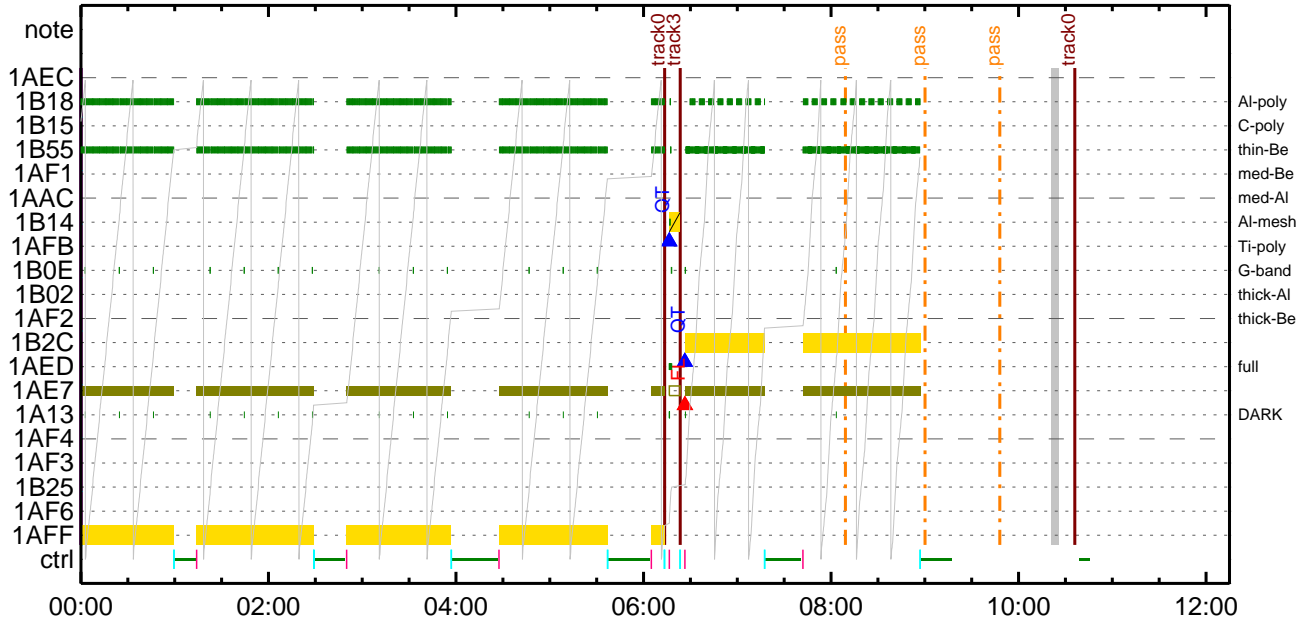
CMDI #0254 2016/10/19



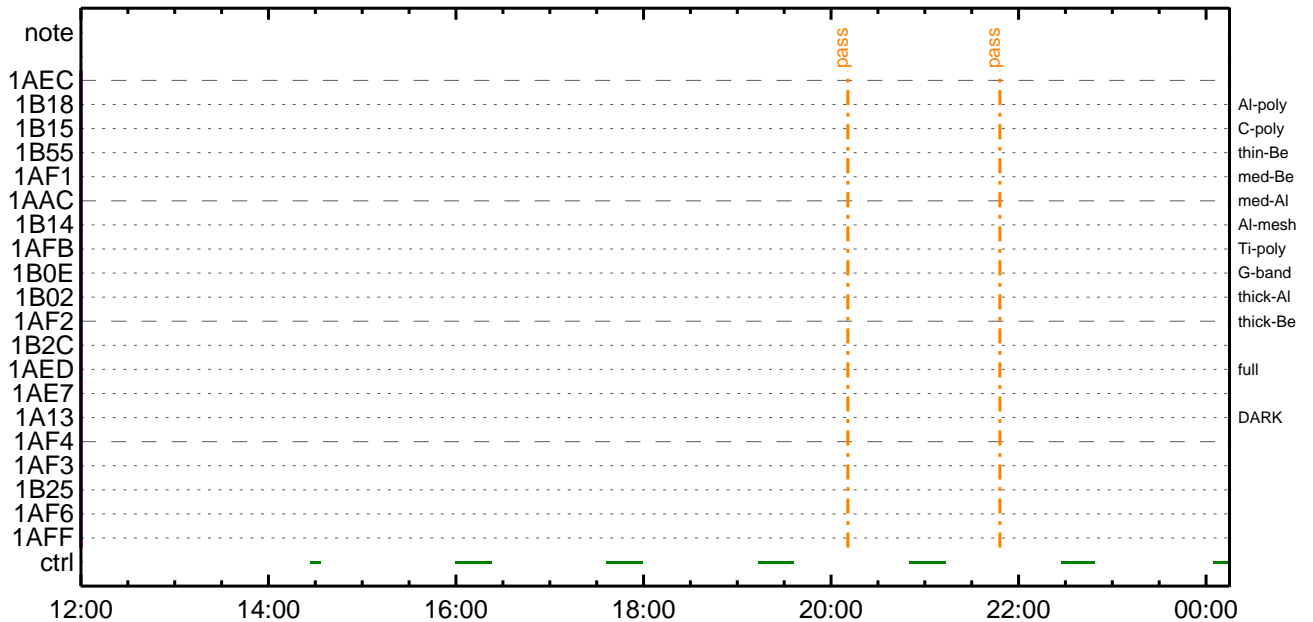
CMDI #0254 2016/10/19



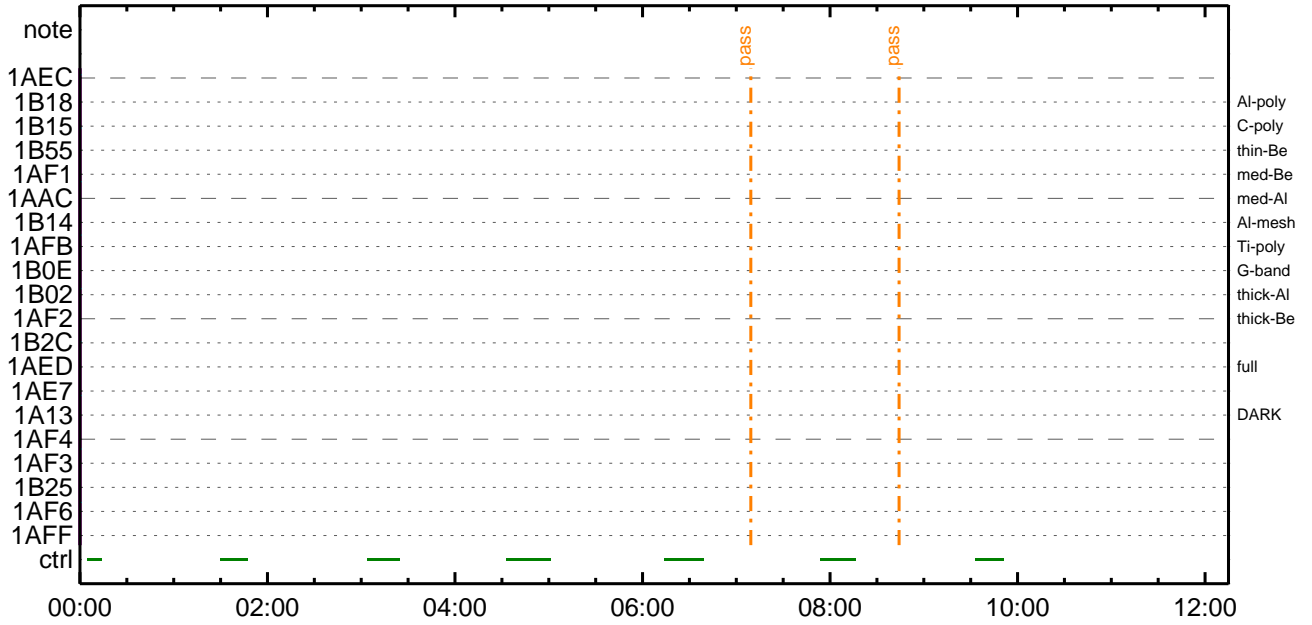
CMDI #0254 2016/10/20



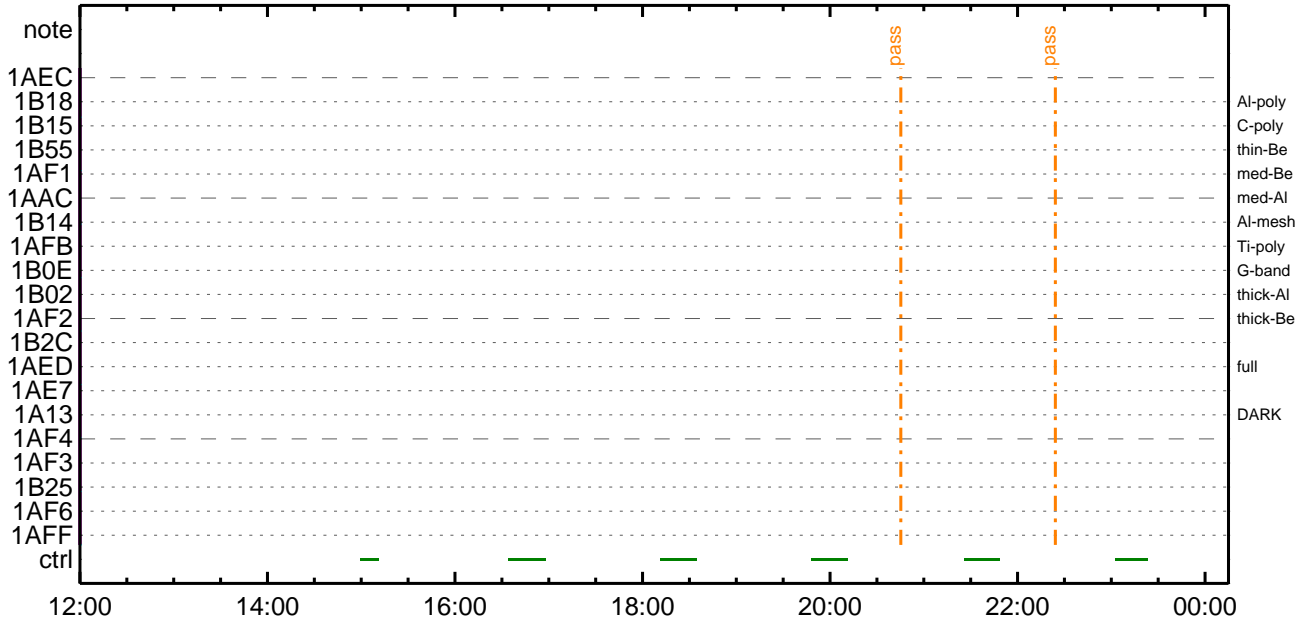
CMDI #0254 2016/10/20



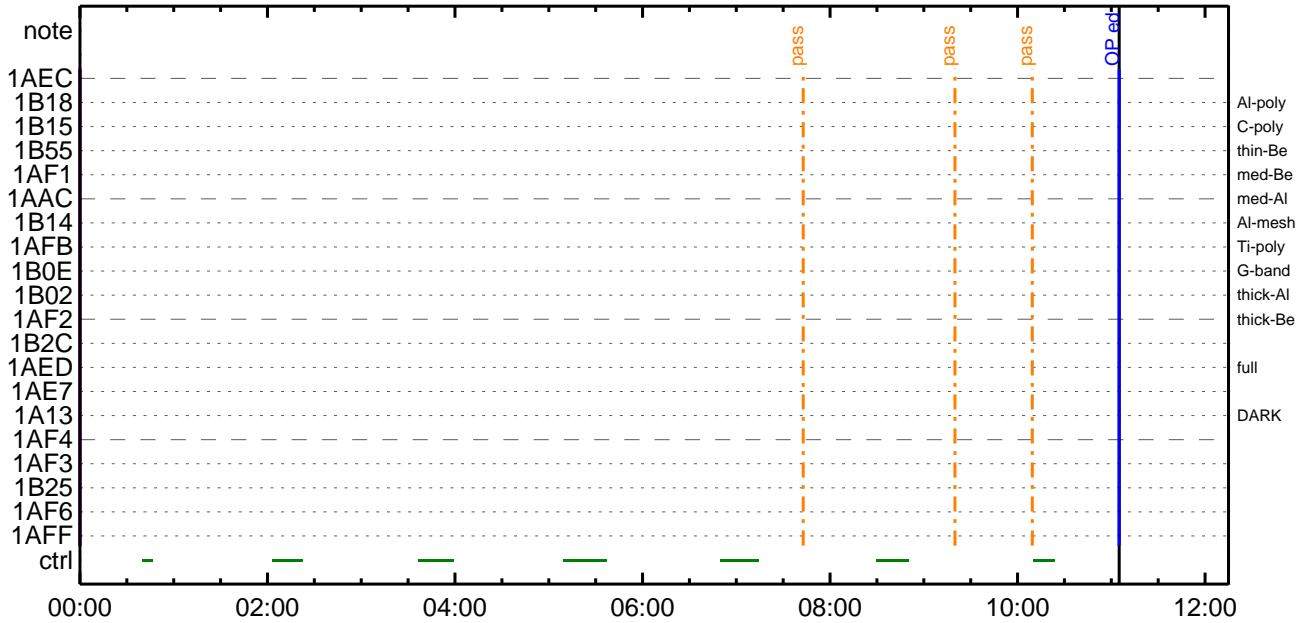
CMDI #0254 2016/10/21



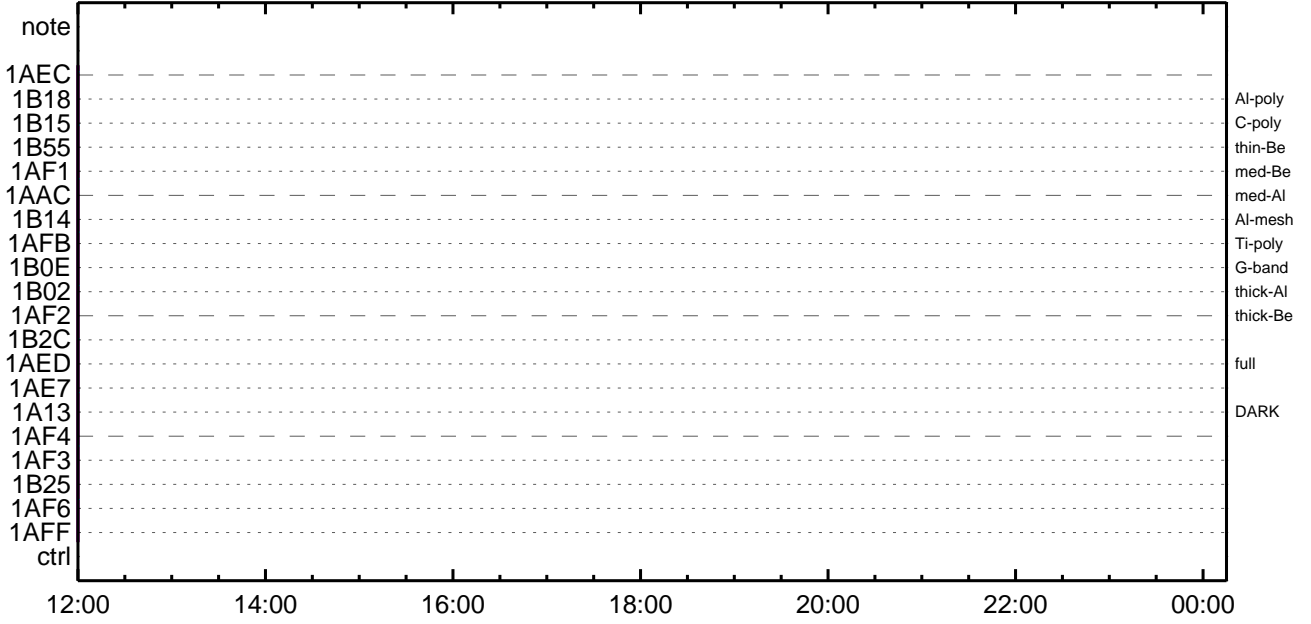
CMDI #0254 2016/10/21



CMDI #0254 2016/10/22



CMDI #0254 2016/10/22




```

0096 C.                SET EDUMP I±°iYÑY¹aÇ¹Ôa|a³aE;f
0097 C.
0098 C. TIY³YFYYÖYÉaðdÄDİ¿(UT)
0099 +. TI 2016-10-18 10:17:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0102 C.
0103 +. TI 2016-10-18 10:17:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0106 C.
0107 +. TI 2016-10-18 10:17:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0110 C.
0111 +. TI 2016-10-18 10:21:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0114 C.
0115 C. °E²¼aİÄè%îÍÑaİYÁY§YÄY-¹àÛ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0117 C.                çç[HK1_TI_CMD_NUM]         EQ      4
0118 C.                çç[HK1_NEXT_EXEC_PIM]      EQ      DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]      EQ      0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]      EQ      3
0131 C.                çç[HK1_DMP_REPEAT_NUM]    EQ      0
0132 C.                çç[HK1_DMA_DMP_PIM]      EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]       EQ      7
0136 C.                çç[HK1_PKT_GEN_TIME]      EQ      0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]   EQ      4M
0139 C.                çç[HK1_DMP_CHK_FLG]      EQ      EXEC
0140 C.
0141 C. YÄYÖY×½ªİ»að³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]      EQ      NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKað³İÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y½, Yİ;¼YÈ;Èaðİã¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]       EQ      2
0150 C.                çç[HK1_PKT_GEN_TIME]      EQ      0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]   EQ      4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2016-10-18 10:21:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2016-10-18 10:21:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 C.                [ ] [HK1_TI_CMD_NUM]       EQ      2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2016-10-18 10:21:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 C.                [ ] [HK1_TI_CMD_NUM]       EQ      1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´úÄîaİ»ö¼YªÈÄa¹aèDCBC•x²è *****
0181 C. (%ã°îYÖYÄYÈYBYYÈYÁYÇYèaÈ%¼aª%Ä»Û¹aè)
0182 S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** YDÿ¹.İ Daily±¿İÑaÈ'Øa¹aèDCBC•x²è *****
0187 S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;ãLOS¥ÁY§YÄY-¼Ä»Û;ä
0192 C.
0193 C. ***** LOS *****

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-184 2016-10-18 11:35:40 104 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Á□•µ°È×Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;È□È¼°ÇÖ□•□¿¼í¹ç□í;çÁ®, ù□¹□è□□□çÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ( )
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 80 20 20)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 08)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 08 20)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 c0 c0 10 10)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 40 c0 10 10)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 40 40 10 10)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c c0 40 10 10)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d 85 83 06 06)
0058 +. DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0f 80 80 06 06)
0060 +. DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 10 80 80 08 08)
0062 +. DC 07-F0 MDP_XRT_FLD_ENA
0063 BC (d8)
0064 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0065 BC (c8)
0066 +. DC 07-F0 MDP_XRT_ARS_DIS
0067 BC (d5)
0068 +. DC 07-F0 MDP_XRT_AEC_RESET
0069 BC (d0)
0070 +. DC 07-F0 MDP_XRT_FLD_RESET
0071 BC (da)
0072 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0073 BC (c4 12)
0074 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0075 BC (c5 07)
0076 . C. ----- Success Verify ? OK / NG ____
0077 C.
0078 C.
0079 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0080 C.
0081 +. DC 07-F0 MDP_XRT_MODE_OBSV
0082 BC (c2)
0083 +. TI 2016-10-18 10:21:02.0
0084 DC 07-F0 MDP_XRT_MODE_OBSV
0085 BC (c2)
0086 . C. ----- Success Verify ? OK / NG ____
0087 C.
0088 C. ***** XRT END *****
0089 C.
0090 . C. ***** MDP `úÁí□í»ö¼Y□ÈÁ□¹□èDCBC•x²è *****
0091 C. (¼á°íYÖYÁYÈY¥YÉYáYçYèÈ¼□□¼Á»Û¹□é)
0092 . S. DC-BC dcbc-402:DCBC
0093 (MDP_known_event)
0094 C.
0095 C.
```

```
0096 . C. ***** ¥D¥!•İ Daily±;İÑòĚ'Ø¹òĚDCBC•x²è *****
0097 . S. DC-BC dcbc-153:DCBC
0098 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0099 C.
0100 C.
0101 . C. ;ãLOS¥Á¥S¥Ã¥¼Â»Ü;ä
0102 C.
0103 . C. ***** LOS *****
0104 C.
```

*** OP Sequence for XRT ***

```

2016/10/18 10:32:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 03 00 00 00 00
2016/10/18 14:20:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 01 00 00 00 00
2016/10/18 14:51:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/10/18 14:51:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/10/18 14:51:34.0 XRT_FLD_RESEt_415_OG [0x19f]
                        MDP_XRT_FLD_RESEt         1 07-F0 da
2016/10/18 14:51:36.0 XRT_PREFLR_STRT_414_OG [0x19e]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2016/10/18 14:54:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2016/10/18 18:00:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 03 00 00 00 00
2016/10/19 06:15:00.0 XRT_TCIB_XRT_S_HTR_A_DIS_404_OG [0x194]
                        TCIB_XRT_S_HTR_A_DIS      0 04-C0
2016/10/19 12:14:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/10/19 12:14:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/10/19 12:14:58.0 XRT_FOCUS_POSITION_439_OG [0x1b7]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2016/10/19 12:15:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 00 2e f9 2e f9
2016/10/19 12:15:18.0 XRT_FLD_DIS_428_OG [0x1ac]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2016/10/19 12:15:20.0 XRT_FLRCTRL_DIS_443_OG [0x1bb]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2016/10/19 12:17:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2016/10/19 12:17:58.0 XRT_QT_PROG_SEt_440_OG [0x1b8]
                        MDP_XRT_QT_PROG_SEt        2 07-F0 c4 10
2016/10/19 12:18:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2016/10/19 12:24:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/10/19 12:24:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/10/19 12:24:58.0 XRT_FOCUS_POSITION_439_OG [0x1b7]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2016/10/19 12:25:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 2e f9 d1 07
2016/10/19 12:25:18.0 XRT_FLD_DIS_428_OG [0x1ac]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2016/10/19 12:25:20.0 XRT_FLRCTRL_DIS_443_OG [0x1bb]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2016/10/19 12:27:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2016/10/19 12:27:58.0 XRT_QT_PROG_SEt_427_OG [0x1ab]
                        MDP_XRT_QT_PROG_SEt        2 07-F0 c4 0a
2016/10/19 12:28:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2016/10/19 12:34:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/10/19 12:34:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/10/19 12:34:58.0 XRT_FOCUS_POSITION_439_OG [0x1b7]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2016/10/19 12:35:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 d1 07 d1 07
2016/10/19 12:35:18.0 XRT_FLD_DIS_428_OG [0x1ac]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2016/10/19 12:35:20.0 XRT_FLRCTRL_DIS_443_OG [0x1bb]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2016/10/19 12:37:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2016/10/19 12:37:58.0 XRT_QT_PROG_SEt_418_OG [0x1a2]
                        MDP_XRT_QT_PROG_SEt        2 07-F0 c4 04
2016/10/19 12:38:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2016/10/19 12:44:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/10/19 12:44:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2016/10/19 12:44:58.0 XRT_FOCUS_POSITION_439_OG [0x1b7]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2016/10/19 12:45:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCU_NM                    5 02-76 00 d1 07 2e f9
2016/10/19 12:45:18.0 XRT_FLD_DIS_428_OG [0x1ac]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2016/10/19 12:45:20.0 XRT_FLRCTRL_DIS_443_OG [0x1bb]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2016/10/19 12:47:56.0 XRT_ARS_DIS_445_OG [0x1bd]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2016/10/19 12:47:58.0 XRT_QT_PROG_SEt_413_OG [0x19d]
                        MDP_XRT_QT_PROG_SEt        2 07-F0 c4 05
2016/10/19 12:48:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2016/10/19 12:59:54.0 XRT_CTRL_MANU_402_OG [0x192]

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2016/10/19	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	12:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	13:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/10/19	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	00	00	00	00
2016/10/19	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/10/19	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/10/19	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/10/19	13:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/10/19	13:02:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/10/19	13:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2016/10/19	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/10/19	14:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/19	14:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	14:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	14:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2016/10/19	14:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01	00	00	00	00
2016/10/19	14:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/10/19	14:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/10/19	14:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/10/19	14:20:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/10/19	14:22:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/10/19	14:22:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2016/10/19	14:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07			
2016/10/19	15:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/19	15:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	15:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	15:25:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/10/19	15:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/10/19	15:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/10/19	15:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430	1	07-F0	e9				
2016/10/19	15:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/19	15:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	15:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	15:43:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/10/19	15:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/10/19	15:48:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/10/19	15:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430	1	07-F0	e9				
2016/10/19	17:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/19	17:01:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	17:01:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	17:01:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da				
2016/10/19	17:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/10/19	17:24:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/10/19	17:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430	1	07-F0	e9				
2016/10/19	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/19	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/19	18:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	

2016/10/19	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2016/10/19	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2016/10/19	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/10/19	18:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]		2	07-F0	c4	0e			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0e			
2016/10/19	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/10/19	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/19	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/19	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2016/10/19	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]		5	02-76	03	00	00	00	00
		AOCU_NM		5	02-76	03	00	00	00	00
2016/10/19	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2016/10/19	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2016/10/19	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2016/10/19	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2016/10/19	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/10/19	18:12:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]		2	07-F0	c4	01			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	01			
2016/10/19	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]		2	07-F0	c5	07			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	07			
2016/10/19	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/10/19	18:38:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/19	18:38:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/19	18:38:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/10/19	18:38:06.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/10/19	18:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/10/19	19:01:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/10/19	19:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/10/19	20:15:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/19	20:15:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/19	20:15:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/10/19	20:15:06.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/10/19	20:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/10/19	20:38:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/10/19	20:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/10/19	21:52:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/19	21:52:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/19	21:52:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/10/19	21:52:36.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/10/19	21:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/10/19	22:15:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/10/19	22:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/10/19	23:29:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/19	23:29:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/19	23:29:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2016/10/19	23:29:36.0	XRT_PREFLR_STRT_414_OG [0x19e]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2016/10/19	23:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2016/10/19	23:44:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/10/19	23:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2016/10/20	00:59:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/20	00:59:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2016/10/20	00:59:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				

2016/10/20	00:59:36.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/10/20	01:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/10/20	01:13:00.0	XRT_Custom_430_OG [0x1ae]							
2016/10/20	01:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/20	02:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	02:29:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	02:29:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/10/20	02:29:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/10/20	02:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/10/20	02:49:00.0	XRT_Custom_430_OG [0x1ae]							
2016/10/20	02:50:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/20	03:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	03:57:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	03:57:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/10/20	03:57:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/10/20	04:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/10/20	04:26:30.0	XRT_Custom_430_OG [0x1ae]							
2016/10/20	04:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/20	05:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	05:37:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	05:37:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/10/20	05:37:06.0	XRT_PREFLR_STRT_414_OG [0x19e]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2016/10/20	05:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2016/10/20	06:04:00.0	XRT_Custom_430_OG [0x1ae]							
2016/10/20	06:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/20	06:13:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	06:13:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	06:13:28.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2016/10/20	06:13:30.0	AOCS_OrE-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2016/10/20	06:13:48.0	XRT_FLD_DIS_406_OG [0x196]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2016/10/20	06:16:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2016/10/20	06:16:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/10/20	06:16:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2016/10/20	06:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/20	06:23:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	06:23:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	06:23:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2016/10/20	06:23:30.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2016/10/20	06:23:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2016/10/20	06:23:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2016/10/20	06:23:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2016/10/20	06:23:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2016/10/20	06:23:56.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2016/10/20	06:26:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2016/10/20	06:26:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07				
2016/10/20	06:26:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2016/10/20	07:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	07:17:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2016/10/20	07:17:34.0	XRT_FLD_RESET_415_OG [0x19f]							

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2016/10/20	07:17:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/10/20	07:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/10/20	07:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/10/20	07:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/10/20	08:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/10/20	08:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/10/20	08:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/10/20	08:57:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/10/20	09:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/10/20	10:36:00.0	AOCS_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00 00 00