

XRT Timeline to be uploaded on 2016/11/19

Period: 2016/11/19 11:02:00 - 2016/11/29 09:33:00

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Normal mode

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XOB #1B56: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (2x2,1ms) - Leak (2x2,1ms) - 360s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
11/19 11:15:00 - 11/19 13:11:54	Track (71.1, -125.4) @ 11/19 11:12:00	# OP start + 10min, EIS spectral atlas at QS
11/20 05:42:30 - 11/20 09:16:30	Track (179.8, -123.2) @ 11/20 05:39:30	EIS sensitivity monitoring
11/20 23:08:30 - 11/21 06:03:24	Fixed (0.0, 920.0)	EIS CH obs at N-pole

PROG= 08 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
└─ Seqn= 26	1-time(s)	2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close	Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 2	10-time(s)	360.0sec
└─ Seqn= 8	1-time(s)	2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
└─ Seqn= 6	1-time(s)	2.0sec
└─ Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
└─ Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1AFF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
11/19 13:15:00 - 11/19 17:59:54	Track (771.1, 185.7) @ 11/19 13:12:00	AR12610 obs (HOP306 15:00-23:00)
11/19 18:13:00 - 11/20 05:29:24	Track (796.7, 187.4) @ 11/19 18:10:00	AR12610 obs (HOP306 15:00-23:00)
11/20 18:31:30 - 11/20 21:59:54	Track (894.5, 195.8) @ 11/20 18:28:30	AR12610 obs (HOP306 17:00-23:00)
11/20 22:03:00 - 11/20 22:46:30	Fixed (904.0, 197.0)	AR12610 obs (fixed pointing)
11/21 18:13:00 - 11/22 06:05:24	Track (-123.8, -23.0) @ 11/21 18:10:00	AR12611 obs (HOP306 18:00-23:00)

PROG= 01 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
└─ Seqn= 56	1-time(s)	2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Open/G-band	Open/G-band close	Safe Norm 3ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Subr= 2	5-time(s)	2.0sec
└─ Seqn= 75	1-time(s)	2.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Seqn= 96	4-time(s)	60.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 15.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 15.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1B14: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T

Term	Pointing (x, y)	Comment
11/19 18:03:00 - 11/19 18:09:54	Fixed (0.0, 0.0)	synoptic
11/20 05:32:30 - 11/20 05:39:24	Fixed (0.0, 0.0)	synoptic, shifted -30.5 min
11/20 18:21:30 - 11/20 18:28:24	Fixed (0.0, 0.0)	synoptic, shifted 18.5 min
11/21 06:06:30 - 11/21 06:13:24	Fixed (0.0, 0.0)	synoptic, shifted 3.5 min
11/21 18:03:00 - 11/21 18:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 14 1-time(s)

Subr= 1	1-time(s)	2.0sec
└─ Seqn= 5	1-time(s)	2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 1	1-time(s)	2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 99	1-time(s)	2.0sec
└─ Al-poly/Open	Al-poly/Open close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open	Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 67	1-time(s)	2.0sec
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B18: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment										
11/20 09:42:30 - 11/20 17:55:00	Track (864.5, 192.7) ^{Ⓜ 11/20 09:39:30}	AR12610 obs (HOP306 17:00-23:00)										
11/21 06:20:30 - 11/21 17:59:54	Fixed (904.0, 197.0)	AR12610 obs										
11/22 06:18:30 - 11/22 08:48:30	Track (-9.7, -22.3) ^{Ⓜ 11/22 06:15:30}	AR12611 obs										
PROG= 10 Inf.-time(s)												
Subr= 1		1-time(s)	2.0sec									
Seqn= 56		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		5-time(s)	2.0sec									
Seqn= 75		1-time(s)	2.0sec									
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 50		4-time(s)	90.0sec									
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	25.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	25.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B15: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(512/4096), Med-Al

Term	Pointing (x, y)	Comment										
11/22 06:08:30 - 11/22 06:15:24	Fixed (0.0, 0.0)	synoptic, shifted 5.5 min										
PROG= 18 1-time(s)												
Subr= 1		1-time(s)	2.0sec									
Seqn= 5		1-time(s)	2.0sec									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1		1-time(s)	2.0sec									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99		1-time(s)	2.0sec									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67		1-time(s)	2.0sec									
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54		1-time(s)	4.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	3ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 46		2-time(s)	2.0sec									
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 58		2-time(s)	2.0sec									
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 71		2-time(s)	2.0sec									
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1AE7: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment										
11/19 11:15:00 - 11/19 13:11:54	Track (7.1, -125.4) ^{Ⓜ 11/19 11:12:00}	# OP start + 10min, EIS spectral atlas at QS										
11/19 13:15:00 - 11/19 17:59:54	Track (771.1, 185.7) ^{Ⓜ 11/19 13:12:00}	AR12610 obs (HOP306 15:00-23:00)										
11/19 18:13:00 - 11/20 05:29:24	Track (796.7, 187.4) ^{Ⓜ 11/19 18:10:00}	AR12610 obs (HOP306 15:00-23:00)										

11/20 05:42:30 - 11/20 09:16:30	Track (179.8, -123.2) @ 11/20 05:39:30	EIS sensitivity monitoring
11/20 09:42:30 - 11/20 17:55:00	Track (864.5, 192.7) @ 11/20 09:39:30	AR12610 obs (HOP306 17:00-23:00)
11/20 18:31:30 - 11/20 21:59:54	Track (894.5, 195.8) @ 11/20 18:28:30	AR12610 obs (HOP306 17:00-23:00)
11/20 22:03:00 - 11/20 22:46:30	Fixed (904.0, 197.0)	AR12610 obs (fixed pointing)
11/20 23:08:30 - 11/21 06:03:24	Fixed (0.0, 920.0)	EIS CH obs at N-pole
11/21 06:20:30 - 11/21 17:59:54	Fixed (904.0, 197.0)	AR12610 obs
11/21 18:13:00 - 11/22 06:05:24	Track (-123.8, -23.0) @ 11/21 18:10:00	AR12611 obs (HOP306 18:00-23:00)
11/22 06:18:30 - 11/22 08:48:30	Track (-9.7, -22.3) @ 11/22 06:15:30	AR12611 obs

PROG= 07 30-time(s)

Subr= 1 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/thick-Al close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn=100 1-time(s) 10.0sec													
thin-Be/Open	med-Be/Open close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec		
med-Be/Open	Open/thick-Al close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Be close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
med-Al/Open	med-Al/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/thick-Al close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 84 1-time(s) 2.0sec													
Open/G-band	Open/G-band open	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band close	Safe	Norm	3ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

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Active Region Search

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NOT USED

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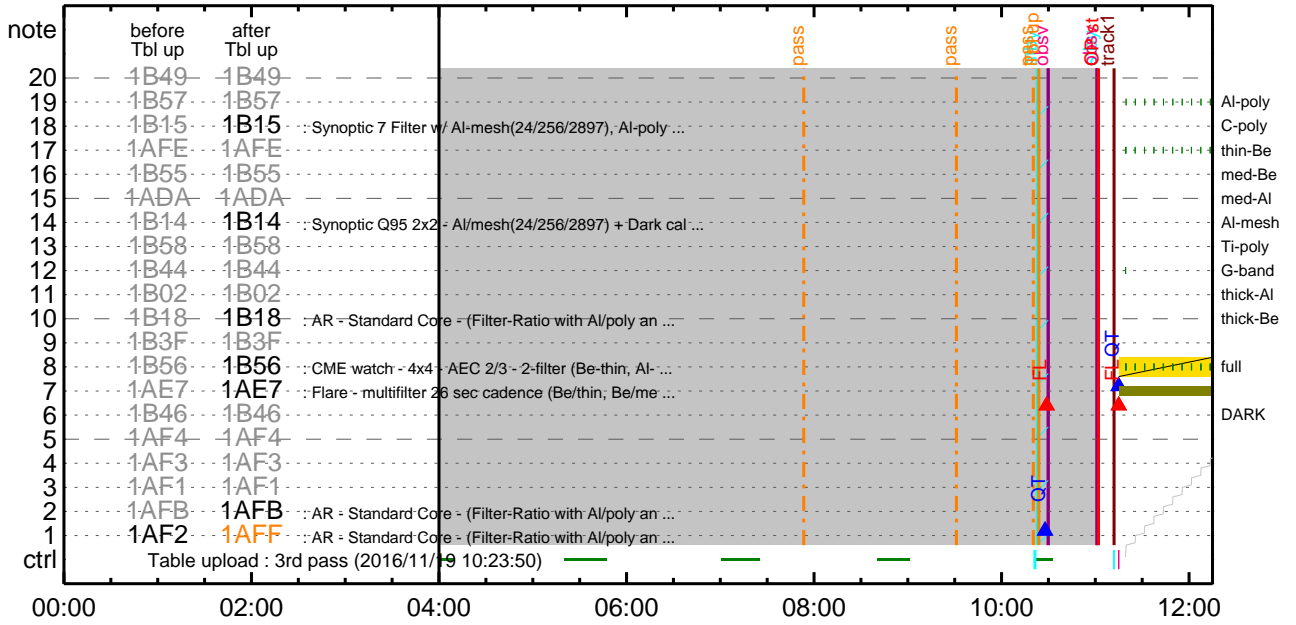
Flare Detection

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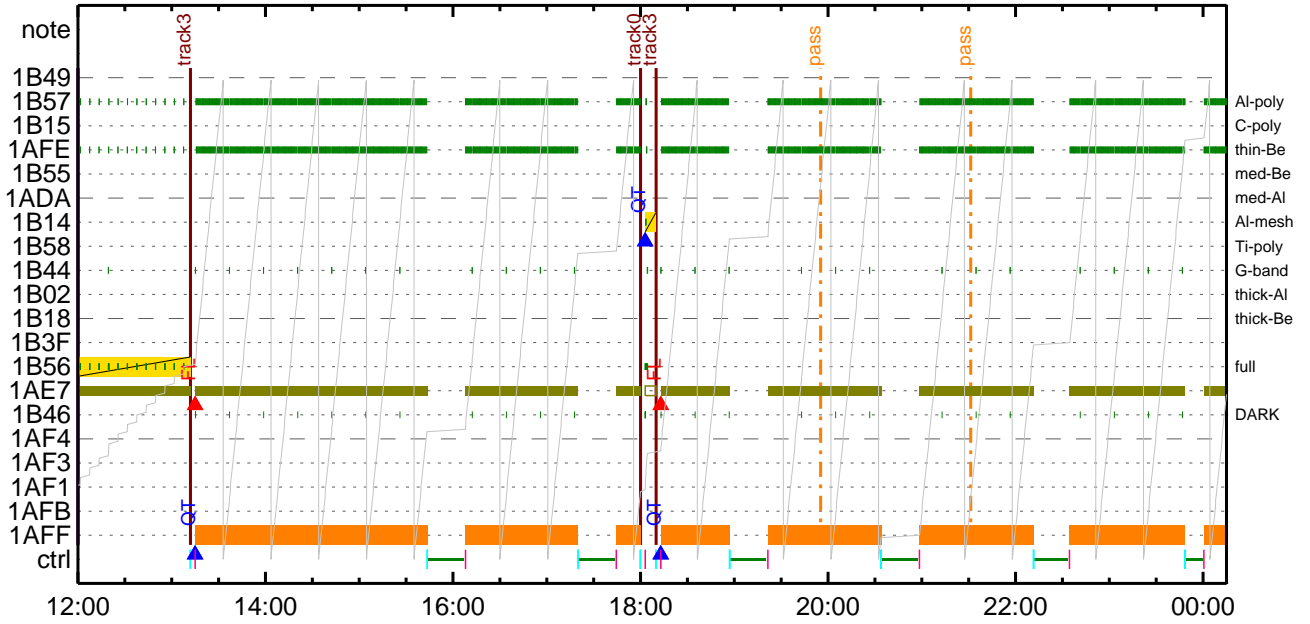
FLD Patrol

Term	Pointing (x, y)	Comment
11/19 18:10:18 - 11/20 05:29:48	Track (796.7, 187.4) @ 11/19 18:10:00	AR12610 obs (HOP306 15:00-23:00)
11/20 05:39:48 - 11/20 18:18:48	Track (179.8, -123.2) @ 11/20 05:39:30	EIS sensitivity monitoring
11/20 18:28:48 - 11/21 06:03:48	Track (894.5, 195.8) @ 11/20 18:28:30	AR12610 obs (HOP306 17:00-23:00)
11/21 06:17:48 - 11/21 18:00:18	Fixed (904.0, 197.0)	AR12610 obs
11/21 18:10:18 - 11/22 06:05:48	Track (-123.8, -23.0) @ 11/21 18:10:00	AR12611 obs (HOP306 18:00-23:00)
11/22 06:15:48 - 11/29 09:33:00	Track (-9.7, -22.3) @ 11/22 06:15:30	AR12611 obs
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

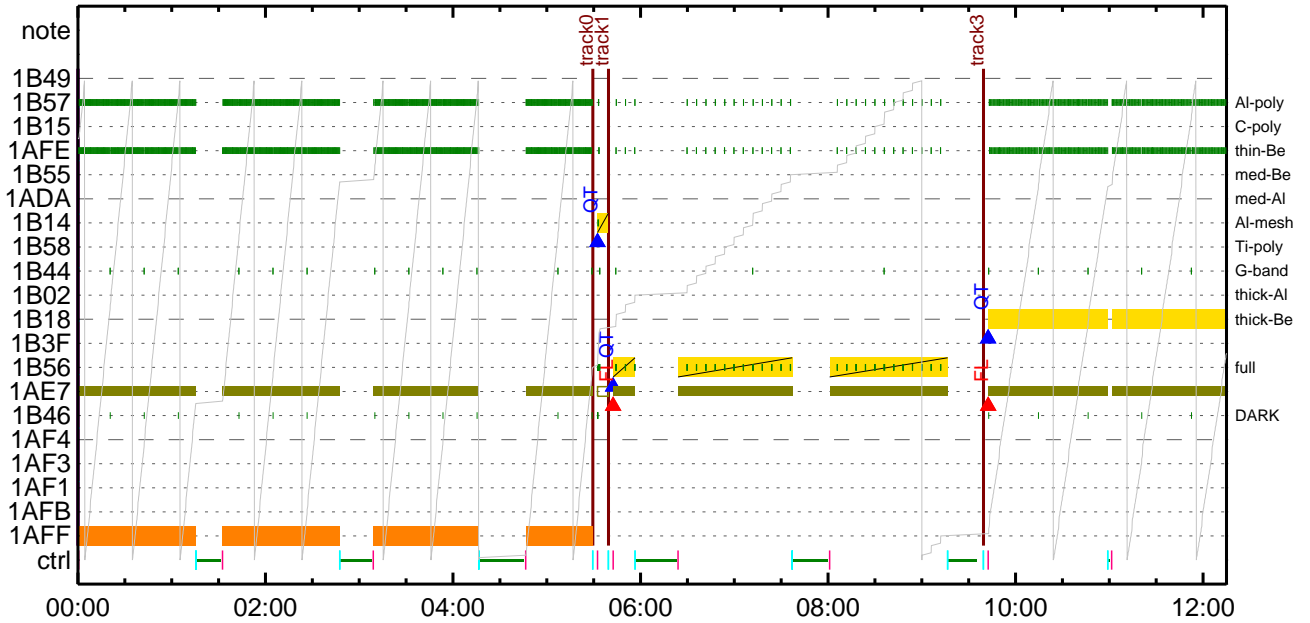
CMDI #0323 2016/11/19



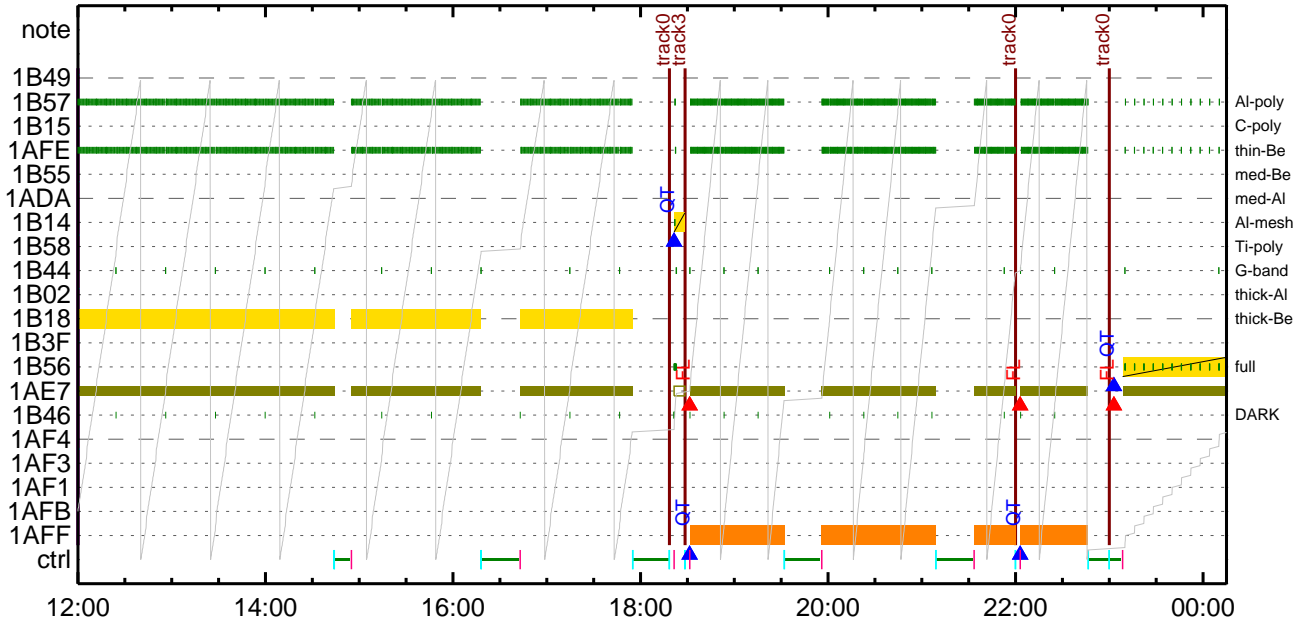
CMDI #0323 2016/11/19



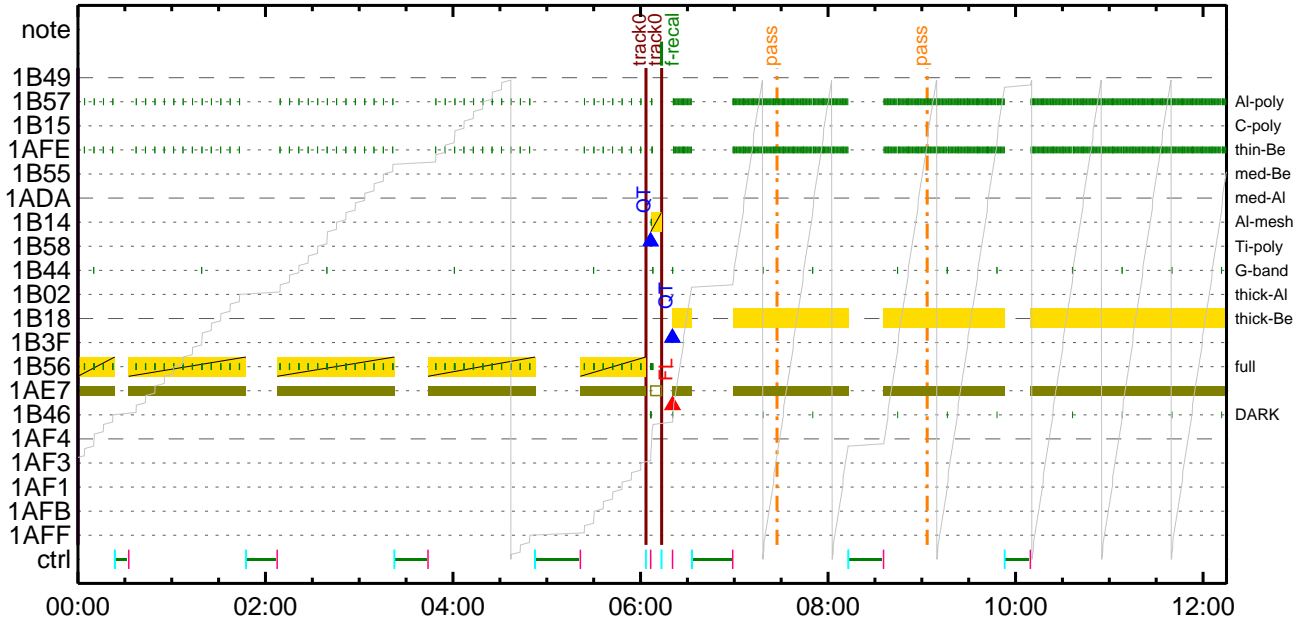
CMDI #0323 2016/11/20



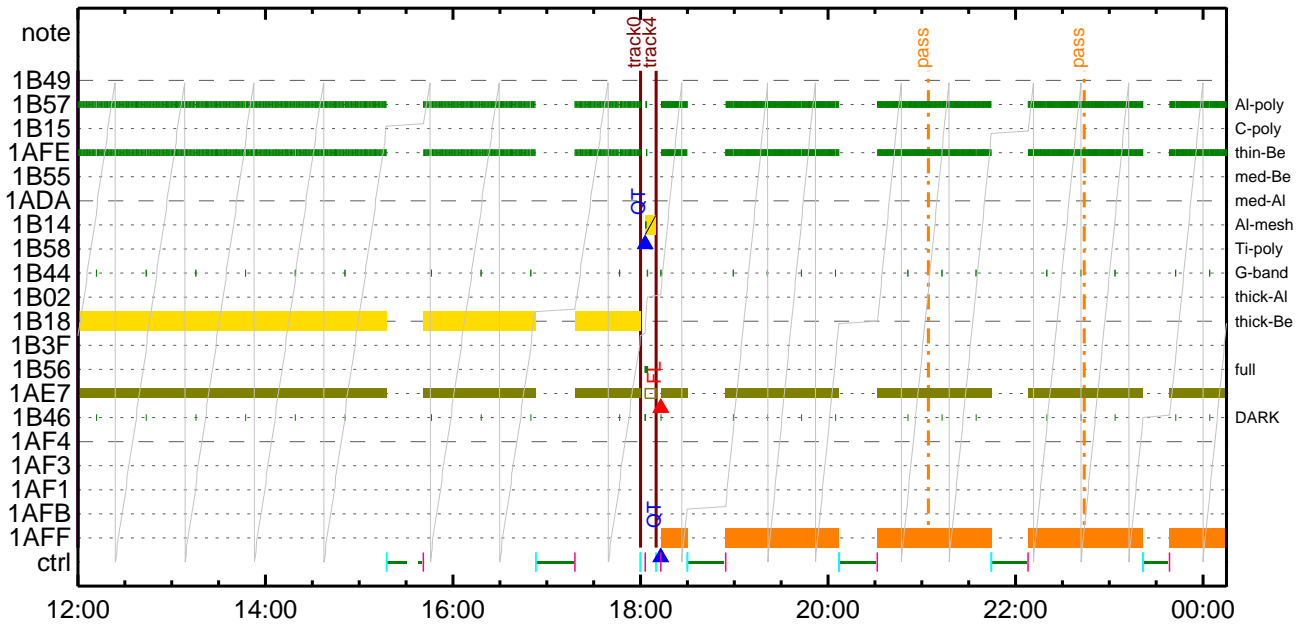
CMDI #0323 2016/11/20



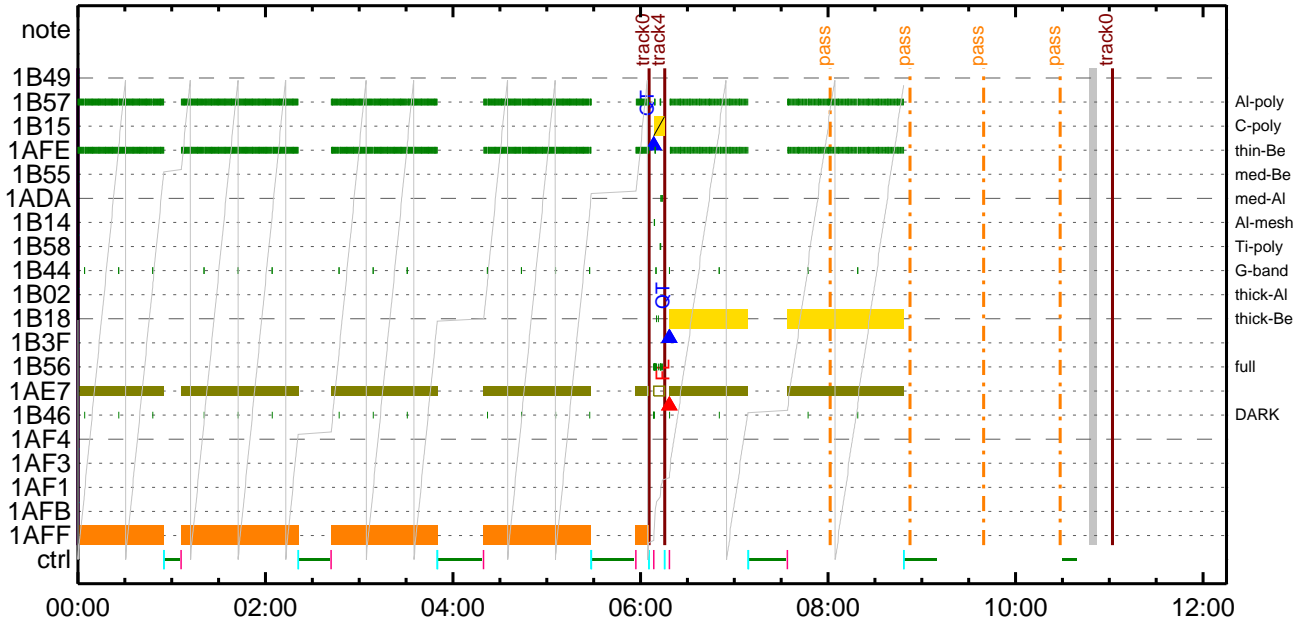
CMDI #0323 2016/11/21



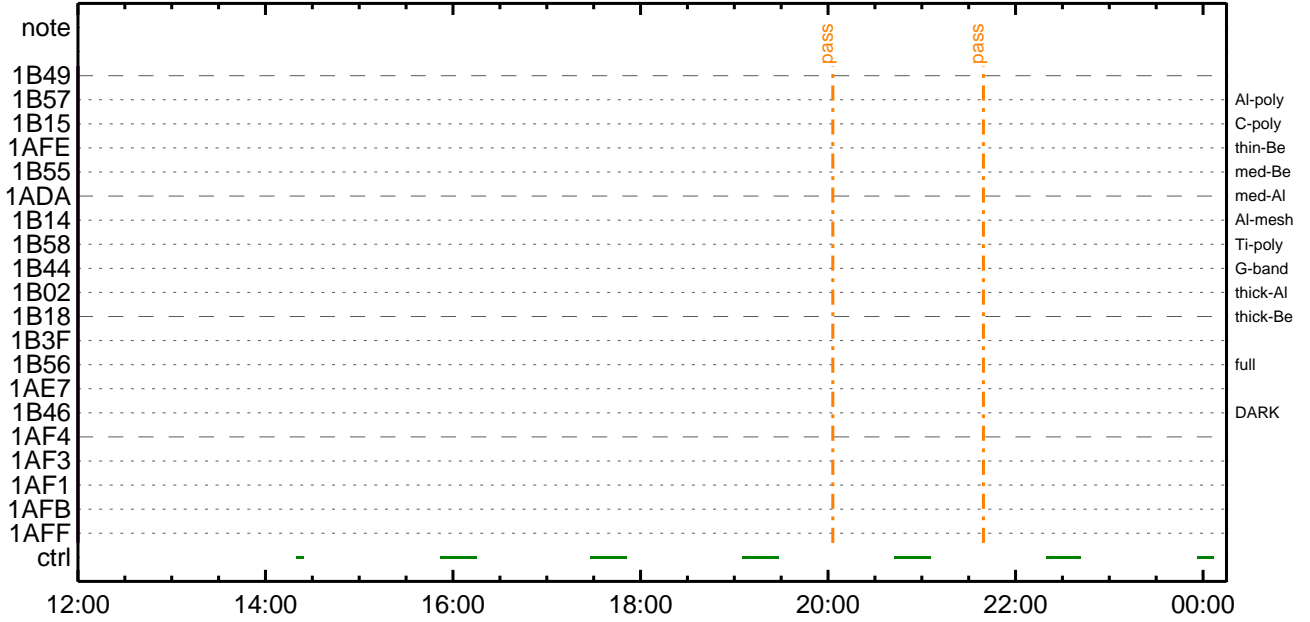
CMDI #0323 2016/11/21



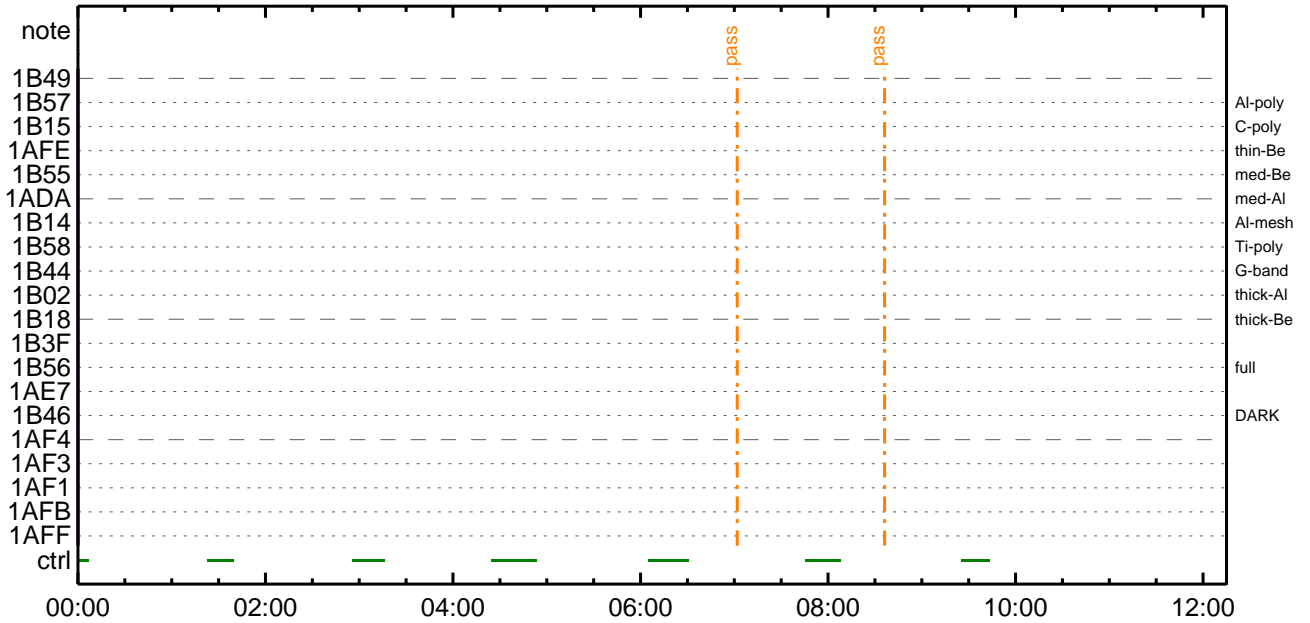
CMDI #0323 2016/11/22



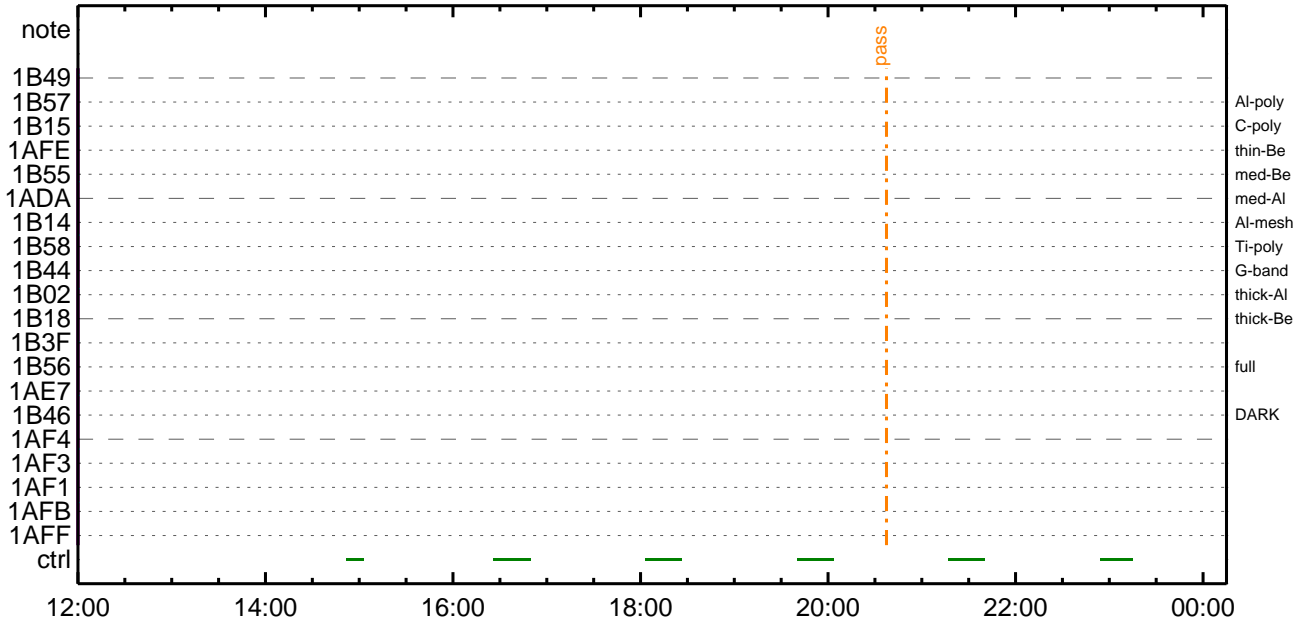
CMDI #0323 2016/11/22



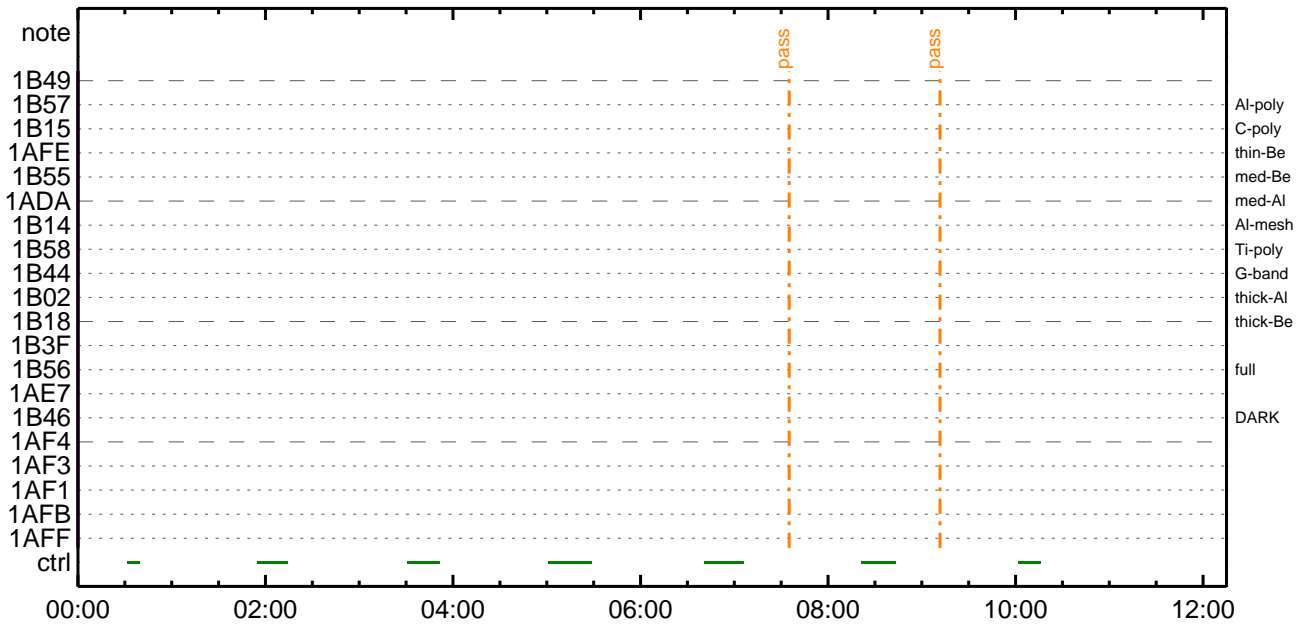
CMDI #0323 2016/11/23



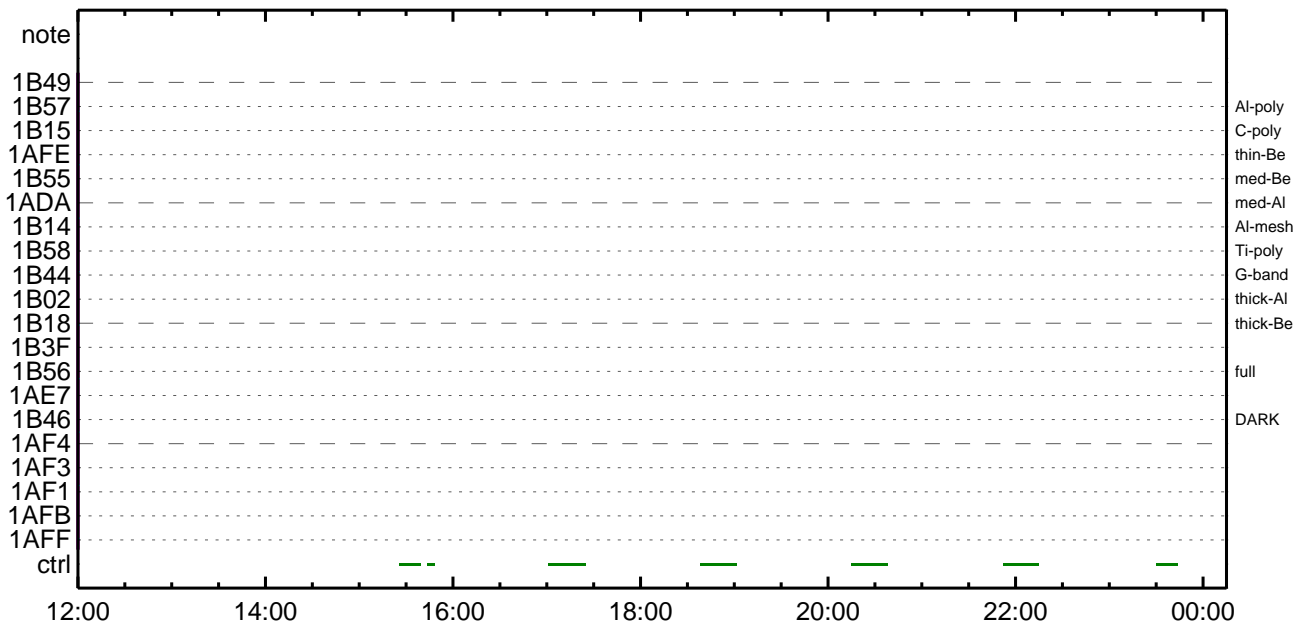
CMDI #0323 2016/11/23



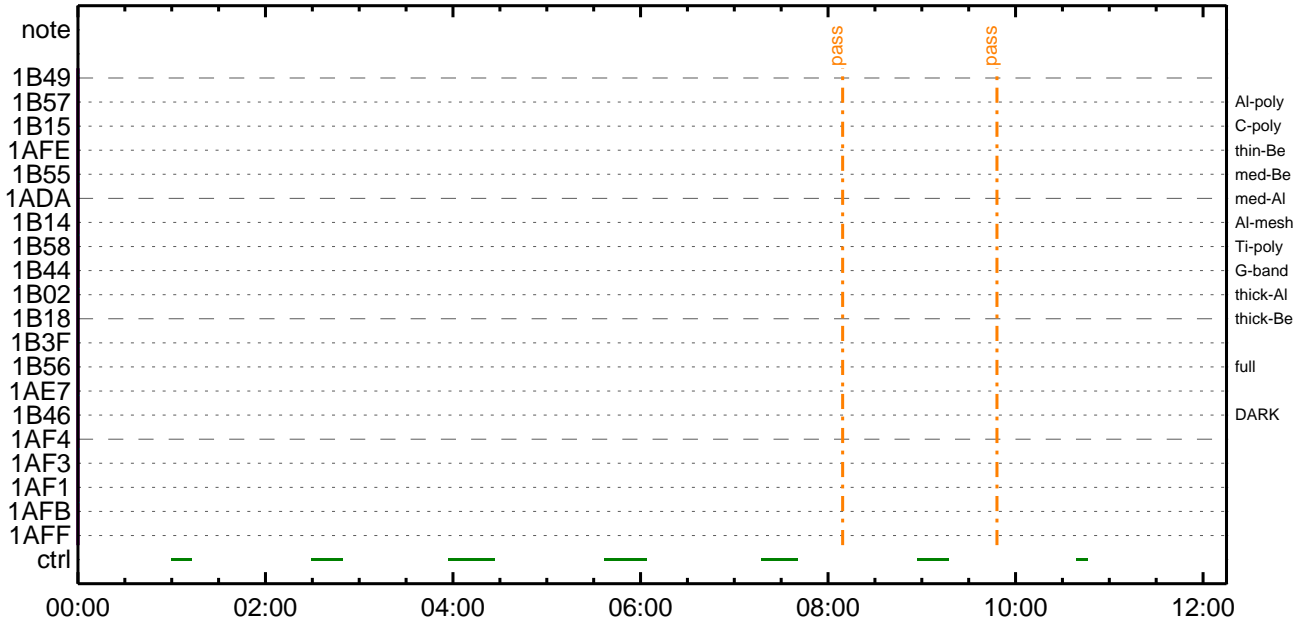
CMDI #0323 2016/11/24



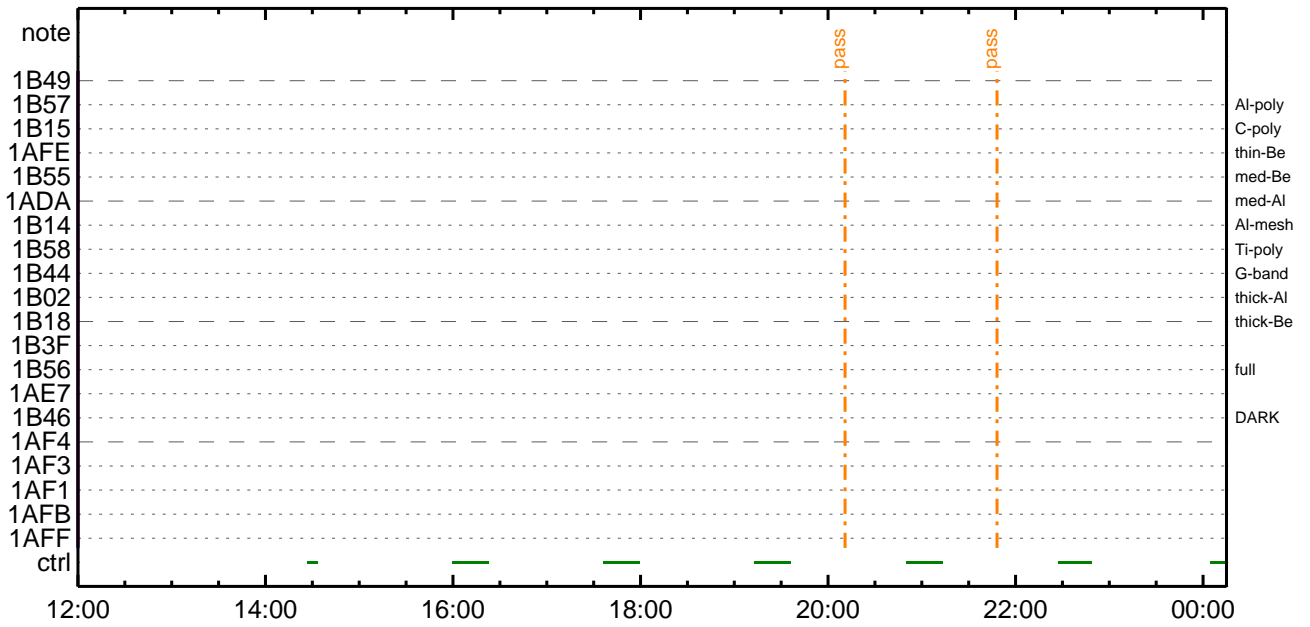
CMDI #0323 2016/11/24



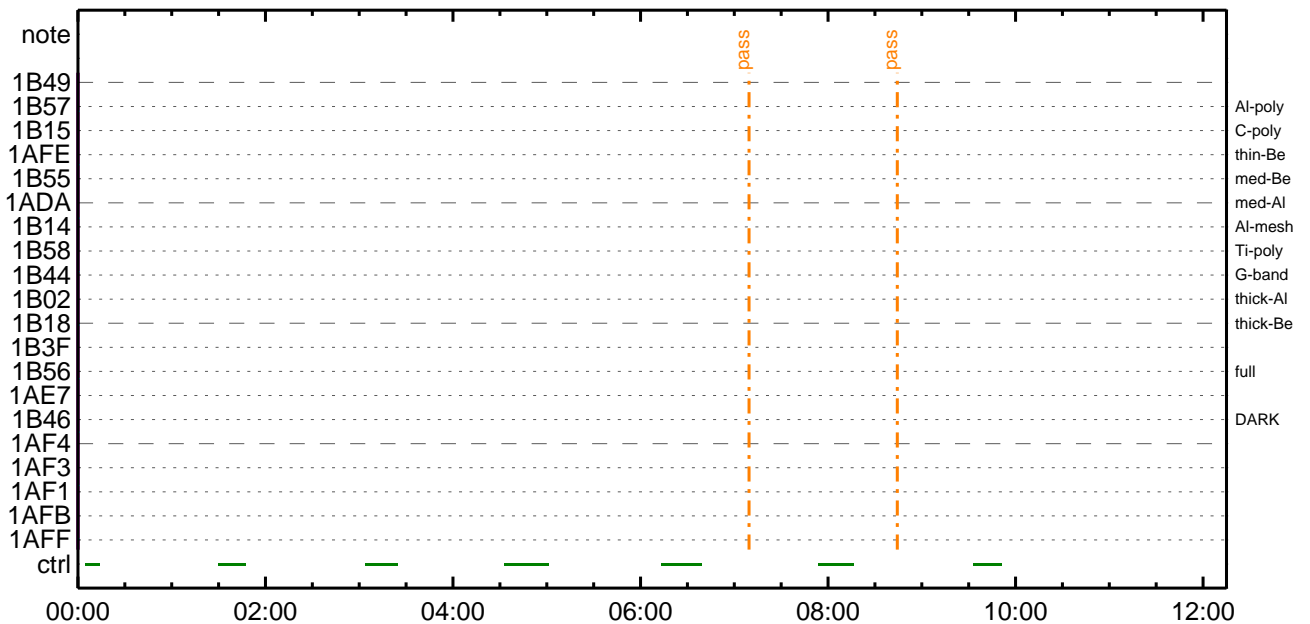
CMDI #0323 2016/11/25



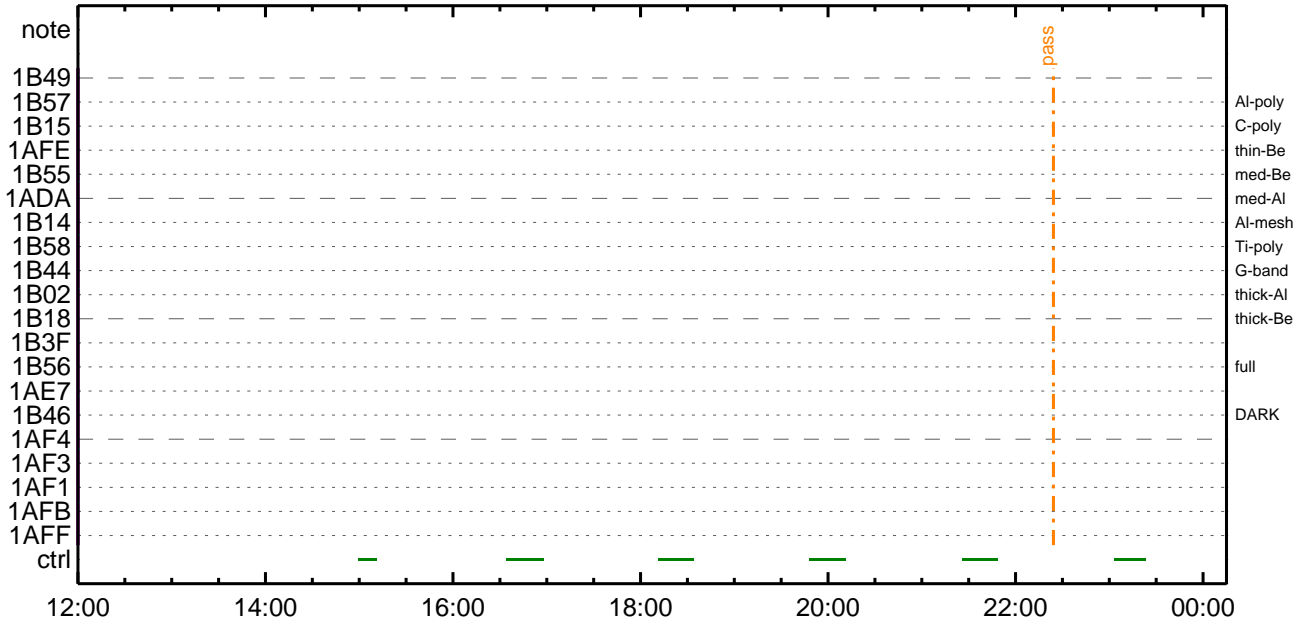
CMDI #0323 2016/11/25



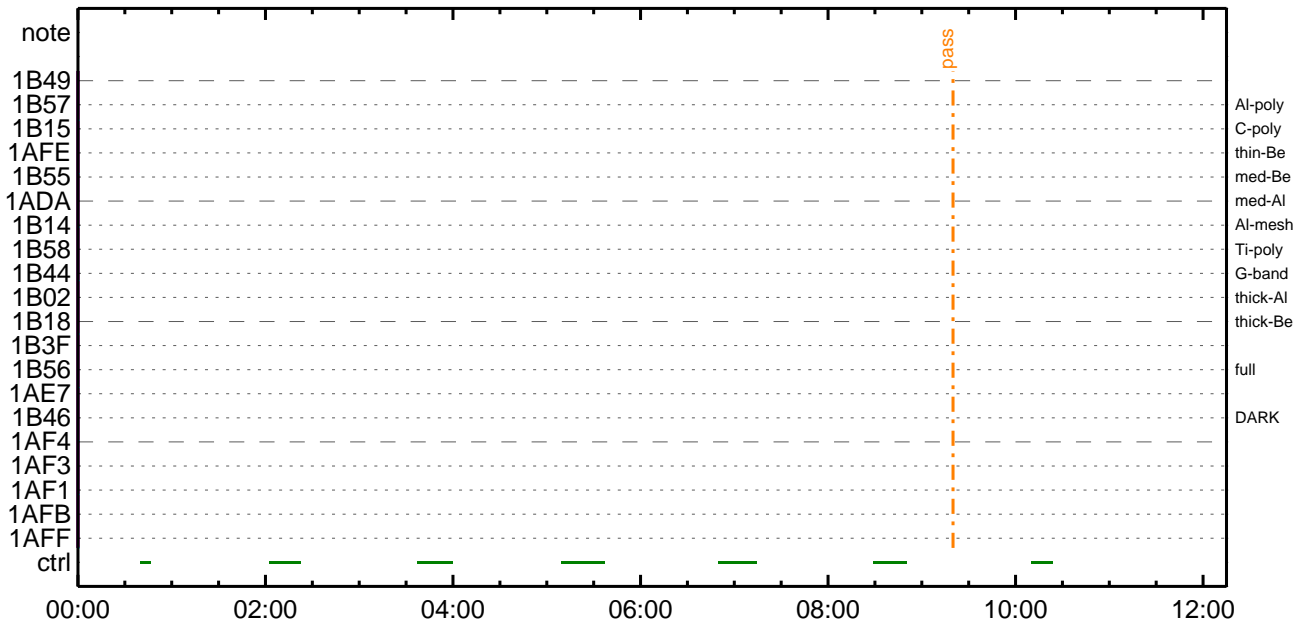
CMDI #0323 2016/11/26



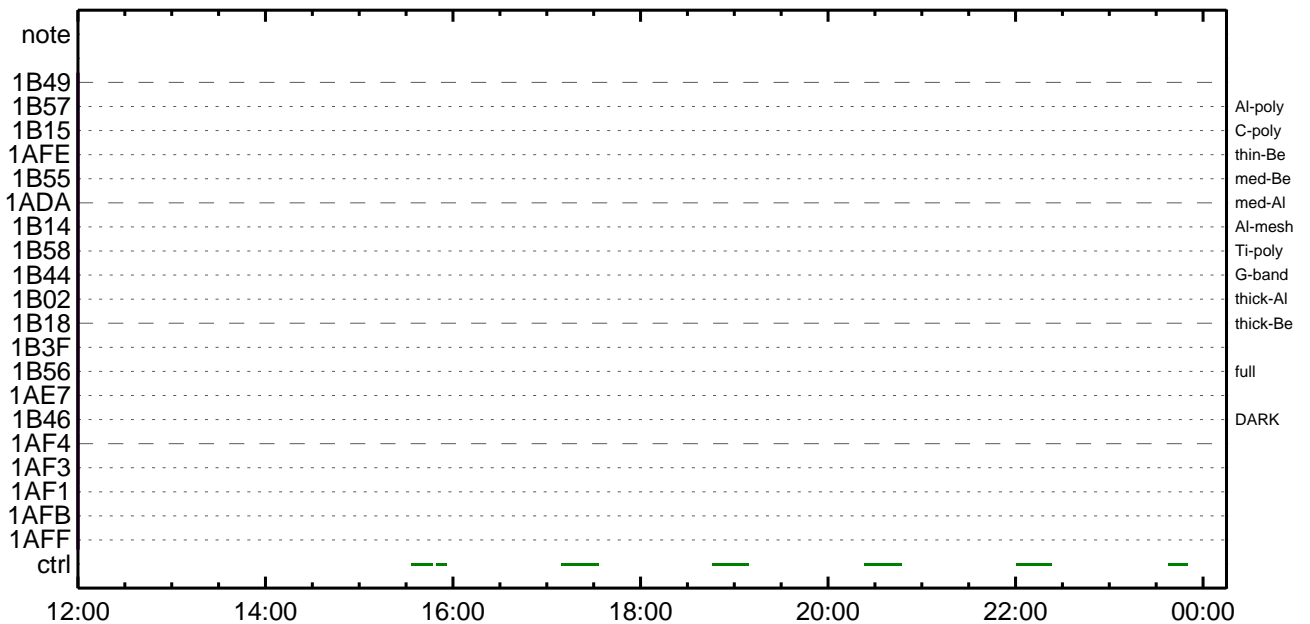
CMDI #0323 2016/11/26



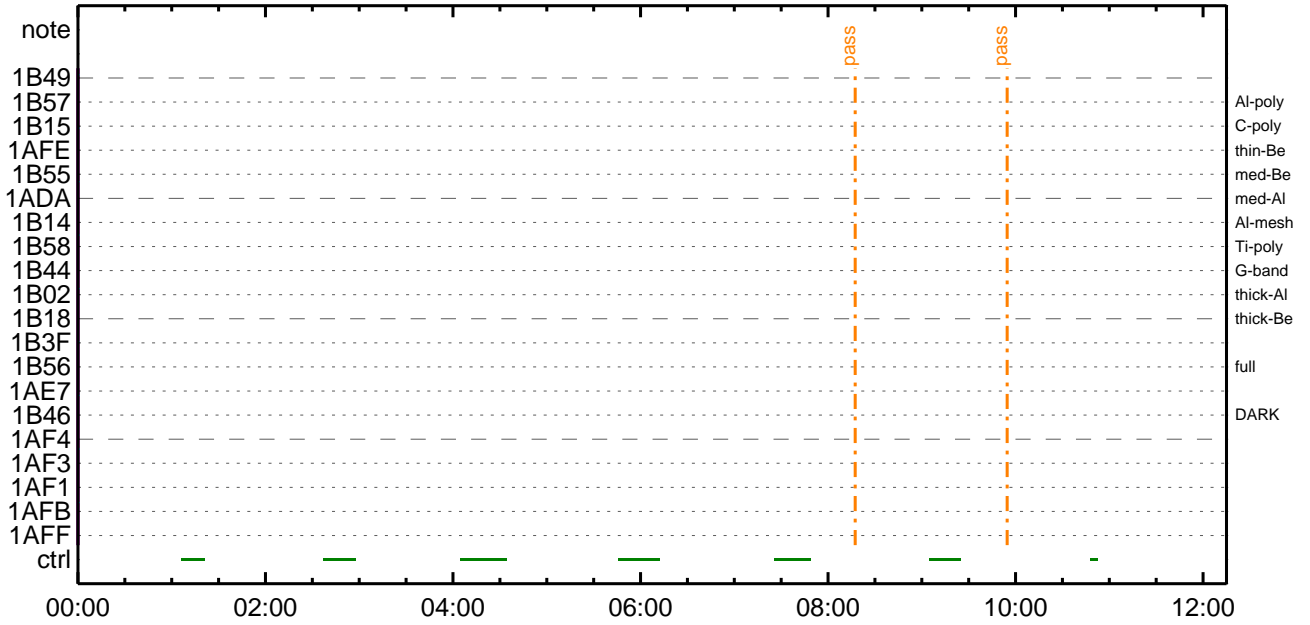
CMDI #0323 2016/11/27



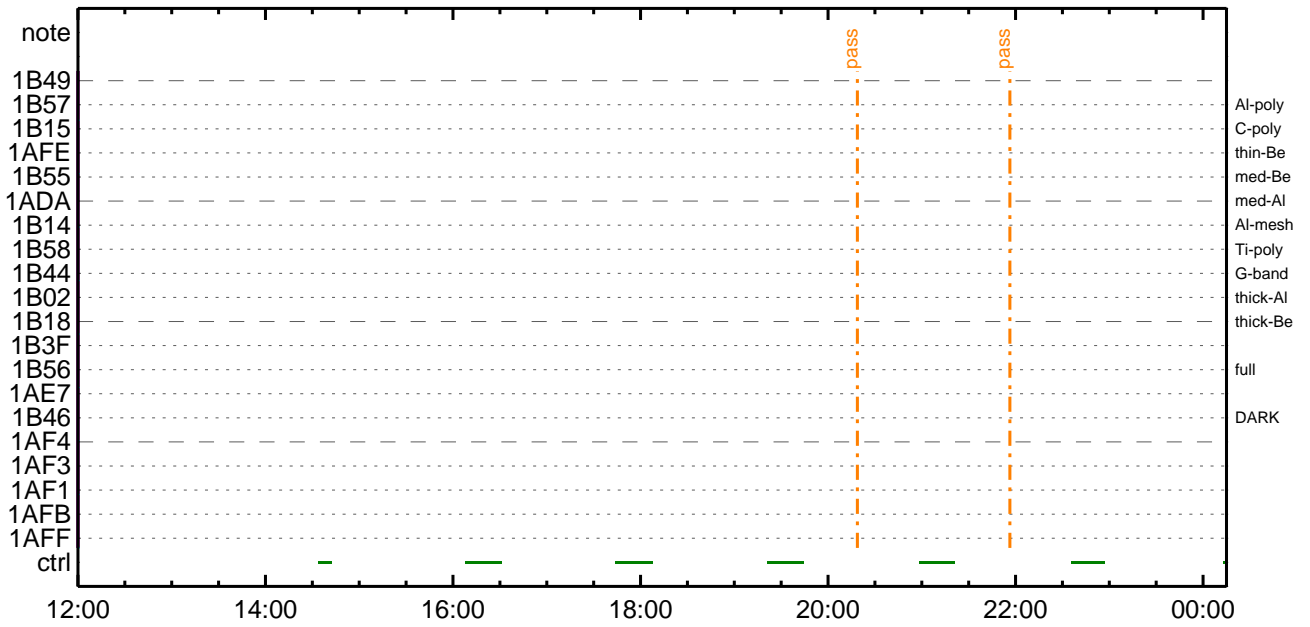
CMDI #0323 2016/11/27



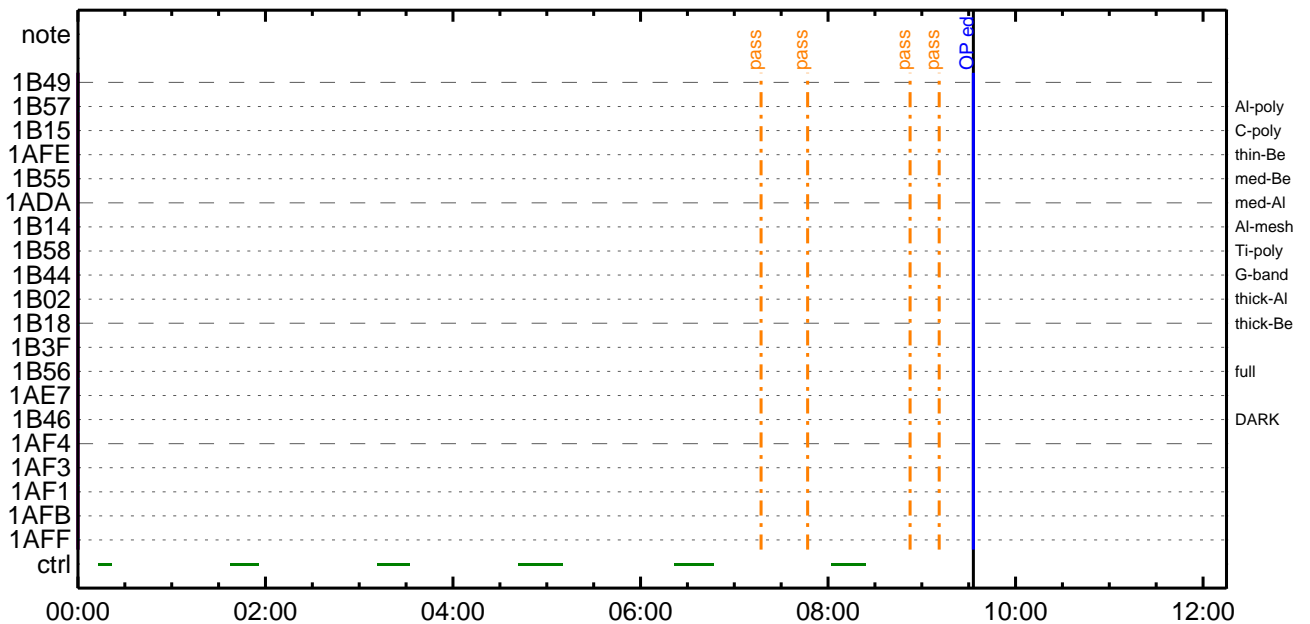
CMDI #0323 2016/11/28



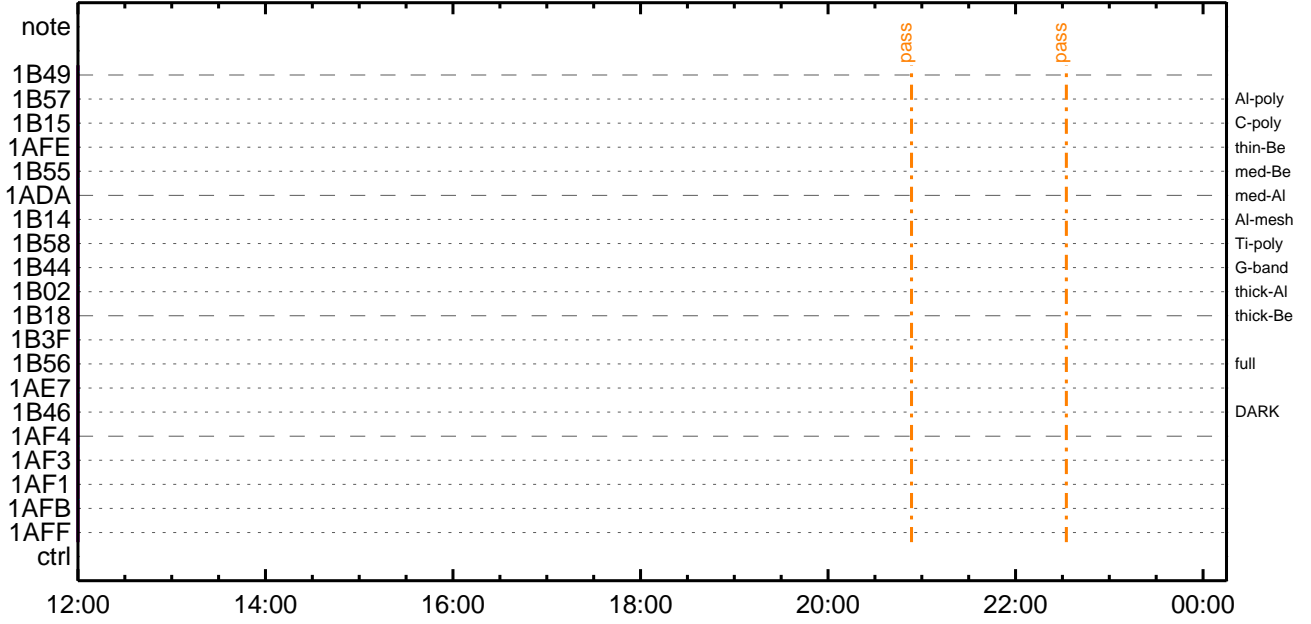
CMDI #0323 2016/11/28



CMDI #0323 2016/11/29



CMDI #0323 2016/11/29




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-250:OP
0104 ( )
0105 S. OG og-250:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°è¥ÅYó¥x;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥ÅYó¥x½ªî»òð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼Ê¹Ç•è²îOK²ð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥ÅYó¥x½ªî»òð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼Ê¹Ç•è²îOK²ð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥ÅYó¥x½ªî»òð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OP²î¼Ê¹Ç•è²îOK²ð³îÇ§
0165 C.
0166 C. ***** °Ê²¼²î¼Ê¹Ç•è²îOK²ð³îÇ§ *****
0167 C. DHU¥â;½YE;Ê¼Y½;¥î;½YE;Êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷çîNG²î¼Ê¹Ç•è²îOK²ð³îÇ§ *****
0180 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0181 C.
0182 C. TIY³¥P¥ó¥E²ððÁDîç(UT)
0183 +. TI 2016-11-19 10:57:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2016-11-19 10:57:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2016-11-19 10:57:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 1m5s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2016-11-19 11:01:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÃîâî»ô¼ÝðËÃð¹æDCBC•x²è *****
0156 C. (¼ã°îÝÓYÁYËYÏYËYáYçYèæE¼¼¼¼¼»Û¹æ)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** YD¥¹•İ Daily±¿İÑæ'Ø¹æDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥Á¥¹¼Á»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.

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*** OP Sequence for XRT ***

2016/11/19	11:11:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/19	11:11:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/19	11:11:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/11/19	11:12:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2016/11/19	11:12:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/11/19	11:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/11/19	11:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/11/19	11:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/11/19	11:12:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/19	11:14:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2016/11/19	11:14:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/11/19	11:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/11/19	13:11:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/19	13:11:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/19	13:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2016/11/19	13:12:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2016/11/19	13:12:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2016/11/19	13:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2016/11/19	13:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2016/11/19	13:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/11/19	13:12:26.0	XRT_FLD_RESET_433_OG [0x1b1]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/19	13:14:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2016/11/19	13:14:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 07		
2016/11/19	13:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/11/19	15:43:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/19	15:43:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/19	15:43:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/19	15:43:36.0	XRT_PREFLR_STRT_414_OG [0x19e]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/11/19	15:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/11/19	16:07:00.0	XRT_Custom_430_OG [0x1ae]					
2016/11/19	16:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/11/19	17:20:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/19	17:20:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/19	17:20:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2016/11/19	17:20:06.0	XRT_PREFLR_STRT_414_OG [0x19e]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2016/11/19	17:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2016/11/19	17:43:30.0	XRT_Custom_430_OG [0x1ae]					
2016/11/19	17:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2016/11/19	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/19	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2016/11/19	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2016/11/19	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2016/11/19	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2016/11/19	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2016/11/19	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2016/11/19	18:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2016/11/19	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					

2016/11/19	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/19	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/19	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/19	18:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/11/19	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2016/11/19	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/19	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/19	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/19	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/19	18:12:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/19	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/11/19	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/19	18:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/19	18:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/19	18:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/19	18:57:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/19	19:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/19	19:20:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/19	19:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/19	20:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/19	20:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/19	20:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/19	20:34:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/19	20:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/19	20:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/19	20:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/19	22:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/19	22:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/19	22:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/19	22:11:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/19	22:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/19	22:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/19	22:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/19	23:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/19	23:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/19	23:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/19	23:48:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/19	23:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/19	23:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/20	00:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/20	01:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/20	01:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/20	01:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/20	01:15:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/20	01:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/20	01:31:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/20	01:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/20	02:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/20	02:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2016/11/20	02:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	02:47:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	02:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/20	03:08:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/20	03:09:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/20	04:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	04:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	04:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	04:16:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	04:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/20	04:45:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/20	04:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/20	05:29:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	05:29:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	05:29:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	05:29:30.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/11/20	05:29:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00			
2016/11/20	05:32:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/11/20	05:32:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2016/11/20	05:32:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/11/20	05:32:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2016/11/20	05:39:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/20	05:39:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	05:39:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	05:39:30.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2016/11/20	05:39:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00			
2016/11/20	05:39:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/11/20	05:39:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/11/20	05:39:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/11/20	05:39:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/11/20	05:42:26.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	05:42:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2016/11/20	05:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07			
2016/11/20	05:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/20	05:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	05:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	05:56:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	05:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/20	06:23:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/20	06:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/20	07:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	07:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	07:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	07:37:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	07:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/20	08:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/20	08:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/20	09:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2016/11/20	09:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	09:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	09:16:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/20	09:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/20	09:39:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	09:39:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	09:39:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2016/11/20	09:39:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03	00	00	00
2016/11/20	09:39:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2016/11/20	09:39:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2016/11/20	09:39:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2016/11/20	09:39:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2016/11/20	09:39:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	09:42:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2016/11/20	09:42:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07		
2016/11/20	09:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/20	10:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	10:59:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	10:59:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	10:59:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/20	11:00:30.0	XRT_Custom_430_OG [0x1ae]							
2016/11/20	11:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/20	11:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/20	14:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	14:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	14:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	14:44:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/20	14:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/20	14:54:00.0	XRT_Custom_430_OG [0x1ae]							
2016/11/20	14:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/20	16:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	16:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	16:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	16:18:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/20	16:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/20	16:42:00.0	XRT_Custom_430_OG [0x1ae]							
2016/11/20	16:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2016/11/20	17:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	17:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	17:55:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2016/11/20	17:55:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2016/11/20	17:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2016/11/20	18:18:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	18:18:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2016/11/20	18:18:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2016/11/20	18:18:30.5	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00
2016/11/20	18:18:48.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2016/11/20	18:21:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			

2016/11/20	18:21:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/11/20	18:21:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2016/11/20	18:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/11/20	18:28:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	18:28:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	18:28:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/11/20	18:28:30.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03	00 00 00 00
2016/11/20	18:28:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/11/20	18:28:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/11/20	18:28:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/11/20	18:28:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/11/20	18:28:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/11/20	18:31:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2016/11/20	18:31:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/11/20	18:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/11/20	19:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	19:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	19:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/11/20	19:32:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/11/20	19:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/11/20	19:55:00.0	XRT_Custom_430_OG [0x1ae]					
2016/11/20	19:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/11/20	21:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	21:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	21:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/11/20	21:09:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/11/20	21:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/11/20	21:32:30.0	XRT_Custom_430_OG [0x1ae]					
2016/11/20	21:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/11/20	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	21:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	21:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2016/11/20	22:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	ee 7f af a6
2016/11/20	22:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2016/11/20	22:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2016/11/20	22:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2016/11/20	22:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2016/11/20	22:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/11/20	22:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2016/11/20	22:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	07
2016/11/20	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2016/11/20	22:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	22:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	22:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2016/11/20	22:46:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2016/11/20	22:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2016/11/20	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2016/11/20	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2016/11/20	22:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/20	23:00:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/11/20	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 ae 36 00 00
2016/11/20	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/20	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/20	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/20	23:00:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/20	23:02:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/20	23:02:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2016/11/20	23:07:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/20	23:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	00:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	00:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	00:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	00:23:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	00:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	00:31:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	00:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	01:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	01:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	01:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	01:47:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	01:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	02:06:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	02:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	03:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	03:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	03:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	03:22:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	03:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	03:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	03:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	04:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	04:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	04:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	04:52:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	04:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	05:20:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	05:21:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	06:03:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	06:03:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	06:03:30.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/11/21	06:03:48.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/11/21	06:06:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/11/21	06:06:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/11/21	06:06:28.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/21	06:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2016/11/21	06:13:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2016/11/21	06:13:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	06:13:28.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	06:13:30.0	AOCs_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_RECAL	2	07-F8	78 00
2016/11/21	06:17:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	AOCU_NM	5	02-76	00 ee 7f af a6
2016/11/21	06:17:48.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/11/21	06:17:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/21	06:17:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/21	06:17:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/21	06:17:56.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/21	06:20:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	06:20:28.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2016/11/21	06:20:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/21	06:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	06:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	06:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	06:33:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	06:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	06:58:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	06:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	08:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	08:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	08:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	08:13:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	08:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	08:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	08:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	09:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	09:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	09:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	09:53:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	09:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	10:08:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	10:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	15:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	15:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	15:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	15:17:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	15:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	15:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	15:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	16:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	16:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	16:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	16:53:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	16:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	17:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	17:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2016/11/21	17:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	18:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2016/11/21	18:00:18.0	XRT_FLD_DIS_406_OG [0x196]	AOCU_NM	5	02-76	00 00 00 00 00
2016/11/21	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2016/11/21	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2016/11/21	18:02:58.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/21	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2016/11/21	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	18:10:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2016/11/21	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00
2016/11/21	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2016/11/21	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2016/11/21	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2016/11/21	18:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2016/11/21	18:12:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	18:12:58.0	XRT_FL_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2016/11/21	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 07
2016/11/21	18:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	18:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	18:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	18:30:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	18:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	18:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	18:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	20:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	20:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	20:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	20:07:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	20:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	20:30:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	20:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	21:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	21:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	21:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	21:44:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	21:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	22:07:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	22:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/21	23:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/21	23:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	23:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/21	23:21:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/21	23:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/21	23:37:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/21	23:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/22	00:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1

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2016/11/22	07:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/22	07:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/22	07:09:06.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/22	07:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/22	07:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2016/11/22	07:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2016/11/22	08:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2016/11/22	08:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/22	08:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2016/11/22	08:48:36.0	XRT_PREFLR_STRT_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2016/11/22	08:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2016/11/22	11:02:00.0	AOCS_OrE-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM		5	02-76	00 00 00 00 00