

XRT Timeline to be uploaded on 2017/12/12

Period: 2017/12/12 11:05:00 - 2017/12/16 10:43:00

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Normal mode

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XOB #1B8F: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(512ms), Al/Poly(1443ms) - w leak image-1msCCD												
Term	Pointing (x, y)						Comment					
12/13 12:33:00 - 12/13 12:39:54	Fixed (-528.4, -528.4)						# XRT Quadrant Pointing (1/4)					
PROG= 01 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 51 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 55 2-time(s) 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 34 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1B90: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-1 ms												
Term	Pointing (x, y)						Comment					
12/13 12:43:00 - 12/13 12:49:54	Fixed (528.4, -528.4)						# XRT Quadrant Pointing (2/4)					
PROG= 08 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 38 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 55 2-time(s) 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 34 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1B91: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-1 ms												
Term	Pointing (x, y)						Comment					
12/13 12:53:00 - 12/13 12:59:54	Fixed (528.4, 528.4)						# XRT Quadrant Pointing (3/4)					
PROG= 16 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 21 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 55 2-time(s) 2.0sec												
└─ Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 34 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1B92: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-1 ms												
Term	Pointing (x, y)						Comment					
12/13 13:03:00 - 12/13 13:09:54	Fixed (-528.4, 528.4)						# XRT Quadrant Pointing (4/4)					
PROG= 12 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 14 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												

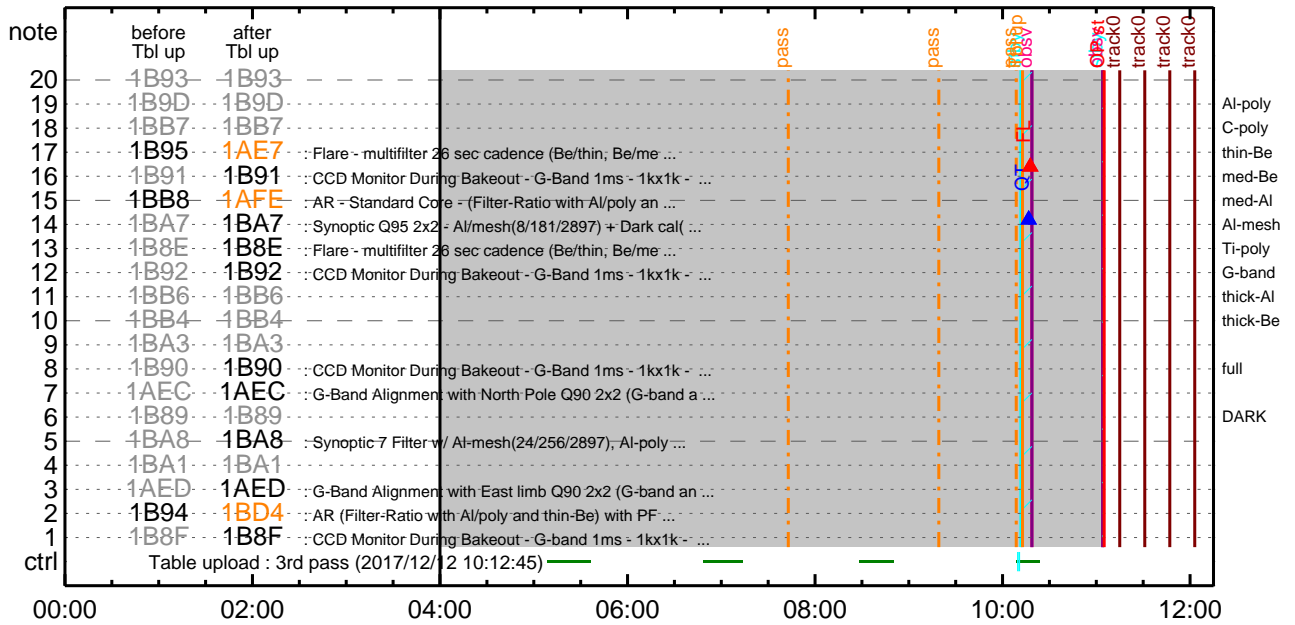
* * * * *

Flare Detection

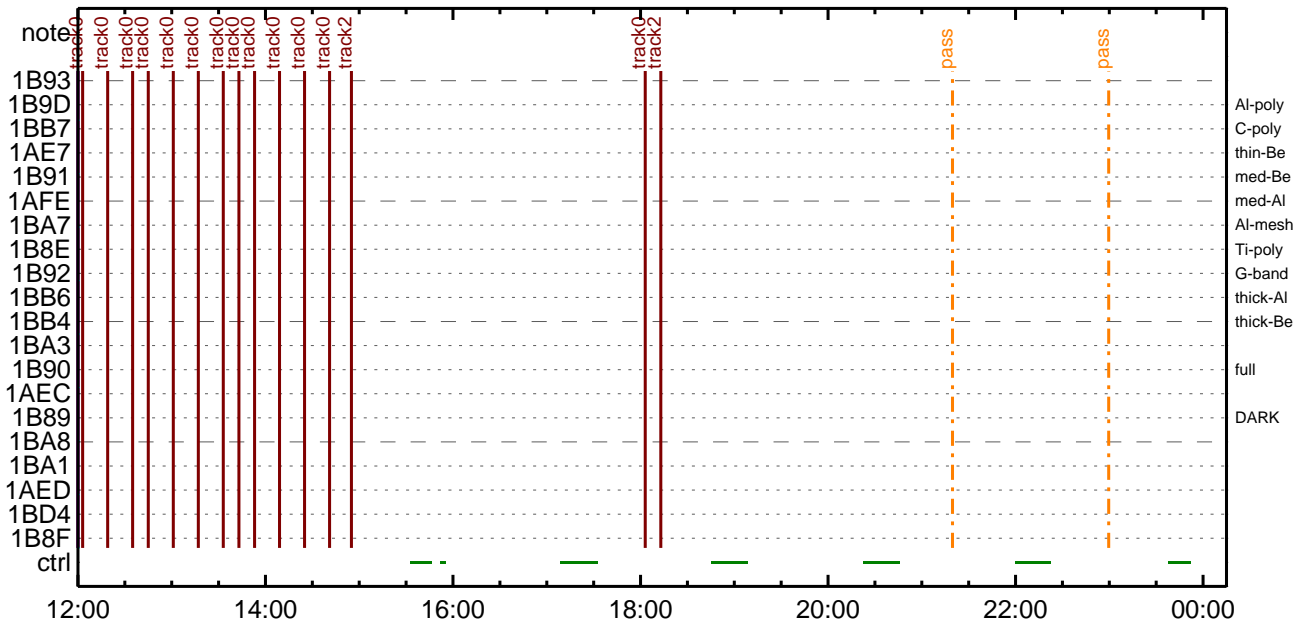
* * * * *

FLD Patrol											
Term		Pointing (x, y)						Comment			
12/13 13:10:18 - 12/13 18:07:18	Track (-659.9, -137.8)	① 12/13 13:10:00				# AR on E-limb					
12/13 22:17:18 - 12/14 05:53:18	Track (-595.4, -136.5)	② 12/13 22:17:00				# AR on E-limb					
12/14 06:03:18 - 12/16 10:43:00	Track (-536.7, -135.4)	③ 12/14 06:03:00				# AR on E-limb					
AI-poly/Open	AI-poly/Open	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

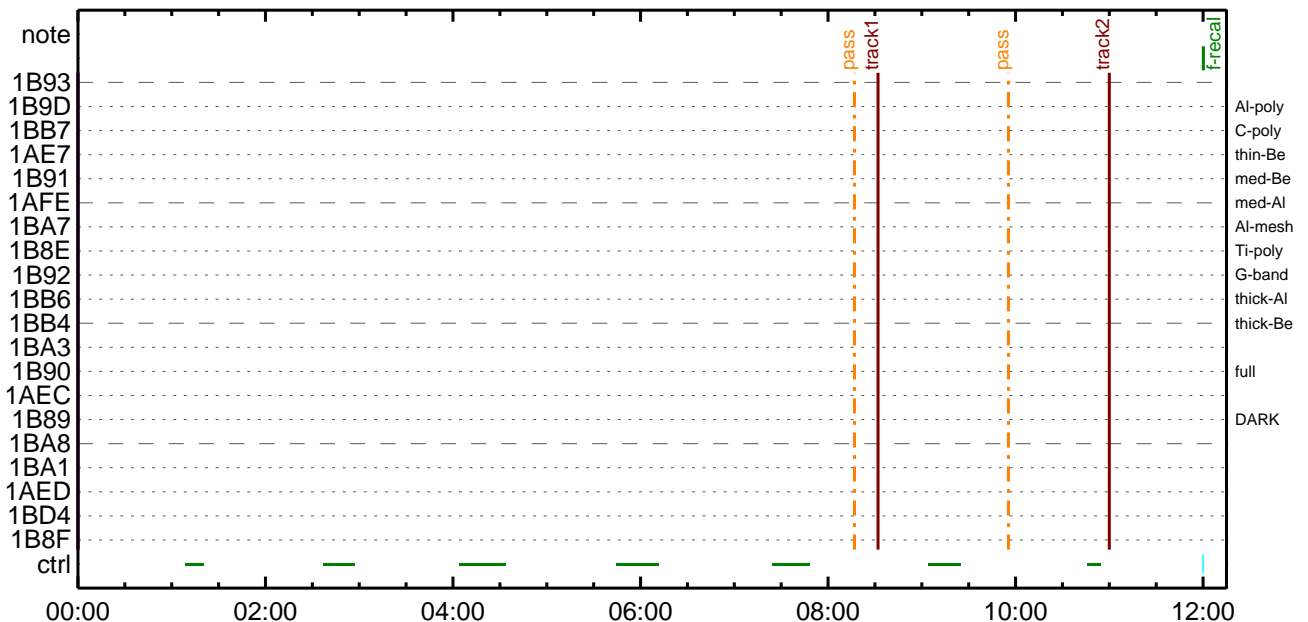
CMDI #0190 2017/12/12



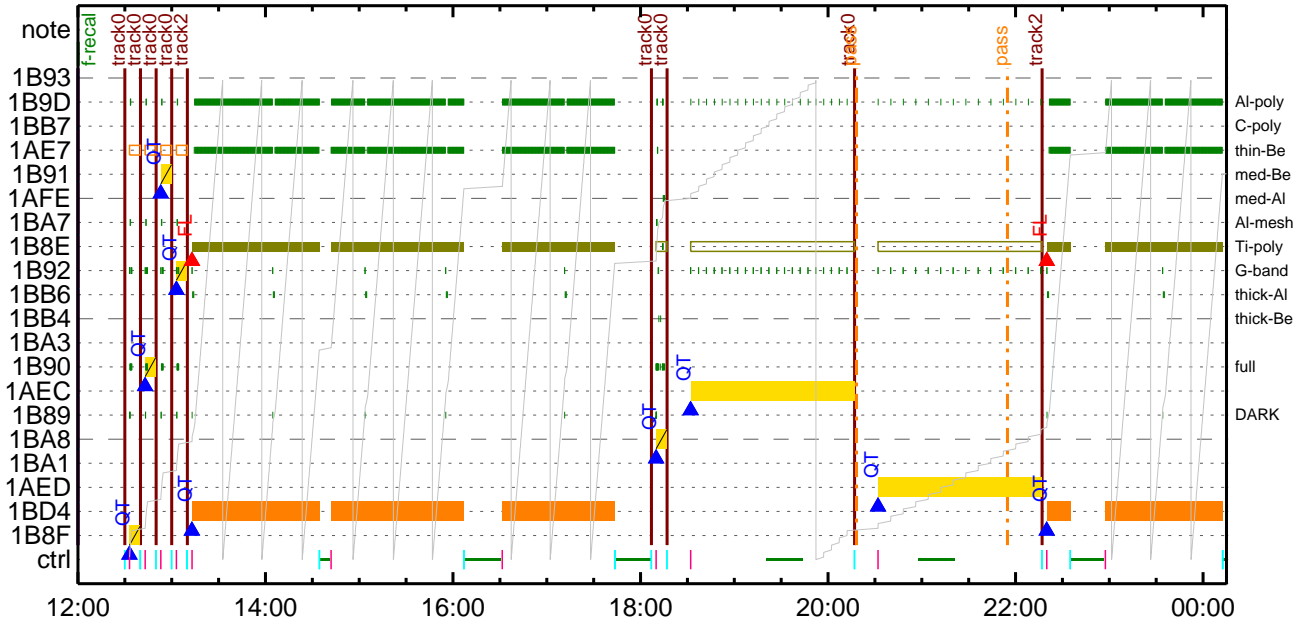
CMDI #0190 2017/12/12



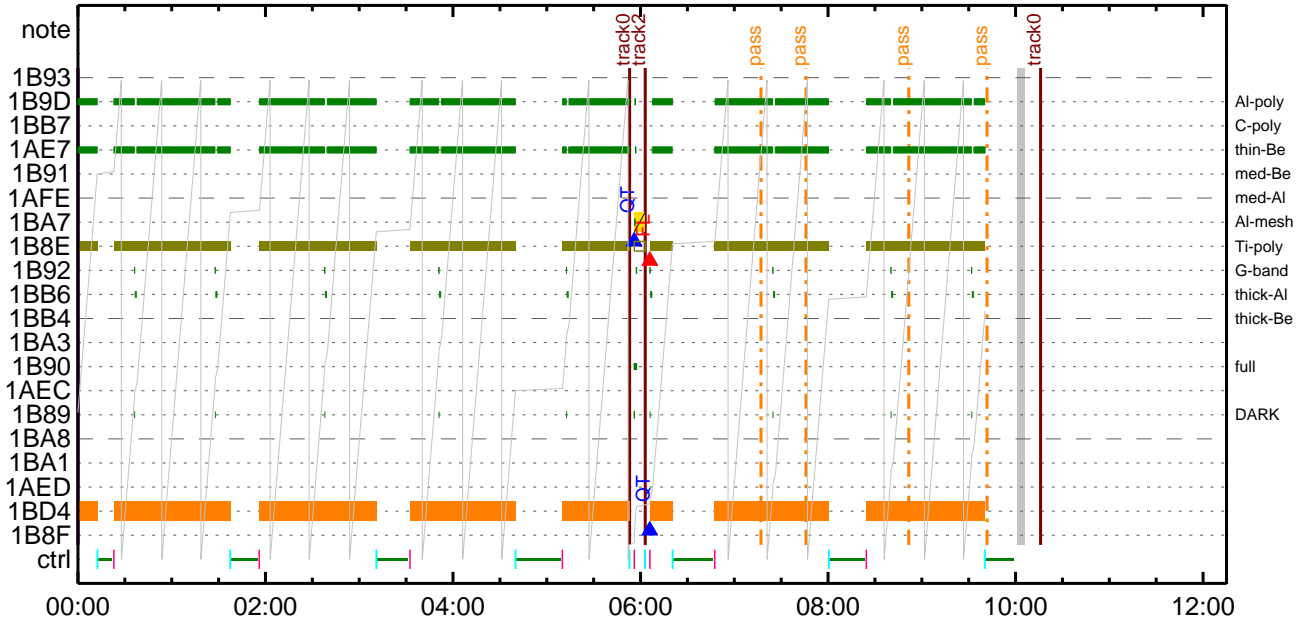
CMDI #0190 2017/12/13



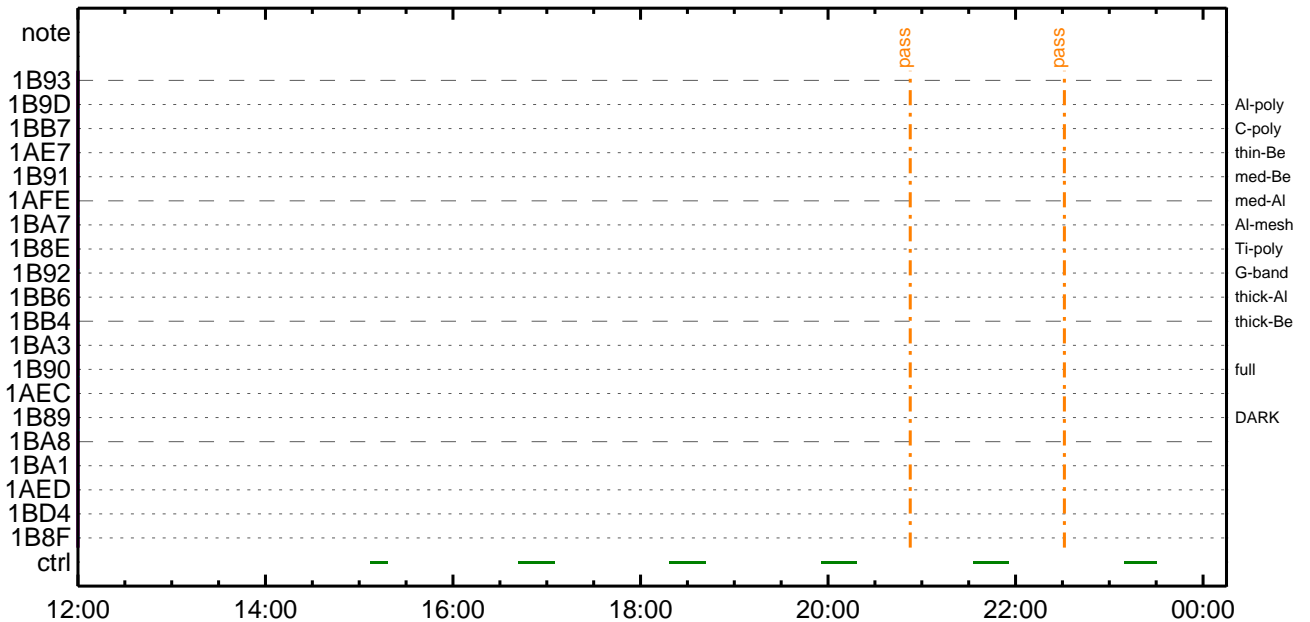
CMDI #0190 2017/12/13



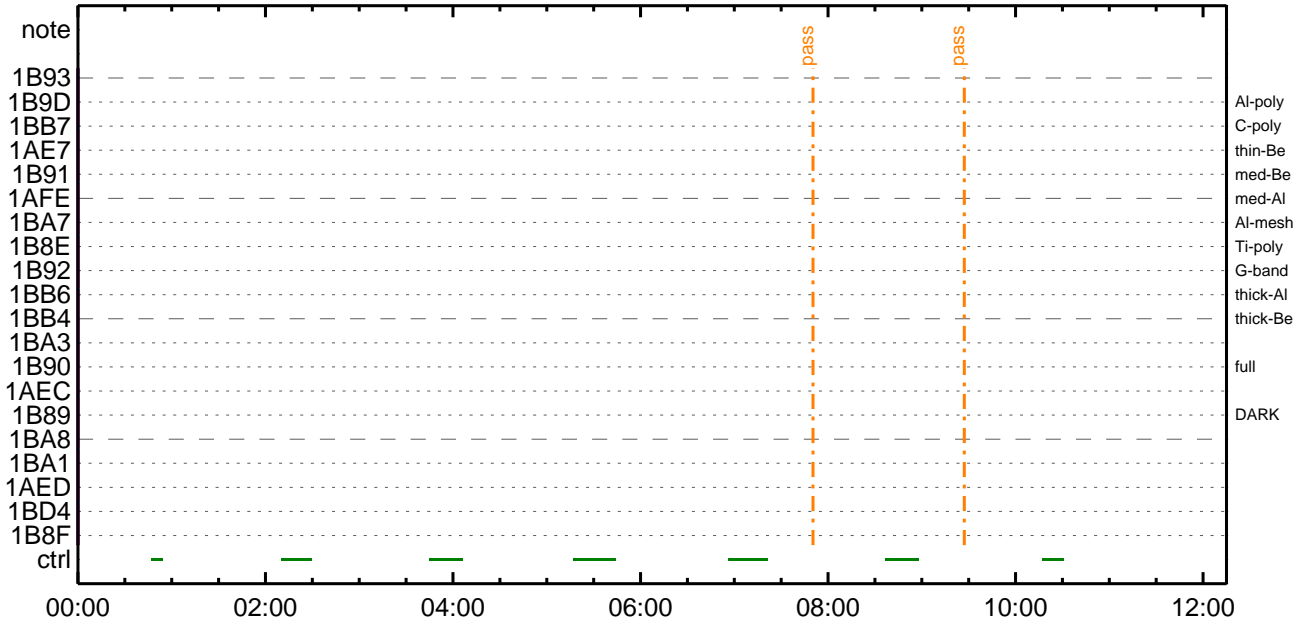
CMDI #0190 2017/12/14



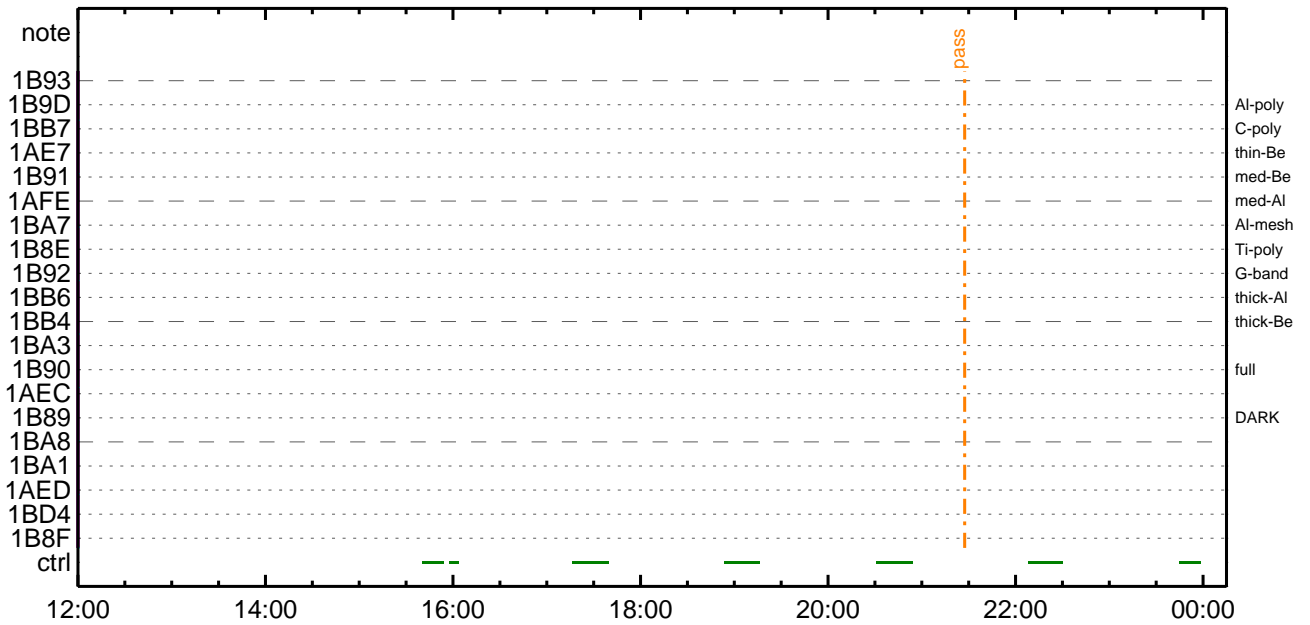
CMDI #0190 2017/12/14



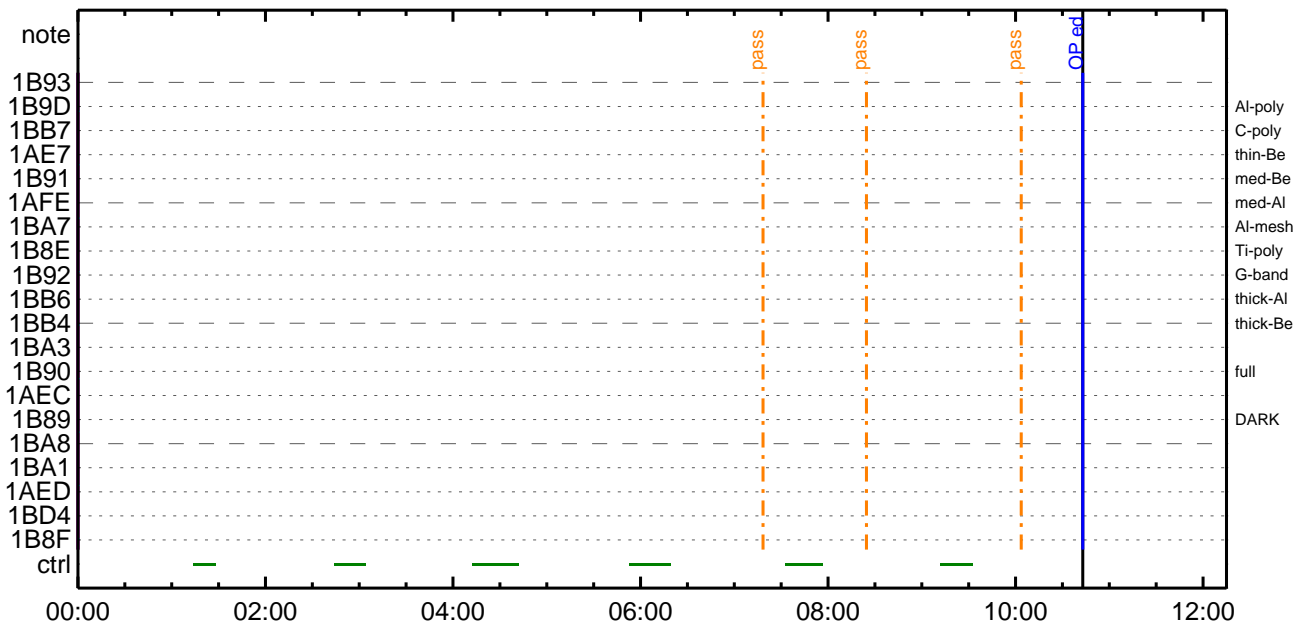
CMDI #0190 2017/12/15



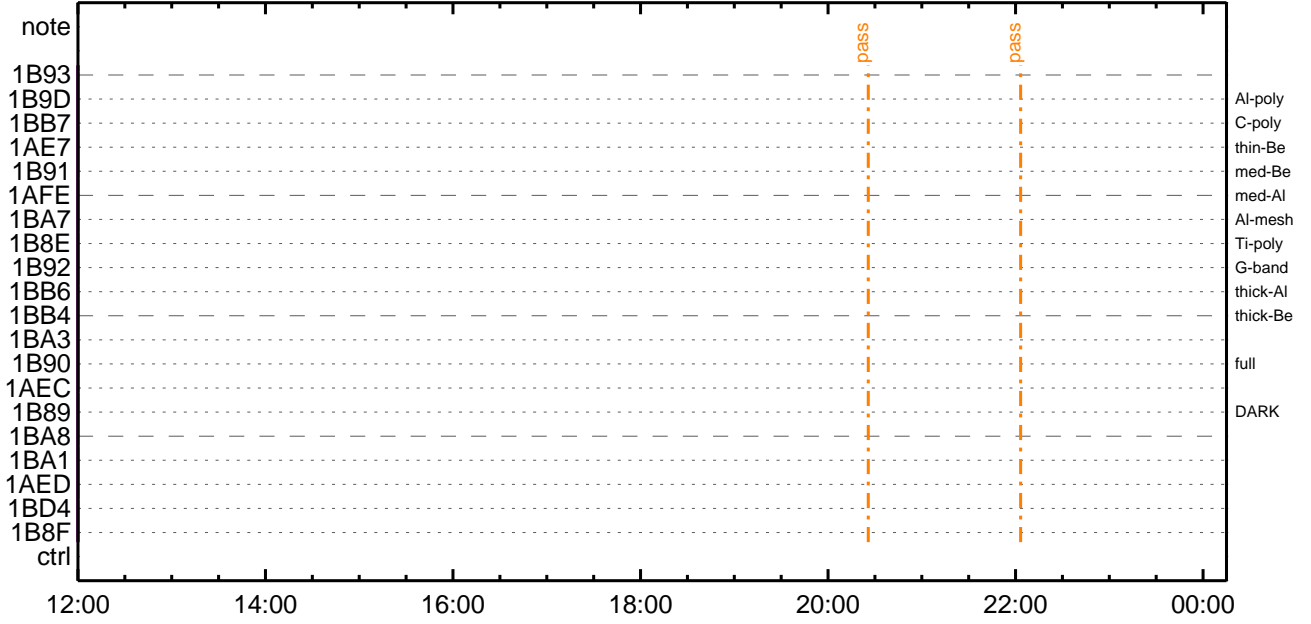
CMDI #0190 2017/12/15



CMDI #0190 2017/12/16



CMDI #0190 2017/12/16




```

0194 C.
0195 +. TI 2017-12-12 11:04:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄè%îíñòîîŷÄŷ§ŷÄŷ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]     EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]    EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]       EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]      EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]      EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]      EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼. ŷî;¼ŷÈ;Èòðîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]       EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]      EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2017-12-12 11:04:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2017-12-12 11:04:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2017-12-12 11:04:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîòî»ò¼ŷòÈÄð¹αèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷâŷçŷèòÈ¼αα¼Ä»Ûα¹αè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îñòÈ´Øα¹αèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. îãLOSŷÄŷŷŷÄŷ-¼Ä»Û;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-047 2017-12-12 12:27:40 108 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÀò•µ°È»Í×ÁÇòíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòí;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 + DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ( )
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 c0 c0 10 10)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 40 c0 10 10)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 40 40 10 10)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0b c0 40 10 10)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0c 85 83 08 08)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0d 80 80 20 08)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 0e 80 80 08 20)
0062 + DC 07-F0 MDP_XRT_ROI_SET
0063 BC (cd 0f 80 80 06 06)
0064 + DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 10 80 80 08 08)
0066 + DC 07-F0 MDP_XRT_FLD_ENA
0067 BC (d8)
0068 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0069 BC (c8)
0070 + DC 07-F0 MDP_XRT_ARS_DIS
0071 BC (d5)
0072 + DC 07-F0 MDP_XRT_AEC_RESET
0073 BC (d0)
0074 + DC 07-F0 MDP_XRT_FLD_RESET
0075 BC (da)
0076 + DC 07-F0 MDP_XRT_QT_PROG_SET
0077 BC (c4 0f)
0078 + DC 07-F0 MDP_XRT_FL_PROG_SET
0079 BC (c5 11)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C.
0083 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0084 C.
0085 +. DC 07-F0 MDP_XRT_MODE_OBSV
0086 BC (c2)
0087 +. TI 2017-12-12 11:04:02.0
0088 DC 07-F0 MDP_XRT_MODE_OBSV
0089 BC (c2)
0090 . C. ----- Success Verify ? OK / NG ____
0091 C.
0092 C. ***** XRT END *****
0093 C.
0094 . C. ***** MDP 'úÁíòí»ò¼YòÈÁò¹òÈDCBC•x²è *****
0095 C. (%á°íYÓYÁYÈYBÝÈYÁYçYèòÈ¼òò¼Á»Û¹òè)
```

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0096 . S. DC-BC dcbc-402:DCBC
0097 (MDP_known_event)
0098 C.
0099 C.
0100 . C. ***** ¥D¥1•İ Daily±;İÑñĒ'Øα¹αēDCBC•x²è *****
0101 . S. DC-BC dcbc-153:DCBC
0102 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0103 C.
0104 C.
0105 . C. ;äLOS¥Á¥S¥Ä¥-¼Ä»Û;ä
0106 C.
0107 . C. ***** LOS *****
0108 C.
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Dec 12, 17 12:28

XRT_OGLIST_0190.chk

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*** OP Sequence for XRT ***

2017/12/12	11:15:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	ac	cd
2017/12/12	11:31:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	d6	67
2017/12/12	11:47:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2017/12/12	12:03:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	29	99
2017/12/12	12:19:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	53	33
2017/12/12	12:35:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2017/12/12	12:45:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2017/12/12	13:01:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	ac	5b	00	00
2017/12/12	13:17:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	b4	b5	24	8b
2017/12/12	13:33:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	d6	36	48	72
2017/12/12	13:43:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	29	ca	b7	8e
2017/12/12	13:53:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	4b	4b	db	75
2017/12/12	14:09:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	53	a5	00	00
2017/12/12	14:25:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	4b	4b	24	8b
2017/12/12	14:41:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	29	db	48	72
2017/12/12	14:55:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	02	00	00	00	00
2017/12/12	18:03:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2017/12/12	18:13:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	02	00	00	00	00
2017/12/13	08:32:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	01	00	00	00	00
2017/12/13	11:00:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	02	00	00	00	00
2017/12/13	12:00:00.0	XRT_CTRL_MANU_414_OG [0x19e]							
		MDP_XRT_CTRL_MANU	1	07-F0					
2017/12/13	12:00:10.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]							
		XRT_FOCUS_RECAL	2	07-F8	78				
2017/12/13	12:04:10.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2017/12/13	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					
2017/12/13	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					
2017/12/13	12:29:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2017/12/13	12:30:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2017/12/13	12:30:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0					
2017/12/13	12:30:20.0	XRT_FLRCTRL_DIS_418_OG [0x1a2]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					
2017/12/13	12:32:56.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0					
2017/12/13	12:32:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0					
2017/12/13	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					
2017/12/13	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					
2017/12/13	12:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					
2017/12/13	12:39:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2017/12/13	12:40:00.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2017/12/13	12:40:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0					
2017/12/13	12:40:20.0	XRT_FLRCTRL_DIS_418_OG [0x1a2]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					
2017/12/13	12:42:56.0	XRT_ARS_DIS_445_OG [0x1bd]							
		MDP_XRT_ARS_DIS	1	07-F0					
2017/12/13	12:42:58.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0					
2017/12/13	12:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					
2017/12/13	12:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					
2017/12/13	12:49:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					
2017/12/13	12:49:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2017/12/13	12:50:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2017/12/13	12:50:18.0	XRT_FLD_DIS_409_OG [0x199]							

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2017/12/13	12:50:20.0	XRT_FLRCTRL_DIS_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/12/13	12:52:56.0	XRT_ARS_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/12/13	12:52:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
2017/12/13	12:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/12/13	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	12:59:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2017/12/13	13:00:00.0	AOCs_OrE-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00	d1 07 2e f9
2017/12/13	13:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2017/12/13	13:00:20.0	XRT_FLRCTRL_DIS_418_OG [0x1a2]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2017/12/13	13:02:56.0	XRT_ARS_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/12/13	13:02:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2017/12/13	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/12/13	13:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	13:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	13:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2017/12/13	13:10:00.0	AOCs_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	02	00 00 00 00
2017/12/13	13:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2017/12/13	13:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2017/12/13	13:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2017/12/13	13:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2017/12/13	13:10:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/12/13	13:12:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2017/12/13	13:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2017/12/13	13:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/12/13	14:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	14:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	14:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/12/13	14:34:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/12/13	14:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/12/13	14:41:00.0	XRT_Custom_430_OG [0x1ae]					
2017/12/13	14:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/12/13	16:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	16:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	16:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/12/13	16:07:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/12/13	16:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/12/13	16:30:30.0	XRT_Custom_430_OG [0x1ae]					
2017/12/13	16:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2017/12/13	17:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	17:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	17:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2017/12/13	17:43:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2017/12/13	17:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2017/12/13	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	18:06:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2017/12/13	18:06:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00

2017/12/13	18:07:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00	
2017/12/13	18:07:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9					
2017/12/13	18:09:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2017/12/13	18:09:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2017/12/13	18:09:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05				
2017/12/13	18:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2017/12/13	18:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2017/12/13	18:16:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2017/12/13	18:16:58.0	XRT_ROI_A_427_OG [0x1ab]	MDP_XRT_ROI_SET	6	07-F0	cd	05	85	83	06	06
			MDP_XRT_ROI_SET	6	07-F0	cd	06	85	83	06	06
			MDP_XRT_ROI_SET	6	07-F0	cd	07	80	60	20	18
			MDP_XRT_ROI_SET	6	07-F0	cd	08	80	80	20	20
			MDP_XRT_ROI_SET	6	07-F0	cd	09	a0	80	18	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0c	85	83	08	08
			MDP_XRT_ROI_SET	6	07-F0	cd	0d	80	80	20	08
			MDP_XRT_ROI_SET	6	07-F0	cd	0e	80	80	08	20
2017/12/13	18:16:58.5	XRT_ROI_B_435_OG [0x1b3]	MDP_XRT_ROI_SET	6	07-F0	cd	0e	80	80	08	20
			MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06	06
			MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80	08	08
2017/12/13	18:17:00.0	AOCS_OrE-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00	ad	59	00	00	
2017/12/13	18:17:03.5	XRT_FOCUS_POSITION_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2017/12/13	18:17:23.5	XRT_FLD_DIS_421_OG [0x1a5]	MDP_XRT_FLD_DIS	1	07-F0	d9					
2017/12/13	18:31:59.5	XRT_FLRCTRL_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2017/12/13	18:32:01.5	XRT_ARS_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2017/12/13	18:32:03.5	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07				
2017/12/13	18:32:05.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2017/12/13	20:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2017/12/13	20:16:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2017/12/13	20:16:58.0	XRT_FOCUS_POSITION_428_OG [0x1ac]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2017/12/13	20:17:00.0	AOCS_OrE-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	00	00	56	35	
2017/12/13	20:17:18.0	XRT_FLD_DIS_421_OG [0x1a5]	MDP_XRT_FLD_DIS	1	07-F0	d9					
2017/12/13	20:31:54.0	XRT_FLRCTRL_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2017/12/13	20:31:56.0	XRT_ARS_DIS_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2017/12/13	20:31:58.0	XRT_QT_PROG_SET_413_OG [0x19d]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03				
2017/12/13	20:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2017/12/13	22:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2017/12/13	22:16:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2017/12/13	22:16:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2017/12/13	22:17:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	02	00	00	00	00	
2017/12/13	22:17:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8					
2017/12/13	22:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2017/12/13	22:17:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0					
2017/12/13	22:17:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5					
2017/12/13	22:17:26.0	XRT_FLD_RESET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da					
2017/12/13	22:19:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02				
2017/12/13	22:19:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d				
2017/12/13	22:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2017/12/13	22:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2017/12/13	22:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2017/12/13	22:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da					
2017/12/13	22:35:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8					

2017/12/13	22:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/12/13	22:56:30.0	XRT_Custom_430_OG [0x1ae]							
2017/12/13	22:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/12/14	00:12:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	00:12:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	00:12:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/12/14	00:12:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/12/14	00:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/12/14	00:22:00.0	XRT_Custom_430_OG [0x1ae]							
2017/12/14	00:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/12/14	01:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	01:37:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	01:37:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/12/14	01:37:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/12/14	01:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/12/14	01:55:00.0	XRT_Custom_430_OG [0x1ae]							
2017/12/14	01:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/12/14	03:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	03:11:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	03:11:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/12/14	03:11:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/12/14	03:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/12/14	03:31:30.0	XRT_Custom_430_OG [0x1ae]							
2017/12/14	03:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/12/14	04:40:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	04:40:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	04:40:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/12/14	04:40:06.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/12/14	04:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/12/14	05:09:00.5	XRT_Custom_430_OG [0x1ae]							
2017/12/14	05:10:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/12/14	05:52:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	05:52:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	05:52:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2017/12/14	05:53:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2017/12/14	05:53:18.0	XRT_FLD_DIS_425_OG [0x1a9]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2017/12/14	05:55:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2017/12/14	05:55:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/12/14	05:55:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2017/12/14	05:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/12/14	06:02:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	06:02:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	06:02:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2017/12/14	06:03:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2017/12/14	06:03:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2017/12/14	06:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2017/12/14	06:03:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2017/12/14	06:03:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2017/12/14	06:03:26.0	XRT_FLD_RESET_433_OG [0x1b1]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2017/12/14	06:05:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2017/12/14	06:05:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2017/12/14	06:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/12/14	06:20:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	06:20:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	06:20:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/12/14	06:20:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/12/14	06:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/12/14	06:46:30.0	XRT_Custom_430_OG [0x1ae]							
2017/12/14	06:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/12/14	08:00:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	08:00:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	08:00:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/12/14	08:00:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/12/14	08:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/12/14	08:23:30.0	XRT_Custom_430_OG [0x1ae]							
2017/12/14	08:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2017/12/14	09:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	09:40:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2017/12/14	09:40:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2017/12/14	09:40:36.0	XRT_PREFLR_STRT_449_OG [0x1c1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2017/12/14	09:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2017/12/14	10:16:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00