

XRT Timeline to be uploaded on 2018/08/09

Period: 2018/08/09 11:35:00 - 2018/08/14 11:16:00

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Normal mode

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XOB #1BE9: HOP81/206 2-filter - Al/poly 16s, Al/mesh 12s 30s cadence, G-band - 384x384 1ms													
Term	Pointing (x, y)						Comment						
08/09 11:48:00 - 08/09 17:24:30	Fixed (-18.0, -976.0)						# OP start + 10min, HOP 81, S-pole						
PROG= 20 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 16 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 90 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3 60-time(s) 2.0sec													
└─ Seqn= 39 1-time(s) 30.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	11.3s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B8B: Synoptic Q95 2x2 - Al/mesh(64/512/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T													
Term	Pointing (x, y)						Comment						
08/09 18:06:30 - 08/09 18:13:24	Fixed (0.0, 0.0)						synoptic, shifted 3.5 min						
08/10 03:43:00 - 08/10 03:49:54	Fixed (0.0, 0.0)						synoptic, shifted -2 hrs						
08/10 17:53:00 - 08/10 17:59:54	Fixed (0.0, 0.0)						synoptic, shifted -10.0 min						
08/11 04:18:00 - 08/11 04:24:54	Fixed (0.0, 0.0)						synoptic, shifted -2 hrs						
PROG= 02 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 27 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 99 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 85 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 23 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with													
Term	Pointing (x, y)						Comment						
08/09 18:16:30 - 08/09 23:56:54	Track (-173.6, -93.0) @ 08/09 18:13:30						Plage						
08/10 02:03:00 - 08/10 03:07:00	Track (-102.7, -94.4) @ 08/10 02:00:00						HOP 173, plage						
08/10 12:03:00 - 08/10 17:49:54	Track (-10.8, -95.5) @ 08/10 12:00:00						Plage						
08/10 18:39:00 - 08/10 23:56:54	Track (44.5, -95.6) @ 08/10 18:00:00						Plage						
08/11 02:35:30 - 08/11 03:42:00	Track (118.0, -95.2) @ 08/11 02:00:00						HOP 173, plage						
PROG= 12 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Subr= 2 5-time(s) 2.0sec													
└─ Seqn= 75 1-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 96 4-time(s) 120.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BE2: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[1024/11571/23142] with 512x512 G-band+Leak - 90min cad) +

Term	Pointing (x, y)	Comment
08/10 00:22:00 - 08/10 01:35:00	Fixed (0.0, 0.0)	HOP 349
08/11 00:00:00 - 08/11 01:59:54	Fixed (0.0, 0.0)	HOP 349

PROG= 16 Inf.-time(s)

Subr= 1	1-time(s)	300.0sec										
Seqn= 1	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	18-time(s)	300.0sec										
Seqn= 8	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C05: HOP173 Prominence/Filament Al-mesh (4sec) and Al-poly (8sec), 1x1, 512x512 at 1064x1048, fixed exposure, Q98, 300s cad

Term	Pointing (x, y)	Comment
08/10 03:53:00 - 08/10 05:59:54	Track (220.0, 493.7) @ 08/10 03:50:00	HOP 173, on-disk prominence
08/11 04:28:00 - 08/11 05:59:54	Track (372.0, 500.2) @ 08/11 04:25:00	HOP 173, on-disk prominence

PROG= 04 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 69	2-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 22	30-time(s)	300.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B94: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 180s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
08/10 06:03:00 - 08/10 11:19:00	Track (913.3, -190.4) @ 08/10 06:00:00	AR 12717
08/11 06:03:00 - 08/11 06:52:30	Fixed (910.0, -160.0)	AR 12717

PROG= 11 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 30	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	20-time(s)	180.0sec										
Seqn= 8	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C06: Eclipse - Full Sun - Al/Poly - 4x4 - 256/1024ms pair expo - Dark(12/128ms) - G-band (VLS open 1ms closed 1ms)

Term	Pointing (x, y)	Comment
08/11 08:00:00 - 08/11 08:32:30	Fixed (0.0, 890.0)	Eclipse 1
08/11 10:55:00 - 08/11 12:02:00	Fixed (0.0, 890.0)	Eclipse 3

PROG= 10 1-time(s)

Subr= 1	2-time(s)	2.0sec										
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└─ Seqn= 23	1-time(s)	2.0sec																		
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec							
└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
└─ Subr= 2	1-time(s)	2.0sec																		
└─ Seqn= 50	60-time(s)	30.0sec																		
└─ Al-poly/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
└─ Seqn= 62	1-time(s)	2.0sec																		
└─ Al-poly/Open	Al-poly/Open	close	Safe	Dark	12ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
└─ Al-poly/Open	Al-poly/Open	close	Safe	Dark	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec							
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval								

XOB #1C07: Eclipse - Full Sun - Al/Poly - 4x4 - 88/723ms pair expo - G-band (VLS open 1ms closed 1ms)

Term	Pointing (x, y)		Comment																			
08/11 09:13:00 - 08/11 10:12:30	Fixed (0.0, 0.0)		Eclipse 2																			
PROG= 19	1-time(s)																					
└─ Subr= 1	1-time(s)		2.0sec																			
└─ Seqn= 23	2-time(s)		2.0sec																			
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec									
└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec									
└─ Seqn= 15	14-time(s)		60.0sec																			
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	86ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec									
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	707ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec									
└─ Subr= 2	1-time(s)		2.0sec																			
└─ Seqn= 15	120-time(s)		15.0sec																			
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	86ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec									
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	707ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec									
└─ Subr= 3	1-time(s)		2.0sec																			
└─ Seqn= 15	16-time(s)		60.0sec																			
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	86ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec									
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	707ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec									
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval										

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)		Comment																	
08/09 11:48:00 - 08/09 17:24:30	Fixed (-18.0, -976.0)		# OP start + 10min, HOP 81, S-pole																	
08/09 18:16:30 - 08/09 23:56:54	Track (-173.6, -93.0) @ 08/09 18:13:30		Plage																	
08/10 00:22:00 - 08/10 01:35:00	Fixed (0.0, 0.0)		HOP 349																	
08/10 02:03:00 - 08/10 03:07:00	Track (-102.7, -94.4) @ 08/10 02:00:00		HOP 173, plage																	
08/10 03:53:00 - 08/10 05:59:54	Track (220.0, 493.7) @ 08/10 03:50:00		HOP 173, on-disk prominence																	
08/10 06:03:00 - 08/10 11:19:00	Track (913.3, -190.4) @ 08/10 06:00:00		AR 12717																	
08/10 12:03:00 - 08/10 17:49:54	Track (-10.8, -95.5) @ 08/10 12:00:00		Plage																	
08/10 18:39:00 - 08/10 23:56:54	Track (44.5, -95.6) @ 08/10 18:00:00		Plage																	
08/11 00:00:00 - 08/11 01:59:54	Fixed (0.0, 0.0)		HOP 349																	
08/11 02:35:30 - 08/11 03:42:00	Track (118.0, -95.2) @ 08/11 02:00:00		HOP 173, plage																	
08/11 04:28:00 - 08/11 05:59:54	Track (372.0, 500.2) @ 08/11 04:25:00		HOP 173, on-disk prominence																	
08/11 06:03:00 - 08/11 06:52:30	Fixed (910.0, -160.0)		AR 12717																	

PROG= 13	30-time(s)																					
└─ Subr= 1	20-time(s)		2.0sec																			
└─ Seqn= 11	1-time(s)		2.0sec																			
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec									
└─ Seqn=100	1-time(s)		10.0sec																			
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384	(1024, 1024)	Q=95	2	0	2.0sec									
└─ med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec									
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec									
└─ Subr= 2	1-time(s)		2.0sec																			
└─ Seqn= 10	1-time(s)		2.0sec																			
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec									
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec									
└─ Seqn= 11	1-time(s)		2.0sec																			
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec									
└─ Seqn= 87	1-time(s)		2.0sec																			
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec									
└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec									
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec									
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec									
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval										

* * * * *

Active Region Search

* * * * *

NOT USED

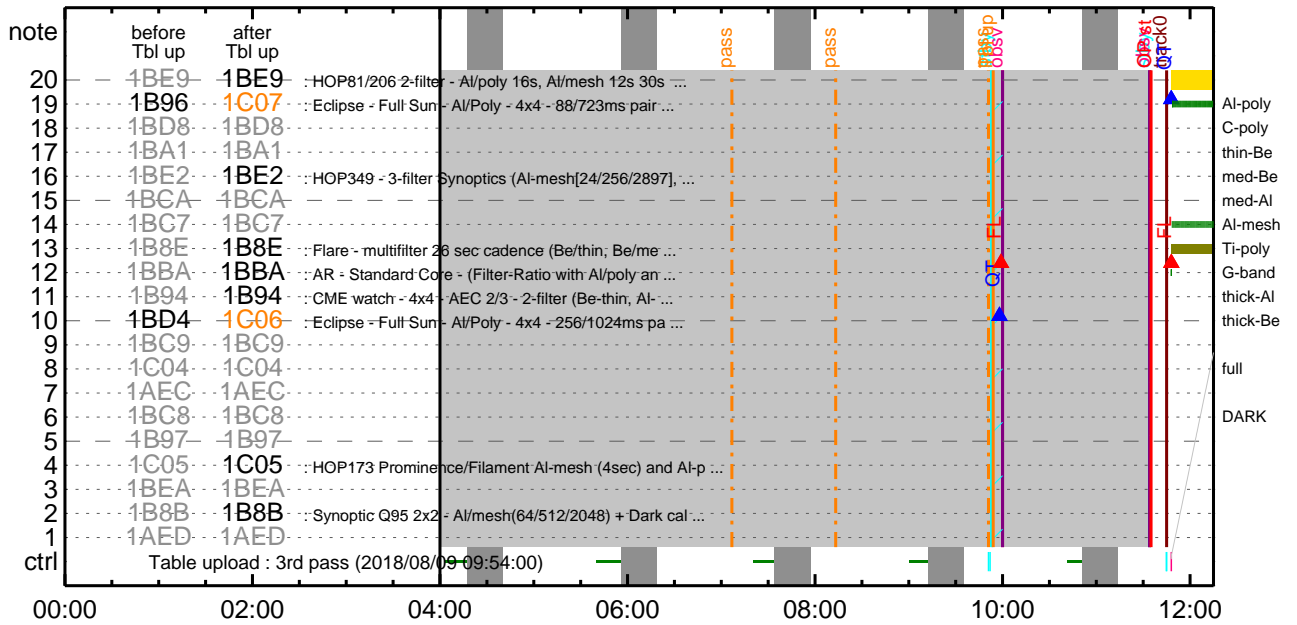
* * * * *

Flare Detection

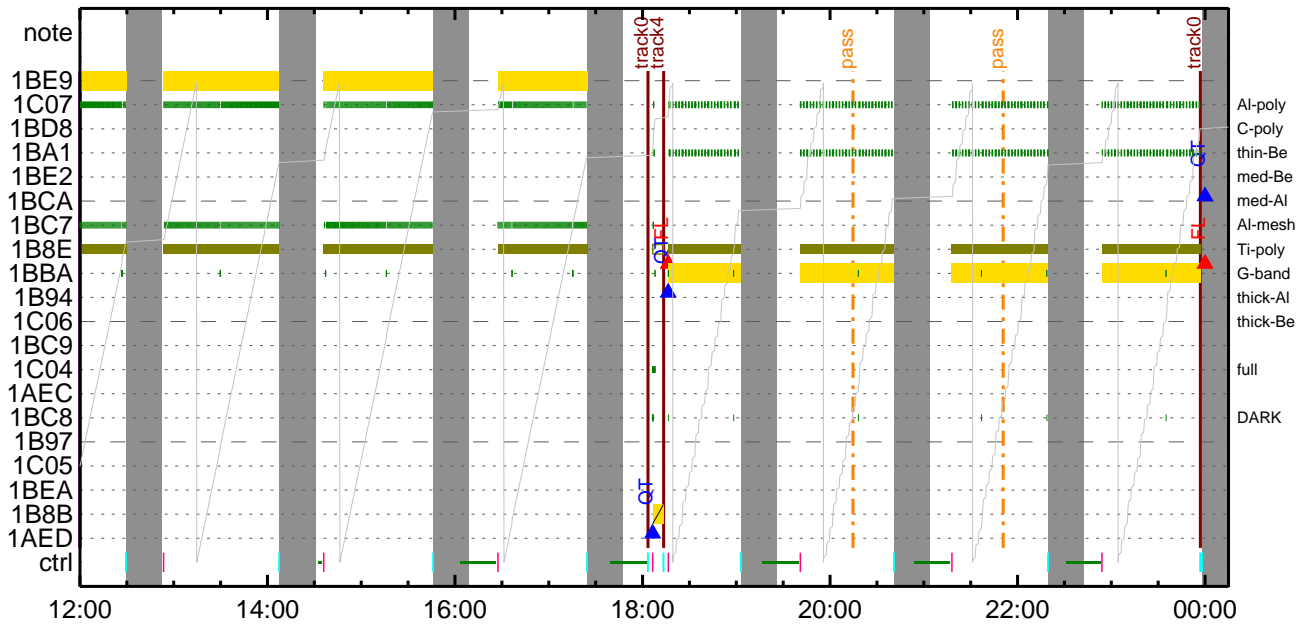
* * * * *

Term	Pointing (x, y)		Comment							
08/09 18:13:48 - 08/10 03:40:18	Track (-173.6, -93.0)	@ 08/09 18:13:30	Plage							
08/10 03:50:18 - 08/10 17:50:18	Track (220.0, 493.7)	@ 08/10 03:50:00	HOP 173, on-disk prominence							
08/10 18:00:18 - 08/11 04:15:18	Track (44.5, -95.6)	@ 08/10 18:00:00	Plage							
08/11 04:25:18 - 08/11 07:35:18	Track (372.0, 500.2)	@ 08/11 04:25:00	HOP 173, on-disk prominence							
08/11 12:05:18 - 08/14 11:16:00	Fixed (910.0, -160.0)		AR 12717							
AI-poly/Open	AI-poly/Open	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval	

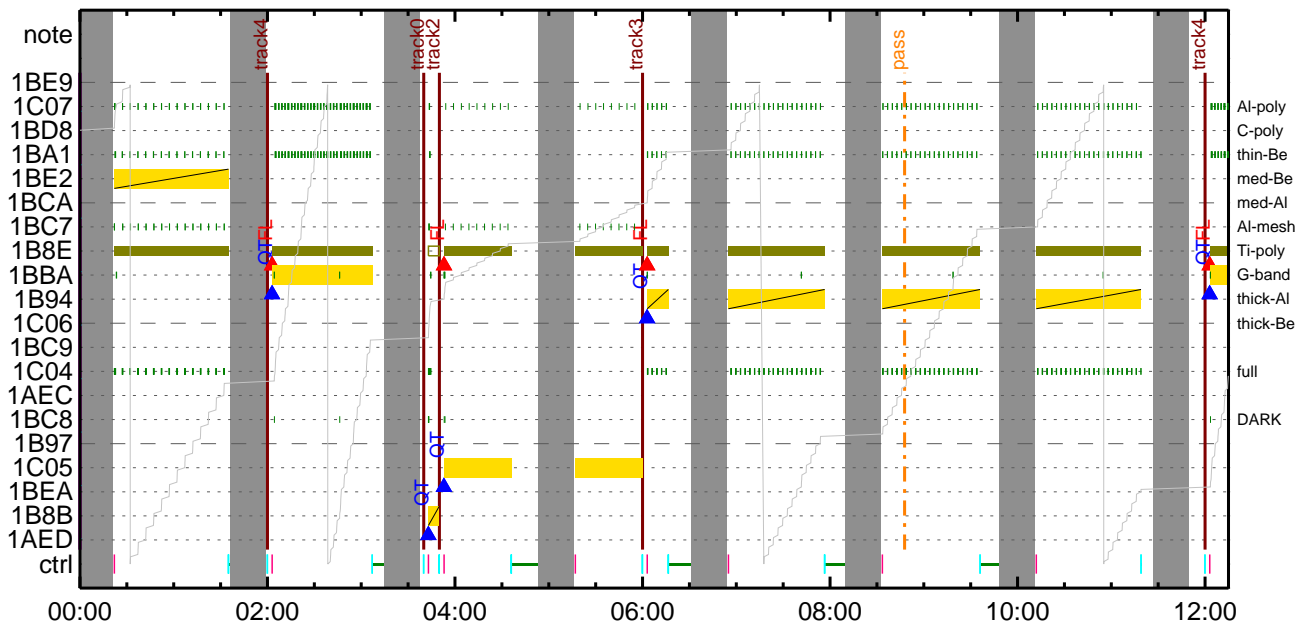
CMDI #0676 2018/08/09



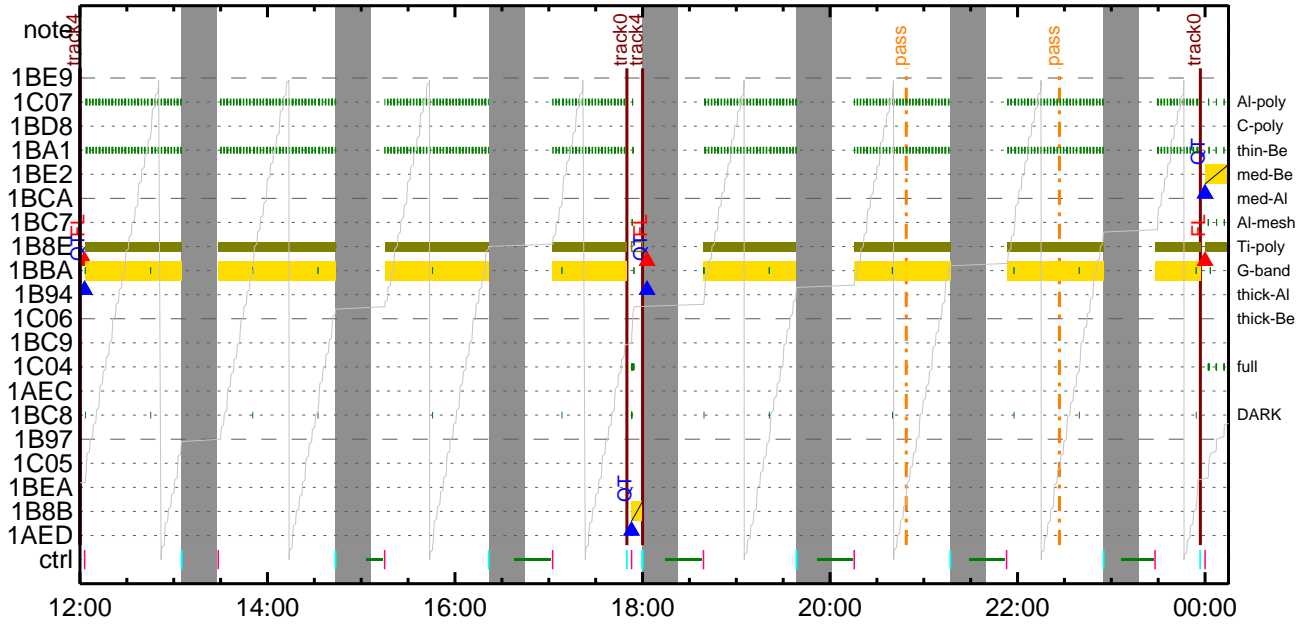
CMDI #0676 2018/08/09



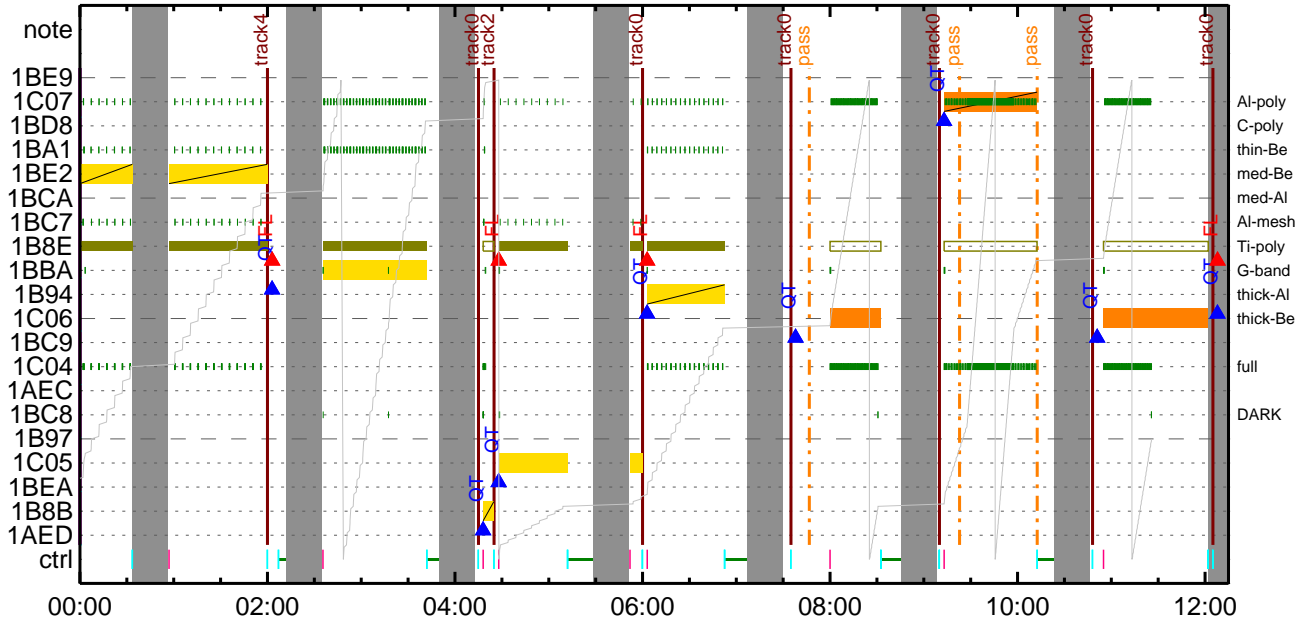
CMDI #0676 2018/08/10



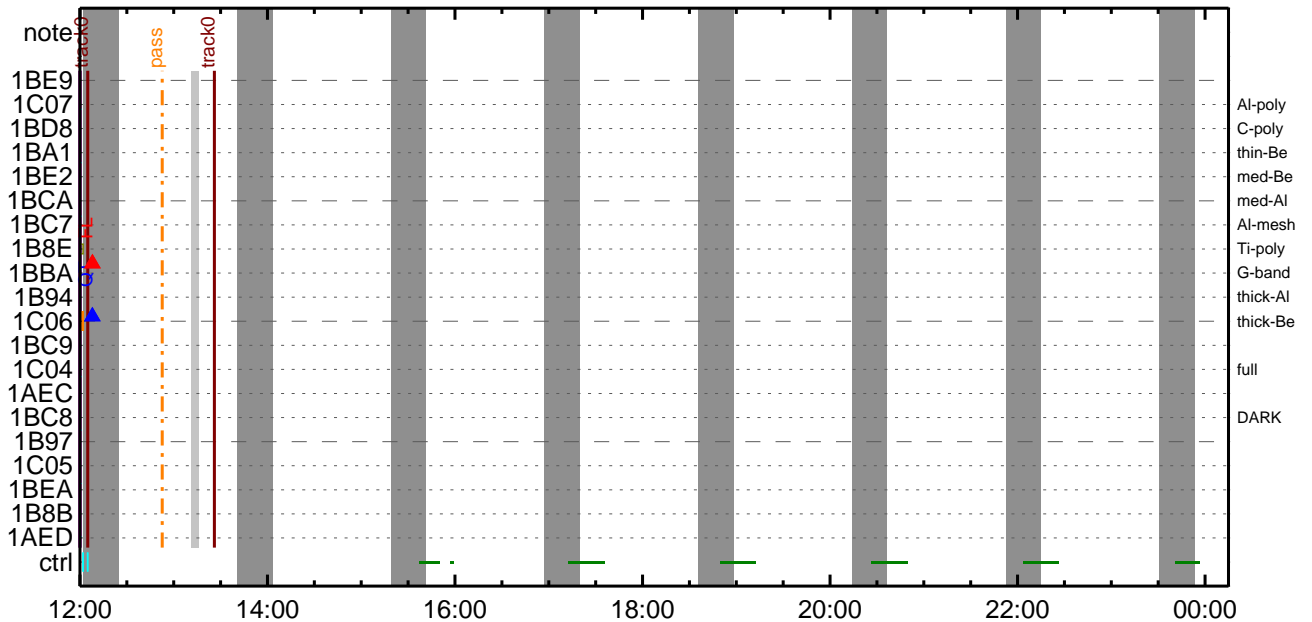
CMDI #0676 2018/08/10



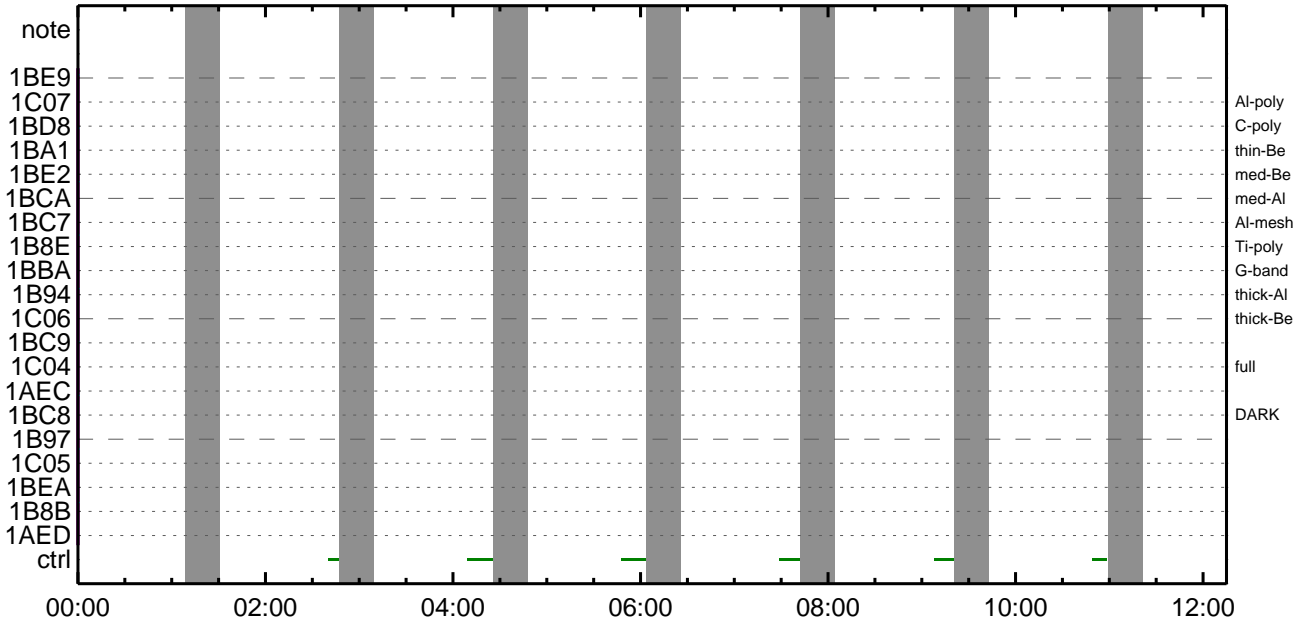
CMDI #0676 2018/08/11



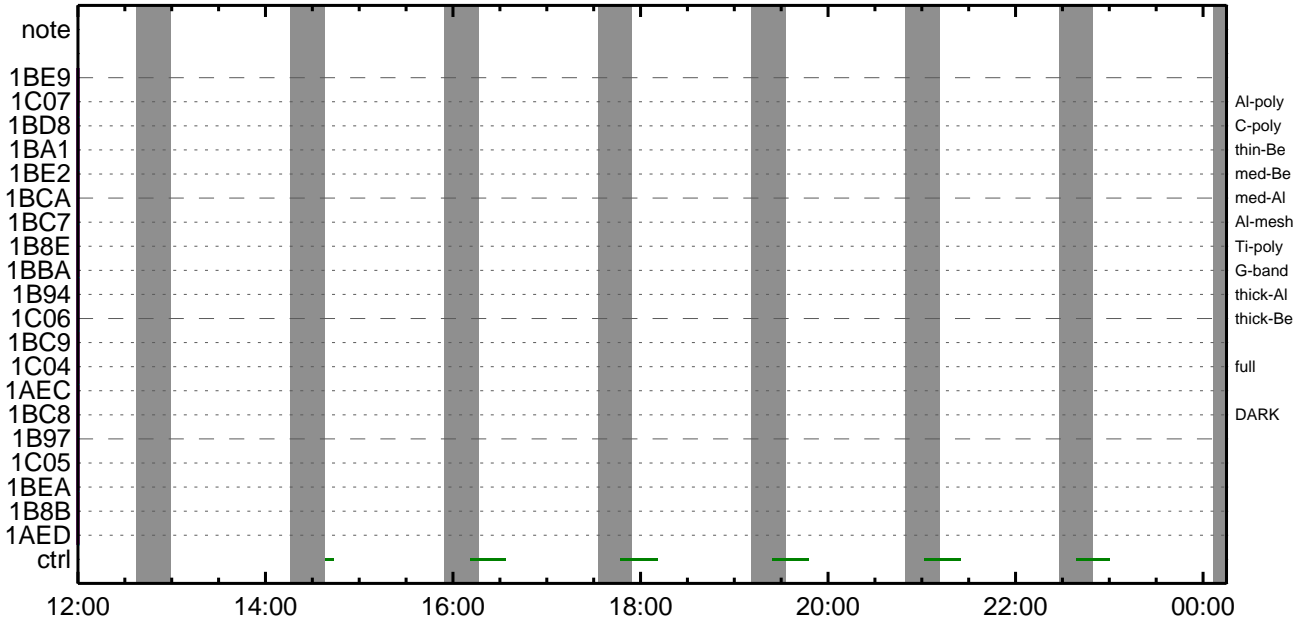
CMDI #0676 2018/08/11



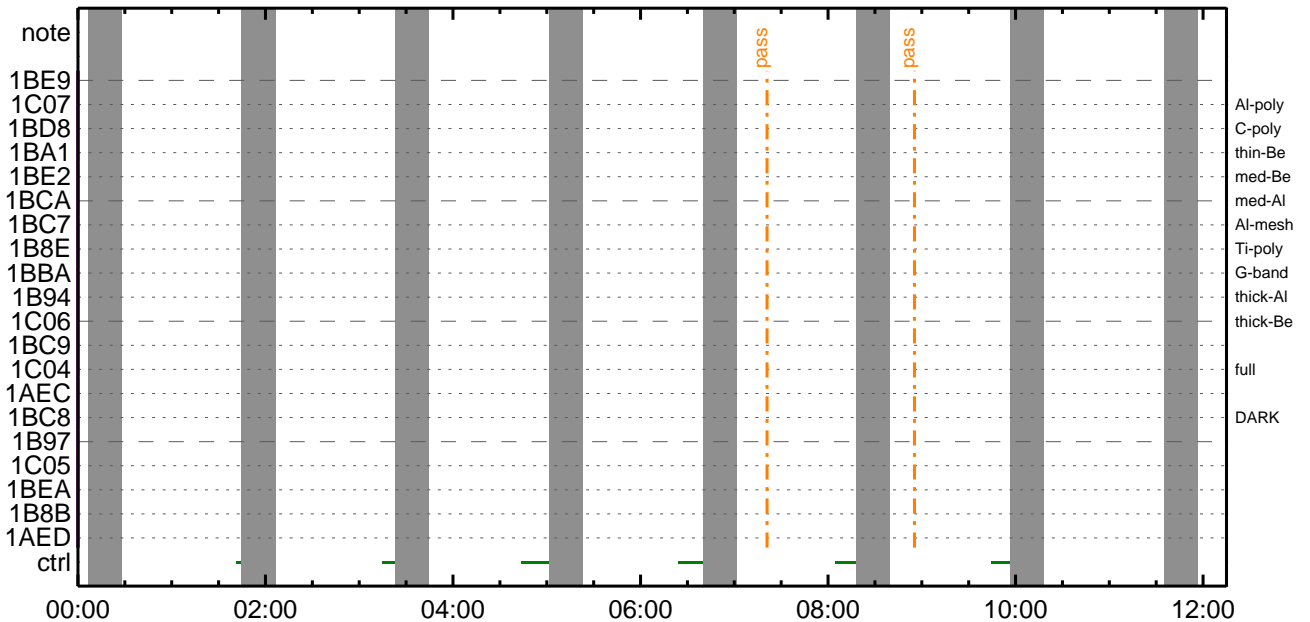
CMDI #0676 2018/08/12



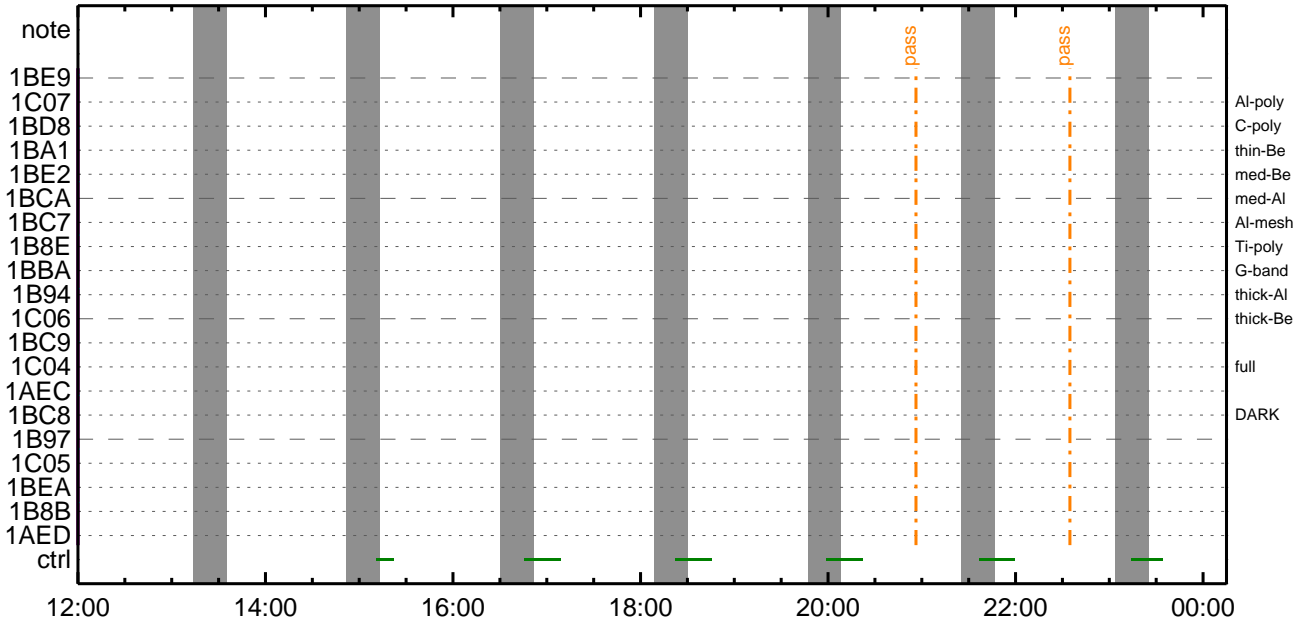
CMDI #0676 2018/08/12



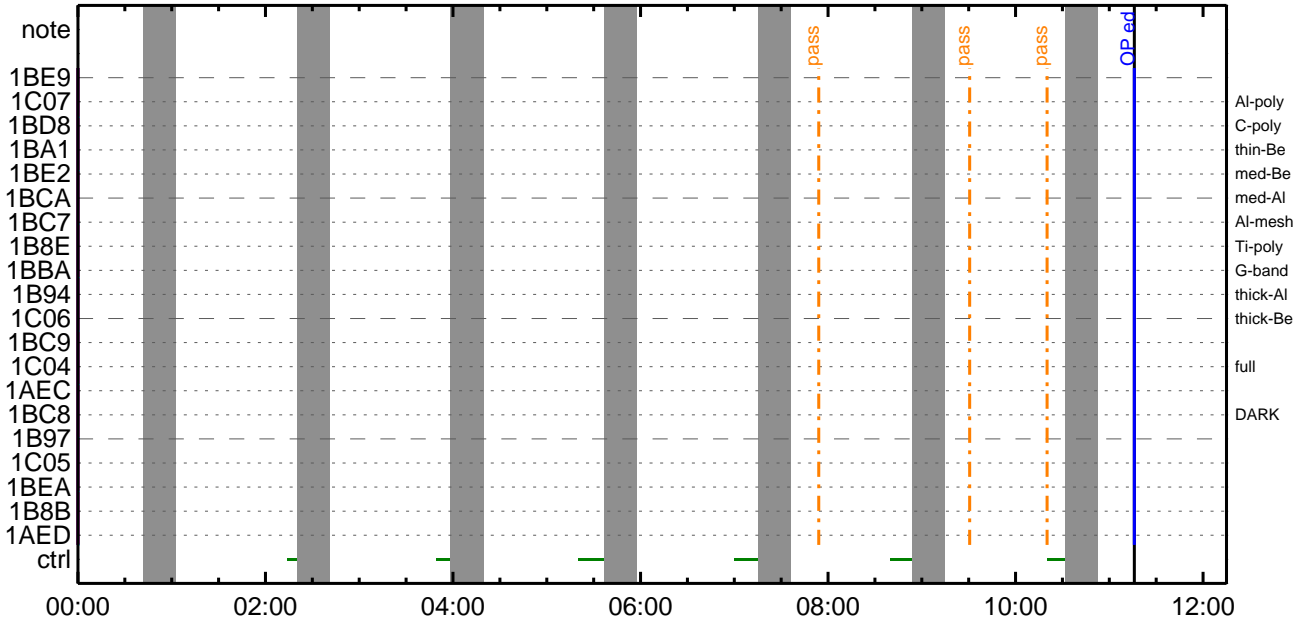
CMDI #0676 2018/08/13



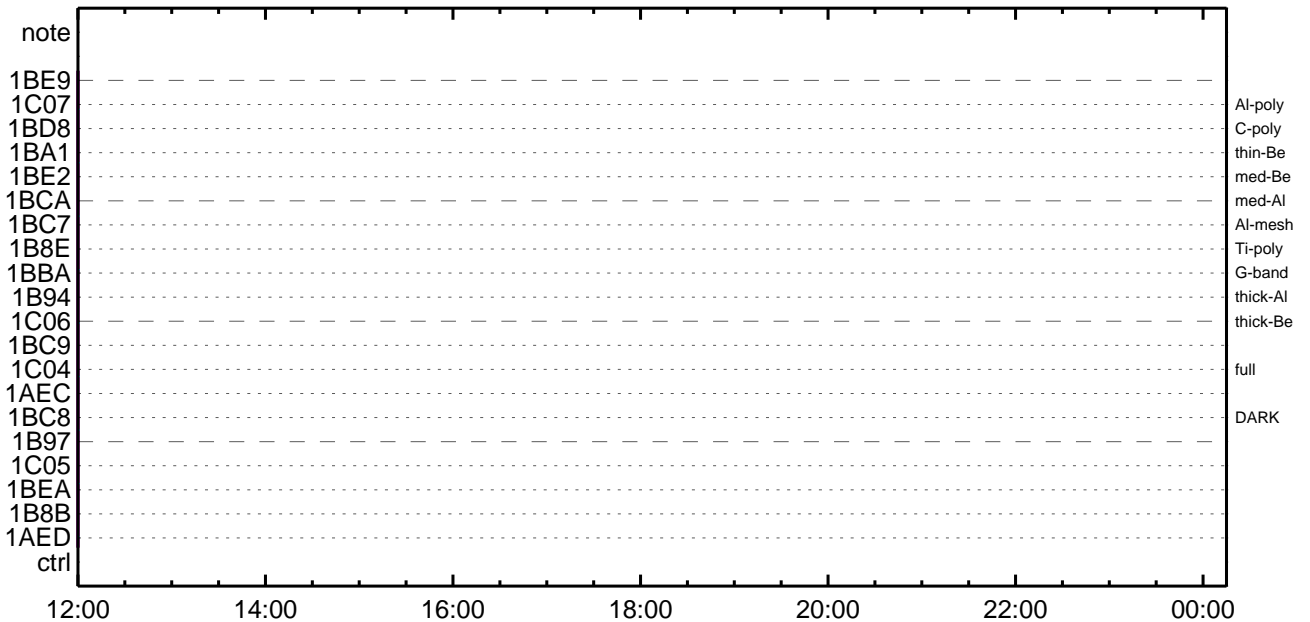
CMDI #0676 2018/08/13



CMDI #0676 2018/08/14



CMDI #0676 2018/08/14




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0096 C.          SET EDUMP I±°iYÑY¹aÇ¹Òa|a³aE;f
0097 C.
0098 C. TIY³YF¥ÖYÉaòdÁDİ¿(UT)
0099 +. TI 2018-08-09 11:30:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2018-08-09 11:30:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2018-08-09 11:30:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2018-08-09 11:34:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °E²¼aİÄè%îÍÑaİYÁY§YÁY-¹àìÜ
0116 C.          çç[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0118 C.          çç[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          çç[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 C. *****
0122 C. TIİî°èYÀYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          çç[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          çç[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          çç[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          çç[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          çç[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          çç[HK1_PKT_FORM_NO]          EQ          7
0136 C.          çç[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          çç[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          çç[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          çç[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 C. YÀYÖY×½ªİ»òð³İÇ§
0142 C.          çç[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKòð³İÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y½,¥ì;¼YÈ;Èòðİá¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          çç[HK1_PKT_FORM_NO]          EQ          2
0150 C.          çç[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          çç[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          çç[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2018-08-09 11:34:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC          (21 02)
0163 +. TI 2018-08-09 11:34:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC          (22)
0166 C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2018-08-09 11:34:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC          (c3)
0176 C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´úÃîaİ»ò¼YªÈÄa¹aèDCBC•x²è *****
0181 C. (%á°îYÖYÁYÉY¥YÉYÁYÇYèòÈ¼aª¼Á»Ü¹aè)
0182 S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** YD¥¹•İ Daily±¿İÑaÈ'Øa¹aèDCBC•x²è *****
0187 S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;ãLOS¥ÁY§YÁY-¼Á»Ü;ã
0192 C.
0193 C. ***** LOS *****

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 27s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2018-08-09 11:34:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP (uAÎqÎ»ö¼ÝqÊÄð¹æDCBC•x²è *****
0156 C. (¼â°ÏYÓYÄEYÏPËYáYçYèæE¼q¼Ä»Û¹æ)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** YDÿ¹•Ï Daily±¿ÎÑæ'Ø¹æDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥Ä¥-¼Ä»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ( )
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 85 83 08 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_AEC_RESET
0149 BC (d0)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 0b)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 0d)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2018-08-09 11:34:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 C.
0170 . C. ***** MDP `úÃîïî»ö¼ýðÊÄðâ¹æDCBC•x²è *****
0171 C. (¼ã°îÿÔÿÄÿÈÿÏÿËÿÁÿÇÿÈÿ¼ã¼Ä»Ûâ¹æ)
0172 . S. DC-BC dcbc-402:DCBC
0173 (MDP_known_event)
0174 C.
0175 C.
0176 . C. ***** ÿÐÿ¹•ÿ Daily±¿ÍÑæÿ'Øâ¹æDCBC•x²è *****
0177 . S. DC-BC dcbc-153:DCBC
0178 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0179 C.
0180 C.
0181 . C. ;ãLOSÿÁÿSÿÄÿ-¼Ä»Û;ã
0182 C.
0183 . C. ***** LOS *****
0184 C.

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*** OP Sequence for XRT ***

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2018/08/09 11:44:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 11:44:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 11:44:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2018/08/09 11:45:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 56 c0 01 99
2018/08/09 11:45:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2018/08/09 11:45:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2018/08/09 11:45:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2018/08/09 11:45:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2018/08/09 11:45:26.0 XRT_FLD_RESET_432_OG [0x1b0]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/09 11:47:56.0 XRT_QT_PROG_SET_429_OG [0x1ad]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 14
2018/08/09 11:47:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2018/08/09 11:48:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/08/09 12:29:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 12:29:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 12:29:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/09 12:29:36.0 XRT_PREFLR_STRT_437_OG [0x1b5]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/08/09 12:32:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/08/09 12:52:30.0 XRT_Custom_430_OG [0x1ae]
2018/08/09 12:53:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/08/09 14:07:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 14:07:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 14:07:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/09 14:07:36.0 XRT_PREFLR_STRT_437_OG [0x1b5]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/08/09 14:10:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/08/09 14:35:00.0 XRT_Custom_430_OG [0x1ae]
2018/08/09 14:36:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/08/09 15:46:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 15:46:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 15:46:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/09 15:46:06.0 XRT_PREFLR_STRT_437_OG [0x1b5]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/08/09 15:49:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/08/09 16:26:30.0 XRT_Custom_430_OG [0x1ae]
2018/08/09 16:27:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/08/09 17:24:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 17:24:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 17:24:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/09 17:24:36.0 XRT_PREFLR_STRT_437_OG [0x1b5]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/08/09 17:27:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/08/09 18:03:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 18:03:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/09 18:03:28.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2018/08/09 18:03:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2018/08/09 18:03:48.0 XRT_FLD_DIS_409_OG [0x199]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2018/08/09 18:03:50.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2018/08/09 18:03:52.0 XRT_ARS_DIS_414_OG [0x19e]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2018/08/09 18:06:28.0 XRT_QT_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 02
2018/08/09 18:06:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0

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2018/08/09	18:13:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	18:13:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	18:13:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/08/09	18:13:30.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2018/08/09	18:13:48.0	XRT_FLD_ENA_405_OG [0x195]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/08/09	18:16:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/08/09	18:16:20.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/08/09	18:16:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/08/09	18:16:24.0	XRT_FLD_RESET_420_OG [0x1a4]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/09	18:16:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2018/08/09	18:16:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/08/09	18:16:30.0	XRT_CTRL_AUTO_442_OG [0x1ba]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/09	19:03:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	19:03:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	19:03:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/09	19:03:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/09	19:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/09	19:40:00.0	XRT_Custom_430_OG [0x1ae]							
2018/08/09	19:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/09	20:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	20:41:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	20:41:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/09	20:41:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/09	20:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/09	21:17:00.0	XRT_Custom_430_OG [0x1ae]							
2018/08/09	21:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/09	22:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	22:19:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	22:19:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/09	22:19:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/09	22:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/09	22:53:00.0	XRT_Custom_430_OG [0x1ae]							
2018/08/09	22:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/09	23:56:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	23:56:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	23:56:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/08/09	23:57:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/08/09	23:57:18.0	XRT_FLD_ENA_405_OG [0x195]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/08/09	23:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	23:58:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/09	23:58:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/09	23:58:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/09	23:59:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/08/09	23:59:50.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/08/09	23:59:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/08/09	23:59:54.0	XRT_FLD_RESET_420_OG [0x1a4]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/09	23:59:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2018/08/09	23:59:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				

2018/08/10	00:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/10	00:21:00.0	XRT_Custom_430_OG [0x1ae]							
2018/08/10	00:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/10	01:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/10	01:35:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/10	01:35:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/10	01:35:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/10	01:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/10	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/10	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/10	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/08/10	02:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2018/08/10	02:00:18.0	XRT_FLD_ENA_405_OG [0x195]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/08/10	02:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/08/10	02:02:50.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/08/10	02:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/08/10	02:02:54.0	XRT_FLD_RESET_420_OG [0x1a4]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/10	02:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2018/08/10	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/08/10	02:03:00.0	XRT_CTRL_AUTO_442_OG [0x1ba]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/10	03:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/10	03:07:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/10	03:07:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/10	03:07:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/10	03:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/10	03:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/10	03:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/10	03:39:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/08/10	03:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/08/10	03:40:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/08/10	03:40:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/08/10	03:40:22.0	XRT_ARS_DIS_414_OG [0x19e]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/08/10	03:42:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2018/08/10	03:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/10	03:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/10	03:49:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/10	03:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/08/10	03:50:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2018/08/10	03:50:18.0	XRT_FLD_ENA_405_OG [0x195]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/08/10	03:52:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/08/10	03:52:50.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/08/10	03:52:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/08/10	03:52:54.0	XRT_FLD_RESET_420_OG [0x1a4]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/10	03:52:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2018/08/10	03:52:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/08/10	03:53:00.0	XRT_CTRL_AUTO_442_OG [0x1ba]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/10	04:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

2018/08/10	04:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	04:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	04:36:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/10	04:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/10	05:16:00.0	XRT_Custom_430_OG [0x1ae]						
2018/08/10	05:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/10	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2018/08/10	06:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	03 00 00 00 00		
2018/08/10	06:00:18.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/08/10	06:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/08/10	06:02:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/08/10	06:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/08/10	06:02:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	06:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2018/08/10	06:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2018/08/10	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/10	06:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	06:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	06:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	06:16:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/10	06:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/10	06:54:00.0	XRT_Custom_430_OG [0x1ae]						
2018/08/10	06:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/10	07:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	07:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	07:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	07:56:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/10	07:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/10	08:32:30.0	XRT_Custom_430_OG [0x1ae]						
2018/08/10	08:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/10	09:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	09:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	09:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	09:36:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/10	09:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/10	10:11:00.5	XRT_Custom_430_OG [0x1ae]						
2018/08/10	10:12:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/10	11:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	11:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	11:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	11:19:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/10	11:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/10	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	11:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2018/08/10	12:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04 00 00 00 00		
2018/08/10	12:00:18.0	XRT_FLD_ENA_405_OG [0x195]						

2018/08/10	12:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/08/10	12:02:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/08/10	12:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/08/10	12:02:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/10	12:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/10	12:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2018/08/10	12:03:00.0	XRT_CTRL_AUTO_442_OG [0x1ba]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2018/08/10	13:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/10	13:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	13:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	13:05:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/10	13:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/10	13:27:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/10	13:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0	
2018/08/10	14:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/10	14:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	14:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	14:43:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/10	14:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/10	15:14:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/10	15:15:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0	
2018/08/10	16:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/10	16:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	16:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	16:21:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/10	16:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/10	17:01:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/10	17:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG [0x1ae]	1	07-F0	c0	
2018/08/10	17:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/10	17:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	17:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	17:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2018/08/10	17:50:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00	00 00 00 00
2018/08/10	17:50:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/08/10	17:50:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/08/10	17:52:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/10	17:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2018/08/10	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/10	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	17:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2018/08/10	18:00:00.5	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	04	00 00 00 00
2018/08/10	18:00:02.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	18:00:04.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/10	18:00:06.5	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/10	18:00:18.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/10	18:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/08/10	18:02:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	

2018/08/10	18:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/08/10	18:02:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	18:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c	
2018/08/10	18:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2018/08/10	18:03:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/10	18:38:00.0	XRT_Custom_430_OG [0x1ae]						
2018/08/10	18:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/10	19:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	19:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	19:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	19:38:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/10	19:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/10	20:14:30.0	XRT_Custom_430_OG [0x1ae]						
2018/08/10	20:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/10	21:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	21:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	21:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	21:17:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/10	21:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/10	21:52:00.0	XRT_Custom_430_OG [0x1ae]						
2018/08/10	21:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/10	22:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	22:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	22:55:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	22:55:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/10	22:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/10	23:27:00.0	XRT_Custom_430_OG [0x1ae]						
2018/08/10	23:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/10	23:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	23:56:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/10	23:56:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2018/08/10	23:57:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00	
2018/08/10	23:57:18.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/08/10	23:59:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/08/10	23:59:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/08/10	23:59:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/08/10	23:59:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/10	23:59:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10	
2018/08/10	23:59:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2018/08/11	00:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/11	00:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/11	00:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/11	00:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/11	00:33:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/11	00:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/11	00:56:00.0	XRT_Custom_430_OG [0x1ae]						
2018/08/11	00:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/11	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/11	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						

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2018/08/11	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	02:00:00.0	AOCs_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2018/08/11	02:00:18.0	XRT_FLD_ENA_405_OG [0x195]	AOCU_NM	5	02-76	04 00 00 00 00	
2018/08/11	02:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/08/11	02:02:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/08/11	02:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/08/11	02:02:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/11	02:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/11	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2018/08/11	02:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/08/11	02:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	02:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	02:07:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/11	02:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/11	02:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/11	02:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/08/11	03:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/11	03:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	03:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	03:42:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/11	03:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/11	04:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/11	04:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	04:14:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	04:15:00.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/08/11	04:15:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/08/11	04:15:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/08/11	04:15:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/08/11	04:17:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/11	04:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2018/08/11	04:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/11	04:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	04:24:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	04:25:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2018/08/11	04:25:18.0	XRT_FLD_ENA_405_OG [0x195]	AOCU_NM	5	02-76	02 00 00 00 00	
2018/08/11	04:27:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/08/11	04:27:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/08/11	04:27:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/08/11	04:27:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/11	04:27:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/11	04:27:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2018/08/11	04:28:00.0	XRT_CTRL_AUTO_442_OG [0x1ba]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/08/11	05:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/11	05:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	05:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	05:12:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/11	05:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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2018/08/11	05:51:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/11	05:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/08/11	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/11	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	06:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/08/11	06:00:18.0	XRT_FLD_ENA_405_OG [0x195]	AOCU_NM	5	02-76	00 0e 35 af 1b	
2018/08/11	06:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/08/11	06:02:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/08/11	06:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/08/11	06:02:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/11	06:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/11	06:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2018/08/11	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/08/11	06:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/11	06:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	06:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	06:52:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/11	06:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/11	07:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/11	07:34:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	07:34:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	07:35:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2018/08/11	07:35:18.0	XRT_FLD_DIS_431_OG [0x1af]	AOCU_NM	5	02-76	00 b0 e6 00 00	
2018/08/11	07:37:54.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/08/11	07:37:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/08/11	07:37:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/11	08:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2018/08/11	08:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/11	08:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	08:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	08:32:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/11	08:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/11	09:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/11	09:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	09:09:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	09:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/08/11	09:10:18.0	XRT_FLD_DIS_431_OG [0x1af]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/08/11	09:12:54.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/08/11	09:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/08/11	09:12:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/11	09:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2018/08/11	10:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/11	10:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	10:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/11	10:12:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/11	10:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2018/08/11	10:47:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/11	10:47:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/11	10:47:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/11	10:48:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2018/08/11	10:48:18.0	XRT_FLD_DIS_431_OG [0x1af]	AOCU_NM	5	02-76	00 b0 e6 00 00
2018/08/11	10:50:54.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2018/08/11	10:50:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2018/08/11	10:50:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/08/11	10:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2018/08/11	12:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/08/11	12:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/11	12:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/11	12:02:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/11	12:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/11	12:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/11	12:04:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/11	12:05:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/08/11	12:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	AOCU_NM	5	02-76	00 0e 35 af 1b
2018/08/11	12:05:18.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/11	12:07:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2018/08/11	12:07:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/08/11	12:07:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2018/08/11	12:07:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/08/11	12:07:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/11	12:07:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2018/08/11	13:26:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
			AOCU_NM	5	02-76	00 00 00 00 00