

XRT Timeline to be uploaded on 2018/08/18

Period: 2018/08/18 09:49:00 - 2018/08/23 10:34:00

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Normal mode

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XOB #1BD8: Synoptic 7 Filter w/ Al-mesh(64/512/2897), Al-poly(45/512/4096), Thin-Be(1024/11571/23142) - Thick-Be(65536), Al-poly+Ti-poly(512/8192), Med													
Term	Pointing (x, y)				Comment								
08/18 10:06:00 - 08/18 10:29:54	Fixed (0.0, 0.0)				# OP start + 10min; Extra synoptic 30min								
08/18 17:41:30 - 08/18 17:48:24	Fixed (0.0, 0.0)				synoptic, shifted -21.5 min								
08/19 05:59:30 - 08/19 06:06:30	Fixed (0.0, 0.0)				synoptic, shifted -3.5 min								
PROG= 18 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 5 1-time(s) 2.0sec													
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec		
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec		
└─ Seqn= 36 1-time(s) 2.0sec													
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
└─ Seqn= 99 1-time(s) 2.0sec													
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
└─ Seqn= 33 1-time(s) 2.0sec													
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
└─ Seqn= 23 1-time(s) 4.0sec													
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs 1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec		
└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs 1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 46 1-time(s) 2.0sec													
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
└─ Seqn= 17 1-time(s) 2.0sec													
└─ med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
└─ med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
└─ Seqn= 25 1-time(s) 2.0sec													
└─ Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
└─ Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1BDC: HOP341 - High cadence (10s Al/poly only) 384x384 at 1064 1048													
Term	Pointing (x, y)				Comment								
08/18 10:33:00 - 08/18 17:38:24	Track (-125.7, -292.8) ^{Ⓢ 08/18 10:30:00}				HOP361 High Cadence AR obs								
PROG= 15 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
└─ Subr= 2 20-time(s) 2.0sec													
└─ Seqn= 74 60-time(s) 10.0sec													
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1BED: AR (Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 20s cad													
Term	Pointing (x, y)				Comment								
08/18 18:27:01 - 08/19 05:56:24	Track (-60.2, -293.8) ^{Ⓢ 08/18 17:48:30}				# AR obs								
PROG= 16 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
└─ Seqn= 71 3-time(s) 2.0sec													
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs 1x1	512x512 (1064, 1048)	Q=98	3	0	2.0sec		
└─ Subr= 2 100-time(s) 2.0sec													
└─ Seqn= 89 1-time(s) 2.0sec													
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec		
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec		
└─ Seqn= 58 1-time(s) 2.0sec													
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec		
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)		Comment									
08/18 10:33:00 - 08/18 17:38:24	Track (-125.7, -292.8) ^{Ⓢ 08/18 10:30:00}		HOP361 High Cadence AR obs									
08/18 18:27:01 - 08/19 05:56:24	Track (-60.2, -293.8) ^{Ⓢ 08/18 17:48:30}		# AR obs									
PROG= 13 30-time(s)												
└─ Subr= 1 20-time(s) 2.0sec												
└─ Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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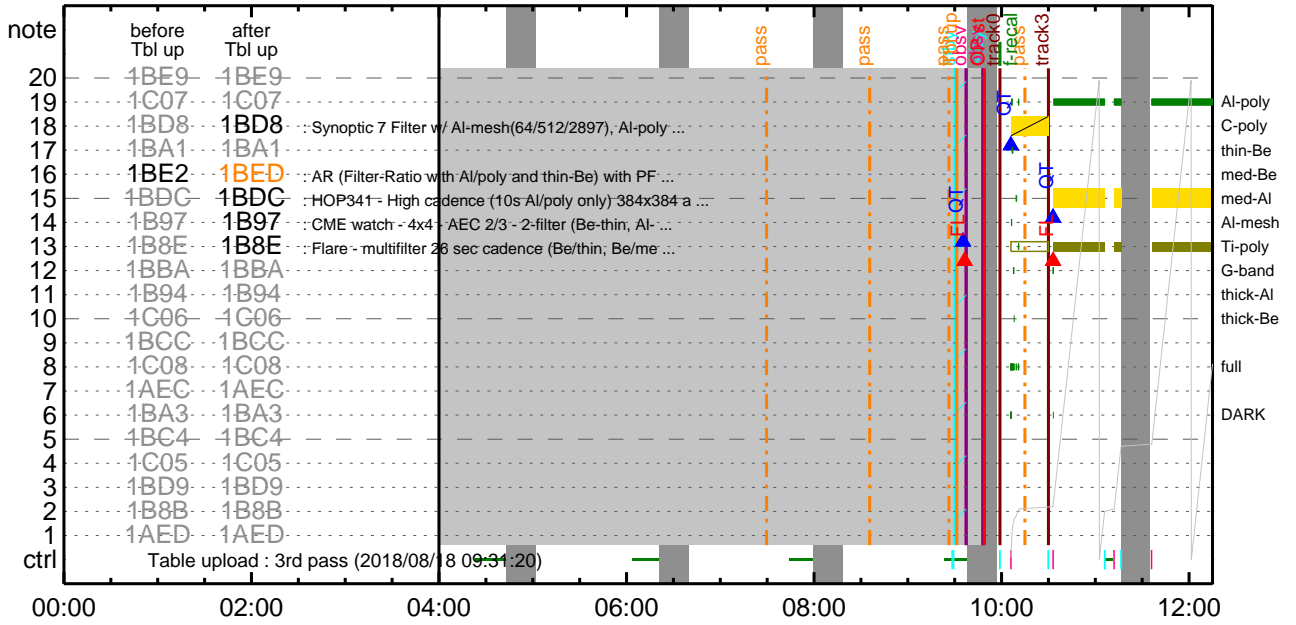
Flare Detection

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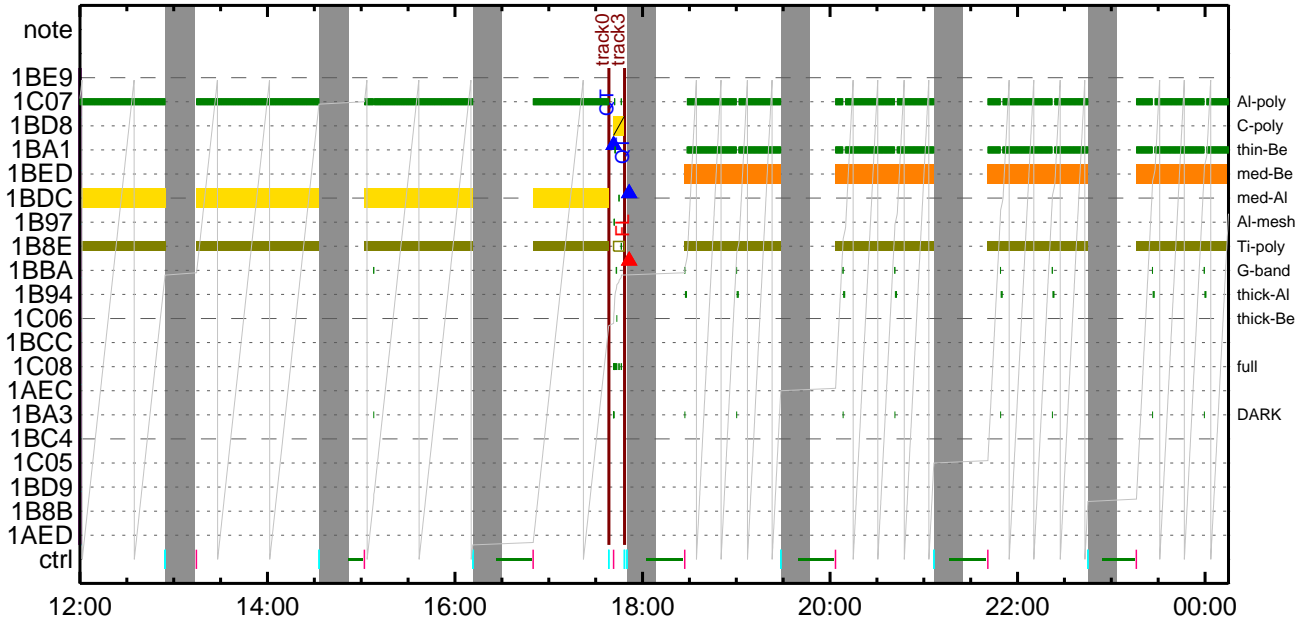
FLD Patrol

Term	Pointing (x, y)		Comment									
08/18 10:30:18 - 08/18 17:38:48	Track (-125.7, -292.8) ^{Ⓢ 08/18 10:30:00}		HOP361 High Cadence AR obs									
08/18 17:48:48 - 08/19 05:56:48	Track (-60.2, -293.8) ^{Ⓢ 08/18 17:48:30}		# AR obs									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50			80sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

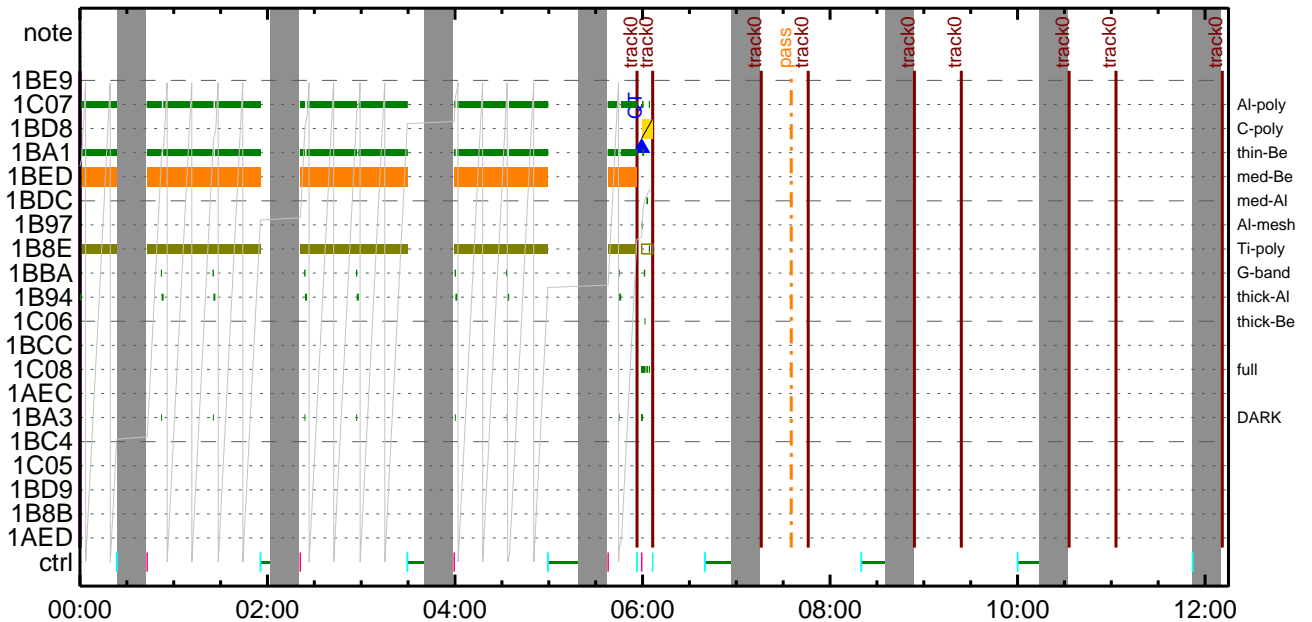
CMDI #0695 2018/08/18



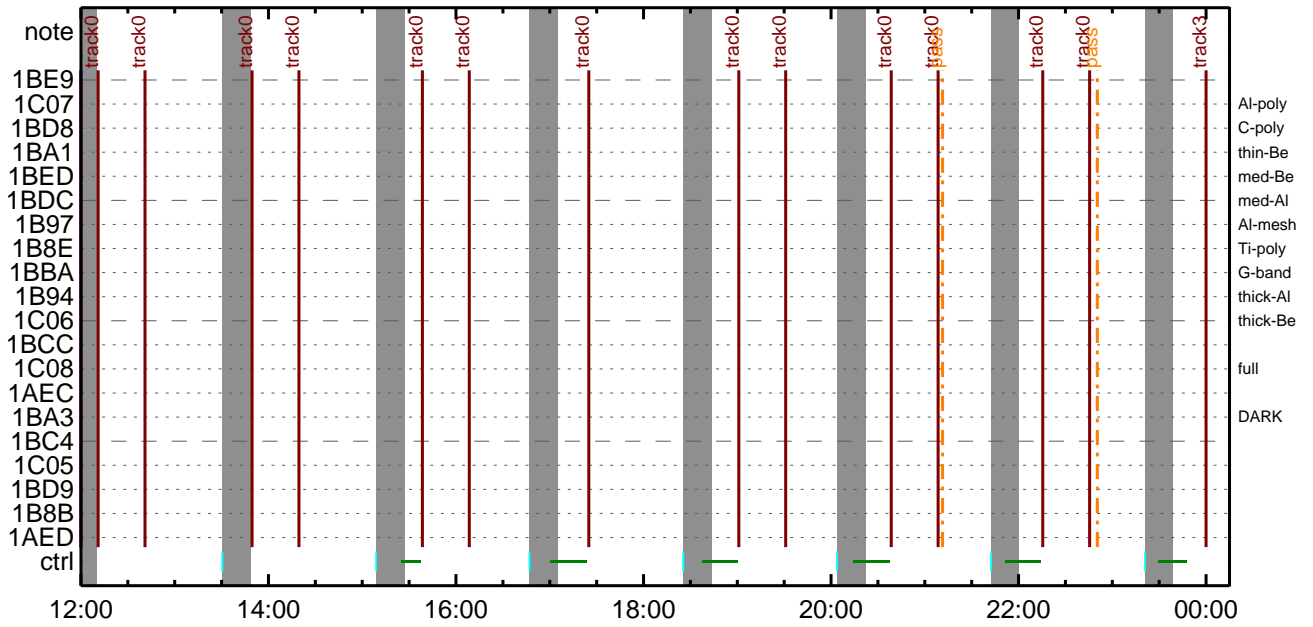
CMDI #0695 2018/08/18



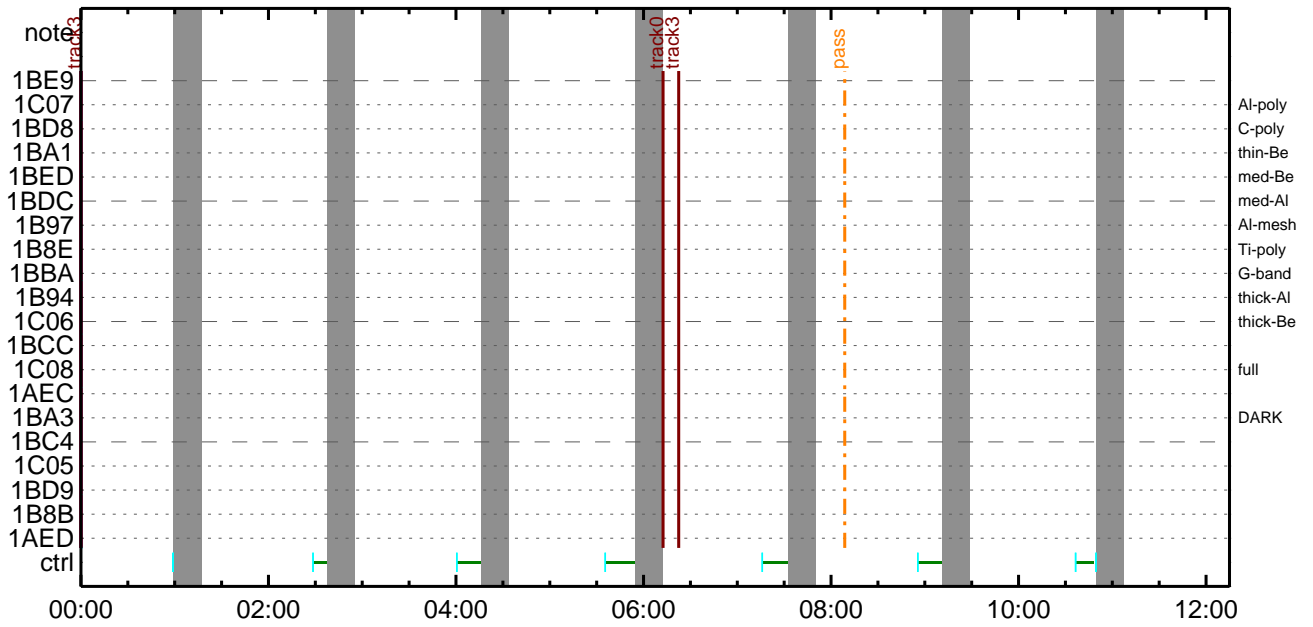
CMDI #0695 2018/08/19



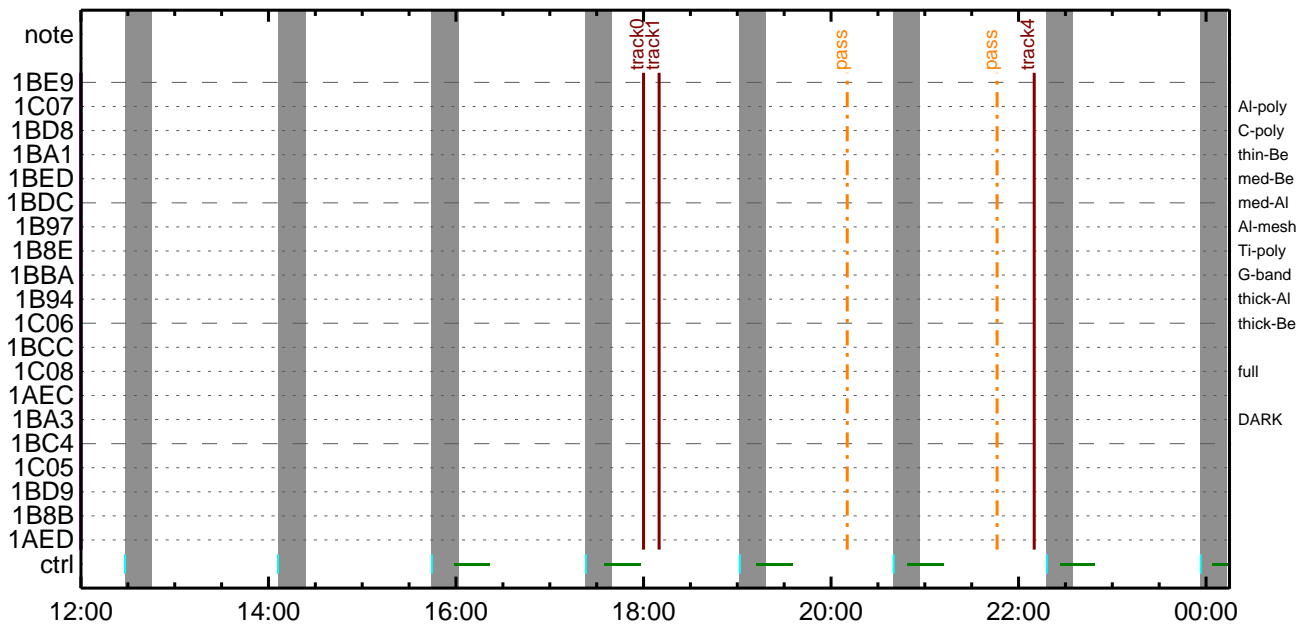
CMDI #0695 2018/08/19



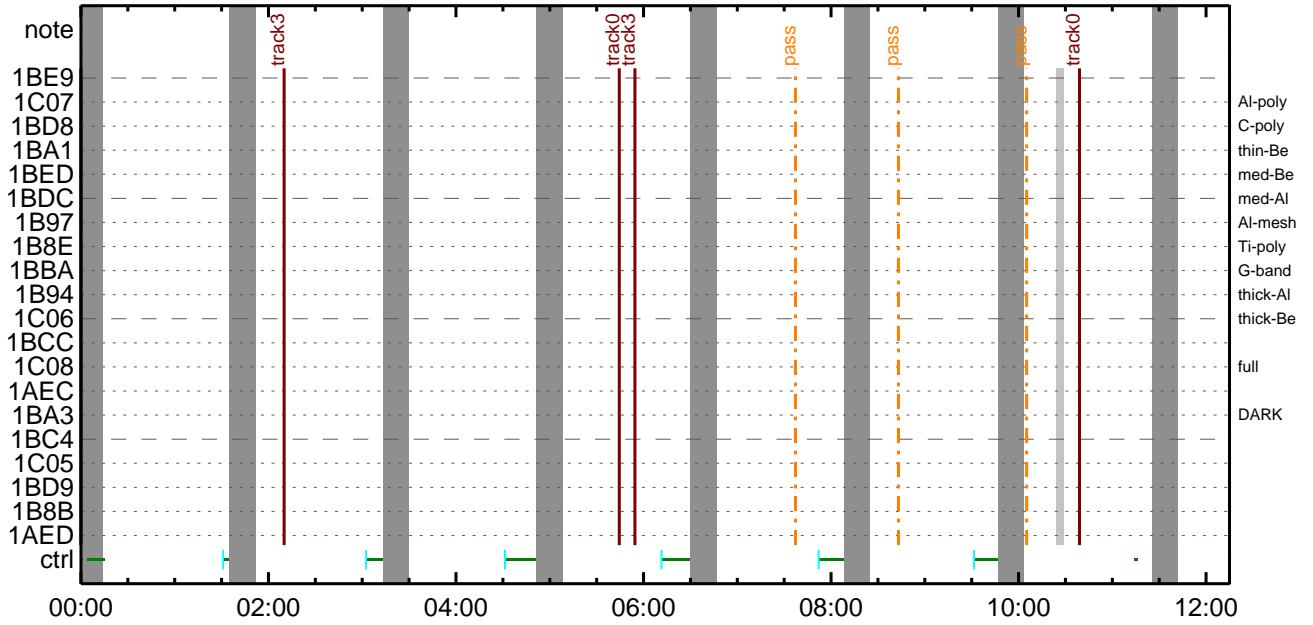
CMDI #0695 2018/08/20



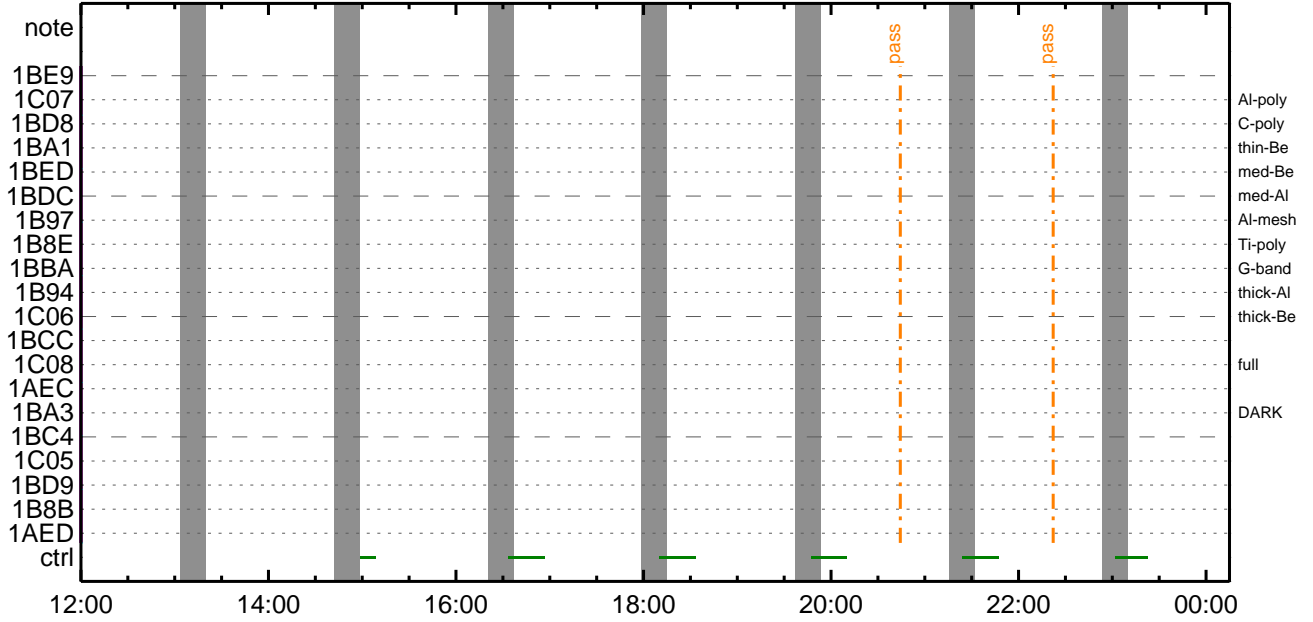
CMDI #0695 2018/08/20



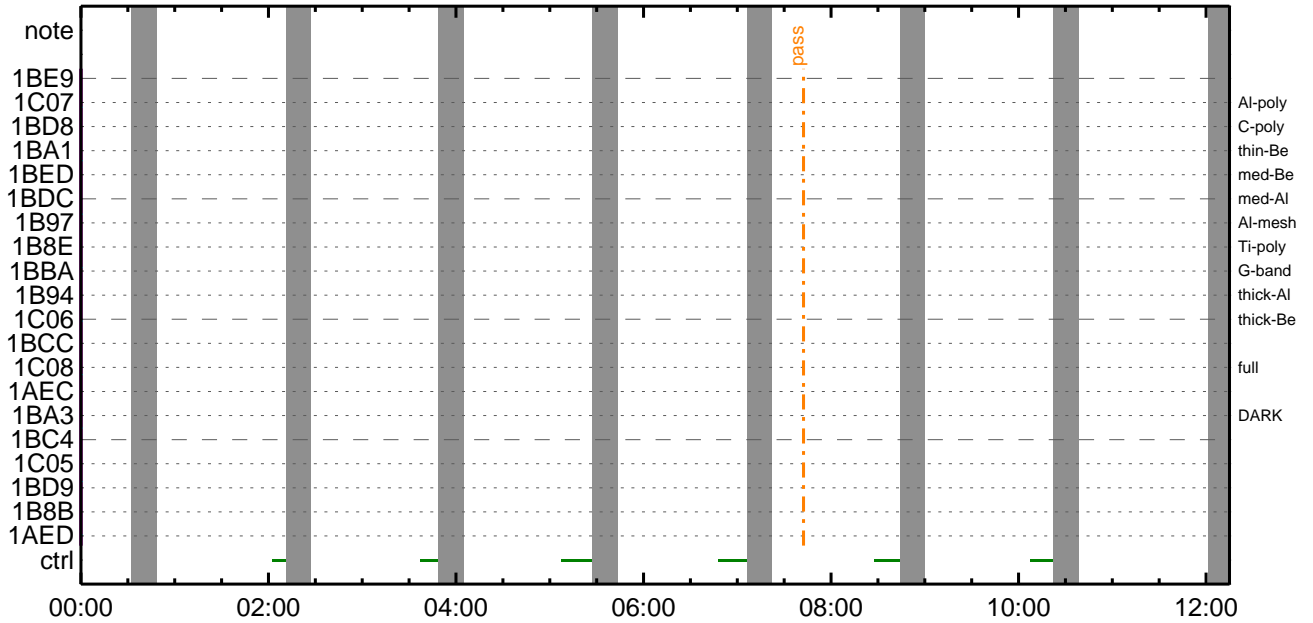
CMDI #0695 2018/08/21



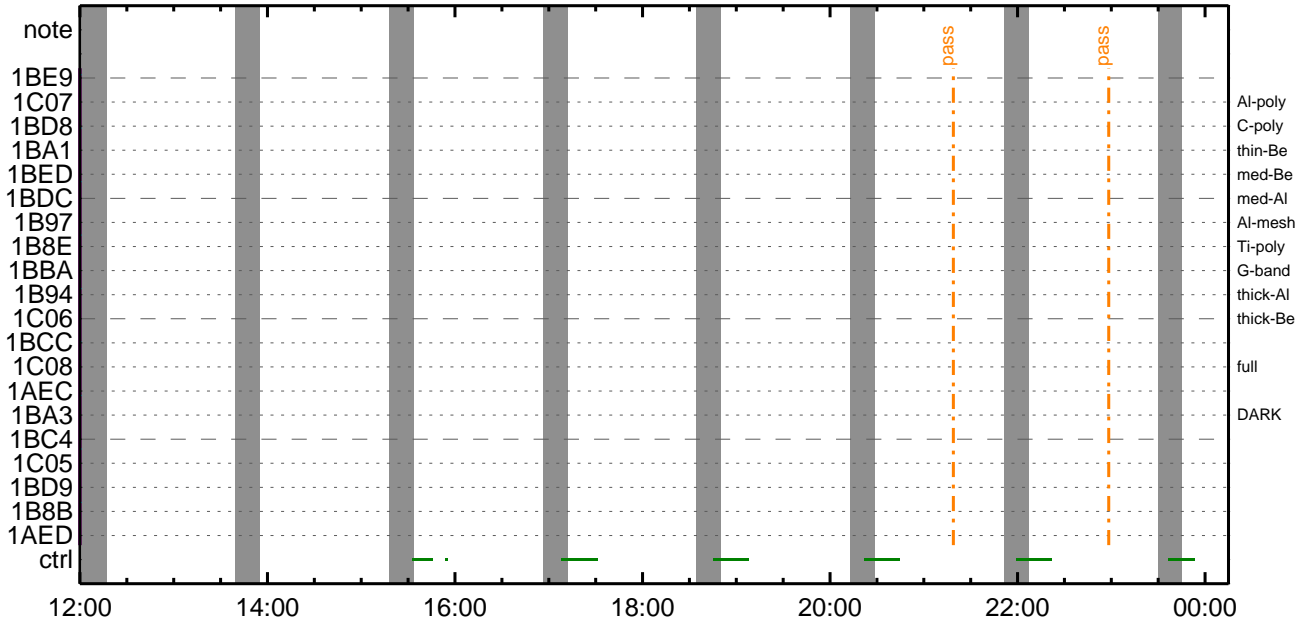
CMDI #0695 2018/08/21



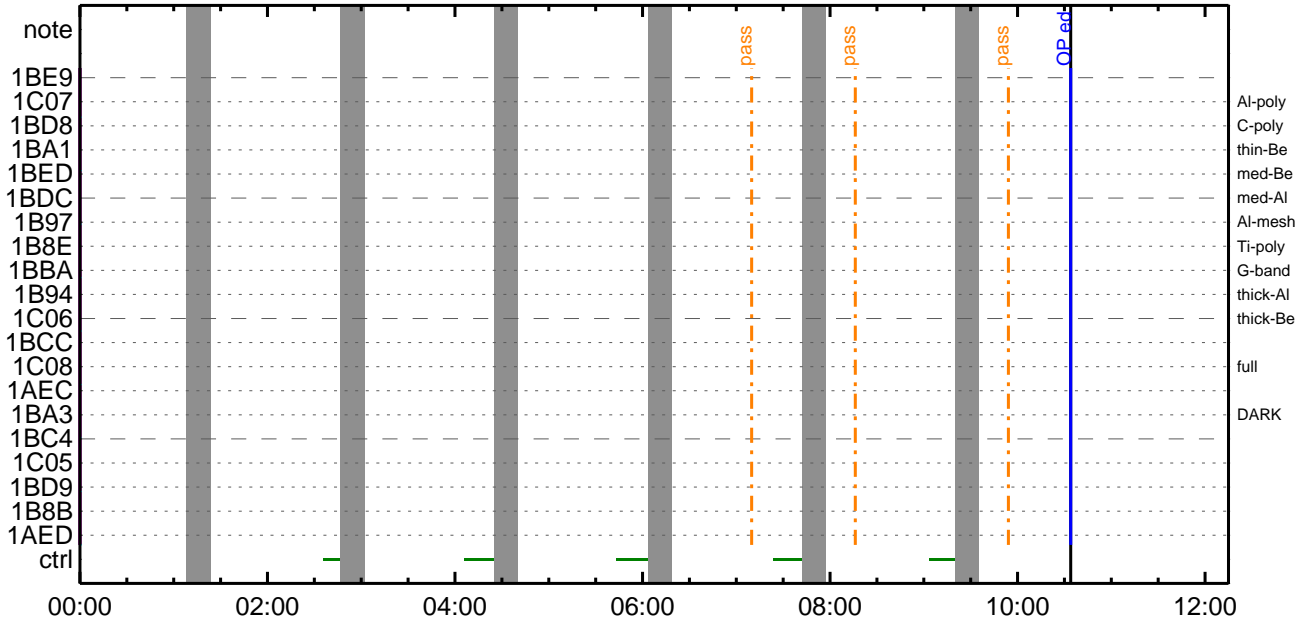
CMDI #0695 2018/08/22



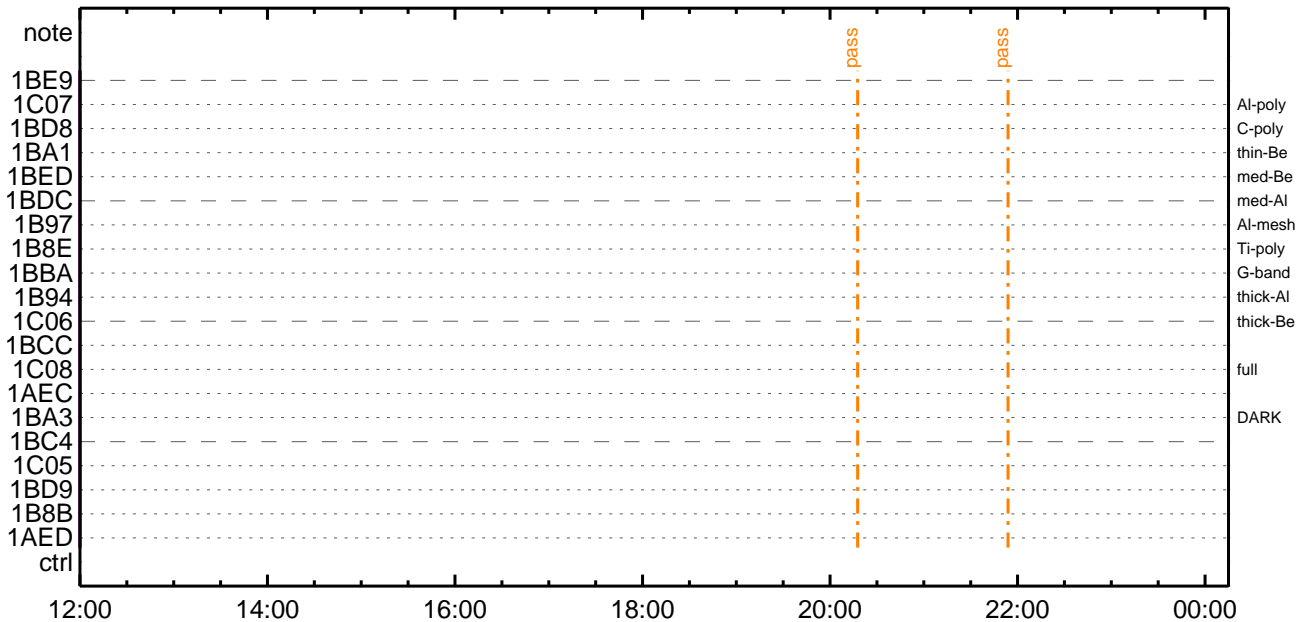
CMDI #0695 2018/08/22



CMDI #0695 2018/08/23



CMDI #0695 2018/08/23




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0096 C.                0300; SET0EDUMP0I00iYNY10C100|030E;E
0097 C.
0098 C. TIY3YBY0Y0E000ADi0(UT)
0099 +. TI 2018-08-18 09:44:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                00[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0102 C.
0103 +. TI 2018-08-18 09:44:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                00[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0106 C.
0107 +. TI 2018-08-18 09:44:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                00[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0110 C.
0111 +. TI 2018-08-18 09:48:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                00[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0114 C.
0115 C. 0E2%0IA0%I0N0IYAYSYAY-1a0U
0116 C.                00[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0117 C.                00[HK1_TI_CMD_NUM]         EQ      4
0118 C.                00[HK1_NEXT_EXEC_PIM]       EQ      DHU
0119 C.                00[HK1_NEXT_EXEC_DC]       EQ      0xB3
0120 C.
0121 C. *****
0122 C. TI0I0EYAY0YX
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;S 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                00[HK1_DMP_TOP_ADRS_1]       EQ      07
0129 C.                00[HK1_DMP_TOP_ADRS_0]       EQ      2B
0130 C.                00[HK1_DMP_BLOCK_NUM]        EQ      3
0131 C.                00[HK1_DMP_REPEAT_NUM]       EQ      0
0132 C.                00[HK1_DMA_DMP_PIM]         EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                00[HK1_PKT_FORM_NO]          EQ      7
0136 C.                00[HK1_PKT_GEN_TIME]         EQ      0.25 s
0137 C.                00[HK1_S_TLM_BIT_RATE]       EQ      32k
0138 C.                00[HK1_X_TLM_BIT_RATE]      EQ      4M
0139 C.                00[HK1_DMP_CHK_FLG]         EQ      EXEC
0140 C.
0141 C. YAY0YX%I0I0003I0S
0142 C.                00[HK1_DMP_CHK_FLG]         EQ      NON
0143 C.
0144 C. RAM ID=TI_TBL0I0E1000e2IOK0003I0S
0145 C.
0146 C. DHUY0I0Y0E0E0Y0Y0Y0I0Y0E0E00I0001
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                00[HK1_PKT_FORM_NO]          EQ      2
0150 C.                00[HK1_PKT_GEN_TIME]         EQ      0.5S
0151 C.                00[HK1_S_TLM_BIT_RATE]       EQ      32K
0152 C.                00[HK1_X_TLM_BIT_RATE]      EQ      4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2018-08-18 09:48:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2018-08-18 09:48:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2018-08-18 09:48:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.                [ ] [HK1_TI_CMD_NUM]       EQ      2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2018-08-18 09:48:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.                [ ] [HK1_TI_CMD_NUM]       EQ      1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 0AI0I000Y0E000AD000eDCBC0x2e *****
0192 C. (%00I0Y0AY0E0Y0E0Y0AY0E0E0E0%0004A0U000e)
0193 S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øα¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-574 2018-08-18 12:20:07 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYŞ;¼Y³YŞYÓYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÄð•µ°Æ»Í×ÁÇçÍYçYÁY×YÍ;¼YÉ;ÈÈÈ¼µ•íÉ;ÈÈÈ¼°ÇÖñ•ç¼í¹ççÍ;çÄ®, ùñ¹ñèñèñçÄ+ç®ñ•ñèññññè;ñ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç®µ;ON
0016 C. *****
0017 C. ç" °ÆÀ, Í×ÈYñÄLOSñèççñ»p´Öñð¹íí, ñ•; çÉÖÍ×ñÈXÁÖONñí¹ÖñÈñ¹ñèññññè;ñ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÁY-¾ÖÄÖñ-°ÁÄèñ•çç; ç°È²¼ñí°ÆÀ, ¼è¼çñð¼Á¹Öñ¹ñè;ñ
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°ÆÀ,
0033 C. *****
0034 C. ç" RESTART;ÈPT1;Èñ•çç¼ñ¼í¹ççÍ; ç°È²¼ñí¼Á¹Öññ°; çDCBC-150ñðçÈèñ;ñ
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÈÄÙÄÖ;ÈÄ•Á°²óÈè;È, áñí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ñ-¼«Æ°Áä»ñçñ•çç; á; ç°È²¼ñ¼ñ¼Á¹Öñ¹ñè;ñ
0055 C. YçYÓYÉYÈÄÙÄÖñèÄ•Á°²óÈèñ-¶áñ¼ñ¼í¹ççÍ´°í»ñ¹ñèñèñçÄÖñ;ñ
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°ÆÀ,
0059 C. *****
0060 C. ç" RESTART;ÈPT2;Èñ•çç¼ñ¼ñ¼í¹ççÍ; ç°È²¼ñí¼Á¹Öññ°; çDCBC-151ñðçÈèñ;ñ
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÈÄÙÄÖ;ÈÄ•Á°²óÈè;È, áñí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áä»ñ; çXÁ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áä»ñ;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç®µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-575 2018-08-18 12:20:07 134 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁY$;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíÊ¿ðÁð•µ°Æ»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÉÈèµ•ííÉ;ÈðÈ¼°ÇÓð•ð¿¼í¹çðÍ;çÁ®, ùñ¹ðèððçÁ+¿®ð•ðÈððð³ðÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-288:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2018-08-18 09:48:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG_____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR _____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 05 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 06 80 80 20 20)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 07 80 80 08 08)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 08 80 80 20 08)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 09 80 80 08 20)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0a 85 83 06 06)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0b 85 83 08 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0f 80 80 06 06)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 10 80 80 08 08)
0092 +. DC 07-F0 MDP_XRT_FLD_ENA
0093 BC (d8)
0094 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0095 BC (c8)
```

```
0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097 BC (d5)
0098 + DC 07-F0 MDP_XRT_AEC_RESET
0099 BC (d0)
0100 + DC 07-F0 MDP_XRT_FLD_RESET
0101 BC (da)
0102 + DC 07-F0 MDP_XRT_QT_PROG_SET
0103 BC (c4 0e)
0104 + DC 07-F0 MDP_XRT_FL_PROG_SET
0105 BC (c5 0d)
0106 . C. ----- Success Verify ? OK / NG ____
0107 C.
0108 C.
0109 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0110 C.
0111 + DC 07-F0 MDP_XRT_MODE_OBSV
0112 BC (c2)
0113 + TI 2018-08-18 09:48:02.0
0114 DC 07-F0 MDP_XRT_MODE_OBSV
0115 BC (c2)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C. ***** XRT END *****
0119 C.
0120 . C. ***** MDP `úÃîñî»ò%ÝñÊÃðñ¹ñèDCBC•x²è *****
0121 C. (%á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ½¼ñ¼Ã»Ûñ¹ñè)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ÝDÝ¹•Ï Daily±¿ÍÑñÊ´Øñ¹ñèDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ¡ãLOSÝÁÝ$ÝÃÝ-¼Ã»Ûñ¹ñè
0132 C.
0133 . C. ***** LOS *****
0134 C.
```

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*** OP Sequence for XRT ***

2018/08/18	09:58:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	09:58:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	09:58:58.0	XRT_FOCUS_RECALIBRATE_436_OG [0x1b4]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2018/08/18	09:59:00.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/08/18	10:02:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/08/18	10:03:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/08/18	10:03:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/08/18	10:03:22.0	XRT_ARS_DIS_414_OG [0x19e]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/08/18	10:05:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2018/08/18	10:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/18	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	10:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	10:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/08/18	10:30:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2018/08/18	10:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/08/18	10:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/08/18	10:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/08/18	10:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/08/18	10:30:26.0	XRT_FLD_RESET_432_OG [0x1b0]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/18	10:32:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2018/08/18	10:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/08/18	10:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/18	11:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	11:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	11:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/18	11:06:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/18	11:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/18	11:11:00.0	XRT_Custom_430_OG [0x1ae]							
2018/08/18	11:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/18	11:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	11:16:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	11:16:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/18	11:16:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/18	11:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/18	11:35:00.0	XRT_Custom_430_OG [0x1ae]							
2018/08/18	11:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/18	12:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	12:54:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	12:54:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/18	12:54:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/18	12:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/18	13:13:30.0	XRT_Custom_430_OG [0x1ae]							
2018/08/18	13:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/18	14:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	14:33:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	14:33:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/18	14:33:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2018/08/18	14:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/18	15:01:00.5	XRT_Custom_430_OG [0x1ae]							
2018/08/18	15:02:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/18	16:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	16:11:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	16:11:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/18	16:11:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/18	16:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/18	16:49:00.0	XRT_Custom_430_OG [0x1ae]							
2018/08/18	16:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/18	17:38:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	17:38:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	17:38:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2018/08/18	17:38:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2018/08/18	17:38:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2018/08/18	17:38:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2018/08/18	17:38:52.0	XRT_ARS_DIS_414_OG [0x19e]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/08/18	17:41:28.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2018/08/18	17:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/18	17:48:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	17:48:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	17:48:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2018/08/18	17:48:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2018/08/18	17:48:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2018/08/18	17:48:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2018/08/18	17:48:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2018/08/18	17:48:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2018/08/18	17:48:56.0	XRT_FLD_RESET_432_OG [0x1b0]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/18	17:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	17:50:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	17:50:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/18	17:50:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/18	17:51:26.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2018/08/18	17:51:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2018/08/18	17:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/18	18:26:01.0	XRT_Custom_430_OG [0x1ae]							
2018/08/18	18:27:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/18	19:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	19:28:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	19:28:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/18	19:28:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/18	19:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/18	20:02:30.0	XRT_Custom_430_OG [0x1ae]							
2018/08/18	20:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2018/08/18	21:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	21:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/18	21:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/18	21:06:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/18	21:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2018/08/18	21:40:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/18	21:41:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/08/18	22:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/18	22:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/18	22:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/18	22:45:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/18	22:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/18	23:15:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/18	23:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/08/19	00:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/19	00:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/19	00:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/19	00:23:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/19	00:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/19	00:42:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/19	00:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/08/19	01:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/19	01:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/19	01:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/19	01:55:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/19	01:55:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/19	01:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/19	02:20:00.0	XRT_Custom_430_OG [0x1ae]					
2018/08/19	02:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/08/19	03:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/19	03:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/19	03:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/19	03:29:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/19	03:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/19	03:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/19	03:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/08/19	04:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/19	04:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/19	04:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/19	04:59:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/19	05:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/19	05:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/19	05:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2018/08/19	05:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/19	05:56:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/19	05:56:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/19	05:56:30.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/08/19	05:56:48.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/08/19	05:56:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/08/19	05:56:52.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/08/19	05:59:28.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/19	05:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2018/08/19	06:06:30.0	AOCS_Ore-point_Start_3_OG [0x099]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/19	06:06:30.5	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	00 58 fd 01 99	
2018/08/19	06:07:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_422_OG [0x1a6]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
		TCIB_XRT_S_HTR_A_ENA		0	04-BC		

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2018/08/19	06:40:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	06:40:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	06:40:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/19	06:40:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/19	06:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/19	07:16:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 50 18 01 99				
2018/08/19	07:46:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 47 33 01 99				
2018/08/19	08:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	08:20:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	08:20:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/19	08:20:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/19	08:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/19	08:54:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 3e 4d 01 99				
2018/08/19	09:24:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 35 68 01 99				
2018/08/19	10:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	10:00:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	10:00:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/19	10:00:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/19	10:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/19	10:33:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 2c 8b 01 99				
2018/08/19	11:03:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 23 a5 01 99				
2018/08/19	11:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	11:52:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	11:52:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/19	11:52:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/19	11:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/19	12:11:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 1a c0 01 99				
2018/08/19	12:41:00.5	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 11 db 01 99				
2018/08/19	13:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	13:30:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	13:30:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/19	13:30:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/19	13:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/19	13:49:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 08 fd 01 99				
2018/08/19	14:19:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 00 fd 01 99				
2018/08/19	15:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	15:09:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	15:09:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/19	15:09:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/19	15:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2018/08/19	15:38:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 f8 19 01 99				
2018/08/19	16:08:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 ef 34 01 99				
2018/08/19	16:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	16:47:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2018/08/19	16:47:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2018/08/19	16:47:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2018/08/19	16:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2018/08/19	17:25:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
		AOCU_NM		5	02-76	00 e6 4e 01 99			
2018/08/19	18:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	18:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	18:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	18:25:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/19	18:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/19	19:01:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
		AOCU_NM		5	02-76	00 dd 69 01 99			
2018/08/19	19:31:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 d4 8c 01 99			
2018/08/19	20:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	20:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	20:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	20:04:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/19	20:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/19	20:38:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
		AOCU_NM		5	02-76	00 cb a6 01 99			
2018/08/19	21:08:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00 c2 c1 01 99			
2018/08/19	21:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	21:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	21:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	21:42:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/19	21:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/19	22:15:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
		AOCU_NM		5	02-76	00 b9 dc 01 99			
2018/08/19	22:45:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 b0 fe 01 99			
2018/08/19	23:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	23:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	23:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/19	23:21:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/19	23:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/20	00:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
		AOCU_NM		5	02-76	03 00 00 00 00			
2018/08/20	00:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	00:59:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	00:59:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	00:59:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/20	01:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/20	02:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/20	02:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	02:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	02:28:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/20	02:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/20	04:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/20	04:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	04:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	04:00:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/20	04:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/20	05:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/20	05:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	05:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			

2018/08/20	05:35:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/20	05:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/20	06:12:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2018/08/20	06:22:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00
2018/08/20	07:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	07:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	07:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/20	07:16:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/20	07:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/20	08:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	08:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	08:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/20	08:55:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/20	08:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/20	10:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	10:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	10:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/20	10:36:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/20	10:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/20	10:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	10:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	10:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/20	10:49:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/20	10:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/20	12:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	12:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	12:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/20	12:28:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/20	12:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/20	14:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	14:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	14:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/20	14:06:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/20	14:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/20	15:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	15:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	15:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/20	15:44:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/20	15:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/20	17:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	17:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/20	17:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/20	17:23:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/20	17:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/20	18:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00
2018/08/20	18:10:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	01 00 00 00 00
2018/08/20	19:01:30.0	XRT_CTRL_MANU_400_OG [0x190]				

2018/08/20	19:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	19:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	19:01:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/20	19:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/20	20:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/20	20:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	20:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	20:40:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/20	20:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/20	22:10:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/20	22:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	04 00 00 00 00			
2018/08/20	22:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	22:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	22:18:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/20	22:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/20	23:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/20	23:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	23:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/20	23:56:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/20	23:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/21	01:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/21	01:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/21	01:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/21	01:31:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/21	01:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/21	02:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/21	03:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	03 00 00 00 00			
2018/08/21	03:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/21	03:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/21	03:02:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/21	03:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/21	04:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/21	04:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/21	04:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/21	04:31:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/21	04:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/21	05:44:30.5	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/21	05:54:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2018/08/21	06:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	03 00 00 00 00			
2018/08/21	06:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/21	06:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/21	06:11:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/21	06:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/21	07:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/21	07:52:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/21	07:52:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			

2018/08/21	07:52:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2018/08/21	07:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2018/08/21	09:31:30.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2018/08/21	09:31:32.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2018/08/21	09:31:34.0	XRT_FLD_RESET_415_OG [0x19f]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2018/08/21	09:31:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2018/08/21	09:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2018/08/21	10:39:00.0	AOCS_ORe-point_Start_1_OG [0x097]									
		AOCU_NM	5	02-76	00 00 00 00 00 00						