

# XRT Timeline to be uploaded on 2018/08/30

Period: 2018/08/30 09:47:00 - 2018/09/04 10:32:00

\* \* \* \* \*

## Normal mode

\* \* \* \* \*

### XOB #1B97: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak last)

Term	Pointing (x, y)	Comment
08/30 10:15:37 - 08/30 11:59:54	Track ( 193.6, 724.5) <sup>Ⓢ 08/30 09:57:00</sup>	# OP start + 10min / SST/IRIS CH/QS Boundary
08/31 06:21:00 - 08/31 06:59:54	Fixed ( 900.0, 112.0)	AR 12720 obs.
08/31 07:03:00 - 08/31 13:59:54	Track ( 112.4, 733.0) <sup>Ⓢ 08/31 07:00:00</sup>	SST/IRIS CH QS boundary
08/31 18:08:30 - 08/31 23:59:30	Fixed ( -18.0, 844.0)	North Pole Coronal Hole for EIS
09/01 00:14:35 - 09/01 02:58:00	Fixed ( -16.0, -970.0)	South Pole Coronal Hole
09/01 05:52:30 - 09/01 06:59:54	Fixed ( 900.0, 112.0)	AR 12720 obs.
09/01 07:03:00 - 09/01 07:47:00	Track ( 185.9, 736.7) <sup>Ⓢ 09/01 07:00:00</sup>	SST/IRIS CH/QS boundary

**PROG= 14 Inf.-time(s)**

- Subr= 1 10-time(s) 360.0sec
  - Seqn= 8 1-time(s) 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
  - Seqn= 6 1-time(s) 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
  - Seqn= 30 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

### XOB #1BEB: HOP327 - Al/poly (Be-Thin) - 128x128 - 1x1 - 4sec cad - Gband - 348x384 - 1ms - AEC1

Term	Pointing (x, y)	Comment
08/30 12:03:00 - 08/30 19:49:54	Fixed ( 952.0, 74.0)	HOP 355 with ALMA

**PROG= 10 Inf.-time(s)**

- Subr= 1 2-time(s) 2.0sec
  - Seqn= 90 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
- Subr= 2 250-time(s) 4.0sec
  - Seqn= 80 1-time(s) 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 128x128 (1064, 1048) Q=95 1 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

### XOB #1BD9: Synoptic Q95 2x2 - Al/mesh(64/512/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T

Term	Pointing (x, y)	Comment
08/30 19:53:00 - 08/30 19:59:54	Fixed ( 0.0, 0.0)	synoptic shifted 1h50m
08/31 06:11:00 - 08/31 06:17:54	Fixed ( 0.0, 0.0)	synoptic, shifted 8.0 min
08/31 17:58:30 - 08/31 18:05:24	Fixed ( 0.0, 0.0)	synoptic, shifted -4.5 min
09/01 05:42:30 - 09/01 05:49:24	Fixed ( 0.0, 0.0)	synoptic, shifted -20.5 min

**PROG= 03 1-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 36 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 99 1-time(s) 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 33 1-time(s) 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 23 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

### XOB #1C09: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
08/30 20:03:00 - 08/31 02:59:54	Fixed ( -18.0, 844.0)	HOP 206 North pole

**PROG= 04 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec



Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Flare mode

\* \* \* \* \*

#### XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
08/30 10:15:37 - 08/30 11:59:54	Track ( 193.6, 724.5) <sup>@ 08/30 09:57:00</sup>	# OP start + 10min / SST/IRIS CH/QS Boundary
08/30 12:03:00 - 08/30 19:49:54	Fixed ( 952.0, 74.0)	HOP 355 with ALMA
08/30 20:03:00 - 08/31 02:59:54	Fixed ( -18.0, 844.0)	HOP 206 North pole
08/31 03:03:00 - 08/31 06:07:54	Fixed ( 0.0, 0.0)	HOP 349
08/31 06:21:00 - 08/31 06:59:54	Fixed ( 900.0, 112.0)	AR 12720 obs.
08/31 07:03:00 - 08/31 13:59:54	Track ( 112.4, 733.0) <sup>@ 08/31 07:00:00</sup>	SST/IRIS CH QS boundary
08/31 14:51:01 - 08/31 17:30:30	Fixed ( 0.0, 0.0)	EIS Sensitivity Monitoring (QS Tracking near Disk Center)
08/31 18:08:30 - 08/31 23:59:30	Fixed ( -18.0, 844.0)	North Pole Coronal Hole for EIS
09/01 00:14:35 - 09/01 02:58:00	Fixed ( -16.0, -970.0)	South Pole Coronal Hole
09/01 03:20:06 - 09/01 05:39:24	Fixed ( 0.0, 0.0)	HOP 349
09/01 05:52:30 - 09/01 06:59:54	Fixed ( 900.0, 112.0)	AR 12720 obs.
09/01 07:03:00 - 09/01 07:47:00	Track ( 185.9, 736.7) <sup>@ 09/01 07:00:00</sup>	SST/IRIS CH/QS boundary

#### PROG= 13 30-time(s)

<b>Subr= 1 20-time(s) 2.0sec</b>												
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>												
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn=100 1-time(s) 10.0sec</b>												
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
└─ med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
└─ <b>Seqn= 10 1-time(s) 2.0sec</b>												
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>												
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn= 87 1-time(s) 2.0sec</b>												
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

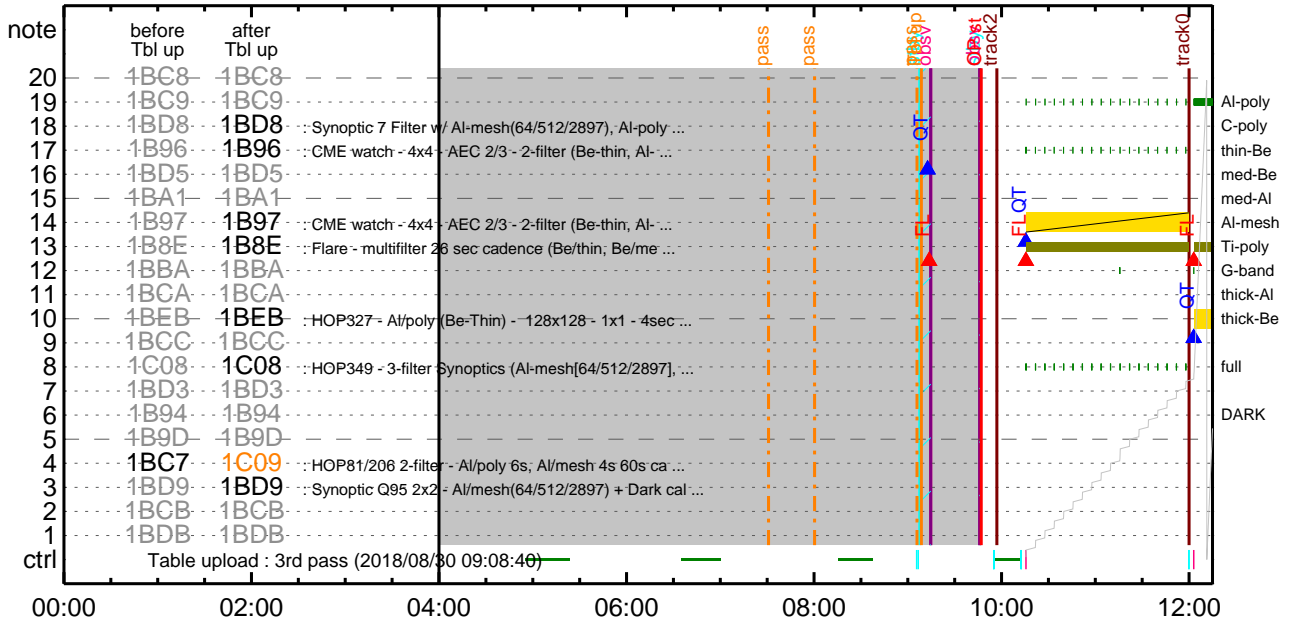
\* \* \* \* \*

### Flare Detection

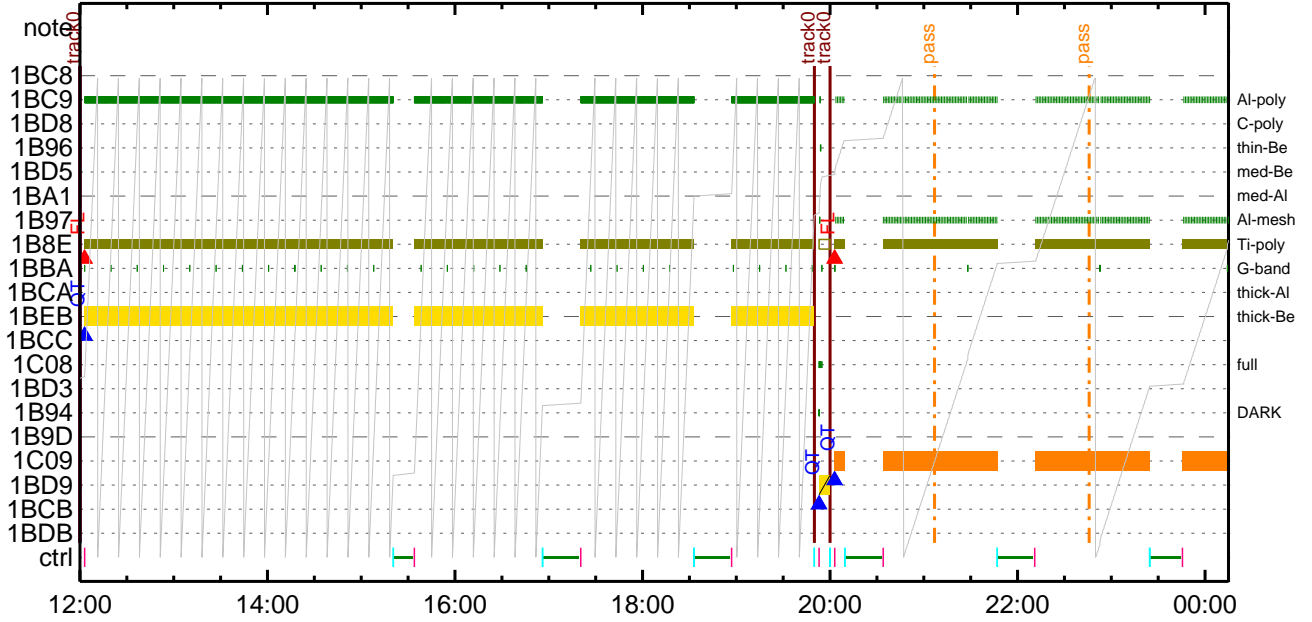
\* \* \* \* \*

FLD Patrol												
Term	Pointing (x, y)		Comment									
08/30 20:00:18 - 08/31 06:08:18	Fixed ( -18.0, 844.0)		HOP 206 North pole									
08/31 06:18:18 - 08/31 14:00:18	Fixed ( 900.0, 112.0)		AR 12720 obs.									
08/31 14:48:19 - 08/31 17:55:48	Fixed ( 0.0, 0.0)		EIS Sensitivity Monitoring (QS Tracking near Disk Center)									
08/31 18:05:48 - 09/01 05:39:48	Fixed ( -18.0, 844.0)		North Pole Coronal Hole for EIS									
09/01 05:49:48 - 09/04 10:32:00	Fixed ( 900.0, 112.0)		AR 12720 obs.									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		80sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

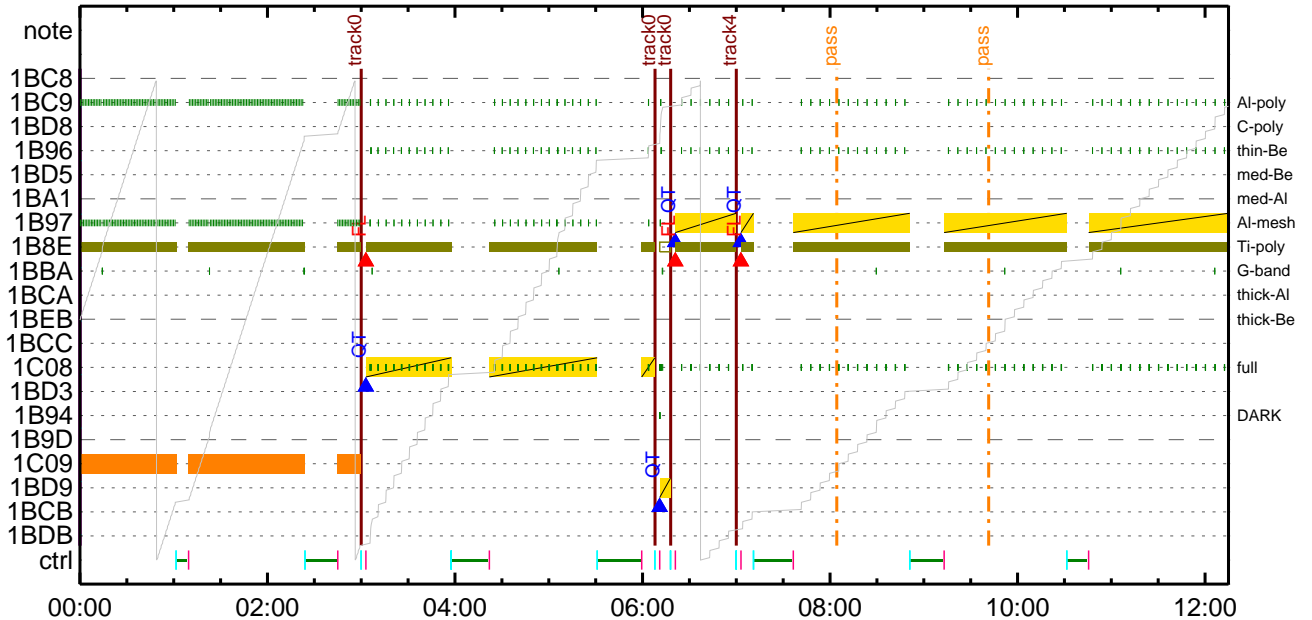
### CMDI #0727 2018/08/30



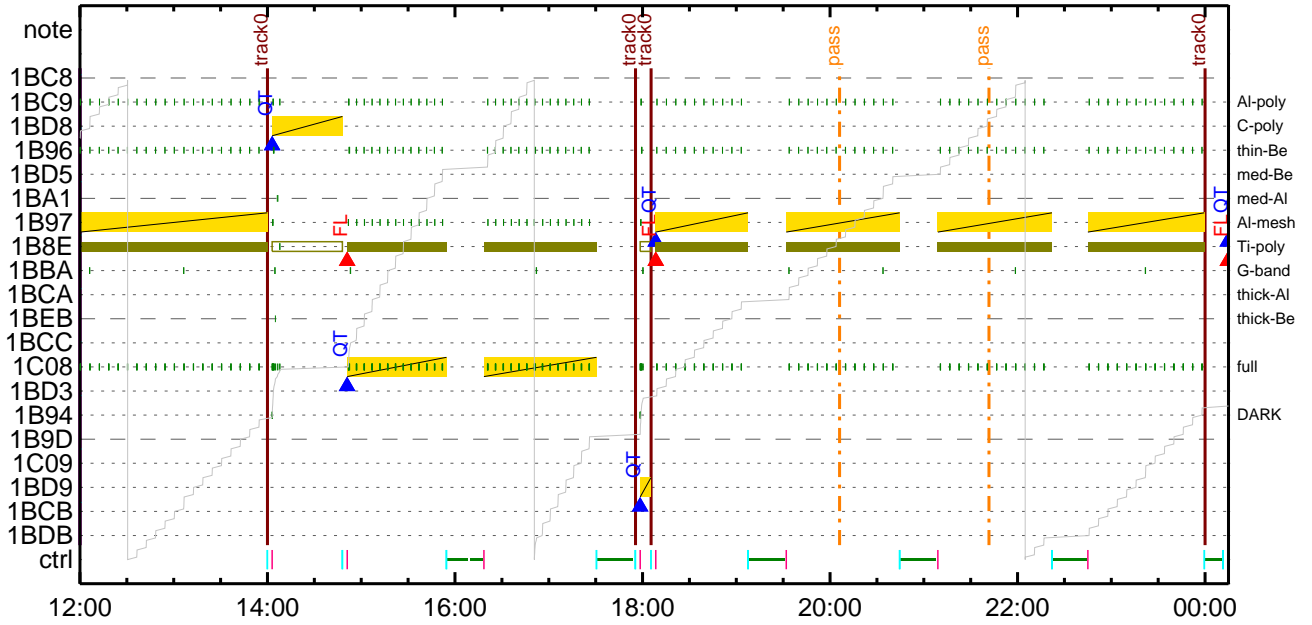
### CMDI #0727 2018/08/30



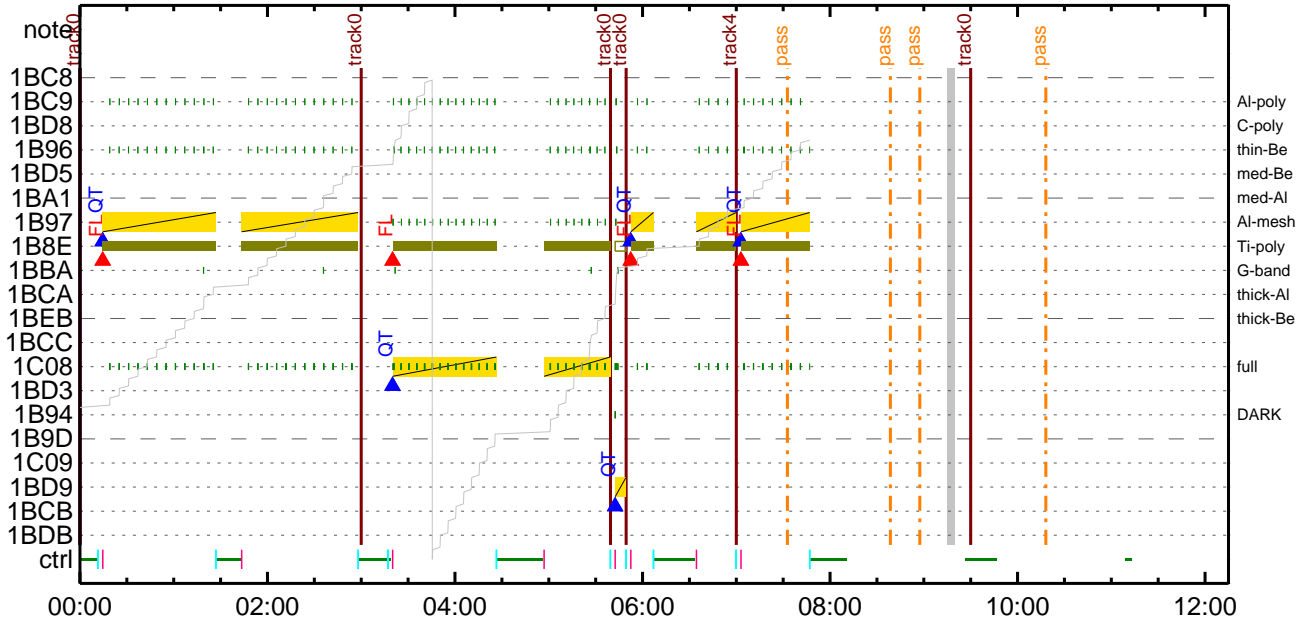
### CMDI #0727 2018/08/31



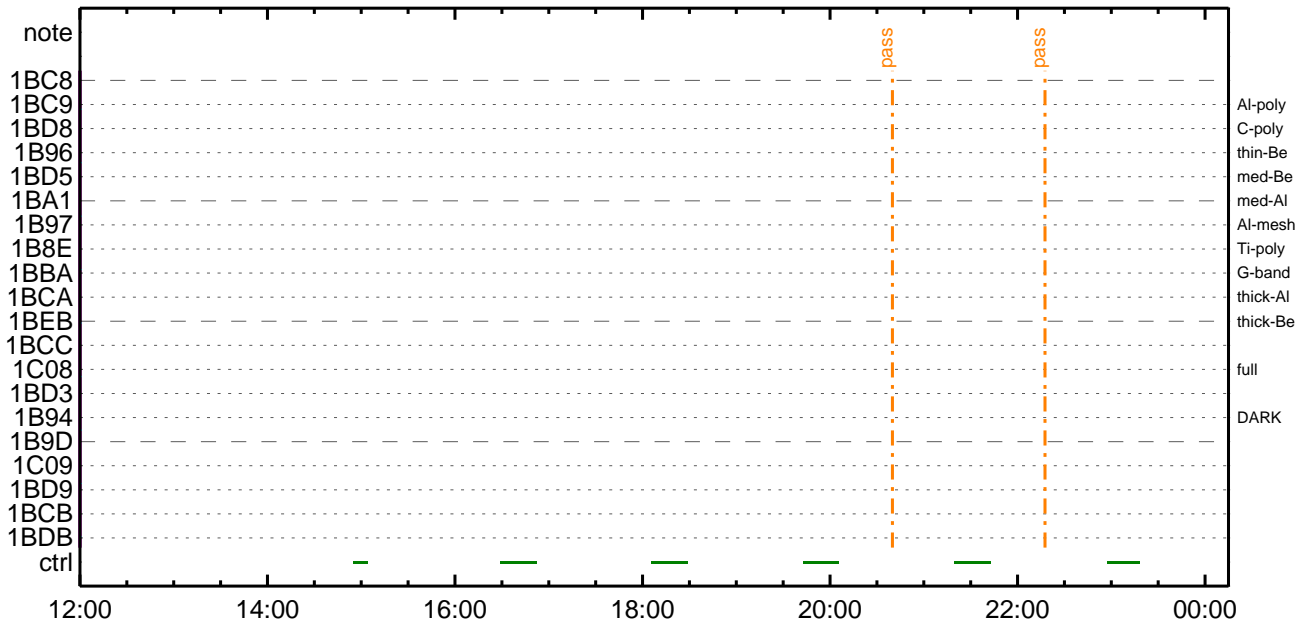
CMDI #0727 2018/08/31



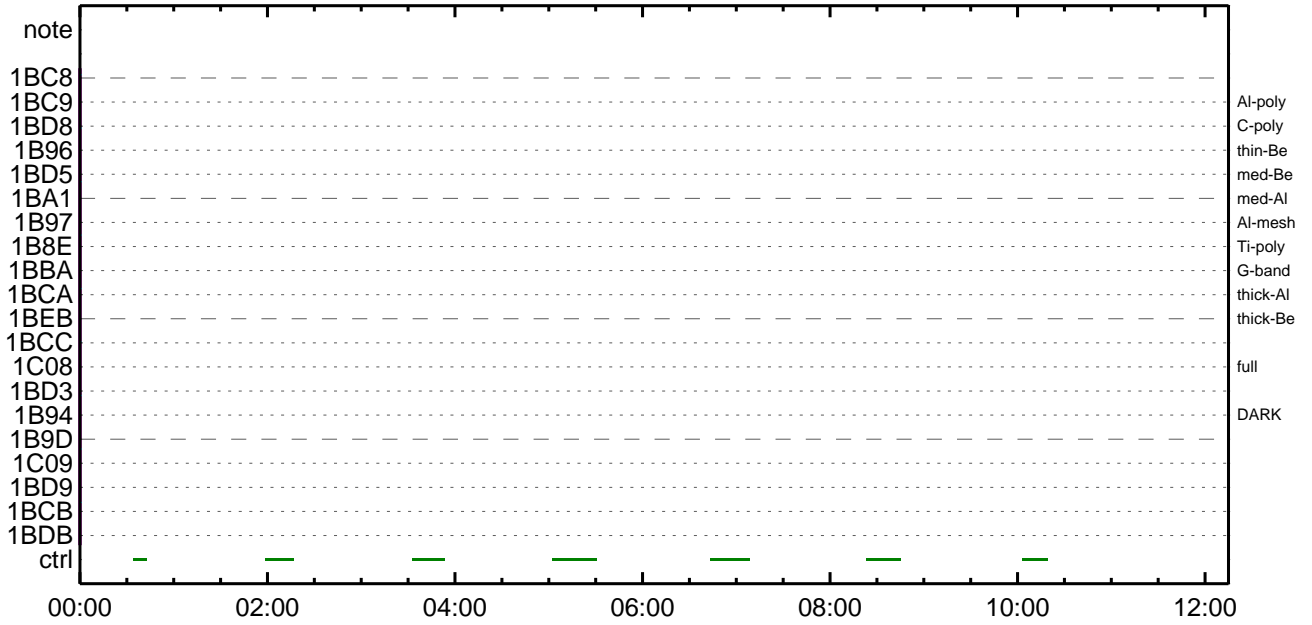
CMDI #0727 2018/09/01



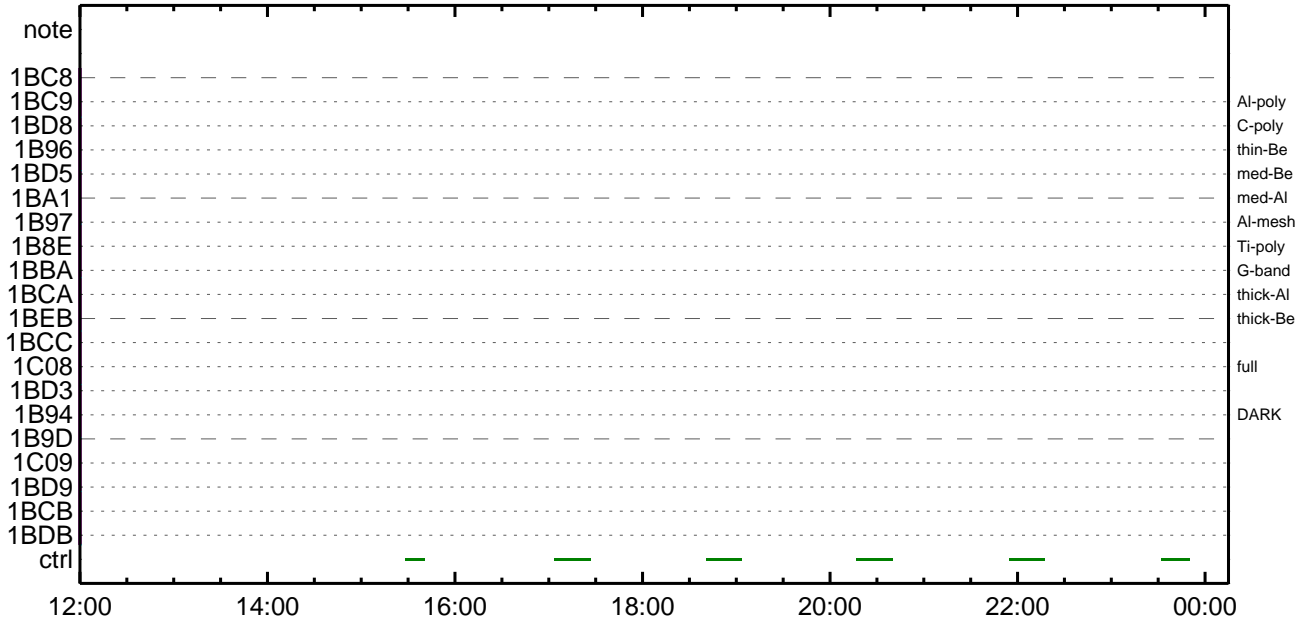
CMDI #0727 2018/09/01



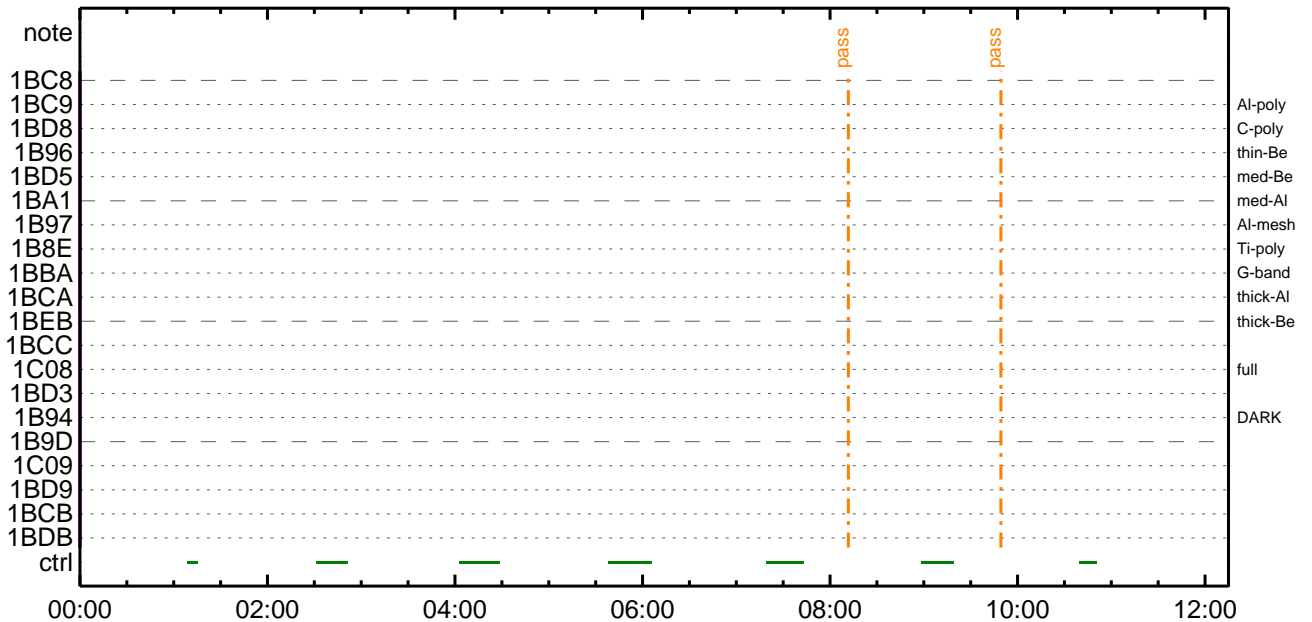
CMDI #0727 2018/09/02



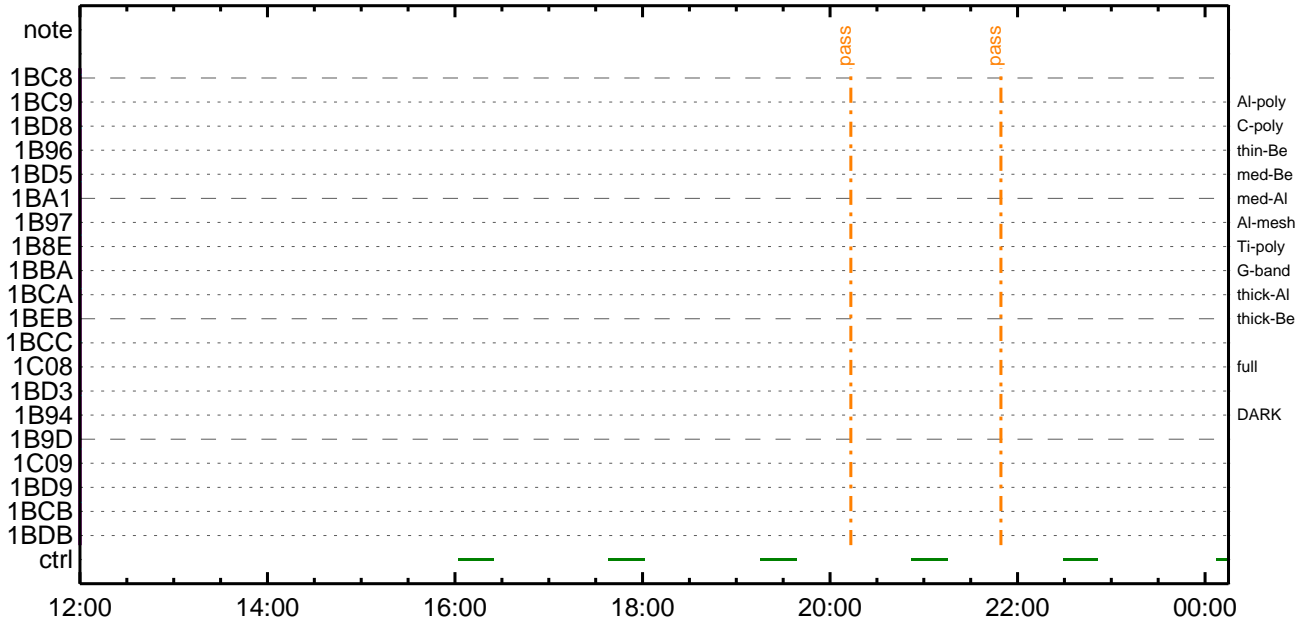
CMDI #0727 2018/09/02



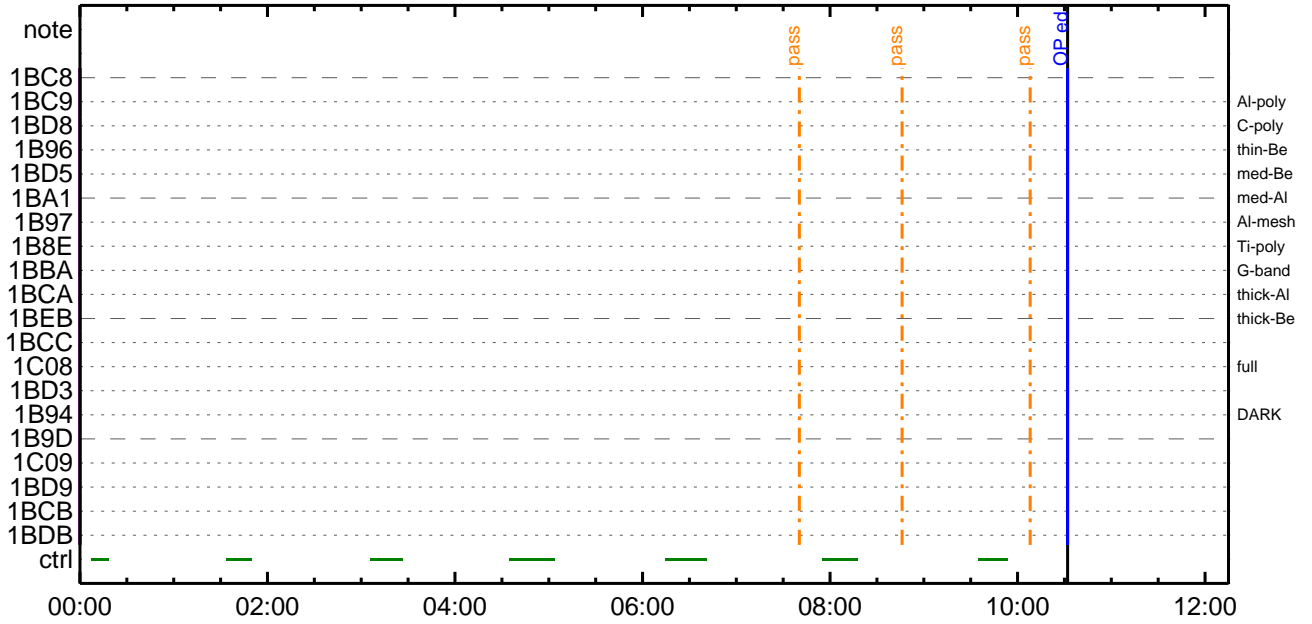
CMDI #0727 2018/09/03



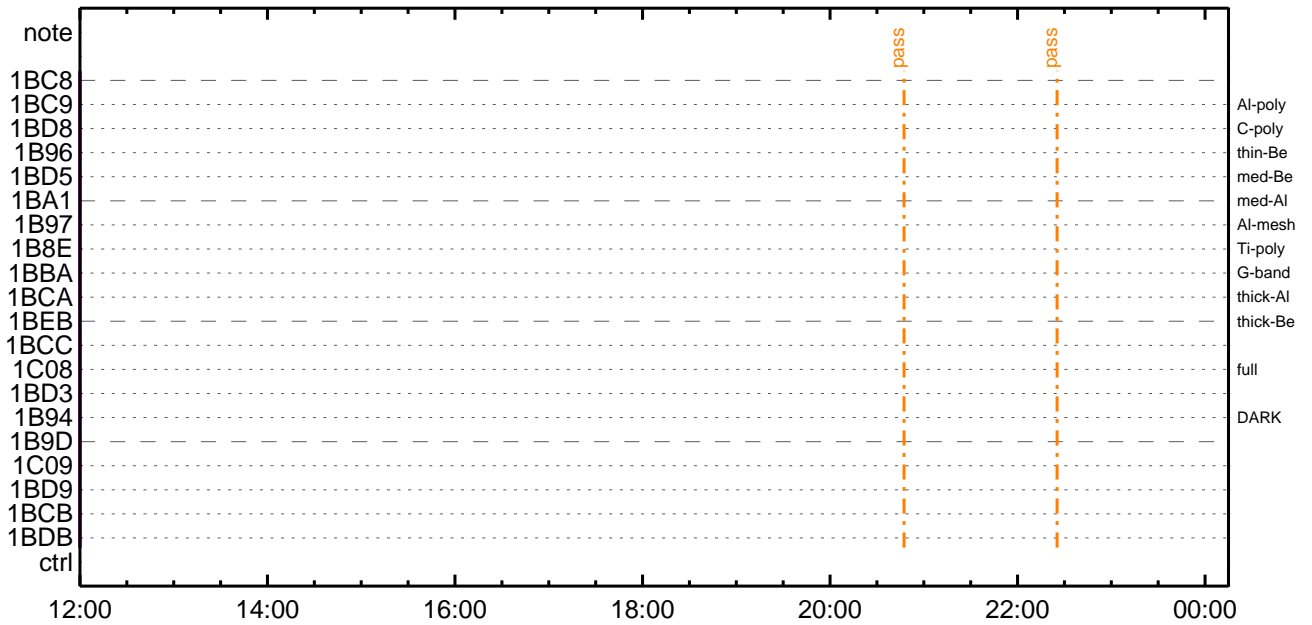
CMDI #0727 2018/09/03



CMDI #0727 2018/09/04



CMDI #0727 2018/09/04







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-600:OP
0104 ( )
0105 S. OG og-600:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ò³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî¼Ë¹ç•è² ïOKòð³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ò³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî¼Ë¹ç•è² ïOKòð³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ò³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî¼Ë¹ç•è² ïOKòð³ ÎÇ§
0165 C.
0166 C. ***** òË²¼ò Î¼Ã´ ¶Á°òËË¬ò°Á÷¿@ (%ãµ-YAYOXx½ª ê¼çòðÁÔËæç¼ª°¬òè¼i¹çòçòâ) *****
0167 C. DHUYã;4YE;Ë¼Y½;Yi;4YE;ËòðËã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |§ OPOG UPLOAD¬-Á÷¿@NGUî¼i¹ç;ç°Ë²¼ò ÎTI-CMDÁ÷¿@ãî¼Ë¹Ô°¬òËòò³òË;f
0180 C. òËò¿;çSETòËDUMPAË±ò¿îY¹ç¹Ô°¬òË³òË;f
0181 C.
0182 C. TIY³YpYóYËòðÁDË¿(UT)
0183 +. TI 2018-08-30 09:42:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2018-08-30 09:42:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2018-08-30 09:42:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

0194 C.  
0195 +. TI 2018-08-30 09:46:59.5  
0196 DC 01-B2 DHU\_OP\_START  
0197 C.                   çç[HK1\_TI\_CMD\_NUM]                   EQ       1COUNTUP  
0198 C.  
0199 C. °Ê²¼αîÄê%îíñαîŷÄŷ§ŷÄŷ-¹àîü  
0200 C.                   çç[HK1\_TI\_CMD\_ENA/DIS]                   EQ       ENA  
0201 C.                   çç[HK1\_TI\_CMD\_NUM]                   EQ       4  
0202 C.                   çç[HK1\_NEXT\_EXEC\_PIM]                   EQ       DHU  
0203 C.                   çç[HK1\_NEXT\_EXEC\_DC]                   EQ       0xB3  
0204 C.  
0205 C. \*\*\*\*\*  
0206 C. TIîŷ°èŷÄŷÖŷ×  
0207 C. \*\*\*\*\*  
0208 C.  
0209 C. TI\_TBL(0x03AB00-0x03AEFF;§ 1024byte)  
0210 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0211 BC                   (03 ab 03 01 02)  
0212 C.                   çç[HK1\_DMP\_TOP\_ADRS\_1]                   EQ       07  
0213 C.                   çç[HK1\_DMP\_TOP\_ADRS\_0]                   EQ       2B  
0214 C.                   çç[HK1\_DMP\_BLOCK\_NUM]                   EQ       3  
0215 C.                   çç[HK1\_DMP\_REPEAT\_NUM]                   EQ       0  
0216 C.                   çç[HK1\_DMA\_DMP\_PIM]                   EQ       DHU  
0217 +. DC 01-22 DHU\_MODE\_CHNG  
0218 BC                   (07 0b f8)  
0219 C.                   çç[HK1\_PKT\_FORM\_NO]                   EQ       7  
0220 C.                   çç[HK1\_PKT\_GEN\_TIME]                   EQ       0.25 s  
0221 C.                   çç[HK1\_S\_TLM\_BIT\_RATE]                   EQ       32k  
0222 C.                   çç[HK1\_X\_TLM\_BIT\_RATE]                   EQ       4M  
0223 C.                   çç[HK1\_DMP\_CHK\_FLG]                   EQ       EXEC  
0224 C.  
0225 C. ŷÄŷÖŷ×½ªî»αò³îÇ§  
0226 C.                   çç[HK1\_DMP\_CHK\_FLG]                   EQ       NON  
0227 C.  
0228 C. RAM ID=TI\_TBLαîŷ°è¹ç·è²îOKαò³îÇ§  
0229 C.  
0230 C. DHUŷâ;¼ŷÉ;Ê¼ŷ¼. ŷî;¼ŷÉ;Êαòîäα¹  
0231 +. DC 01-22 DHU\_MODE\_CHNG  
0232 BC                   (02 0a f8)  
0233 C.                   çç[HK1\_PKT\_FORM\_NO]                   EQ       2  
0234 C.                   çç[HK1\_PKT\_GEN\_TIME]                   EQ       0.5S  
0235 C.                   çç[HK1\_S\_TLM\_BIT\_RATE]                   EQ       32K  
0236 C.                   çç[HK1\_X\_TLM\_BIT\_RATE]                   EQ       4M  
0237 C.  
0238 C. \*\*\*\*\*  
0239 C. SOT TI command set  
0240 C. \*\*\*\*\*  
0241 C. Execute, after the success of OP upload.  
0242 +. TI 2018-08-30 09:46:16.0  
0243 DC 07-F0 MDP\_SOT\_MODE\_STBY  
0244 BC                   (41)  
0245 C. -----  
0246 C.       HK1\_TI\_CMD\_NUM                   = 1 CNTUP [ ]  
0247 C. -----  
0248 C. \*\*\*\*\* SOT END \*\*\*\*\*  
0249 C. Stop EIS observation and temporarily disable EIS mode changes  
0250 C.  
0251 C.  
0252 C. \*\*\*\*\* Start EIS operation (TI set) \*\*\*\*\*  
0253 C. Execute, after the success of OP upload.  
0254 C. Set EIS TI-commands  
0255 +. TI 2018-08-30 09:46:30.0  
0256 DC 07-FC EIS\_MODE\_MANU  
0257 BC                   (21 02)  
0258 +. TI 2018-08-30 09:46:40.0  
0259 DC 07-FC EIS\_MODE\_CHG\_DIS  
0260 BC                   (22)  
0261 C.                   [    ] [HK1\_TI\_CMD\_NUM]                   EQ       2 COUNTUP  
0262 C. \*\*\*\*\* End EIS operation (TI set) \*\*\*\*\*  
0263 C.  
0264 C.  
0265 C.  
0266 C. \*\*\*\*\* XRT START \*\*\*\*\*  
0267 C. Execute, after the success of OP upload.  
0268 +. TI 2018-08-30 09:46:00.0  
0269 DC 07-F0 MDP\_XRT\_MODE\_STBY  
0270 BC                   (c3)  
0271 C.                   [    ] [HK1\_TI\_CMD\_NUM]                   EQ       1COUNTUP  
0272 C.  
0273 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0274 C.  
0275 C. \*\*\*\*\* MDP ´ûÃîαî»ö¼ŷ°èÄÐα¹αèDCBC·x²è \*\*\*\*\*  
0276 C. (¼ª°îŷÖŷÄŷÉŷŷŷÉŷáŷçŷèè¼αα¼Ä»Ûα¹αè)  
0277 C. S. DC-BC dcbc-402:DCBC  
0278 C. (MDP\_known\_event)  
0279 C.  
0280 C.  
0281 C. \*\*\*\*\* ŷÐŷ¹·î Daily±çîñαè'Øα¹αèDCBC·x²è \*\*\*\*\*  
0282 C. S. DC-BC dcbc-153:DCBC  
0283 C. (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0284 C.  
0285 C.  
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã  
0287 C.  
0288 C. \*\*\*\*\* LOS \*\*\*\*\*  
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-601 2018-08-30 13:54:50 85 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿Àß•µ°Æ»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈßÈ¼°ÇÔß•¿¿¼í¹ç¿Í;çÁ®, ù¿¹¿ßßßßçÁ+¿®ß•¿Èßßß³ßÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_CHG_ENA
0047 BC (20)
0048 . C. Verify EIS_MODE_CHG_FLG is ENA
0049 +. DC 07-FC EIS_MODE_MANU
0050 BC (21 02)
0051 . C. Verify EIS in MANUAL mode
0052 . C. Estimated OBSTBL upload time is 31s
0053 C. *****
0054 C. EIS START OBSTBL LOAD
0055 C. *****
0056 . S. RAM ram-820:EIS_OBSTBL
0057 ( )
0058 +. DC 07-FC EIS_DUMP_OBSTBL
0059 BC (07 07 07 00 00 70 00)
0060 C.
0061 C. Execute, after the success of OBSTBL upload.
0062 C. Set EIS TI-commands
0063 +. TI 2018-08-30 09:46:50.0
0064 DC 07-FC EIS_MODE_CHG_ENA
0065 BC (20)
0066 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0067 C. *****
0068 C. EIS END OBSTBL LOAD
0069 C. *****
0070 C.
0071 . C. ***** MDP `ûÁî¿î»ô¼Y¿ÈÁß¿¹¿ÈDCBC•×²è *****
0072 C. (¼á°íYÓYÁYÈYB¥YÉYáYçYÈ%¼¿¼¿¼Á»Û¿¹¿è)
0073 . S. DC-BC dcbc-402:DCBC
0074 (MDP_known_event)
0075 C.
0076 C.
0077 . C. ***** YD¥¹•İ Daily±¿İÑ¿È`Ø¿¹¿èDCBC•×²è *****
0078 . S. DC-BC dcbc-153:DCBC
0079 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080 C.
0081 C.
0082 . C. ;ãLOSÁY$YÁY~¼Á»Û;ã
0083 C.
0084 . C. ***** LOS *****
0085 C.
```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ( )
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 85 83 02 02)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 20 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 80 80 08 20)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_AEC_RESET
0149 BC (d0)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 11)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 0d)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2018-08-30 09:46:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 . C. *****
0170 C. SOT table upload
0171 C. *****
0172 . C. < Stop SP table >
0173 +. DC 07-F0 MDP_SP_CTRL_MANU
0174 BC (61)
0175 C. -----
0176 C. MDP_SP_CTRL_MODE = MANU [ ]
0177 C. -----
0178 C.
0179 . C. <Upload SP Observation Table>
0180 . S. RAM ram-283:MDP_OBS_S
0181 ( )
0182 C.
0183 . C. < Dump RAMID=MDP_OBS_S >
0184 +. DC 07-F0 MDP_DUMP_SPTBL
0185 BC (83 07 00 00 00 38 b8)
0186 C. -----
0187 C. MDP_OBS_S verify = OK/NG [ ]
0188 C. -----
0189 C.
0190 C. *****
0191 C. SOT TI command set
0192 C. *****
0193 C. Execute, after the success of TBL upload.

```

```
0194 +. TI 2018-08-30 09:46:18.0
0195 DC 07-F0 MDP_SOT_MODE_OBSV
0196 BC (40)
0197 . C. -----
0198 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0199 C. -----
0200 C.
0201 C.
0202 . C. ***** MDP 'úÃîñî»ô¼ÝðËÄðñ¹ñèDCBC•x²è *****
0203 C. (¼ã°îÝÓÝÄÝËÝÞÝËÝáÝçÝèñ¼ñ¼Ä»Ûñ¹ñè)
0204 . S. DC-BC dcbc-402:DCBC
0205 (MDP_known_event)
0206 C.
0207 C.
0208 . C. ***** ÝÐÝ¹•İ Daily±¿İÑñË´Øñ¹ñèDCBC•x²è *****
0209 . S. DC-BC dcbc-153:DCBC
0210 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0211 C.
0212 C.
0213 . C. ;ãLOSÝÁÝ§ÝÃÝ¬¼Ä»Û;ã
0214 C.
0215 . C. ***** LOS *****
0216 C.
```

Aug 30, 18 13:55

XRT\_OGLIST\_0727.chk

Page 1/8

\*\*\* OP Sequence for XRT \*\*\*

```

2018/08/30 09:55:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 09:55:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 09:55:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/30 09:55:06.0 XRT_PREFLR_STRT_437_OG [0x1b5]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/08/30 09:57:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 02 04 fd 01 99
2018/08/30 09:58:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/08/30 10:12:31.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 10:12:33.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 10:12:35.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2018/08/30 10:12:55.0 XRT_FLD_ENA_405_OG [0x195]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2018/08/30 10:15:25.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2018/08/30 10:15:27.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2018/08/30 10:15:29.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2018/08/30 10:15:31.0 XRT_FLD_RESET_420_OG [0x1a4]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/30 10:15:33.0 XRT_QT_PROG_SET_433_OG [0x1b1]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0e
2018/08/30 10:15:35.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2018/08/30 10:15:37.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/08/30 11:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 11:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 11:59:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2018/08/30 12:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 f9 69 ab 65
2018/08/30 12:00:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2018/08/30 12:00:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2018/08/30 12:00:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2018/08/30 12:00:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2018/08/30 12:00:26.0 XRT_FLD_RESET_432_OG [0x1b0]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/30 12:02:56.0 XRT_QT_PROG_SET_426_OG [0x1aa]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0a
2018/08/30 12:02:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2018/08/30 12:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/08/30 15:20:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 15:20:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 15:20:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/30 15:20:36.0 XRT_PREFLR_STRT_437_OG [0x1b5]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/08/30 15:23:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/08/30 15:33:00.0 XRT_Custom_430_OG [0x1ae]
2018/08/30 15:34:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/08/30 16:56:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 16:56:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 16:56:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/30 16:56:06.0 XRT_PREFLR_STRT_437_OG [0x1b5]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2018/08/30 16:59:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2018/08/30 17:19:30.0 XRT_Custom_430_OG [0x1ae]
2018/08/30 17:20:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2018/08/30 18:33:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 18:33:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2018/08/30 18:33:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2018/08/30 18:33:06.0 XRT_PREFLR_STRT_437_OG [0x1b5]

```

2018/08/30	18:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2018/08/30	18:56:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/30	18:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/30	19:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/30	19:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/30	19:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/08/30	19:50:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/08/30	19:50:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/08/30	19:50:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/08/30	19:50:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/30	19:52:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2018/08/30	19:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/30	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/30	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/30	19:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2018/08/30	20:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 b4 fe 01 99	
2018/08/30	20:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/08/30	20:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/08/30	20:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/08/30	20:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/08/30	20:00:26.0	XRT_FLD_RESET_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/30	20:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2018/08/30	20:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/08/30	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/30	20:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/30	20:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/30	20:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/30	20:09:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/30	20:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/30	20:33:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/30	20:34:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/30	21:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/30	21:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/30	21:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/30	21:47:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/30	21:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/30	22:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/30	22:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/30	23:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/30	23:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/30	23:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/08/30	23:24:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/08/30	23:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/30	23:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/08/30	23:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/08/31	01:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/31	01:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/08/31	01:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	



2018/08/31	01:01:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/31	01:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/31	01:08:30.0	XRT_Custom_430_OG [0x1ae]						
2018/08/31	01:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/31	02:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	02:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	02:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/31	02:24:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/31	02:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/31	02:44:00.0	XRT_Custom_430_OG [0x1ae]						
2018/08/31	02:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/31	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	02:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2018/08/31	03:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2018/08/31	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/08/31	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/08/31	03:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/08/31	03:00:24.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/08/31	03:02:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/31	03:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2018/08/31	03:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2018/08/31	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/31	03:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	03:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	03:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/31	03:57:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/31	04:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/31	04:21:00.0	XRT_Custom_430_OG [0x1ae]						
2018/08/31	04:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/31	05:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	05:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	05:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/31	05:31:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/31	05:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/31	05:58:30.0	XRT_Custom_430_OG [0x1ae]						
2018/08/31	05:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/31	06:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	06:07:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	06:07:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2018/08/31	06:08:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2018/08/31	06:08:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2018/08/31	06:08:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2018/08/31	06:08:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/08/31	06:10:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2018/08/31	06:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/31	06:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	06:17:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	06:17:58.0	XRT_FOCUS_POSITION_406_OG [0x196]						

2018/08/31	06:18:00.0	AOCS_OrE-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	f6	0d	b0 00
2018/08/31	06:18:18.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2018/08/31	06:20:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2018/08/31	06:20:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2018/08/31	06:20:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2018/08/31	06:20:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/31	06:20:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2018/08/31	06:20:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2018/08/31	06:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/08/31	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/31	06:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/31	06:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2018/08/31	07:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	04	04	fd	01 99
2018/08/31	07:00:18.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2018/08/31	07:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2018/08/31	07:02:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2018/08/31	07:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2018/08/31	07:02:54.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/31	07:02:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e		
2018/08/31	07:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2018/08/31	07:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/08/31	07:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/31	07:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/31	07:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/31	07:11:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/31	07:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/31	07:35:30.0	XRT_Custom_430_OG [0x1ae]							
2018/08/31	07:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/08/31	08:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/31	08:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/31	08:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/31	08:51:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/31	08:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/31	09:12:00.0	XRT_Custom_430_OG [0x1ae]							
2018/08/31	09:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/08/31	10:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/31	10:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/31	10:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2018/08/31	10:31:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2018/08/31	10:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2018/08/31	10:44:30.0	XRT_Custom_430_OG [0x1ae]							
2018/08/31	10:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2018/08/31	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/31	13:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2018/08/31	13:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2018/08/31	14:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00 00
2018/08/31	14:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2018/08/31	14:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							

Aug 30, 18 13:55

## XRT\_OGLIST\_0727.chk

Page 5/8

2018/08/31	14:00:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2018/08/31	14:02:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/08/31	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2018/08/31	14:47:55.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/08/31	14:47:57.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/31	14:47:59.5	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/31	14:48:19.5	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/08/31	14:48:21.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2018/08/31	14:48:23.5	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/08/31	14:48:25.5	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_AEC_RESET	1	07-F0	d0
2018/08/31	14:50:55.5	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/08/31	14:50:57.5	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/31	14:50:59.5	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2018/08/31	14:51:01.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/08/31	15:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/08/31	15:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/31	15:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/31	15:54:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/31	15:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/31	16:17:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/31	16:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2018/08/31	17:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/08/31	17:30:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/31	17:30:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/31	17:30:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/31	17:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/08/31	17:55:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/08/31	17:55:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/31	17:55:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/31	17:55:30.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/08/31	17:55:48.0	XRT_FLD_DIS_409_OG [0x199]	AOCS_OrE-point_Start_3_OG [0x099]			
2018/08/31	17:55:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	AOCU_NM	5	02-76	00 00 00 00 00
2018/08/31	17:55:52.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_FLD_DIS	1	07-F0	d9
2018/08/31	17:58:28.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2018/08/31	17:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/08/31	18:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2018/08/31	18:05:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/08/31	18:05:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/31	18:05:30.0	AOCS_OrE-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/08/31	18:05:48.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/08/31	18:05:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	5	02-76	00 b4 fe 01 99
2018/08/31	18:05:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLD_ENA	1	07-F0	d8
2018/08/31	18:05:54.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/08/31	18:08:24.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_AEC_RESET	1	07-F0	d0
2018/08/31	18:08:26.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2018/08/31	18:08:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2018/08/31	18:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2018/08/31	18:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d

2018/08/31	19:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/31	19:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	19:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	19:07:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/31	19:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/31	19:31:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/31	19:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/31	20:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	20:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	20:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/31	20:44:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/31	20:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/31	21:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/31	21:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	22:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	22:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	22:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/31	22:22:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/08/31	22:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/08/31	22:44:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/08/31	22:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	23:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	23:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/08/31	23:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/08/31	23:59:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/09/01	00:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/09/01	00:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	AOCU_NM	5	02-76	00 56 35 01 68		
2018/09/01	00:11:29.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/09/01	00:11:31.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/01	00:11:33.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/01	00:11:53.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2018/09/01	00:11:55.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/09/01	00:11:57.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/09/01	00:11:59.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/09/01	00:14:29.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/09/01	00:14:31.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/09/01	00:14:33.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2018/09/01	00:14:35.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2018/09/01	01:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/09/01	01:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/01	01:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/01	01:27:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da		
2018/09/01	01:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/09/01	01:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/09/01	01:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/09/01	02:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/01	02:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/01	02:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		

Aug 30, 18 13:55

## XRT\_OGLIST\_0727.chk

Page 7/8

2018/09/01	02:58:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/09/01	03:00:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/09/01	03:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/09/01	03:17:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/01	03:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/01	03:17:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/09/01	03:17:24.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/09/01	03:17:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/09/01	03:17:28.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/09/01	03:17:30.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/09/01	03:20:00.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/01	03:20:02.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2018/09/01	03:20:04.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/09/01	03:20:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/01	04:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/01	04:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/01	04:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/01	04:26:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/09/01	04:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/09/01	04:56:00.0	XRT_Custom_430_OG [0x1ae]					
2018/09/01	04:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/01	05:39:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/01	05:39:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/01	05:39:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/09/01	05:39:30.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/09/01	05:39:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/09/01	05:39:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/09/01	05:39:52.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/09/01	05:42:28.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2018/09/01	05:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/01	05:49:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/01	05:49:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/01	05:49:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/09/01	05:49:30.0	AOCs_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 f6 0d b0 00	
2018/09/01	05:49:48.0	XRT_FLD_ENA_405_OG [0x195]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/09/01	05:52:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/09/01	05:52:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/09/01	05:52:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/09/01	05:52:24.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/01	05:52:26.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2018/09/01	05:52:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/09/01	05:52:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/01	06:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/01	06:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/01	06:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/01	06:07:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/09/01	06:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

Aug 30, 18 13:55

## XRT\_OGLIST\_0727.chk

Page 8/8

2018/09/01	06:33:30.0	XRT_Custom_430_OG [0x1ae]			
2018/09/01	06:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/09/01	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/09/01	06:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/09/01	06:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2018/09/01	07:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	04 04 fd 01 99
2018/09/01	07:00:18.0	XRT_FLD_ENA_405_OG [0x195]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2018/09/01	07:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2018/09/01	07:02:50.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2018/09/01	07:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2018/09/01	07:02:54.0	XRT_FLD_RESET_420_OG [0x1a4]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2018/09/01	07:02:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2018/09/01	07:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2018/09/01	07:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2018/09/01	07:47:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/09/01	07:47:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2018/09/01	07:47:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2018/09/01	07:47:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2018/09/01	07:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2018/09/01	09:30:00.0	AOCS_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00