

# XRT Timeline to be uploaded on 2018/09/08

Period: 2018/09/08 11:15:00 - 2018/09/13 09:50:00

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## Normal mode

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XOB #1BE9: HOP81/206 2-filter - Al/poly 16s, Al/mesh 12s 30s cadence, G-band - 384x384 1ms													
Term	Pointing (x, y)							Comment					
09/08 11:28:00 - 09/08 19:29:54	Fixed ( -20.0, 906.0)							# OP start + 10min HOP 81 N-pole					
<b>PROG= 11 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 16 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec													
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 90 1-time(s) 30.0sec													
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec													
└─ Subr= 3 60-time(s) 2.0sec													
└─ Seqn= 39 1-time(s) 30.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 11.3s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec													
└─ Al-poly/Open Al-poly/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec													
└─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #1BD9: Synoptic Q95 2x2 - Al/mesh(64/512/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T													
Term	Pointing (x, y)							Comment					
09/08 19:33:00 - 09/08 19:39:54	Fixed ( 0.0, 0.0)							synoptic, shifted -1.5 hours					
<b>PROG= 03 1-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 5 1-time(s) 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec													
└─ Seqn= 36 1-time(s) 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 99 1-time(s) 2.0sec													
└─ Al-poly/Open Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 33 1-time(s) 2.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ thin-Be/Open thin-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Seqn= 23 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
└─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #1BD4: AR (Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 30s cad-2													
Term	Pointing (x, y)							Comment					
09/08 19:43:00 - 09/09 02:46:00	Track ( 461.8, -87.2) <sup>09/08 19:40:00</sup>							AR observations					
<b>PROG= 05 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec													
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec													
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec													
└─ Seqn= 71 3-time(s) 2.0sec													
└─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 3 0 2.0sec													
└─ Subr= 2 100-time(s) 30.0sec													
└─ Seqn= 89 1-time(s) 15.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec													
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec													
└─ Seqn= 58 1-time(s) 12.0sec													
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec													
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec													
└─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #1C0B: HOP349 - 3-filter Synoptics (Al-mesh[64/512/2897], Al-poly[45/512/4096], thin-Be[1024/11571/23142] with 512x512 G-band 1ms+Leak - 90min c													
Term	Pointing (x, y)							Comment					
09/09 03:06:30 - 09/09 06:23:54	Fixed ( 0.0, 0.0)							HOP 349					
<b>PROG= 01 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 300.0sec													
└─ Seqn= 36 1-time(s) 2.0sec													
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 99 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 33 1-time(s) 2.0sec</b>												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 30 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 30-time(s) 120.0sec</b>												
<b>Seqn= 8 1-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 29 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1BD8: Synoptic 7 Filter w/ Al-mesh(64/512/2897), Al-poly(45/512/4096), Thin-Be(1024/11571/23142) - Thick-Be(65536), Al-poly+Ti-poly(512/8192), Med**

Term	Pointing (x, y)	Comment
09/09 06:31:00 - 09/09 06:41:00	Fixed ( 0.0, 0.0)	HOP 349
<b>PROG= 18 1-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 5 1-time(s) 2.0sec</b>		
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
<b>Seqn= 36 1-time(s) 2.0sec</b>		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 99 1-time(s) 2.0sec</b>		
Al-poly/Open	Al-poly/Open	close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 33 1-time(s) 2.0sec</b>		
thin-Be/Open	thin-Be/Open	close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 23 1-time(s) 4.0sec</b>		
Open/G-band	Open/G-band	open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 46 1-time(s) 2.0sec</b>		
Open/thick-Be	Open/thick-Be	close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 17 1-time(s) 2.0sec</b>		
med-Al/Open	med-Al/Open	close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
med-Al/Open	med-Al/Open	close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
<b>Seqn= 25 1-time(s) 2.0sec</b>		
Al-poly/Ti-poly	Al-poly/thick-Al	close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512**

Term	Pointing (x, y)	Comment
09/08 11:28:00 - 09/08 19:29:54	Fixed ( -20.0, 906.0)	# OP start + 10min HOP 81 N-pole
09/08 19:43:00 - 09/09 02:46:00	Track ( 461.8, -87.2) @ 09/08 19:40:00	AR observations
09/09 03:06:30 - 09/09 06:23:54	Fixed ( 0.0, 0.0)	HOP 349
<b>PROG= 13 30-time(s)</b>		
<b>Subr= 1 20-time(s) 2.0sec</b>		
<b>Seqn= 11 1-time(s) 2.0sec</b>		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn=100 1-time(s) 10.0sec</b>		
thin-Be/Open	med-Be/Open	close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		

<b>Seqn= 10</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
<b>Seqn= 11</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
<b>Seqn= 87</b>		<b>1-time(s)</b>		<b>2.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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### Active Region Search

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NOT USED

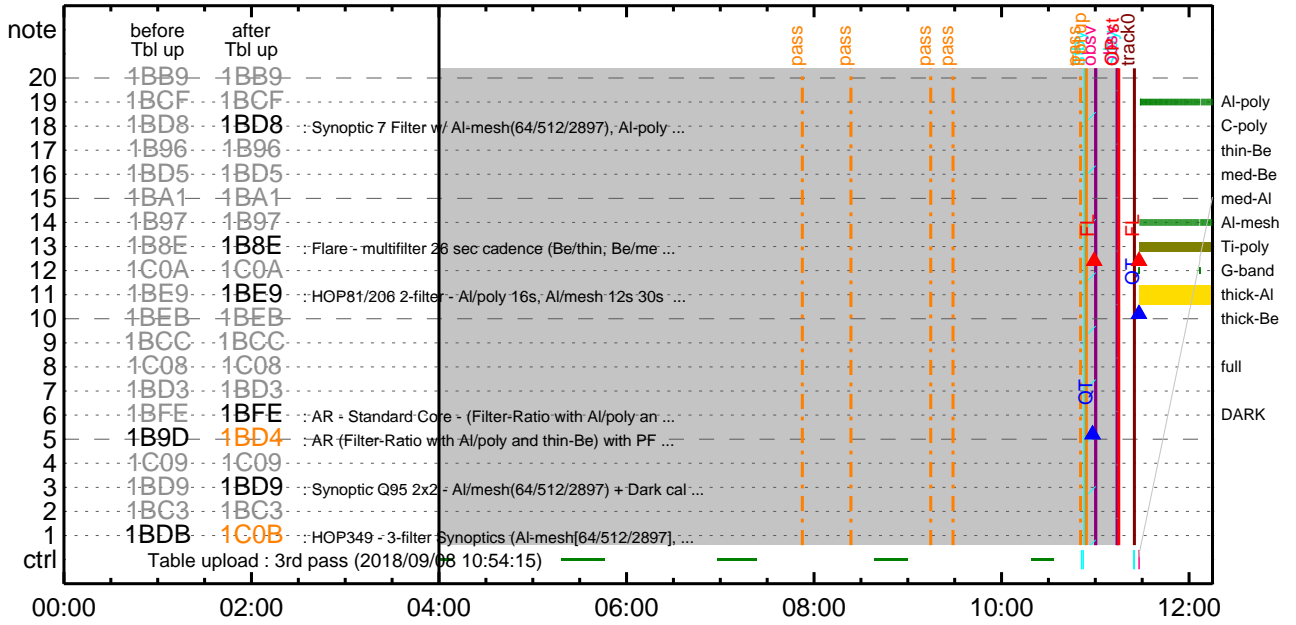
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### Flare Detection

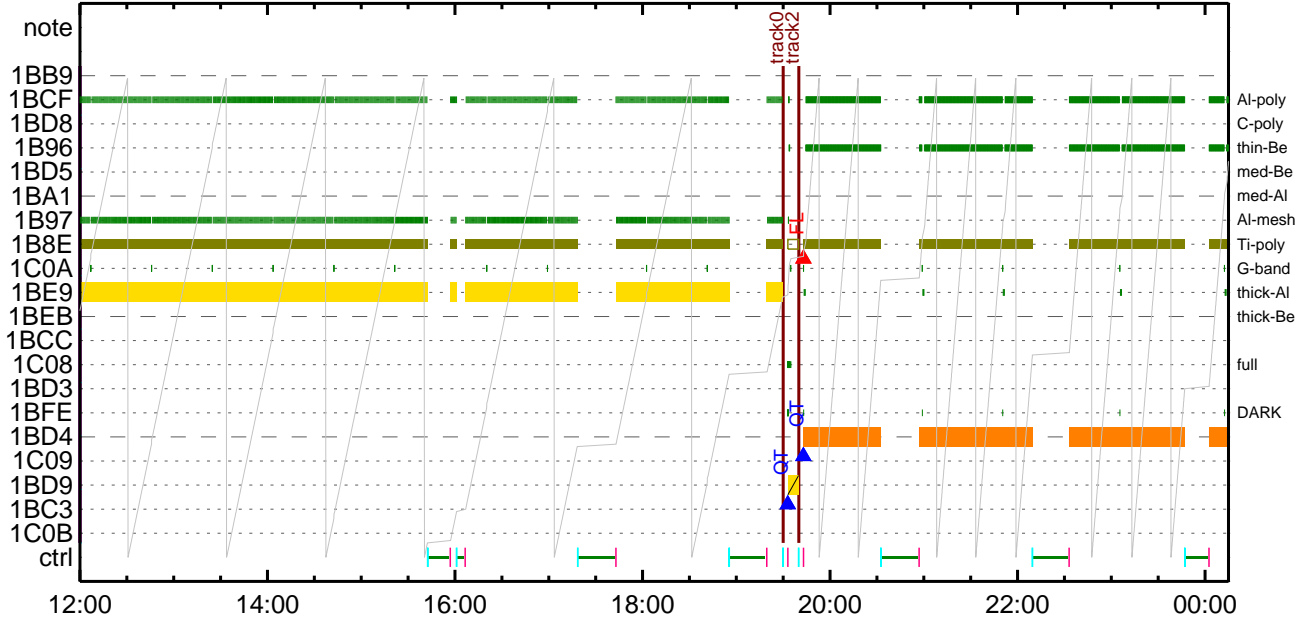
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<b>FLD Patrol</b>											
Term		Pointing (x, y)						Comment			
09/08 19:40:18 - 09/09 06:28:18		Track ( 461.8, -87.2) <sup>® 09/08 19:40:00</sup>						AR observations			
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

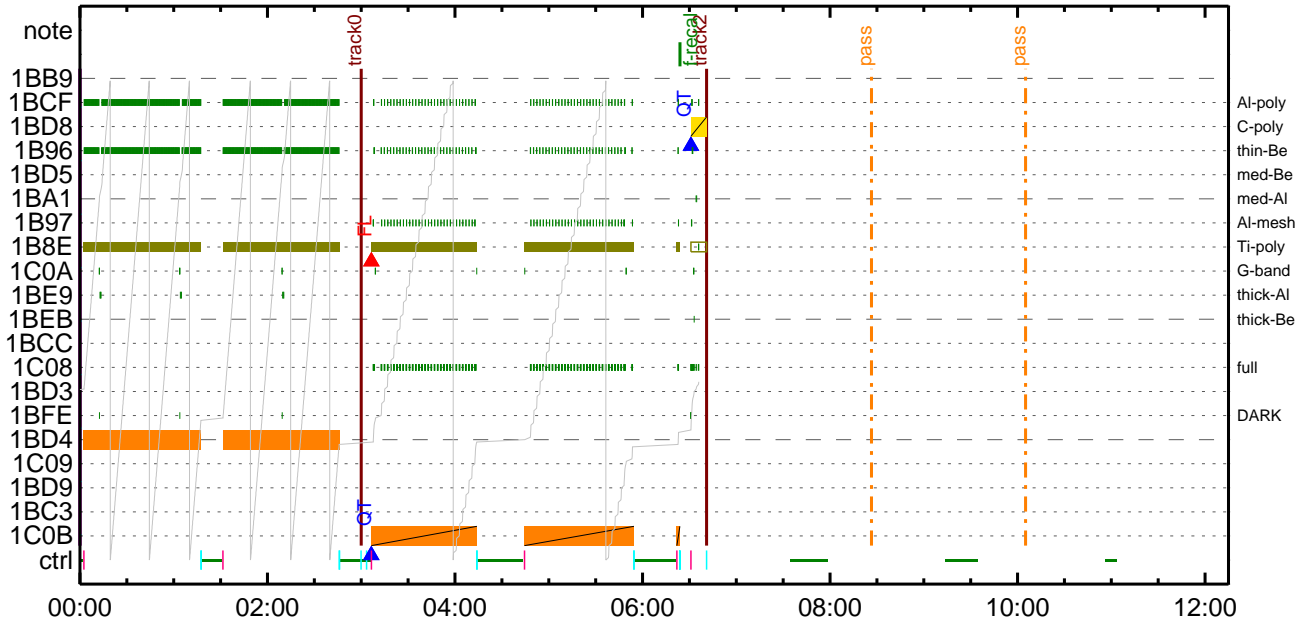
### CMDI #0751 2018/09/08



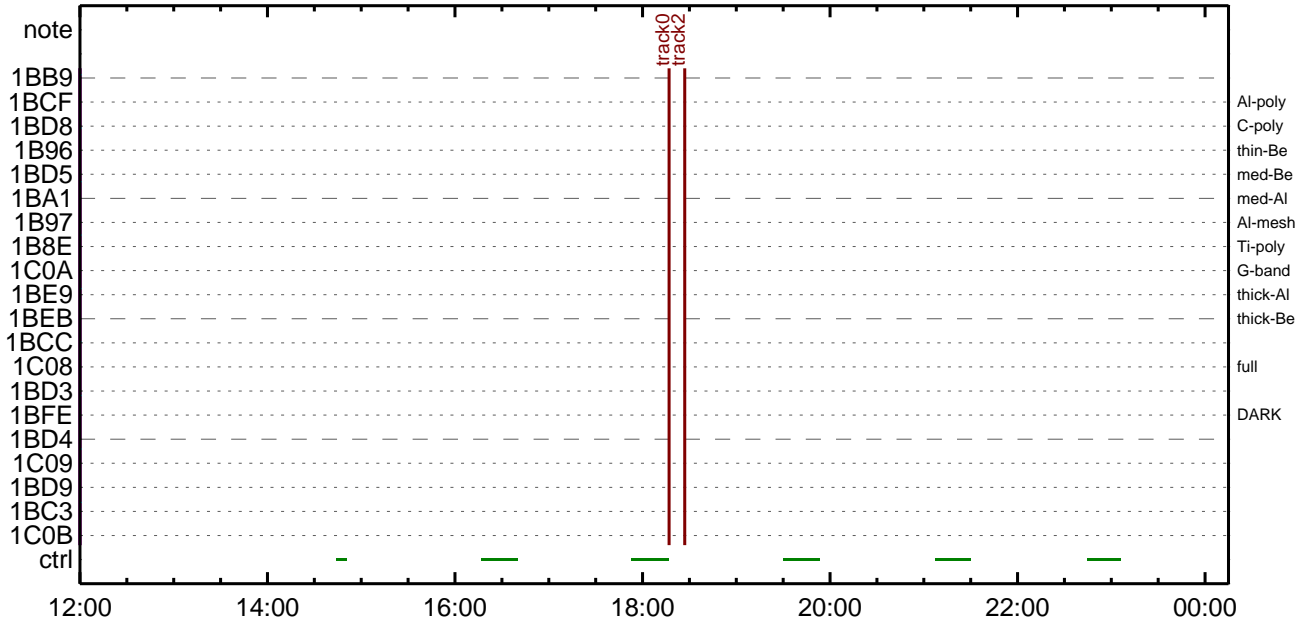
### CMDI #0751 2018/09/08



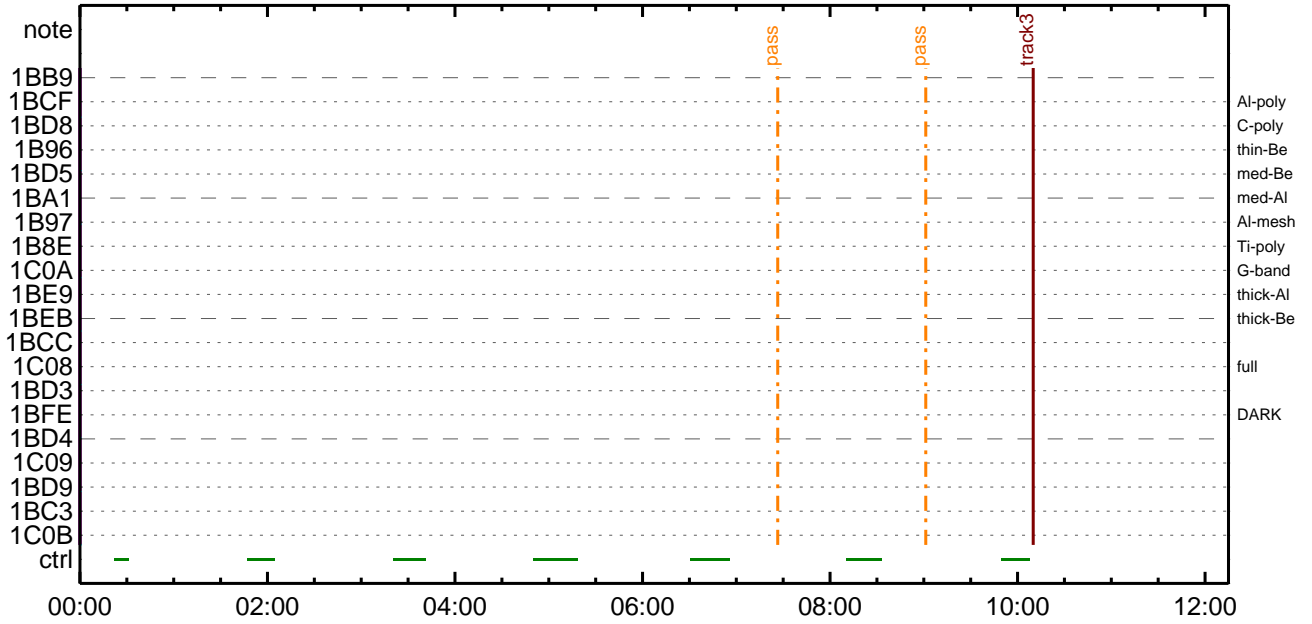
### CMDI #0751 2018/09/09



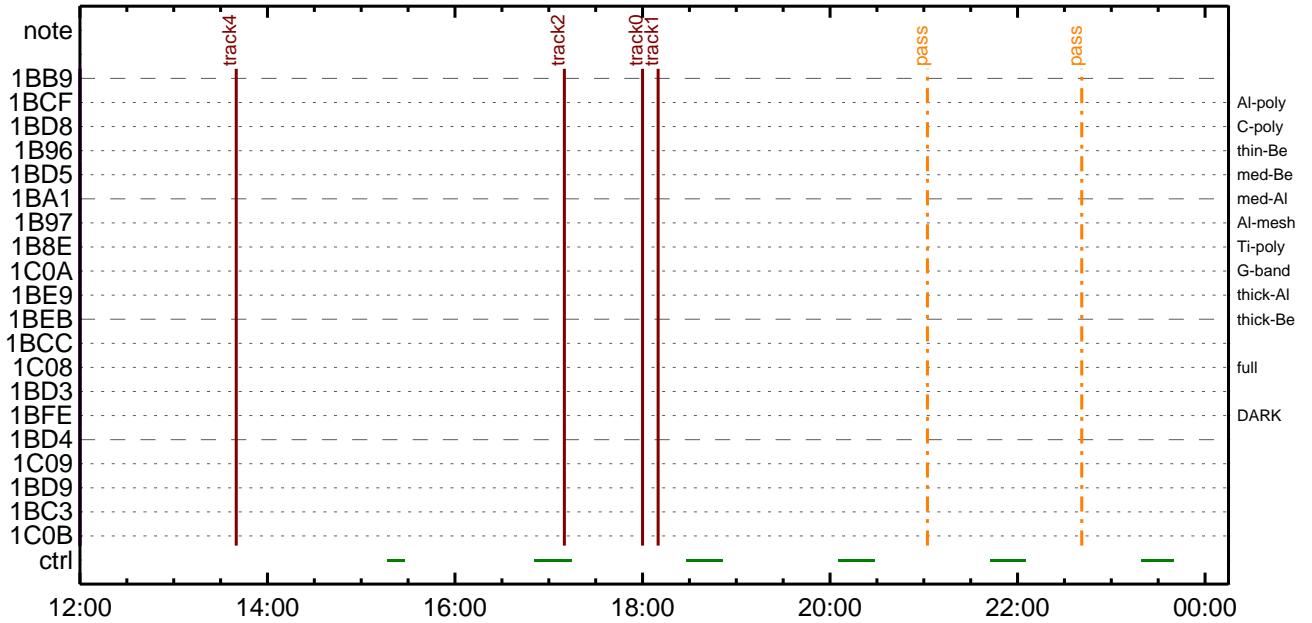
CMDI #0751 2018/09/09



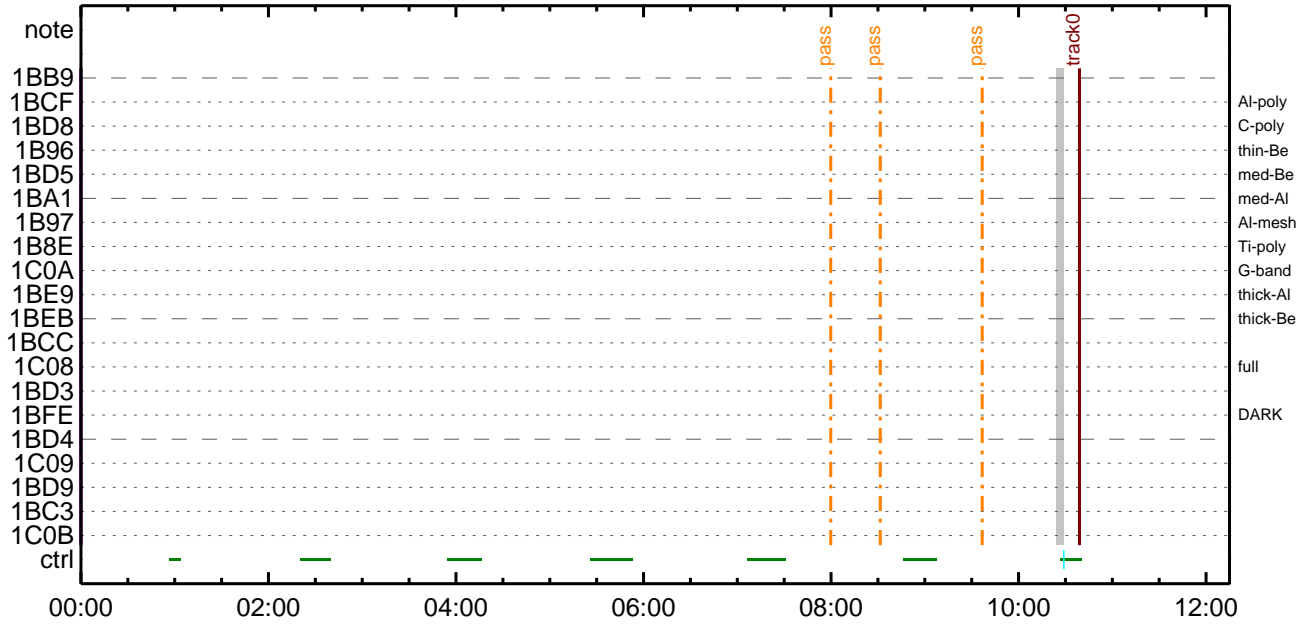
CMDI #0751 2018/09/10



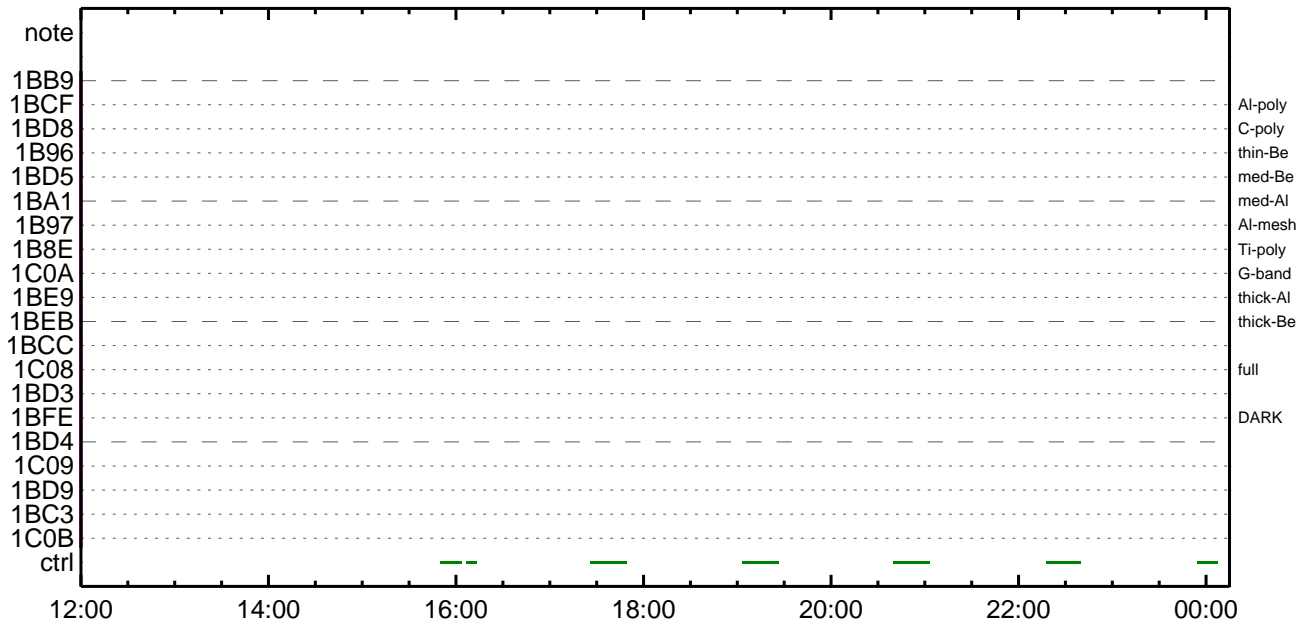
CMDI #0751 2018/09/10



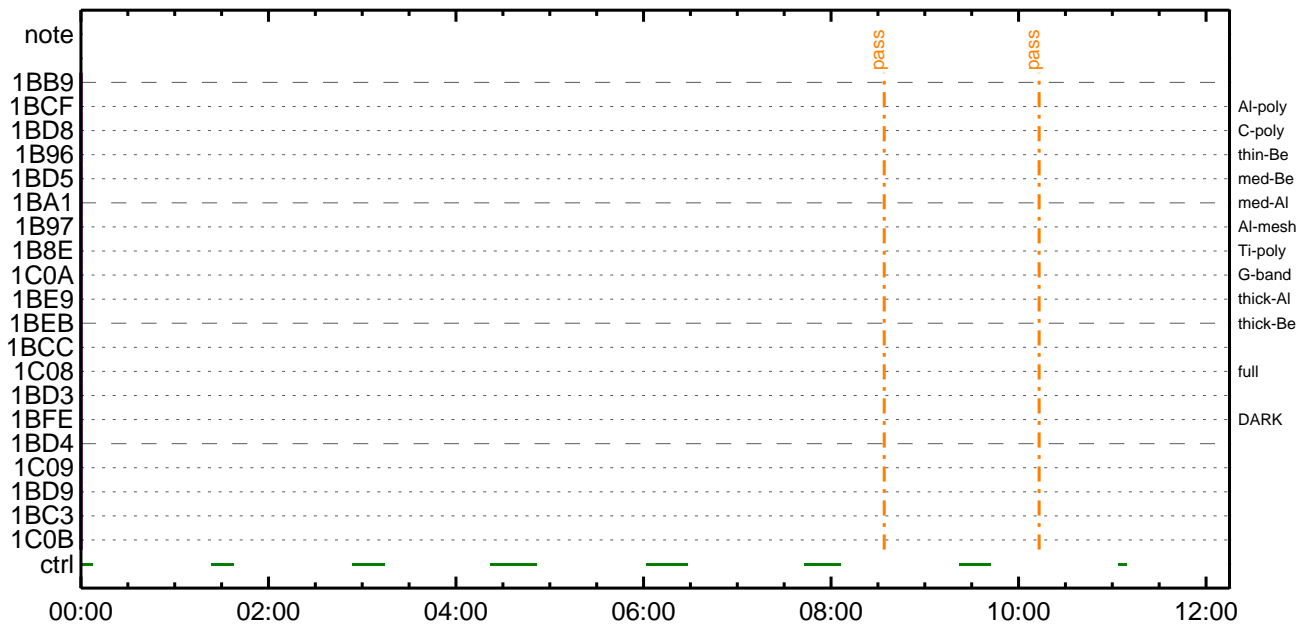
CMDI #0751 2018/09/11



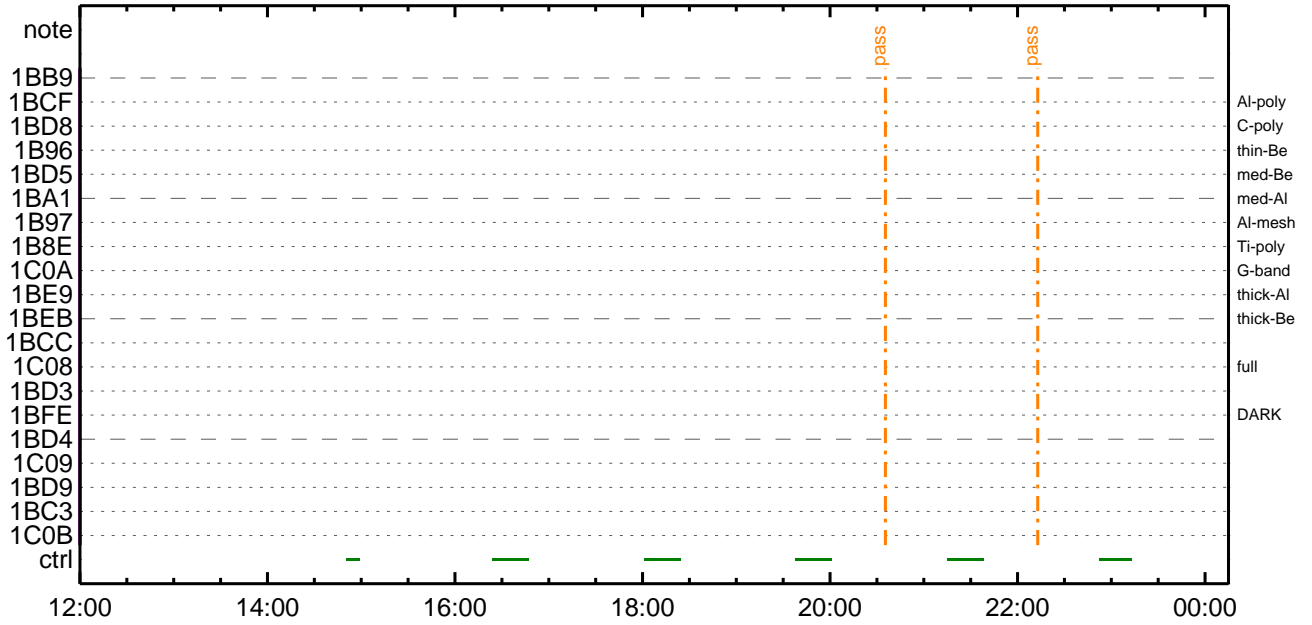
CMDI #0751 2018/09/11



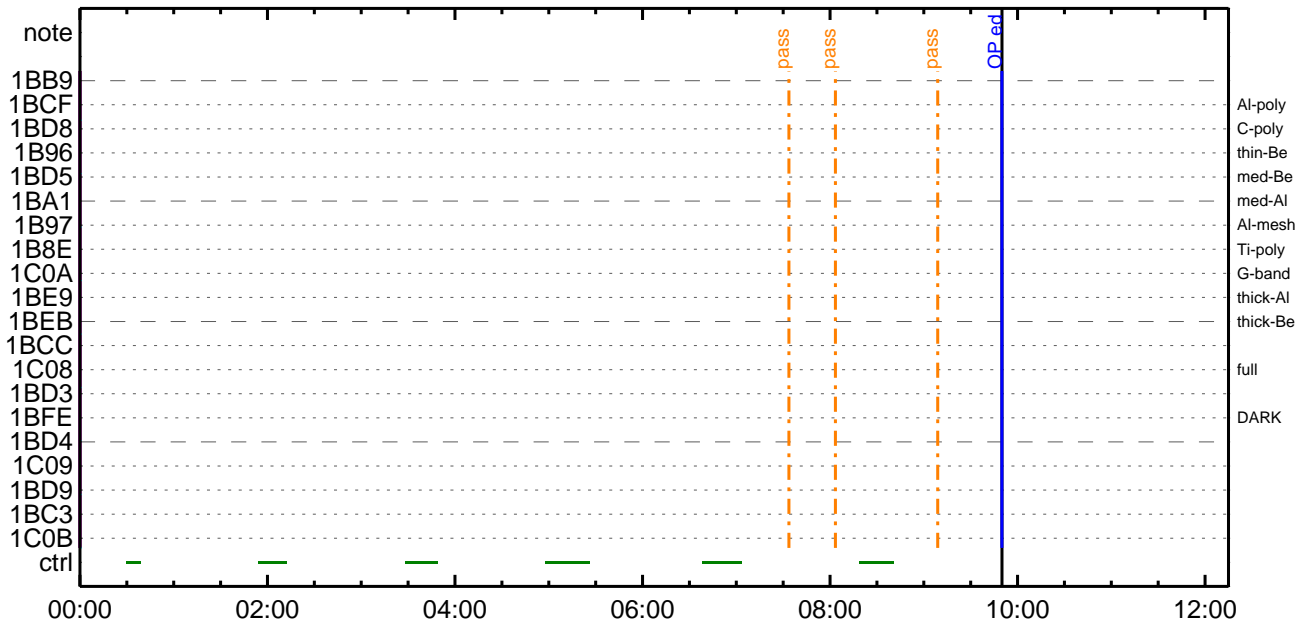
CMDI #0751 2018/09/12



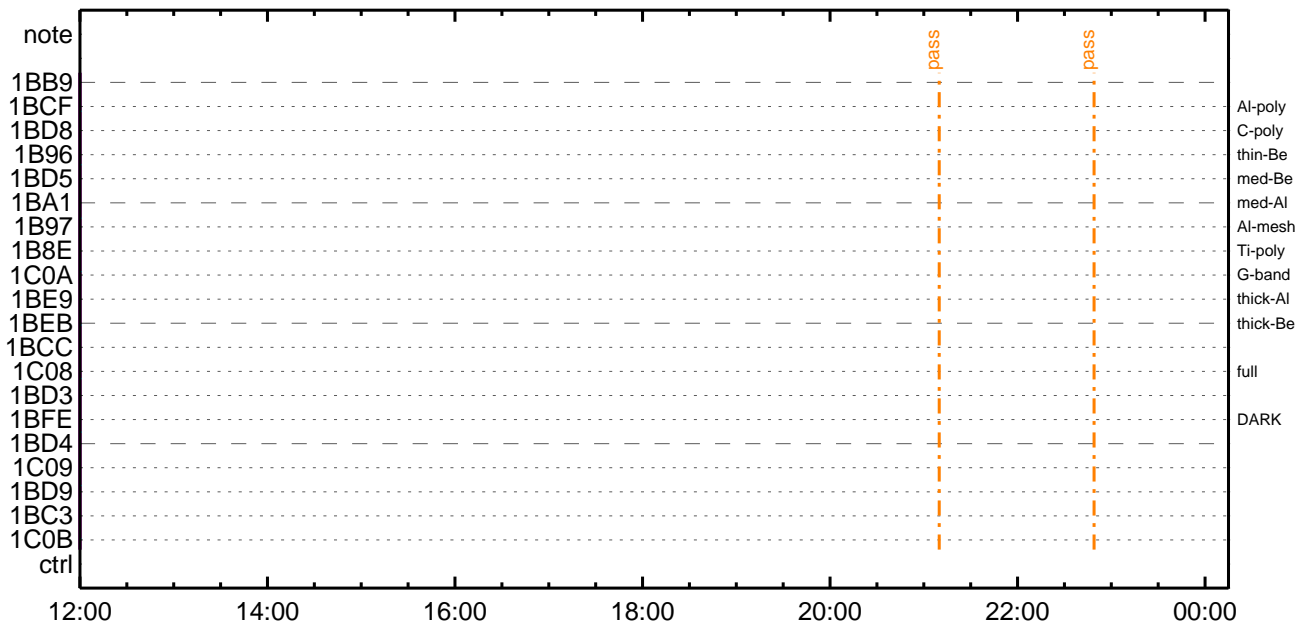
CMDI #0751 2018/09/12



CMDI #0751 2018/09/13



CMDI #0751 2018/09/13







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-620:OP
0104 ( )
0105 S. OG og-620:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°î½Y¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2018-09-08 11:10:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2018-09-08 11:10:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2018-09-08 11:10:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2018-09-08 11:14:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄê%îíñαîî¥Á¥§¥Á¥-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]       EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]    EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]       EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]      EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ          EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHU¥â;¼¥É;È¼¥½. ¥î;¼¥È;Èαòîáα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]       EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]      EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2018-09-08 11:14:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2018-09-08 11:14:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2018-09-08 11:14:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2018-09-08 11:14:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´úÃîαî»ö¼ÝαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îî¥Á¥È¥¥¥¥¥¥¥ç¥èè%¼αα¼Á»Üα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥-¼Á»Ü;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-622 2018-09-08 11:51:19 134 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY~¼Á»Û;ã
0005 C.
0006 C. YÄYß;¼Y³YFÏÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁíËçðÀð•µ°¸»Í×ÁÇçúYçYÄY×Yí;¼YÉ;ËÈè%µ•íÉ;ÈðÉ¼°ÇÖð•α¿¼í¹çαÍ;çÄ®, ùα¹αèðαçÁ+¿®α•ðÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-285:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2018-09-08 11:14:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ( )
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR _____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 05 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 06 85 83 06 06)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 07 80 80 20 20)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 08 80 80 20 08)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 09 80 80 08 20)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0a 85 83 08 08)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0b 80 80 08 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0f 80 80 06 06)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 10 80 80 08 08)
0092 +. DC 07-F0 MDP_XRT_FLD_ENA
0093 BC (d8)
0094 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0095 BC (c8)

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0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097 BC (d5)
0098 + DC 07-F0 MDP_XRT_AEC_RESET
0099 BC (d0)
0100 + DC 07-F0 MDP_XRT_FLD_RESET
0101 BC (da)
0102 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0103 BC (c4 06)
0104 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0105 BC (c5 0d)
0106 . C. ----- Success Verify ? OK / NG ____
0107 C.
0108 C.
0109 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0110 C.
0111 +. DC 07-F0 MDP_XRT_MODE_OBSV
0112 BC (c2)
0113 +. TI 2018-09-08 11:14:02.0
0114 DC 07-F0 MDP_XRT_MODE_OBSV
0115 BC (c2)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C. ***** XRT END *****
0119 C.
0120 . C. ***** MDP `úÃîñî»ò%ÝñÊÃðñ¹ñèDCBC•x²è *****
0121 C. (%á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ½¼ñ¼Ã»Ûñ¹ñè)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ÝDÝ¹•Ï Daily±¿ÍÑñÊ´Øñ¹ñèDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ¡ãLOSÝÁÝ$ÝÃÝ-¼Ã»Ûñ¹ñè
0132 C.
0133 . C. ***** LOS *****
0134 C.
```

Sep 08, 18 11:51

XRT\_OGLIST\_0751.chk

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\*\*\* OP Sequence for XRT \*\*\*

2018/09/08	11:24:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	11:24:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	11:24:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2018/09/08	11:25:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 af 75 01 ca		
2018/09/08	11:25:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2018/09/08	11:25:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2018/09/08	11:25:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2018/09/08	11:25:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/09/08	11:25:26.0	XRT_FLD_RESET_432_OG [0x1b0]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/09/08	11:27:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2018/09/08	11:27:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2018/09/08	11:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/09/08	15:42:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	15:42:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	15:42:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/09/08	15:42:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/09/08	15:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/09/08	15:56:00.0	XRT_Custom_430_OG [0x1ae]					
2018/09/08	15:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/09/08	16:01:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	16:01:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	16:01:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/09/08	16:01:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/09/08	16:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/09/08	16:05:30.0	XRT_Custom_430_OG [0x1ae]					
2018/09/08	16:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/09/08	17:18:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	17:18:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	17:18:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/09/08	17:18:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/09/08	17:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/09/08	17:42:00.0	XRT_Custom_430_OG [0x1ae]					
2018/09/08	17:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/09/08	18:55:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	18:55:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	18:55:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2018/09/08	18:55:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2018/09/08	18:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2018/09/08	19:18:30.0	XRT_Custom_430_OG [0x1ae]					
2018/09/08	19:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2018/09/08	19:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	19:29:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2018/09/08	19:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2018/09/08	19:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2018/09/08	19:30:18.0	XRT_FLD_DIS_409_OG [0x199]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2018/09/08	19:30:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2018/09/08	19:30:22.0	XRT_ARS_DIS_414_OG [0x19e]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2018/09/08	19:32:58.0	XRT_QT_PROG_SET_449_OG [0x1c1]					

2018/09/08	19:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2018/09/08	19:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/08	19:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/08	19:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/08	19:40:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2018/09/08	19:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 03 02 01	ca
2018/09/08	19:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/09/08	19:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/09/08	19:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/09/08	19:40:26.0	XRT_FLD_RESET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/09/08	19:42:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/08	19:42:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2018/09/08	19:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2018/09/08	20:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/08	20:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/08	20:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/08	20:32:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/08	20:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/09/08	20:56:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/09/08	20:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/09/08	22:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/08	22:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/08	22:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/08	22:09:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/08	22:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/09/08	22:32:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/09/08	22:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/09/08	23:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/08	23:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/08	23:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/08	23:47:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/08	23:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/09/09	00:01:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/09/09	00:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/09/09	01:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/09	01:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	01:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	01:17:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/09	01:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/09/09	01:30:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/09/09	01:31:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2018/09/09	02:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/09	02:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	02:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	02:46:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/09	02:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/09/09	02:59:54.0	XRT_CTRL_MANU_422_OG [0x1a6]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/09/09	03:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_CTRL_MANU_422_OG [0x1a6]				
		AOCU_NM	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			AOCU_NM	5	02-76	00 00 00 00	00



2018/09/09	03:03:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	03:03:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/09/09	03:03:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2018/09/09	03:03:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2018/09/09	03:03:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2018/09/09	03:03:54.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/09/09	03:06:24.0	XRT_FLD_RESET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/09	03:06:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2018/09/09	03:06:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2018/09/09	03:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/09	04:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	04:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	04:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/09	04:14:06.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/09/09	04:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/09/09	04:43:30.0	XRT_Custom_430_OG [0x1ae]					
2018/09/09	04:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/09	05:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	05:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	05:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2018/09/09	05:54:36.0	XRT_PREFLR_STRT_437_OG [0x1b5]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2018/09/09	05:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2018/09/09	06:21:00.5	XRT_Custom_430_OG [0x1ae]					
2018/09/09	06:22:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/09	06:23:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	06:23:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	06:23:58.0	XRT_FOCUS_RECALIBRATE_416_OG [0x1a0]	XRT_FOCUS_RECAL	2	07-F8	78 00	
2018/09/09	06:27:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2018/09/09	06:28:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2018/09/09	06:28:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2018/09/09	06:28:22.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2018/09/09	06:30:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2018/09/09	06:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2018/09/09	06:41:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 03 02 01 ca	
2018/09/09	06:41:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/09	06:43:00.5	XRT_TCIB_XRT_S_HTR_A_ENA_425_OG [0x1a9]	TCIB_XRT_S_HTR_A_ENA	0	04-BC		
2018/09/09	18:17:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/09/09	18:27:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 03 02 01 ca	
2018/09/10	10:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00	
2018/09/10	13:40:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	04 00 00 00 00	
2018/09/10	17:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 03 02 01 ca	
2018/09/10	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2018/09/10	18:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	01 00 00 00 00	
2018/09/11	10:29:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2018/09/11	10:39:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	