

XRT Timeline to be uploaded on 2019/02/26

Period: 2019/02/26 11:03:00 - 2019/03/02 09:55:00

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Normal mode

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XOB #1BC7: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(2048ms), Al/Poly(4096ms) - w leak image-1ms

Term	Pointing (x, y)	Comment
02/27 12:20:00 - 02/27 12:26:54	Fixed (-528.4, -528.4)	XRT quadrant pointing (1/4)
PROG= 05 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 51 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 3 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 34 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1BC8: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
02/27 12:30:00 - 02/27 12:36:54	Fixed (528.4, -528.4)	(2/4)
PROG= 07 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 38 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 3 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 34 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1BC9: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
02/27 12:40:00 - 02/27 12:46:54	Fixed (528.4, 528.4)	(3/4)
PROG= 12 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 21 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 3 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 34 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1BCA: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
02/27 12:50:00 - 02/27 12:56:54	Fixed (-528.4, 528.4)	(4/4)
PROG= 08 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512)	Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512)	Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512)	Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		

Seqn= 8		1-time(s)		2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec		
Seqn= 6		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec		
Seqn= 29		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
02/27 13:14:00 - 02/27 17:59:54	Track (-185.6, -169.6) @ 02/27 13:07:00	HOP364
02/27 18:13:00 - 02/28 01:59:54	Track (-141.5, -168.5) @ 02/27 18:10:00	HOP364
02/28 02:03:00 - 02/28 06:08:24	Fixed (0.0, 0.0)	HOP349
02/28 06:21:30 - 02/28 08:51:30	Track (-34.1, -167.1) @ 02/28 06:18:30	HOP364

PROG= 13 30-time(s)

Subr= 1		20-time(s)		2.0sec											
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Seqn=100		1-time(s)		10.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384	(1024, 1024)	Q=95	2	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 87		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

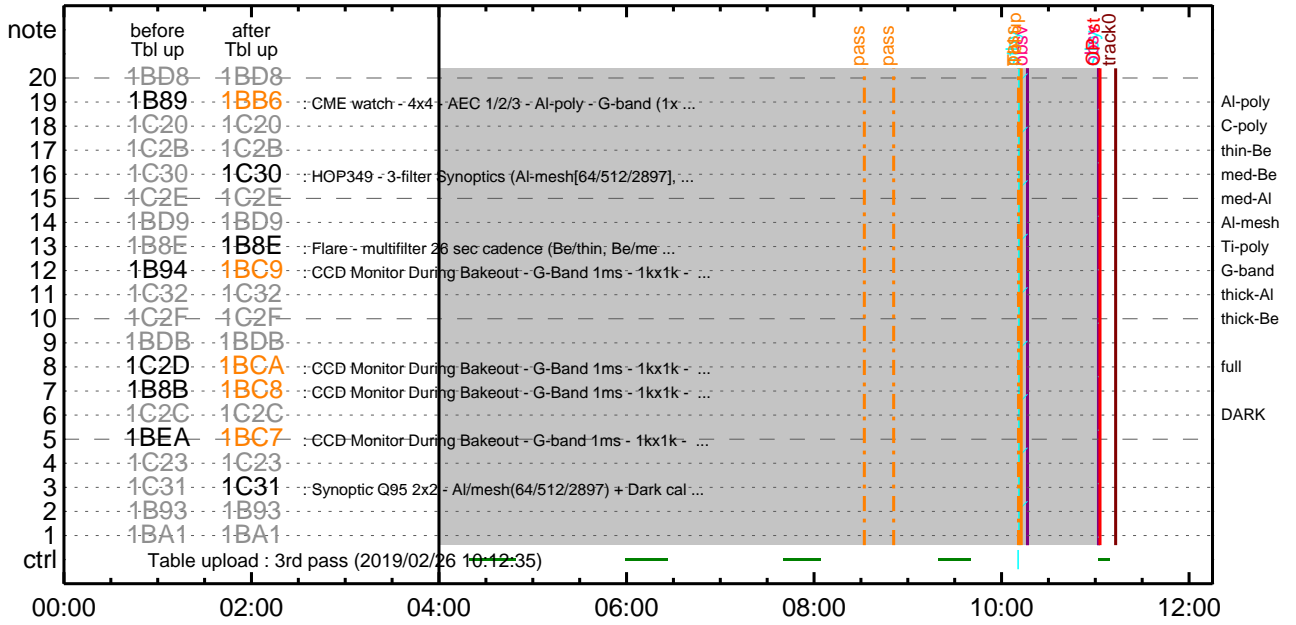
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Flare Detection

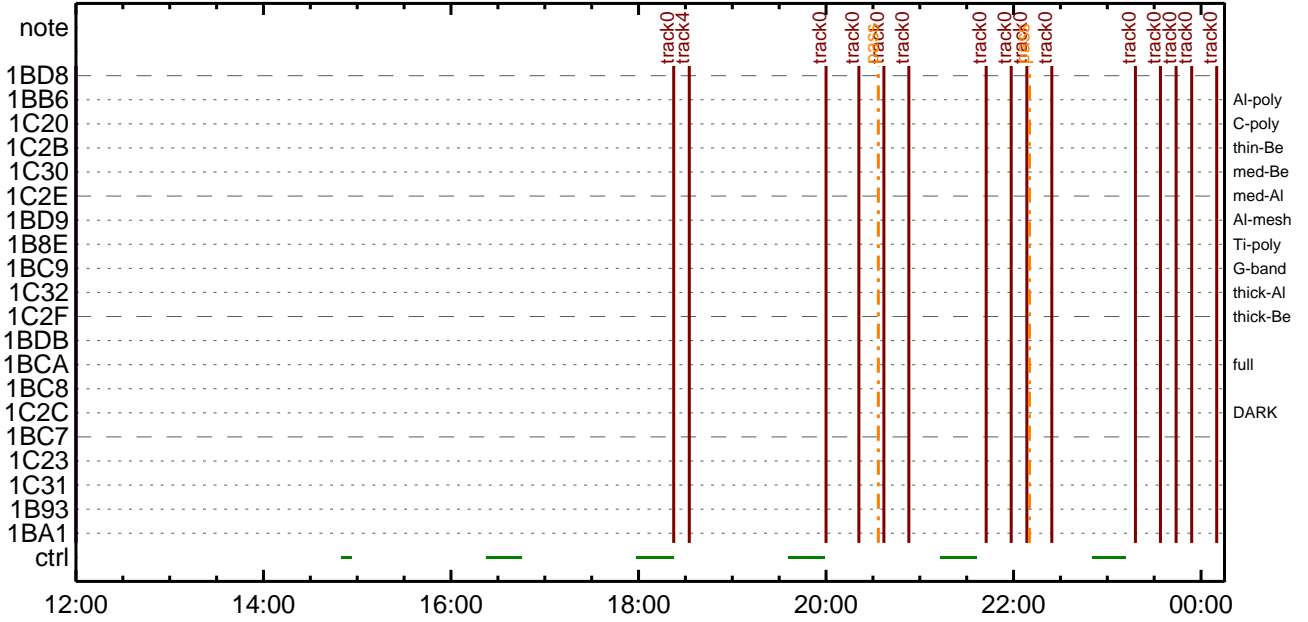
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FLD Patrol												
Term	Pointing (x, y)		Comment									
02/27 13:11:18 - 02/27 18:00:18	Track (-185.6, -169.6) @ 02/27 13:07:00		HOP364									
02/27 18:10:18 - 02/28 06:08:48	Track (-141.5, -168.5) @ 02/27 18:10:00		HOP364									
02/28 06:18:48 - 03/02 09:55:00	Track (-34.1, -167.1) @ 02/28 06:18:30		HOP364									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8			Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

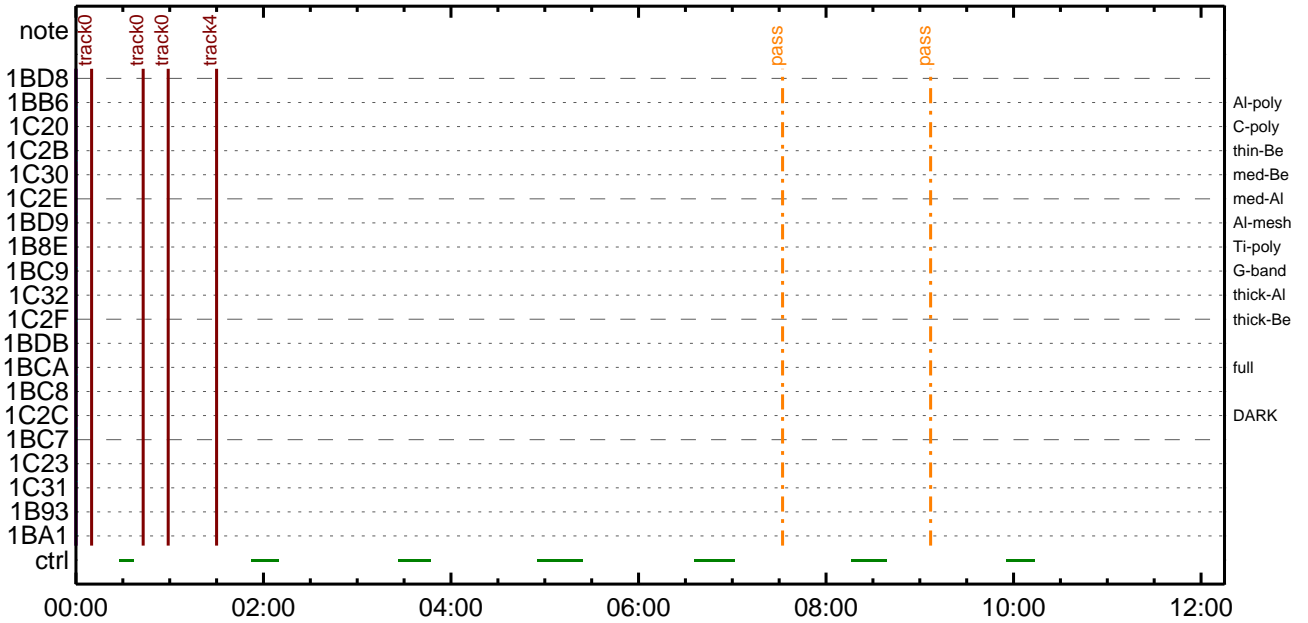
CMDI #0082 2019/02/26



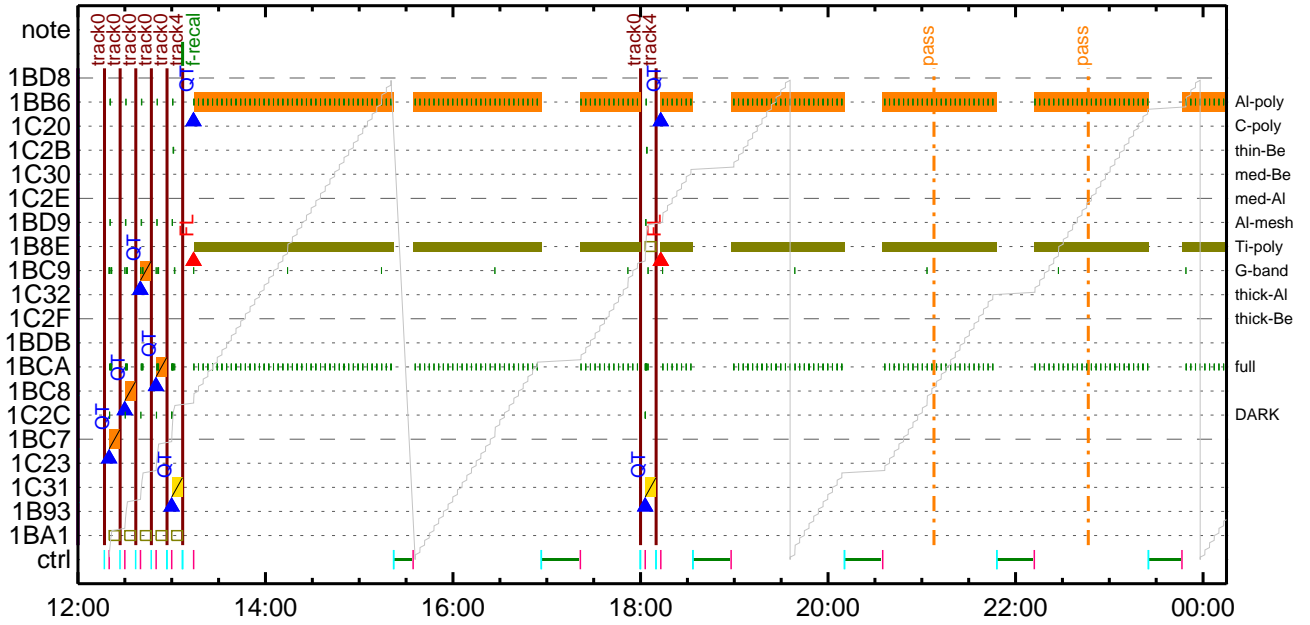
CMDI #0082 2019/02/26



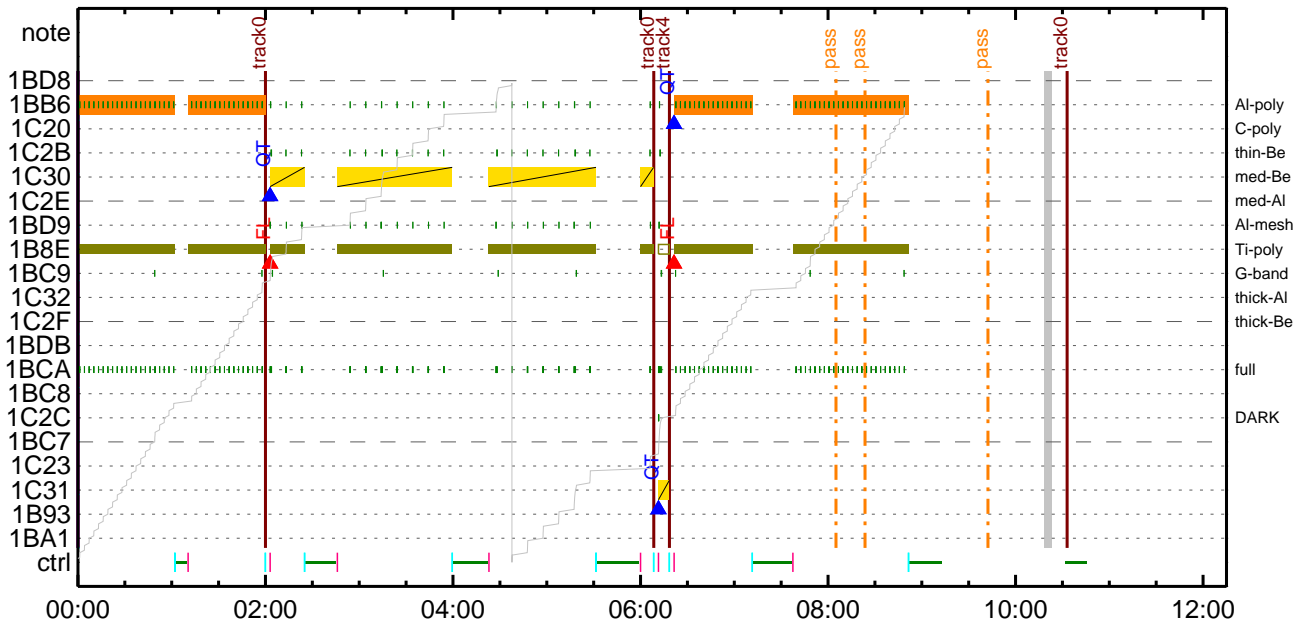
CMDI #0082 2019/02/27



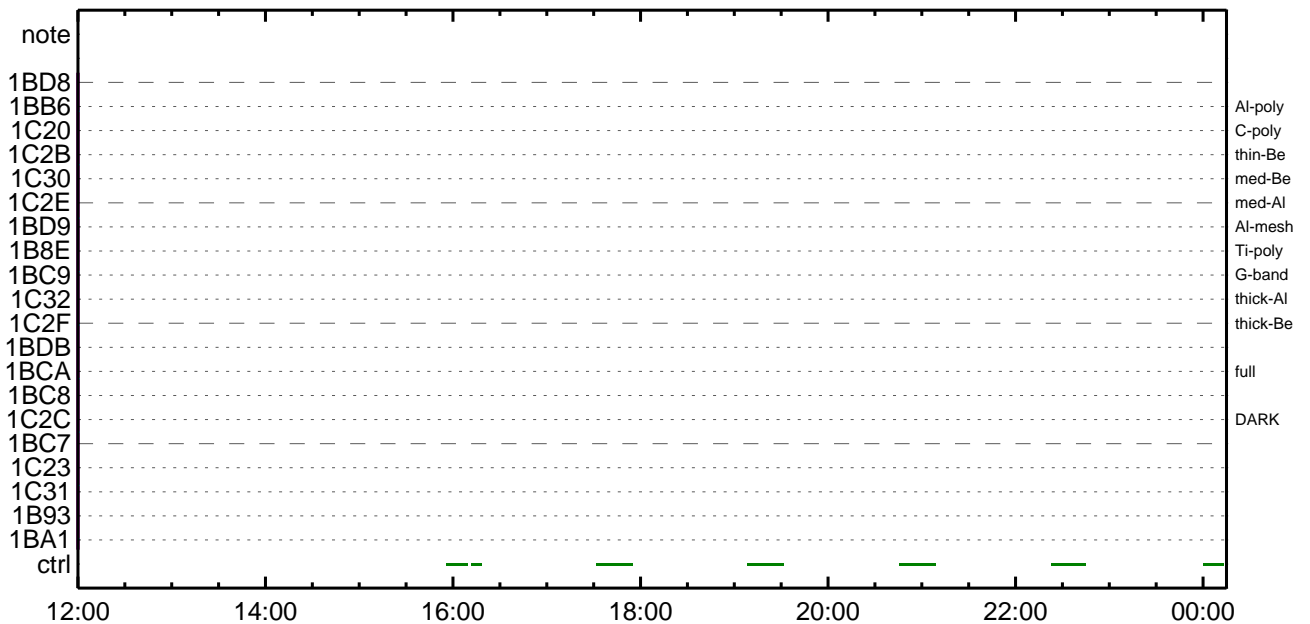
CMDI #0082 2019/02/27



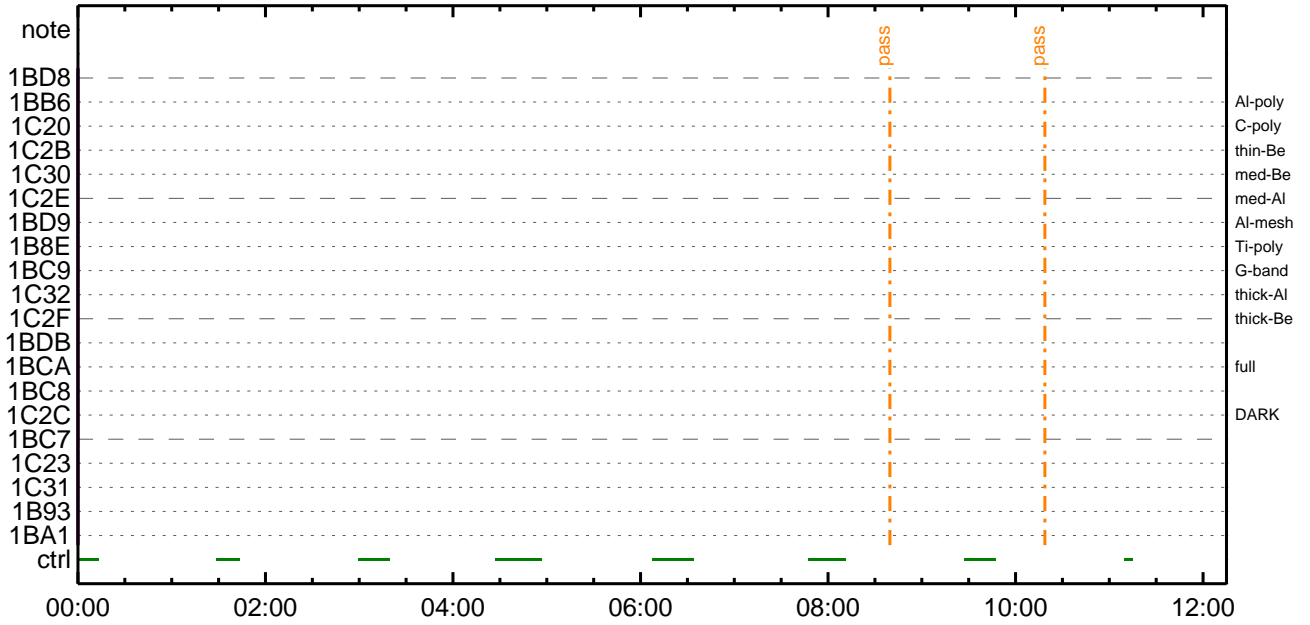
CMDI #0082 2019/02/28



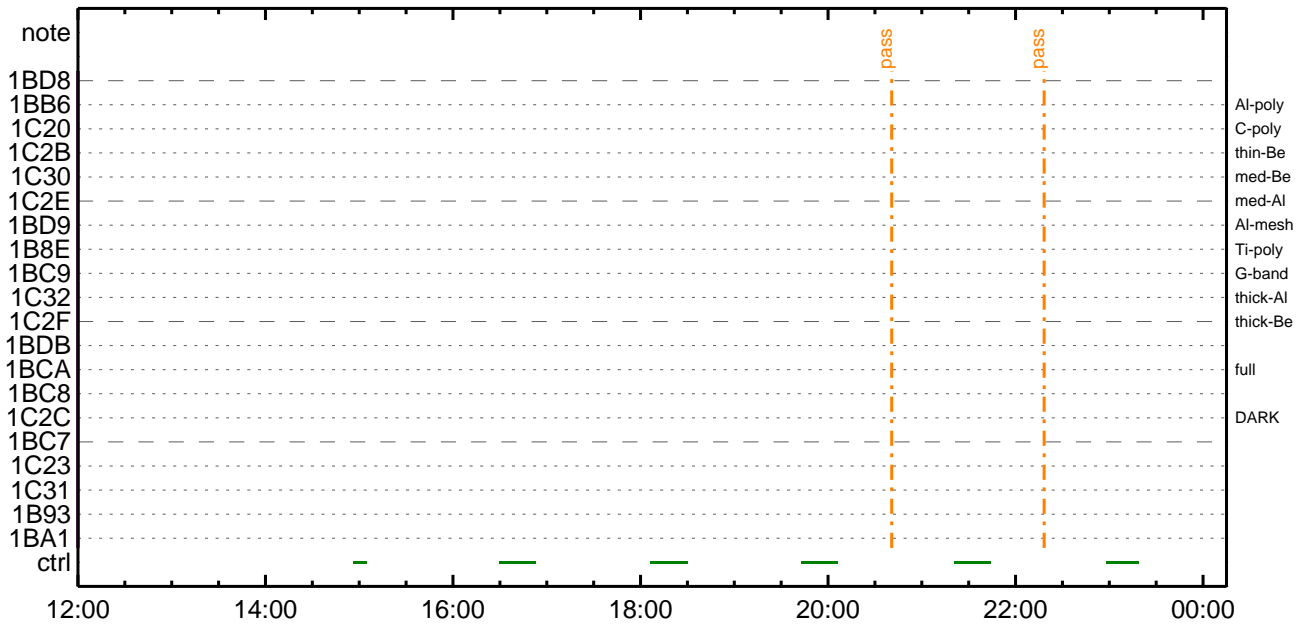
CMDI #0082 2019/02/28



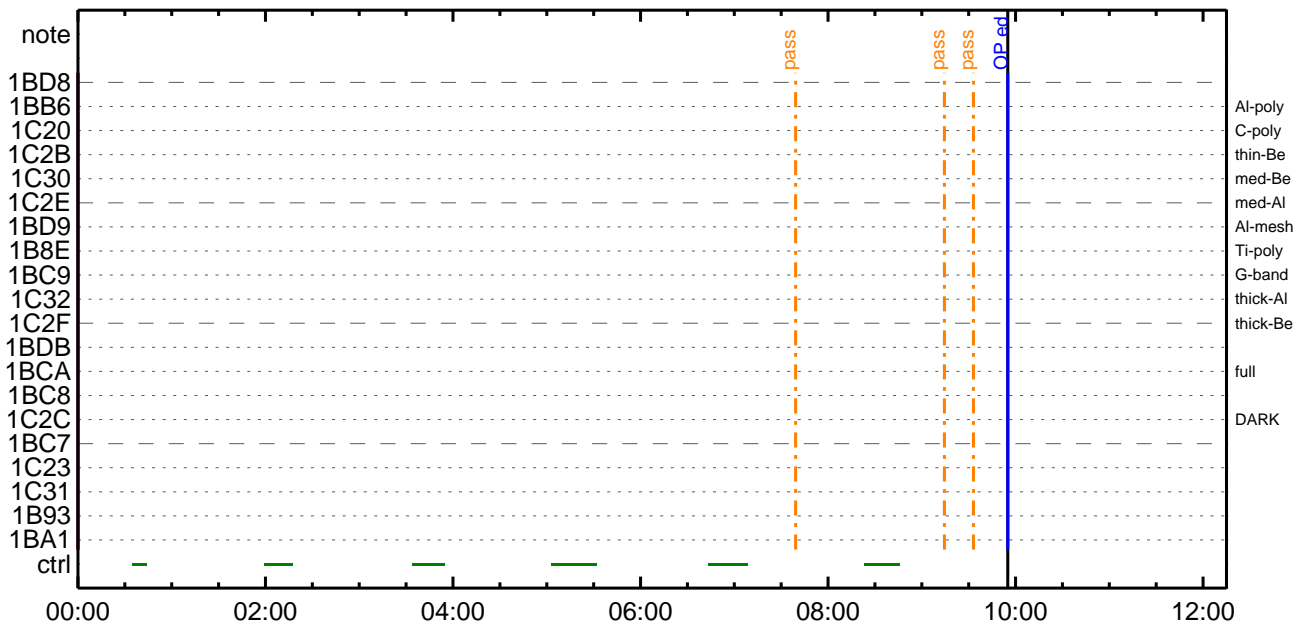
CMDI #0082 2019/03/01



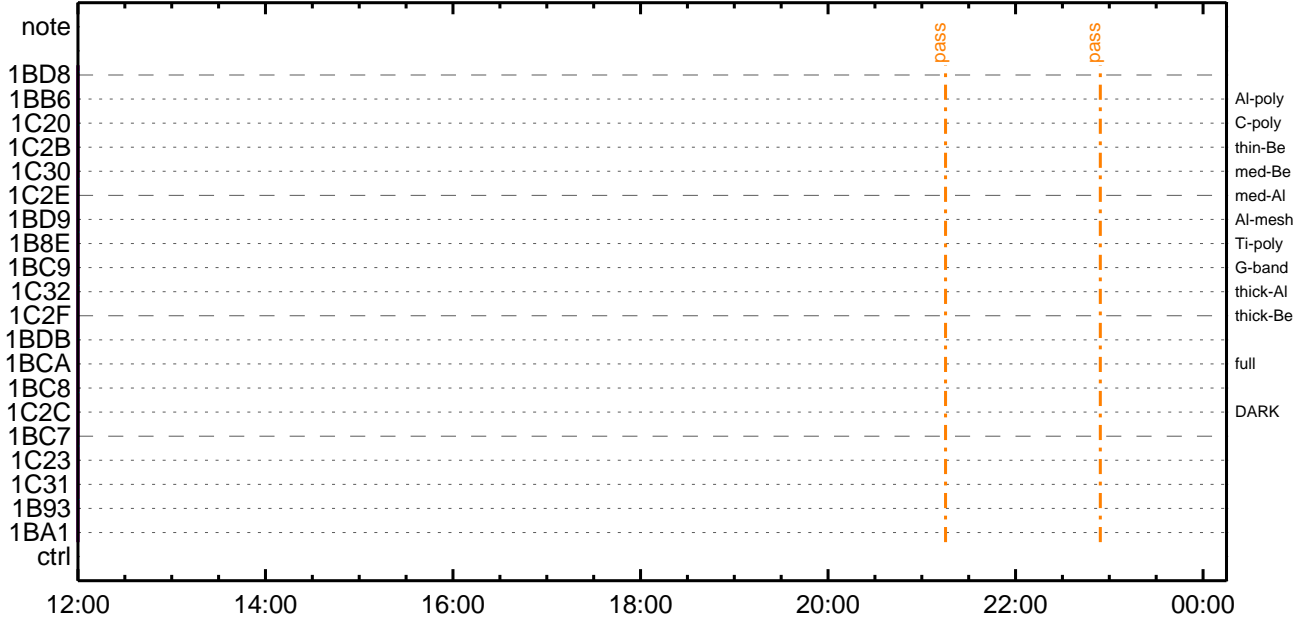
CMDI #0082 2019/03/01



CMDI #0082 2019/03/02



CMDI #0082 2019/03/02




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-947:OP
0104 ( )
0105 S. OG og-947:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.          c{[HK1_DMP_TOP_ADRS_1]           EQ      40
0113 C.          c{[HK1_DMP_TOP_ADRS_0]           EQ      0
0114 C.          c{[HK1_DMP_BLOCK_NUM]            EQ     127
0115 C.          c{[HK1_DMP_REPEAT_NUM]           EQ      0
0116 C.          c{[HK1_DMA_DMP_PIM]              EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.          c{[HK1_PKT_FORM_NO]              EQ      7
0120 C.          c{[HK1_PKT_GEN_TIME]             EQ     0.25 s
0121 C.          c{[HK1_S_TLM_BIT_RATE]           EQ     32k
0122 C.          c{[HK1_X_TLM_BIT_RATE]           EQ      4M
0123 C.          c{[HK1_DMP_CHK_FLG]              EQ     EXEC
0124 C. YAYOXx1/2^i>od^iC$
0125 C.          c{[HK1_DMP_CHK_FLG]              EQ     NON
0126 C. RAM ID=NMOGaf^E^1c.e^iOKod^iC$
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.          c{[HK1_DMP_TOP_ADRS_1]           EQ     41
0132 C.          c{[HK1_DMP_TOP_ADRS_0]           EQ      0
0133 C.          c{[HK1_DMP_BLOCK_NUM]            EQ     127
0134 C.          c{[HK1_DMP_REPEAT_NUM]           EQ      0
0135 C.          c{[HK1_DMA_DMP_PIM]              EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.          c{[HK1_PKT_FORM_NO]              EQ      7
0139 C.          c{[HK1_PKT_GEN_TIME]             EQ     0.25 s
0140 C.          c{[HK1_S_TLM_BIT_RATE]           EQ     32k
0141 C.          c{[HK1_X_TLM_BIT_RATE]           EQ      4M
0142 C.          c{[HK1_DMP_CHK_FLG]              EQ     EXEC
0143 C. YAYOXx1/2^i>od^iC$
0144 C.          c{[HK1_DMP_CHK_FLG]              EQ     NON
0145 C. RAM ID=NMOGaf^E^1c.e^iOKod^iC$
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.          c{[HK1_DMP_TOP_ADRS_1]           EQ     42
0151 C.          c{[HK1_DMP_TOP_ADRS_0]           EQ      0
0152 C.          c{[HK1_DMP_BLOCK_NUM]            EQ     65
0153 C.          c{[HK1_DMP_REPEAT_NUM]           EQ      0
0154 C.          c{[HK1_DMA_DMP_PIM]              EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.          c{[HK1_PKT_FORM_NO]              EQ      7
0158 C.          c{[HK1_PKT_GEN_TIME]             EQ     0.25 s
0159 C.          c{[HK1_S_TLM_BIT_RATE]           EQ     32k
0160 C.          c{[HK1_X_TLM_BIT_RATE]           EQ      4M
0161 C.          c{[HK1_DMP_CHK_FLG]              EQ     EXEC
0162 C. YAYOXx1/2^i>od^iC$
0163 C.          c{[HK1_DMP_CHK_FLG]              EQ     NON
0164 C. RAM ID=NMOG, RAM ID=OPaf^E^1c.e^iOKod^iC$
0165 C.
0166 C. ***** oE^2/4oI^A^¶A^oEE^-oA^:z@ (%au-YAYOXx1/2e^1/2caDAOãæoc^1a^"oE%i^1coca) *****
0167 C. DHUYa;4YE;E^1/2^, Y;1;4YE;EodIã^1
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.          c{[HK1_PKT_FORM_NO]              EQ      2
0171 C.          c{[HK1_PKT_GEN_TIME]             EQ     0.5S
0172 C.          c{[HK1_S_TLM_BIT_RATE]           EQ     32K
0173 C.          c{[HK1_X_TLM_BIT_RATE]           EQ      4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |s OPOG UPLOADa-A^:z@NGuI^1c; c^oE^2/4oI^TI-CMDA^:z@oI^A^1Oa.oEoqo^3oE; f
0180 C.          a^oE; cSETaEDUMPaIã±oIY^1c^1Oa|o^3oE; f
0181 C.
0182 C. TIY^3YpYOXEodãD^1c(UT)
0183 +. TI 2019-02-26 10:58:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.          c{[HK1_TI_CMD_NUM]                EQ     1COUNTUP
0186 C.
0187 +. TI 2019-02-26 10:58:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.          c{[HK1_TI_CMD_NUM]                EQ     1COUNTUP
0190 C.
0191 +. TI 2019-02-26 10:58:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.          c{[HK1_TI_CMD_NUM]                EQ     1COUNTUP

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-281:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2019-02-26 11:02:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 06 c0 c0 10 10)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 07 80 80 20 20)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 08 40 c0 10 10)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 09 40 40 10 10)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 0a c0 40 10 10)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0b 80 80 20 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0c 80 80 08 20)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 0d 80 80 08 08)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 0f 80 80 06 06)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 10 80 80 08 08)
0176 + DC 07-F0 MDP_XRT_FLD_ENA
0177 BC (d8)
0178 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0179 BC (c8)
0180 + DC 07-F0 MDP_XRT_ARS_DIS
0181 BC (d5)
0182 + DC 07-F0 MDP_XRT_AEC_RESET
0183 BC (d0)
0184 + DC 07-F0 MDP_XRT_FLD_RESET
0185 BC (da)
0186 . C. ----- Success Verify ? OK / NG ____
0187 C.
0188 C.
0189 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0190 C.
0191 +. DC 07-F0 MDP_XRT_MODE_OBSV
0192 BC (c2)
0193 +. TI 2019-02-26 11:02:02.0

```

```
0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 . C. ----- Success Verify ? OK / NG ____
0197 C.
0198 C. ***** XRT END *****
0199 C.
0200 . C. ***** MDP `úÃîñî»ò¼ŷñÊÃĐñ¹ñèDCBC•x²è *****
0201 C. (¼á°îŷÓŷÃŷÈŷŷŷËŷáŷçŷèñÈ¼ñ¼Ã»Ûñ¹ñè)
0202 . S. DC-BC dcbc-402:DCBC
0203 (MDP_known_event)
0204 C.
0205 C.
0206 . C. ***** ŷĐŷ¹•İ Daily±¿İññÈ´Øñ¹ñèDCBC•x²è *****
0207 . S. DC-BC dcbc-153:DCBC
0208 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209 C.
0210 C.
0211 . C. ;ãLOSŷÁŷŸŷÃŷ¬¼Ã»Û¿ä
0212 C.
0213 . C. ***** LOS *****
0214 C.
```

*** OP Sequence for XRT ***

```

2019/02/26 11:13:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 00 54 cc 01 ca
2019/02/26 18:22:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2019/02/26 18:32:30.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 04 00 00 00 00
2019/02/26 20:00:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 00 00 00 ac cd
2019/02/26 20:21:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 00 00 00 d6 67
2019/02/26 20:37:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2019/02/26 20:53:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCU_NM                    5 02-76 00 00 00 00 29 99
2019/02/26 21:42:30.0 AOCs_OrE-point_Start_7_OG [0x09d]
                        AOCU_NM                    5 02-76 00 00 00 00 53 33
2019/02/26 21:58:30.0 AOCs_OrE-point_Start_8_OG [0x09e]
                        AOCU_NM                    5 02-76 00 d6 36 b7 8e
2019/02/26 22:08:30.0 AOCs_OrE-point_Start_9_OG [0x09f]
                        AOCU_NM                    5 02-76 00 b4 b5 db 75
2019/02/26 22:24:30.0 AOCs_OrE-point_Start_10_OG [0x0a0]
                        AOCU_NM                    5 02-76 00 ac 5b 00 00
2019/02/26 23:18:00.0 AOCs_OrE-point_Start_11_OG [0x0a1]
                        AOCU_NM                    5 02-76 00 b4 b5 24 8b
2019/02/26 23:34:00.0 AOCs_OrE-point_Start_12_OG [0x0a2]
                        AOCU_NM                    5 02-76 00 d6 36 48 72
2019/02/26 23:44:00.0 AOCs_OrE-point_Start_13_OG [0x0a3]
                        AOCU_NM                    5 02-76 00 29 ca b7 8e
2019/02/26 23:54:00.0 AOCs_OrE-point_Start_14_OG [0x0a4]
                        AOCU_NM                    5 02-76 00 4b 4b db 75
2019/02/27 00:10:00.0 AOCs_OrE-point_Start_15_OG [0x0a5]
                        AOCU_NM                    5 02-76 00 53 a5 00 00
2019/02/27 00:43:00.0 AOCs_OrE-point_Start_16_OG [0x0a6]
                        AOCU_NM                    5 02-76 00 4b 4b 24 8b
2019/02/27 00:59:00.0 AOCs_OrE-point_Start_17_OG [0x0a7]
                        AOCU_NM                    5 02-76 00 29 db 48 72
2019/02/27 01:30:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 04 00 00 00 00
2019/02/27 06:12:00.0 XRT_TCIB_XRT_S_HTR_A_DIS_445_OG [0x1bd]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2019/02/27 12:16:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2019/02/27 12:16:56.0 XRT_FOCUS_POSITION_446_OG [0x1be]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2019/02/27 12:17:00.0 AOCs_OrE-point_Start_18_OG [0x0a8]
                        AOCU_NM                    5 02-76 00 2e f9 2e f9
2019/02/27 12:17:16.0 XRT_FLD_DIS_422_OG [0x1a6]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2019/02/27 12:17:18.0 XRT_FLRCTRL_DIS_427_OG [0x1ab]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2019/02/27 12:19:54.0 XRT_ARS_DIS_429_OG [0x1ad]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2019/02/27 12:19:56.0 XRT_QT_PROG_SET_433_OG [0x1b1]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 05
2019/02/27 12:20:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2019/02/27 12:26:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2019/02/27 12:26:56.0 XRT_FOCUS_POSITION_446_OG [0x1be]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2019/02/27 12:27:00.0 AOCs_OrE-point_Start_19_OG [0x0a9]
                        AOCU_NM                    5 02-76 00 2e f9 d1 07
2019/02/27 12:27:16.0 XRT_FLD_DIS_422_OG [0x1a6]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2019/02/27 12:27:18.0 XRT_FLRCTRL_DIS_427_OG [0x1ab]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2019/02/27 12:29:54.0 XRT_ARS_DIS_441_OG [0x1b9]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2019/02/27 12:29:56.0 XRT_QT_PROG_SET_414_OG [0x19e]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 07
2019/02/27 12:30:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2019/02/27 12:36:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2019/02/27 12:36:56.0 XRT_FOCUS_POSITION_446_OG [0x1be]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2019/02/27 12:37:00.0 AOCs_OrE-point_Start_20_OG [0x0aa]
                        AOCU_NM                    5 02-76 00 d1 07 d1 07
2019/02/27 12:37:16.0 XRT_FLD_DIS_422_OG [0x1a6]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2019/02/27 12:37:18.0 XRT_FLRCTRL_DIS_427_OG [0x1ab]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2019/02/27 12:39:54.0 XRT_ARS_DIS_441_OG [0x1b9]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2019/02/27 12:39:56.0 XRT_QT_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 0c
2019/02/27 12:40:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2019/02/27 12:46:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2019/02/27 12:46:56.0 XRT_FOCUS_POSITION_446_OG [0x1be]

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2019/02/27	12:47:00.0	AOCS_OrE-point_Start_21_OG [0x0ab]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
			AOCU_NM	5	02-76	00	d1	07	2e f9
2019/02/27	12:47:16.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2019/02/27	12:47:18.0	XRT_FLRCTRL_DIS_427_OG [0x1ab]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2019/02/27	12:49:54.0	XRT_ARS_DIS_441_OG [0x1b9]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/02/27	12:49:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2019/02/27	12:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/02/27	12:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/02/27	12:56:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/02/27	12:56:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2019/02/27	12:57:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00 00
2019/02/27	12:57:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2019/02/27	12:57:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2019/02/27	12:57:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/02/27	12:59:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2019/02/27	13:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/02/27	13:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/02/27	13:06:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/02/27	13:06:58.0	XRT_FOCUS_RECALIBRATE_417_OG [0x1a1]	XRT_FOCUS_RECAL	2	07-F8	78	00		
2019/02/27	13:07:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04	00	00	00 00
2019/02/27	13:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2019/02/27	13:11:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/02/27	13:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/02/27	13:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/02/27	13:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/02/27	13:11:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/02/27	13:13:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2019/02/27	13:13:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2019/02/27	13:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/02/27	15:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/02/27	15:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/02/27	15:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/02/27	15:22:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/02/27	15:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/02/27	15:33:30.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2019/02/27	15:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/02/27	16:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/02/27	16:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/02/27	16:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/02/27	16:56:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/02/27	16:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/02/27	17:20:30.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2019/02/27	17:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/02/27	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/02/27	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/02/27	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2019/02/27	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00 00
2019/02/27	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			

2019/02/27	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2019/02/27	18:00:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/02/27	18:02:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2019/02/27	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/02/27	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/27	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/27	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2019/02/27	18:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04	00 00 00 00
2019/02/27	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/02/27	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/02/27	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/02/27	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/02/27	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/02/27	18:12:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
2019/02/27	18:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/02/27	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/02/27	18:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/27	18:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/27	18:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/02/27	18:33:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/02/27	18:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/02/27	18:57:00.0	XRT_Custom_430_OG [0x1ae]					
2019/02/27	18:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/02/27	20:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/27	20:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/27	20:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/02/27	20:10:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/02/27	20:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/02/27	20:34:00.0	XRT_Custom_430_OG [0x1ae]					
2019/02/27	20:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/02/27	21:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/27	21:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/27	21:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/02/27	21:48:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/02/27	21:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/02/27	22:11:00.0	XRT_Custom_430_OG [0x1ae]					
2019/02/27	22:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/02/27	23:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/27	23:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/27	23:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/02/27	23:25:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/02/27	23:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/02/27	23:45:30.0	XRT_Custom_430_OG [0x1ae]					
2019/02/27	23:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/02/28	01:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/28	01:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/02/28	01:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/02/28	01:02:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/02/28	01:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					

2019/02/28	01:09:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/02/28	01:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/02/28	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/02/28	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	02:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/02/28	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00
2019/02/28	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2019/02/28	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/02/28	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2019/02/28	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/02/28	02:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/02/28	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2019/02/28	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/02/28	02:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/02/28	02:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	02:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	02:25:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/02/28	02:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/02/28	02:45:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/02/28	02:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/02/28	03:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/02/28	03:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	03:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	03:59:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/02/28	04:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/02/28	04:22:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/02/28	04:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/02/28	05:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/02/28	05:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	05:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	05:31:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/02/28	05:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/02/28	05:59:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/02/28	06:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/02/28	06:08:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/02/28	06:08:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	06:08:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	06:08:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/02/28	06:08:48.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00
2019/02/28	06:08:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2019/02/28	06:08:52.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2019/02/28	06:11:28.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/02/28	06:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2019/02/28	06:18:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/02/28	06:18:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	06:18:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	06:18:30.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
			AOCU_NM	5	02-76	04 00 00 00 00

2019/02/28	06:18:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2019/02/28	06:18:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/02/28	06:18:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2019/02/28	06:18:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2019/02/28	06:18:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/02/28	06:21:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2019/02/28	06:21:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/02/28	06:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/02/28	07:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	07:11:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	07:11:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/02/28	07:11:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/02/28	07:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/02/28	07:36:30.0	XRT_Custom_430_OG [0x1ae]			
2019/02/28	07:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/02/28	08:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	08:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/02/28	08:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/02/28	08:51:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/02/28	08:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/02/28	10:33:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00