

# XRT Timeline to be uploaded on 2019/03/05

Period: 2019/03/05 10:03:00 - 2019/03/09 10:44:00

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Normal mode

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XOB #1B93: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 30s cadence, G-band - 384x384 1ms													
Term	Pointing (x, y)				Comment								
03/05 10:29:00 - 03/05 16:12:54	Fixed ( -20.0, 866.0)				# OP start + 10min / HOP81 N-pole								
03/06 09:31:00 - 03/06 16:57:54	Fixed ( -20.0, -974.0)				HOP81 S-pole (DSM)								
<b>PROG= 02 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 16 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 90 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3 60-time(s) 2.0sec													
└─ Seqn= 57 1-time(s) 30.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BC0: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Th													
Term	Pointing (x, y)				Comment								
03/05 16:16:00 - 03/05 16:22:54	Fixed ( 0.0, 0.0)				synoptic, shifted manually								
03/06 06:27:00 - 03/06 06:33:54	Fixed ( 0.0, 0.0)				HOP349 and synoptic								
03/07 05:58:30 - 03/07 06:05:24	Fixed ( 0.0, 0.0)				HOP349 and synoptic								
<b>PROG= 11 1-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 49 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 72 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 64 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 23 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with													
Term	Pointing (x, y)				Comment								
03/05 16:26:00 - 03/06 01:59:54	Track ( -591.1, -253.6) @ 03/05 16:23:00				# AR tracking								
<b>PROG= 14 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Subr= 2 4-time(s) 2.0sec													
└─ Seqn= 75 1-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 93 4-time(s) 300.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1C2C: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[1024/11571/23142] with 512x512 G-band+Leak - 45min cad) +**

Term	Pointing (x, y)	Comment
03/06 02:03:00 - 03/06 06:23:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic
03/07 02:03:00 - 03/07 05:55:24	Fixed ( 0.0, 0.0)	HOP349 and synoptic

**PROG= 15 Inf.-time(s)**

- Subr= 1 1-time(s) 600.0sec
  - Seqn= 1 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 99 1-time(s) 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 33 1-time(s) 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 30 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
- Subr= 2 4-time(s) 600.0sec
  - Seqn= 8 1-time(s) 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
  - Seqn= 6 1-time(s) 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
  - Seqn= 29 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

**XOB #19D7: PFB observation for possible flare -w/leak test, 60s cad**

Term	Pointing (x, y)	Comment
03/06 06:37:00 - 03/06 09:07:00	Track ( -489.1, -244.8) <sup>03/06 06:34:00</sup>	# AR tracking
03/06 17:11:00 - 03/07 01:59:54	Track ( -407.4, -239.3) <sup>03/06 17:08:00</sup>	# AR tracking
03/07 06:08:30 - 03/07 10:15:00	Track ( -301.9, -234.1) <sup>03/07 06:05:30</sup>	# AR tracking

**PROG= 06 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 91 2-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 3 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 0 3 2.0sec
- Subr= 2 2-time(s) 2.0sec
  - Seqn= 2 30-time(s) 60.0sec
    - thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 3 1 2.0sec
    - Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) DPCM 3 1 2.0sec
    - thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 3 2 2.0sec
    - Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) DPCM 3 2 2.0sec
    - thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) DPCM 3 3 2.0sec
    - Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) DPCM 3 3 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

**XOB #1B8D: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(512/8192/23142) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-Al**

Term	Pointing (x, y)	Comment
03/06 17:01:00 - 03/06 17:07:54	Fixed ( 0.0, 0.0)	synoptic, shifted manually

**PROG= 09 1-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 1 1-time(s) 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 99 1-time(s) 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Al-poly/Open Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 54 1-time(s) 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 23 1-time(s) 4.0sec
    - Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec

Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
Seqn= 46	1-time(s)	2.0sec										
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 55	1-time(s)	2.0sec										
med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 40	1-time(s)	2.0sec										
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
03/05 10:29:00 - 03/05 16:12:54	Fixed ( -20.0, 866.0)	# OP start + 10min / HOP81 N-pole
03/05 16:26:00 - 03/06 01:59:54	Track ( -591.1, -253.6) <sup>@ 03/05 16:23:00</sup>	# AR tracking
03/06 02:03:00 - 03/06 06:23:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic
03/06 06:37:00 - 03/06 09:07:00	Track ( -489.1, -244.8) <sup>@ 03/06 06:34:00</sup>	# AR tracking
03/06 09:31:00 - 03/06 16:57:54	Fixed ( -20.0, -974.0)	HOP81 S-pole (DSM)
03/06 17:11:00 - 03/07 01:59:54	Track ( -407.4, -239.3) <sup>@ 03/06 17:08:00</sup>	# AR tracking
03/07 02:03:00 - 03/07 05:55:24	Fixed ( 0.0, 0.0)	HOP349 and synoptic
03/07 06:08:30 - 03/07 10:15:00	Track ( -301.9, -234.1) <sup>@ 03/07 06:05:30</sup>	# AR tracking

#### PROG= 13 30-time(s)

<b>Subr= 1 20-time(s) 2.0sec</b>												
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100	1-time(s)	10.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
Seqn= 10	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

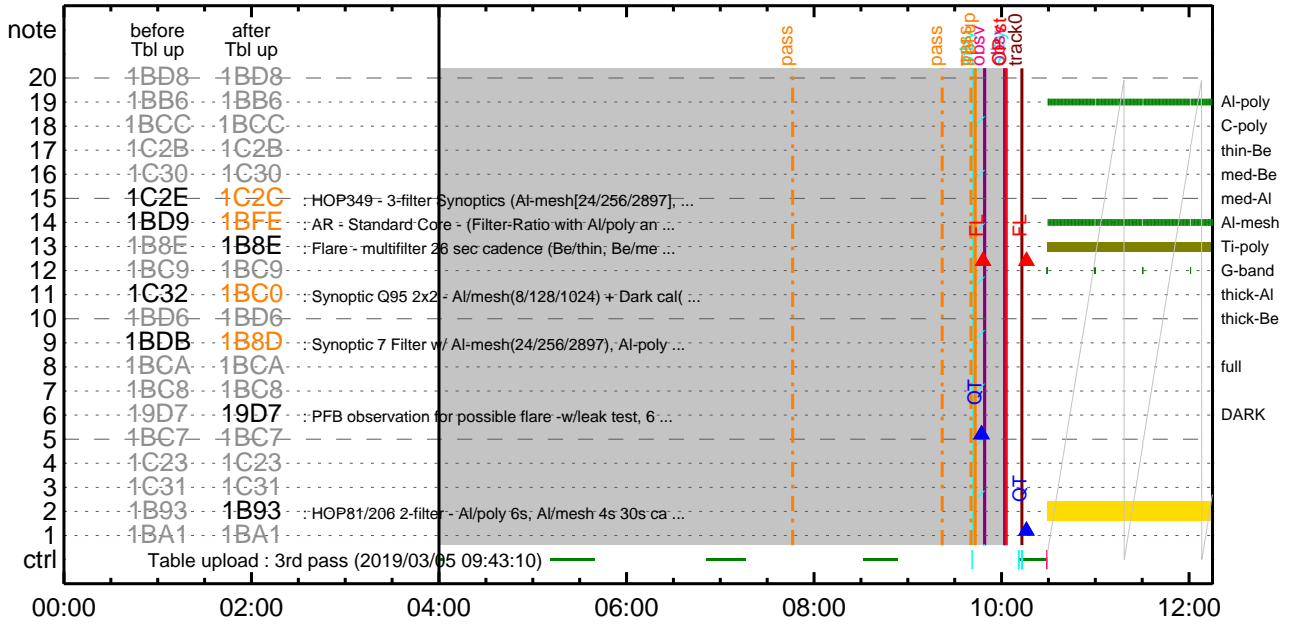
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### Flare Detection

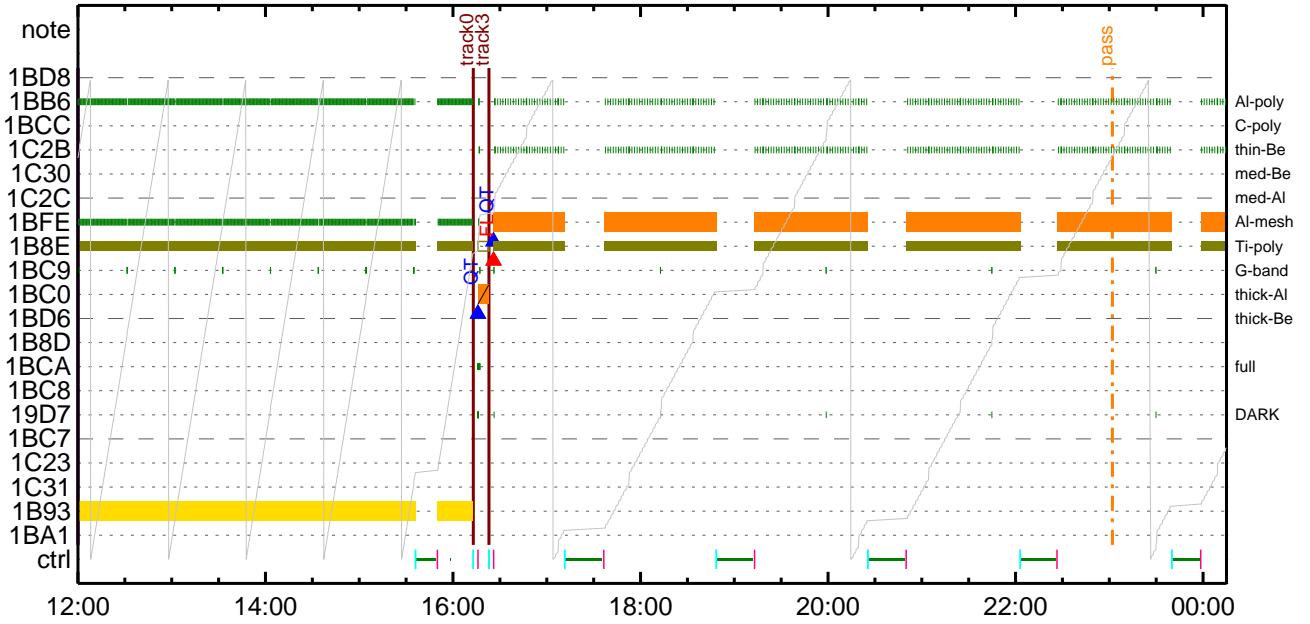
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FLD Patrol												
Term	Pointing (x, y)	Comment										
03/05 16:23:18 - 03/06 06:24:18	Track ( -591.1, -253.6) <sup>@ 03/05 16:23:00</sup>	# AR tracking										
03/06 06:34:18 - 03/06 16:58:18	Track ( -489.1, -244.8) <sup>@ 03/06 06:34:00</sup>	# AR tracking										
03/06 17:08:18 - 03/07 05:55:48	Track ( -407.4, -239.3) <sup>@ 03/06 17:08:00</sup>	# AR tracking										
03/07 06:05:48 - 03/09 10:44:00	Track ( -301.9, -234.1) <sup>@ 03/07 06:05:30</sup>	# AR tracking										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

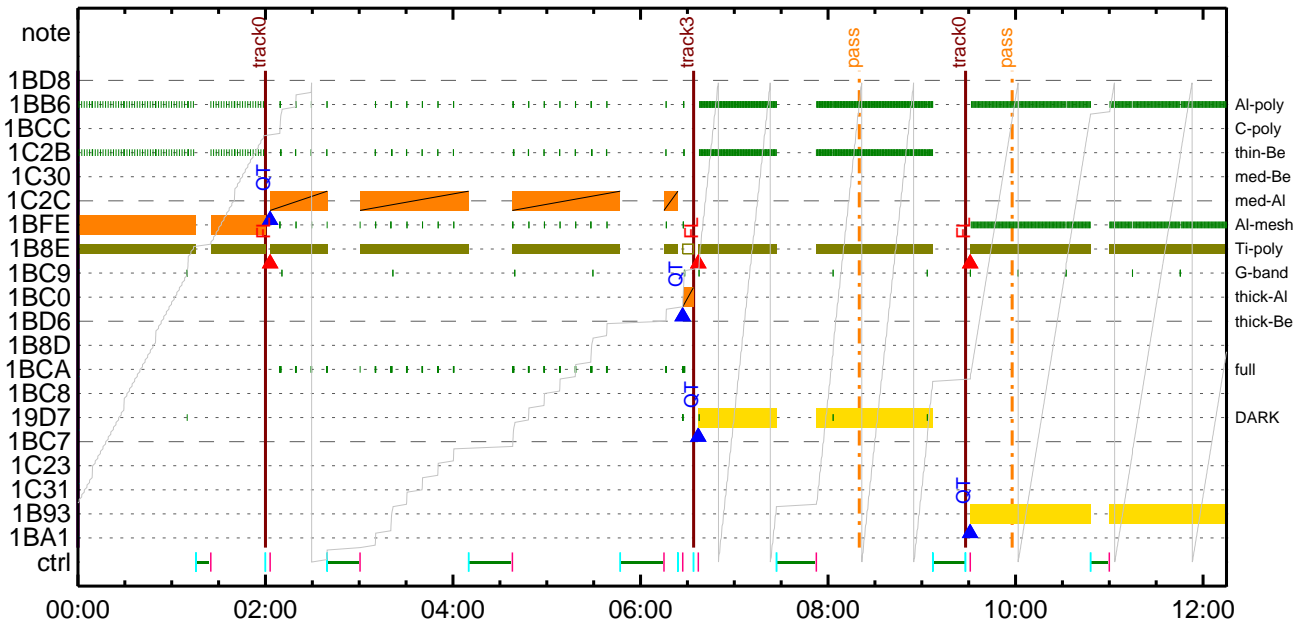
### CMDI #0098 2019/03/05



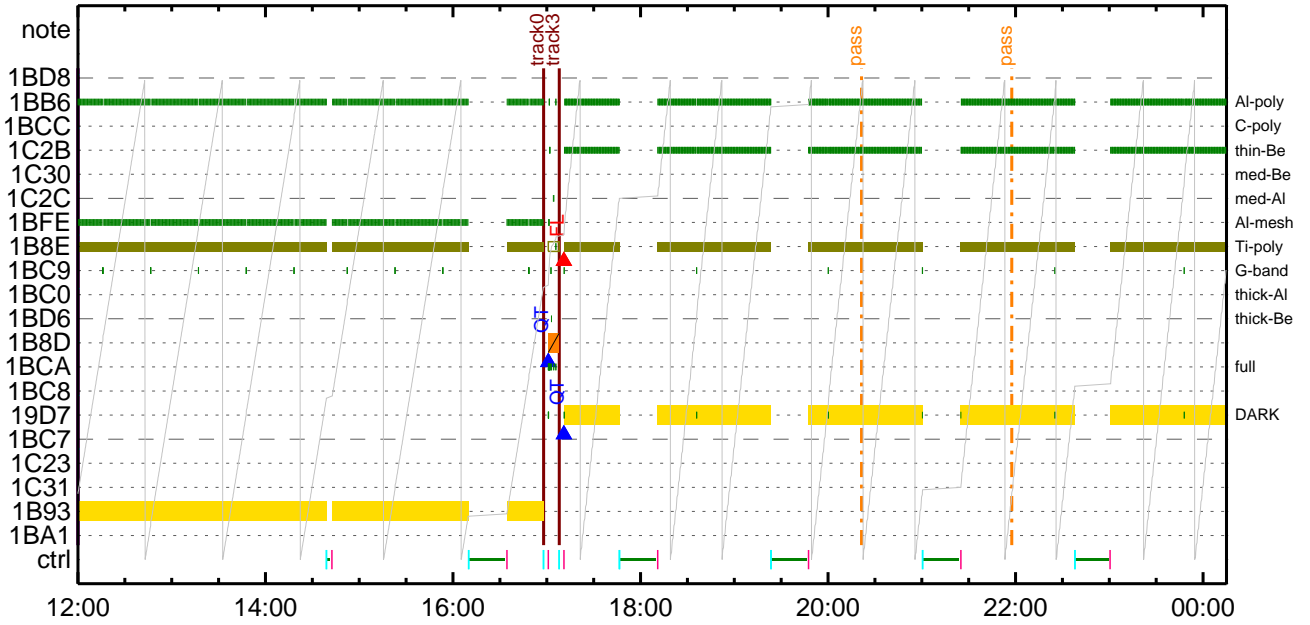
### CMDI #0098 2019/03/05



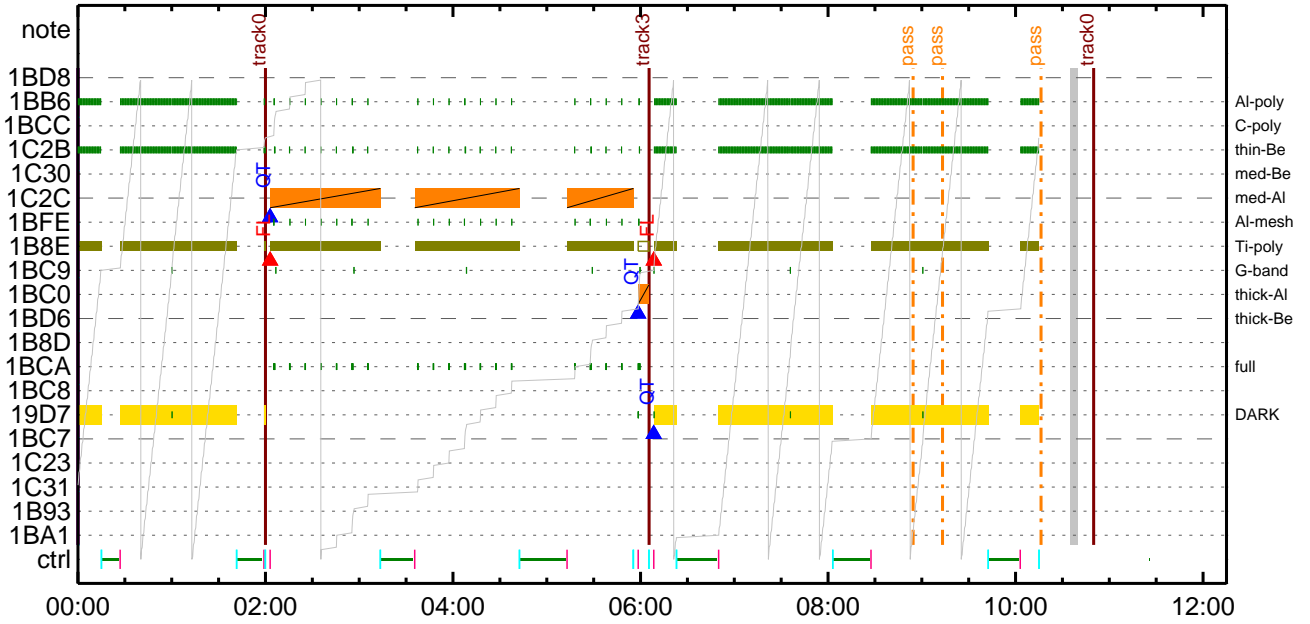
### CMDI #0098 2019/03/06



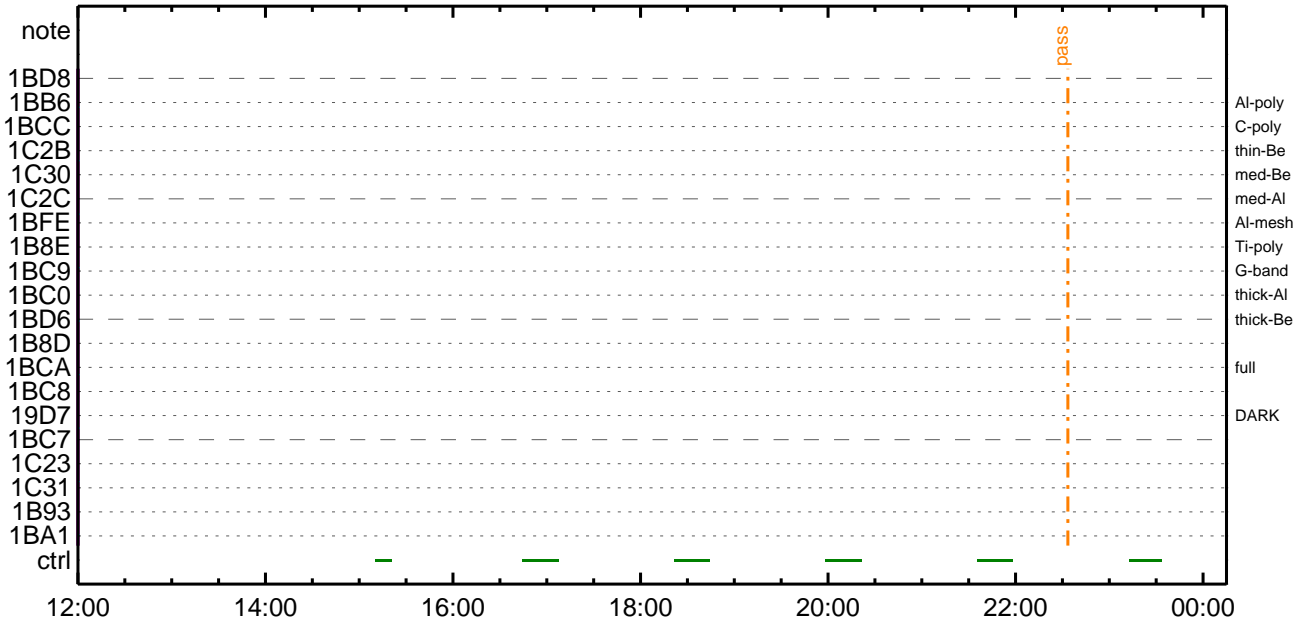
CMDI #0098 2019/03/06



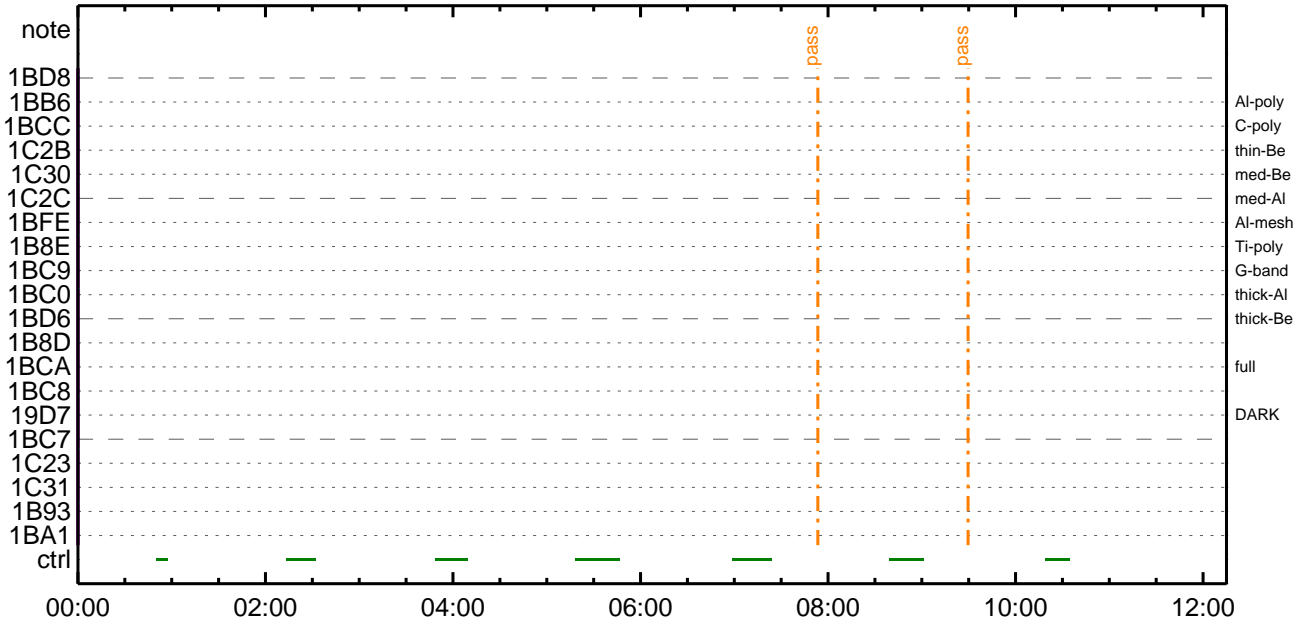
CMDI #0098 2019/03/07



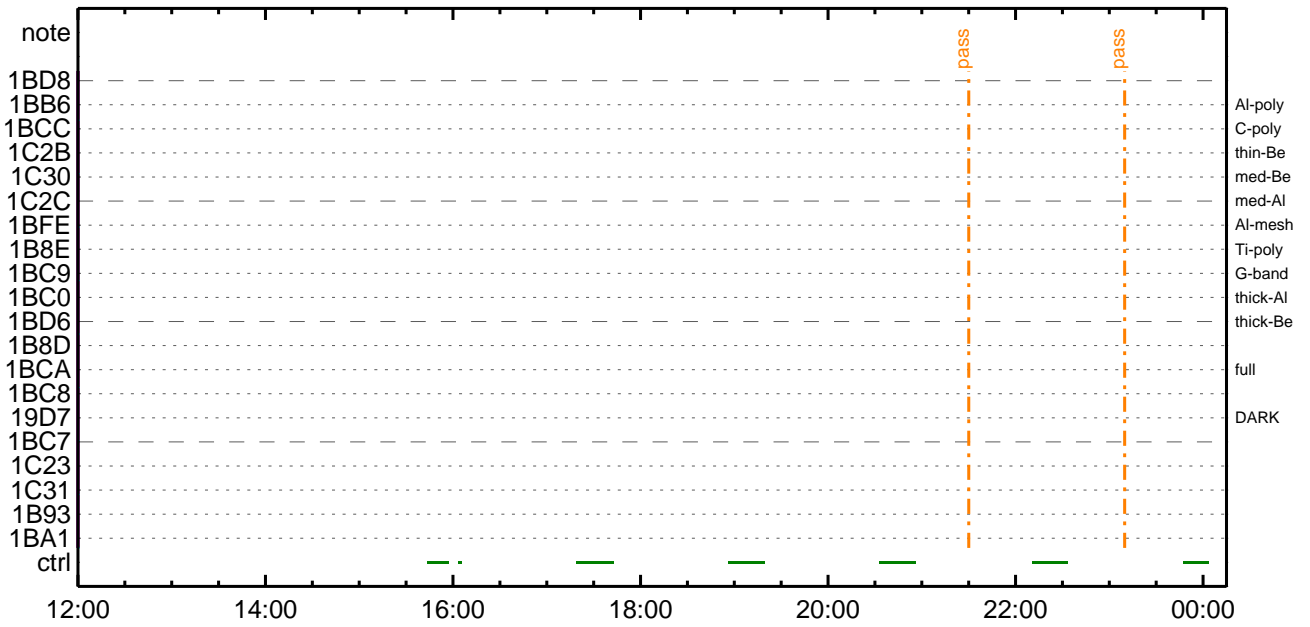
CMDI #0098 2019/03/07



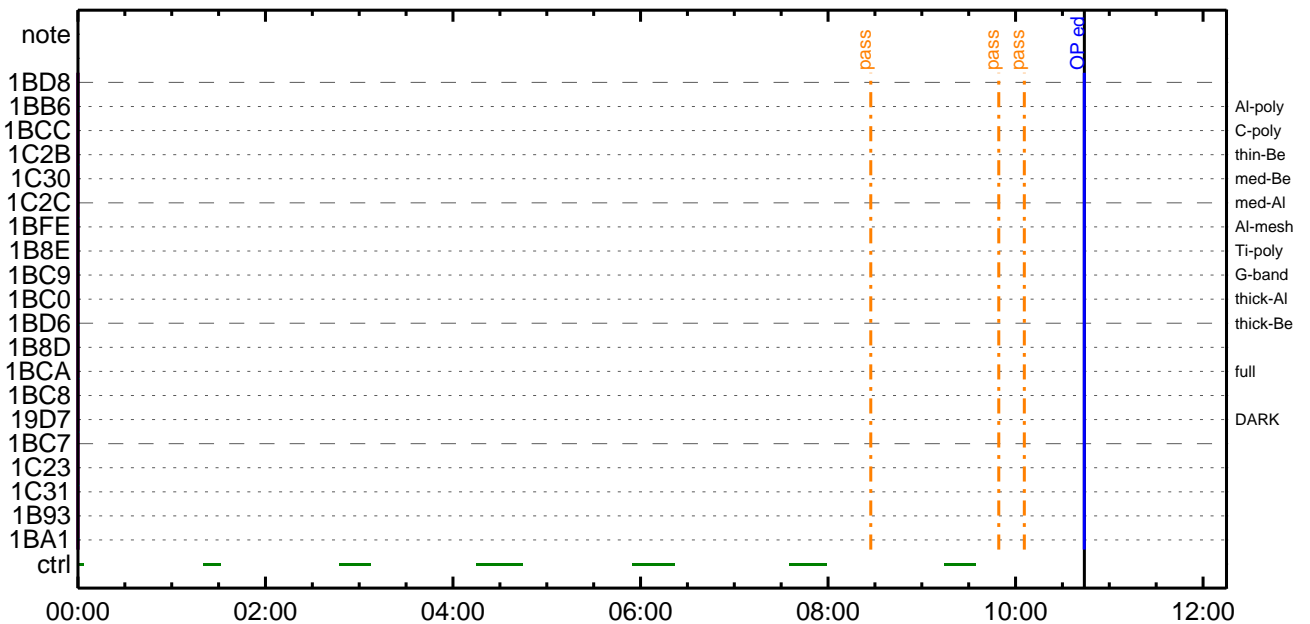
CMDI #0098 2019/03/08

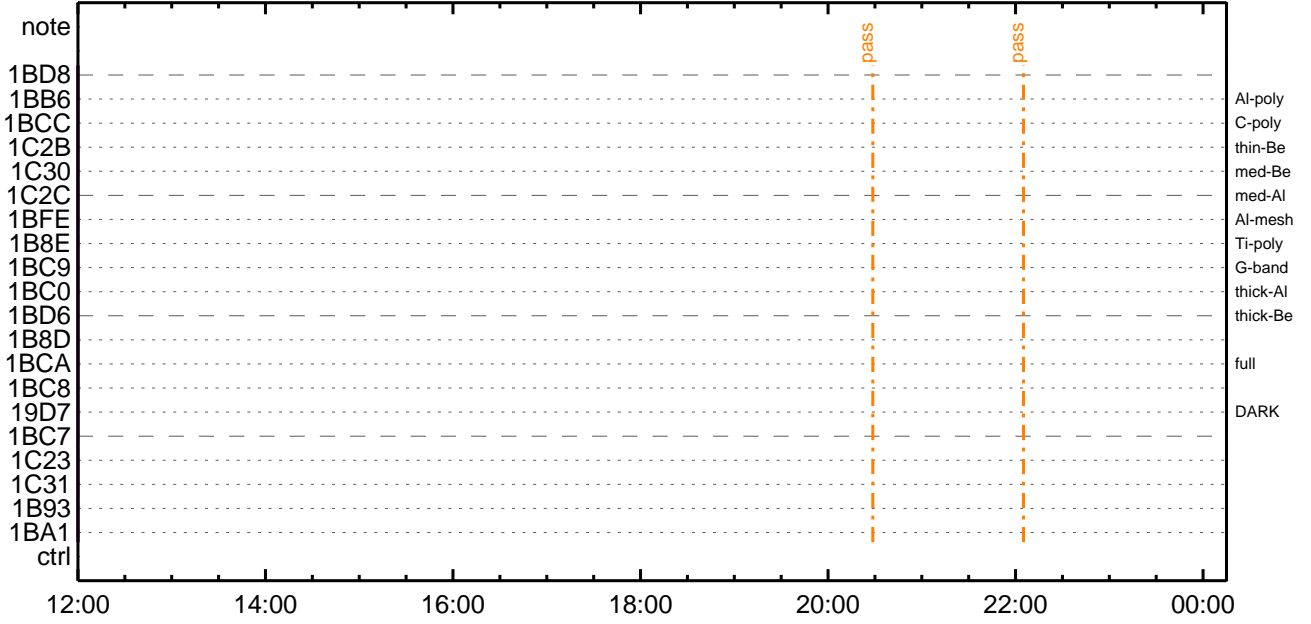


CMDI #0098 2019/03/08



CMDI #0098 2019/03/09









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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-960:OP
0104 ( )
0105 S. OG og-960:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½^aî»oð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGqî¼E¹ç•ë²îOKoð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½^aî»oð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGqî¼E¹ç•ë²îOKoð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½^aî»oð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPqî¼E¹ç•ë²îOKoð³îÇ§
0165 C.
0166 C. ***** oE²¼oî¼Ä´¶Á°oEÉ¬oÁ÷¿@ (¼áµ-YAYOXx½^e½çoðÁÓÆoç¼^a¬°oE¼i¹çoçoâ) *****
0167 C. DHUYâ;4YE;E½Y½, Y1;4YE;Eoðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |§ OPOG UPLOADo¬Á÷¿@NGuî¼i¹ç;ç°E²¼oî¼TI-CMDÁ÷¿@oî¼Á¹Ôo•oEoð³oE;f
0180 C. oBp¿;çSEToEDUMPaî¼E±°iYNY¹ç¹Ôo|o³oE;f
0181 C.
0182 C. TIY³YB¥OYÉoðoÁDî¿(UT)
0183 +. TI 2019-03-05 09:58:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2019-03-05 09:58:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2019-03-05 09:58:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 6570722.4 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 6s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2019-03-05 10:02:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÃîî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0165 C. (¼á°îÝÓÝÄÝËÝÞÝËÝ¼ÝèñË¼ð¼Ä»Û¹ñè)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝÐÝ¹•İ Daily±;İÑñË'Øñ¹ñèDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÝÄÝSÝÄÝ-¼Ä»Û;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-962 2019-03-05 11:18:21 98 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ã
0005  C.
0006  C. YÁY$;¼Y³YFYÓYÉÁ+¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCs : Reload orbital element (send every contact) *****
0010  C.  Áí;È□¿□Á□•µ°Æ»Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;È□È¼°Ç□□•□¿¼í¹ç□Í;çÁ®, ù□¹□è□È□ÇÁ+¿®□•□È□□□³□È;f
0011  +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  C.
0015  C. ***** XRT START *****
0016  C.
0017  +. DC 07-F0 MDP_XRT_CTRL_MANU
0018  BC (c1)
0019  + DC 07-F0 MDP_XRT_MODE_STBY
0020  BC (c3)
0021  . C. ----- Success Verify ?          OK / NG ____
0022  C.
0023  C. XRT Obs. Table Upload
0024  . S. RAM ram-291:MDP_OBS_X
0025  ( )
0026  C.
0027  +. DC 07-F0 MDP_DUMP_XRTTBL
0028  BC (84 07 00 00 00 3a d4)
0029  . C. ----- Comparison Check ?          OK / ERR ____
0030  C.
0031  C.
0032  +. DC 07-F0 MDP_XRT_ROI_SET
0033  BC (cd 01 b1 b1 04 04)
0034  + DC 07-F0 MDP_XRT_ROI_SET
0035  BC (cd 02 b1 b1 08 08)
0036  + DC 07-F0 MDP_XRT_ROI_SET
0037  BC (cd 03 b1 b1 08 08)
0038  + DC 07-F0 MDP_XRT_ROI_SET
0039  BC (cd 04 b1 b1 06 06)
0040  + DC 07-F0 MDP_XRT_ROI_SET
0041  BC (cd 05 85 83 06 06)
0042  + DC 07-F0 MDP_XRT_ROI_SET
0043  BC (cd 06 85 83 06 06)
0044  + DC 07-F0 MDP_XRT_ROI_SET
0045  BC (cd 07 80 80 20 20)
0046  + DC 07-F0 MDP_XRT_ROI_SET
0047  BC (cd 08 80 80 20 08)
0048  + DC 07-F0 MDP_XRT_ROI_SET
0049  BC (cd 09 80 80 08 20)
0050  + DC 07-F0 MDP_XRT_ROI_SET
0051  BC (cd 0a 80 80 08 08)
0052  + DC 07-F0 MDP_XRT_ROI_SET
0053  BC (cd 0f 80 80 06 06)
0054  + DC 07-F0 MDP_XRT_ROI_SET
0055  BC (cd 10 80 80 08 08)
0056  + DC 07-F0 MDP_XRT_FLD_ENA
0057  BC (d8)
0058  + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059  BC (c8)
0060  + DC 07-F0 MDP_XRT_ARS_DIS
0061  BC (d5)
0062  + DC 07-F0 MDP_XRT_AEC_RESET
0063  BC (d0)
0064  + DC 07-F0 MDP_XRT_FLD_RESET
0065  BC (da)
0066  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067  BC (c4 06)
0068  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069  BC (c5 0d)
0070  . C. ----- Success Verify ?          OK / NG ____
0071  C.
0072  C.
0073  . C. All OK?  Yes--> Please Proceed. / No --> Stop here.
0074  C.
0075  +. DC 07-F0 MDP_XRT_MODE_OBSV
0076  BC (c2)
0077  +. TI 2019-03-05 10:02:02.0
0078  DC 07-F0 MDP_XRT_MODE_OBSV
0079  BC (c2)
0080  . C. ----- Success Verify ?          OK / NG ____
0081  C.
0082  C. ***** XRT END *****
0083  C.
0084  . C. ***** MDP `úÁí□í»ò¼Y□ÈÁ□□¹□èDCBC•x²è *****
0085  C. (¼ã°íYÓYÁYÉY¥YÉYáYçYè□È¼°¼Á»Ü¹□è)
0086  . S. DC-BC dcbc-402:DCBC
0087  (MDP_known_event)
0088  C.
0089  C.
0090  . C. ***** YDY¹•í Daily±¿íÑ□È´Ø□¹□èDCBC•x²è *****
0091  . S. DC-BC dcbc-153:DCBC
0092  (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0093  C.
0094  C.
0095  . C. ;ãLOSÁY$YÁY-¼Á»Ü;ã
```

0096 C.  
0097 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0098 C.

Mar 05, 19 11:18

XRT\_OGLIST\_0098.chk

Page 1/7

\*\*\* OP Sequence for XRT \*\*\*

2019/03/05	10:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/05	10:11:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/05	10:11:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/05	10:11:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/03/05	10:12:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/05	10:12:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/05	10:12:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2019/03/05	10:13:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 b3 03 01 ca				
2019/03/05	10:13:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/03/05	10:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/03/05	10:13:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/03/05	10:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/03/05	10:13:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/05	10:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/03/05	10:15:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2019/03/05	10:15:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2019/03/05	10:28:00.0	XRT_Custom_430_OG [0x1ae]							
2019/03/05	10:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/05	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/05	15:36:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/05	15:36:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/05	15:36:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/03/05	15:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/03/05	15:49:00.0	XRT_Custom_430_OG [0x1ae]							
2019/03/05	15:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/05	16:12:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/05	16:12:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/05	16:12:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2019/03/05	16:13:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2019/03/05	16:13:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2019/03/05	16:13:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2019/03/05	16:13:22.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/03/05	16:15:58.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2019/03/05	16:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/05	16:22:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/05	16:22:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/05	16:22:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2019/03/05	16:23:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03 03 02 01 ca				
2019/03/05	16:23:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/03/05	16:23:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/03/05	16:23:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/03/05	16:23:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/03/05	16:23:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/05	16:25:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2019/03/05	16:25:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2019/03/05	16:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/05	17:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							

2019/03/05	17:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/05	17:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/05	17:11:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/05	17:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/05	17:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/05	17:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2019/03/05	18:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2019/03/05	18:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/05	18:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/05	18:48:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/05	18:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/05	19:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/05	19:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/05	20:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2019/03/05	20:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/05	20:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/05	20:25:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/05	20:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/05	20:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/05	20:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/05	22:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2019/03/05	22:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/05	22:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/05	22:03:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/05	22:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/05	22:25:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/05	22:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/05	23:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2019/03/05	23:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/05	23:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/05	23:40:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/05	23:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/05	23:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/05	23:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/06	01:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2019/03/06	01:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/06	01:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/06	01:15:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/06	01:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/06	01:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/06	01:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/06	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2019/03/06	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/06	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/06	02:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/03/06	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCS_Ore-point_Start_2_OG [0x098]					
2019/03/06	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	5	02-76	00 00 00 00 00		
2019/03/06	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/03/06	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_ARS_DIS	1	07-F0	d5		



Mar 05, 19 11:18

## XRT\_OGLIST\_0098.chk

Page 3/7

2019/03/06	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/06	02:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2019/03/06	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/03/06	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	02:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	02:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	02:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/06	02:39:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/06	02:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/06	02:59:30.0	XRT_Custom_430_OG [0x1ae]					
2019/03/06	03:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	04:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	04:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	04:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/06	04:10:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/06	04:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/06	04:37:00.0	XRT_Custom_430_OG [0x1ae]					
2019/03/06	04:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	05:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	05:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	05:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/06	05:47:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/06	05:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/06	06:14:00.0	XRT_Custom_430_OG [0x1ae]					
2019/03/06	06:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	06:23:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	06:23:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	06:23:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2019/03/06	06:24:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2019/03/06	06:24:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2019/03/06	06:24:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/03/06	06:26:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2019/03/06	06:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	06:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	06:33:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	06:33:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2019/03/06	06:34:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03	03 02 01 ca
2019/03/06	06:34:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/03/06	06:34:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/03/06	06:34:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/03/06	06:34:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/03/06	06:34:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/06	06:36:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2019/03/06	06:36:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/03/06	06:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	07:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	07:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	07:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	

Mar 05, 19 11:18

## XRT\_OGLIST\_0098.chk

Page 4/7

2019/03/06	07:27:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/03/06	07:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/03/06	07:51:30.0	XRT_Custom_430_OG [0x1ae]						
2019/03/06	07:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/03/06	09:07:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	09:07:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	09:07:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/03/06	09:07:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/03/06	09:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/03/06	09:27:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	09:27:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	09:27:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2019/03/06	09:28:00.0	AOCS_Ore-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	00 56 97 01 ca			
2019/03/06	09:28:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/03/06	09:28:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/03/06	09:28:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/03/06	09:28:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/03/06	09:28:26.0	XRT_FLD_RESET_434_OG [0x1b2]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/03/06	09:30:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2019/03/06	09:30:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2019/03/06	09:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/03/06	10:48:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	10:48:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	10:48:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/03/06	10:48:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/03/06	10:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/03/06	10:59:00.0	XRT_Custom_430_OG [0x1ae]						
2019/03/06	11:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/03/06	14:39:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	14:39:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	14:39:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/03/06	14:39:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/03/06	14:41:30.0	XRT_Custom_430_OG [0x1ae]						
2019/03/06	14:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/03/06	14:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/03/06	16:10:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	16:10:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	16:10:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/03/06	16:10:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/03/06	16:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/03/06	16:33:30.0	XRT_Custom_430_OG [0x1ae]						
2019/03/06	16:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/03/06	16:57:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	16:57:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/03/06	16:57:58.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2019/03/06	16:58:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2019/03/06	16:58:18.0	XRT_FLD_DIS_409_OG [0x199]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2019/03/06	16:58:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2019/03/06	16:58:22.0	XRT_ARS_DIS_443_OG [0x1bb]						

2019/03/06	17:00:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/03/06	17:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2019/03/06	17:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	17:07:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	17:07:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	17:08:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2019/03/06	17:08:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	03 02 01 ca
2019/03/06	17:08:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/03/06	17:08:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/03/06	17:08:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/03/06	17:08:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/03/06	17:10:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/06	17:10:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2019/03/06	17:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/03/06	17:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	17:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	17:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	17:46:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/06	17:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/06	18:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/06	18:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2019/03/06	19:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/03/06	19:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	19:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	19:23:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	19:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/06	19:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/06	19:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/06	21:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/03/06	21:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	21:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	21:00:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	21:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/06	21:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/06	21:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/06	22:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/03/06	22:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/06	22:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	22:38:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/06	22:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/06	22:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/06	23:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/07	00:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/03/07	00:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/07	00:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/07	00:15:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/07	00:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/07	00:26:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/07	00:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

2019/03/07	01:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/07	01:41:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	01:41:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	01:41:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/07	01:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/07	01:58:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/07	01:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/07	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/03/07	02:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2019/03/07	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/03/07	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/03/07	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/03/07	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/03/07	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/07	02:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2019/03/07	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/03/07	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/07	03:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	03:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	03:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/07	03:13:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/07	03:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/07	03:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/07	03:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	04:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	04:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	04:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/07	04:42:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/07	04:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/07	05:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/07	05:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	05:55:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	05:55:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	05:55:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/03/07	05:55:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2019/03/07	05:55:50.5	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2019/03/07	05:55:52.5	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/03/07	05:58:28.5	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2019/03/07	05:58:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/07	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	06:05:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/07	06:05:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2019/03/07	06:05:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 03 02 01 ca		
2019/03/07	06:05:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/03/07	06:05:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/03/07	06:05:52.0	XRT_AEC_RESET_448_OG [0x1c0]						

2019/03/07	06:05:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2019/03/07	06:05:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/03/07	06:08:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/03/07	06:08:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2019/03/07	06:08:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/03/07	06:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/03/07	06:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/03/07	06:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/03/07	06:23:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/03/07	06:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/03/07	06:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/03/07	06:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG			
2019/03/07	08:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/03/07	08:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/03/07	08:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/03/07	08:03:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/03/07	08:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/03/07	08:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/03/07	08:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG			
2019/03/07	09:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/03/07	09:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/03/07	09:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/03/07	09:42:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/03/07	09:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/03/07	10:02:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/03/07	10:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG			
2019/03/07	10:15:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/03/07	10:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/03/07	10:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/03/07	10:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00