

XRT Timeline to be uploaded on 2019/03/23

Period: 2019/03/23 10:48:00 - 2019/03/28 10:28:00

* * * * *

Normal mode

* * * * *

XOB #1BB9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
03/23 11:08:30 - 03/23 17:56:00	Track (881.4, 150.9) @ 03/23 10:58:00	# OP start + 10min AR12736
03/24 01:03:00 - 03/24 06:03:54	Track (923.4, 135.9) @ 03/24 01:00:00	# AR12736
03/24 06:17:00 - 03/24 17:59:54	Track (934.8, 130.1) @ 03/24 06:14:00	# AR12736
03/25 01:08:00 - 03/25 06:05:54	Track (854.9, 65.8) @ 03/25 01:00:00	# AR12735
03/25 06:23:00 - 03/25 10:29:30	Track (876.3, 60.4) @ 03/25 06:16:00	# AR12735
03/25 18:06:30 - 03/25 21:59:54	Track (915.7, 47.9) @ 03/25 18:03:30	# AR12735
03/26 01:03:00 - 03/26 05:37:54	Track (933.3, 40.4) @ 03/26 01:00:00	# AR12735
03/26 05:51:00 - 03/26 11:04:54	Fixed (942.7, 35.4)	# AR12735, fixed pointing

PROG= 17 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 92 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
 - Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Subr= 2 5-time(s) 2.0sec
 - Seqn= 75 1-time(s) 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Seqn= 96 4-time(s) 90.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
----------------	----------------	-----	------	-------	------	-----	-----	--------------------	-------	------------	----------

XOB #1BC0: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Th

Term	Pointing (x, y)	Comment
03/23 18:23:00 - 03/23 18:29:54	Fixed (0.0, 0.0)	synoptic, shifted 20.0 min
03/24 06:07:00 - 03/24 06:13:54	Fixed (0.0, 0.0)	synoptic, shifted 4.0 min
03/24 18:03:00 - 03/24 18:09:54	Fixed (0.0, 0.0)	synoptic
03/25 06:09:00 - 03/25 06:15:54	Fixed (0.0, 0.0)	synoptic, shifted 6.0 min
03/25 17:56:00 - 03/25 18:03:24	Fixed (0.0, 0.0)	synoptic, shifted -6.5 min
03/26 05:41:00 - 03/26 05:47:54	Fixed (0.0, 0.0)	synoptic, shifted -22.0 min

PROG= 11 1-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 5 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - Seqn= 49 1-time(s) 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 72 1-time(s) 2.0sec
 - Al-poly/Open Al-poly/Open close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 64 1-time(s) 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 23 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
 - Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
----------------	----------------	-----	------	-------	------	-----	-----	--------------------	-------	------------	----------

XOB #1BA1: HOP336 1-filter - Al/poly -384x384, 512ms and 4s, 60s-cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
03/23 18:33:00 - 03/23 21:59:54	Track (4.5, -90.0) @ 03/23 18:30:00	# HOP370
03/24 18:13:00 - 03/24 21:45:30	Track (1.2, -419.9) @ 03/24 18:10:00	# HOP370
03/25 10:47:00 - 03/25 14:13:54	Fixed (0.0, 704.0)	# HOP336 Northern Hemisphere
03/25 14:17:00 - 03/25 17:29:00	Fixed (0.0, -596.0)	# HOP336 Southern Hemisphere

PROG= 01 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec

└─ Seqn= 16 2-time(s) 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec	
└─ Seqn= 90 1-time(s) 30.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
└─ Subr= 3 15-time(s) 2.0sec	
└─ Seqn= 68 1-time(s) 60.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
└─ Al-poly/Open	Al-poly/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1C2C: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[1024/11571/23142] with 512x512 G-band+Leak - 45min cad) +

Term	Pointing (x, y)	Comment
03/23 22:03:00 - 03/24 00:59:54	Fixed (0.0, 0.0)	# HOP349
03/24 22:09:30 - 03/25 00:59:54	Fixed (0.0, 0.0)	# HOP349
03/25 22:03:00 - 03/26 00:59:54	Fixed (0.0, 0.0)	# HOP349

PROG= 10 Inf.-time(s)

└─ Subr= 1 1-time(s) 600.0sec	
└─ Seqn= 1 1-time(s) 2.0sec	
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 99 1-time(s) 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open	Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Al-poly/Open	Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 33 1-time(s) 2.0sec	
└─ thin-Be/Open	thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open	thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open	thin-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 30 1-time(s) 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
└─ Subr= 2 4-time(s) 600.0sec	
└─ Seqn= 8 1-time(s) 2.0sec	
└─ thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
└─ Al-poly/Open	Al-poly/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
└─ Seqn= 29 1-time(s) 2.0sec	
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Flare mode

* * * * *

XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
03/23 11:08:30 - 03/23 17:56:00	Track (881.4, 150.9) @ 03/23 10:58:00	# OP start + 10min AR12736
03/23 18:33:00 - 03/23 21:59:54	Track (4.5, -90.0) @ 03/23 18:30:00	# HOP370
03/23 22:03:00 - 03/24 00:59:54	Fixed (0.0, 0.0)	# HOP349
03/24 01:03:00 - 03/24 06:03:54	Track (923.4, 135.9) @ 03/24 01:00:00	# AR12736
03/24 06:17:00 - 03/24 17:59:54	Track (934.8, 130.1) @ 03/24 06:14:00	# AR12736
03/24 18:13:00 - 03/24 21:45:30	Track (1.2, -419.9) @ 03/24 18:10:00	# HOP370
03/24 22:09:30 - 03/25 00:59:54	Fixed (0.0, 0.0)	# HOP349
03/25 01:08:00 - 03/25 06:05:54	Track (854.9, 65.8) @ 03/25 01:00:00	# AR12735
03/25 06:23:00 - 03/25 10:29:30	Track (876.3, 60.4) @ 03/25 06:16:00	# AR12735
03/25 10:47:00 - 03/25 14:13:54	Fixed (0.0, 704.0)	# HOP336 Northern Hemisphere
03/25 14:17:00 - 03/25 17:29:00	Fixed (0.0, -596.0)	# HOP336 Southern Hemisphere
03/25 18:06:30 - 03/25 21:59:54	Track (915.7, 47.9) @ 03/25 18:03:30	# AR12735
03/25 22:03:00 - 03/26 00:59:54	Fixed (0.0, 0.0)	# HOP349
03/26 01:03:00 - 03/26 05:37:54	Track (933.3, 40.4) @ 03/26 01:00:00	# AR12735
03/26 05:51:00 - 03/26 11:04:54	Fixed (942.7, 35.4)	# AR12735, fixed pointing

PROG= 13 30-time(s)

└─ Subr= 1 20-time(s) 2.0sec	
└─ Seqn= 11 1-time(s) 2.0sec	
└─ Al-poly/Open	Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Seqn=100 1-time(s) 10.0sec	
└─ thin-Be/Open	med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
└─ med-Be/Open	Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Open/thick-Al	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec	
└─ Seqn= 10 1-time(s) 2.0sec	
└─ med-Al/Open	med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Seqn= 11 1-time(s) 2.0sec	
└─ Al-poly/Open	Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec

Seqn= 87	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

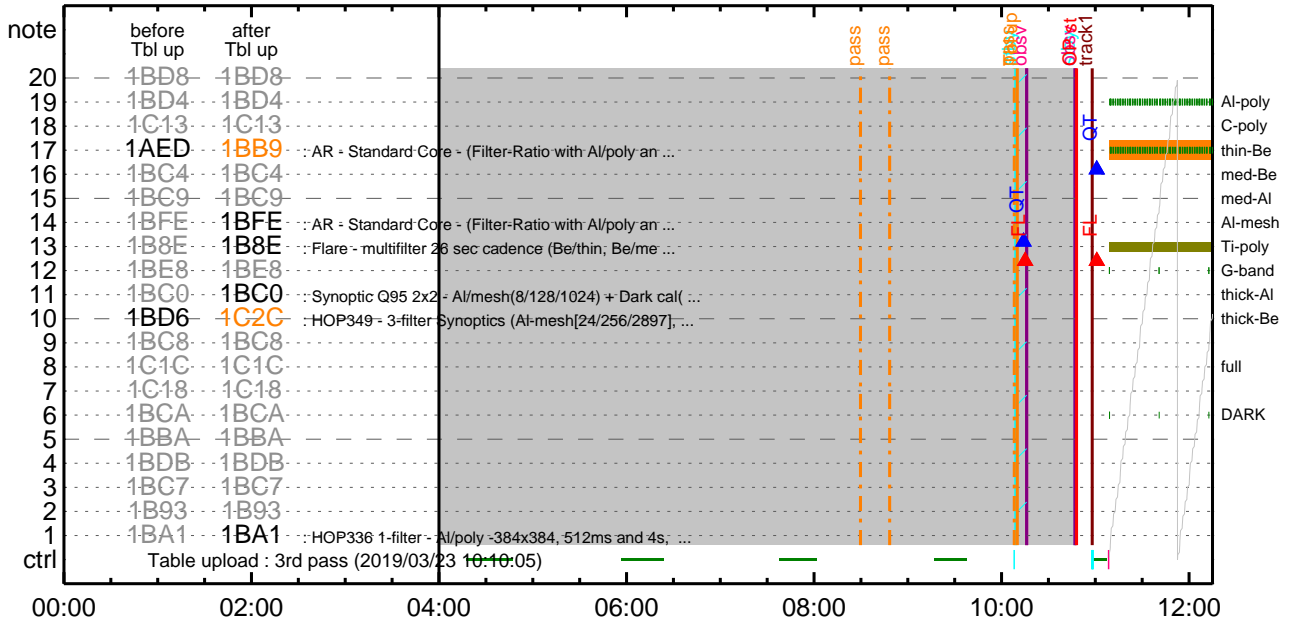
* * * * *

Flare Detection

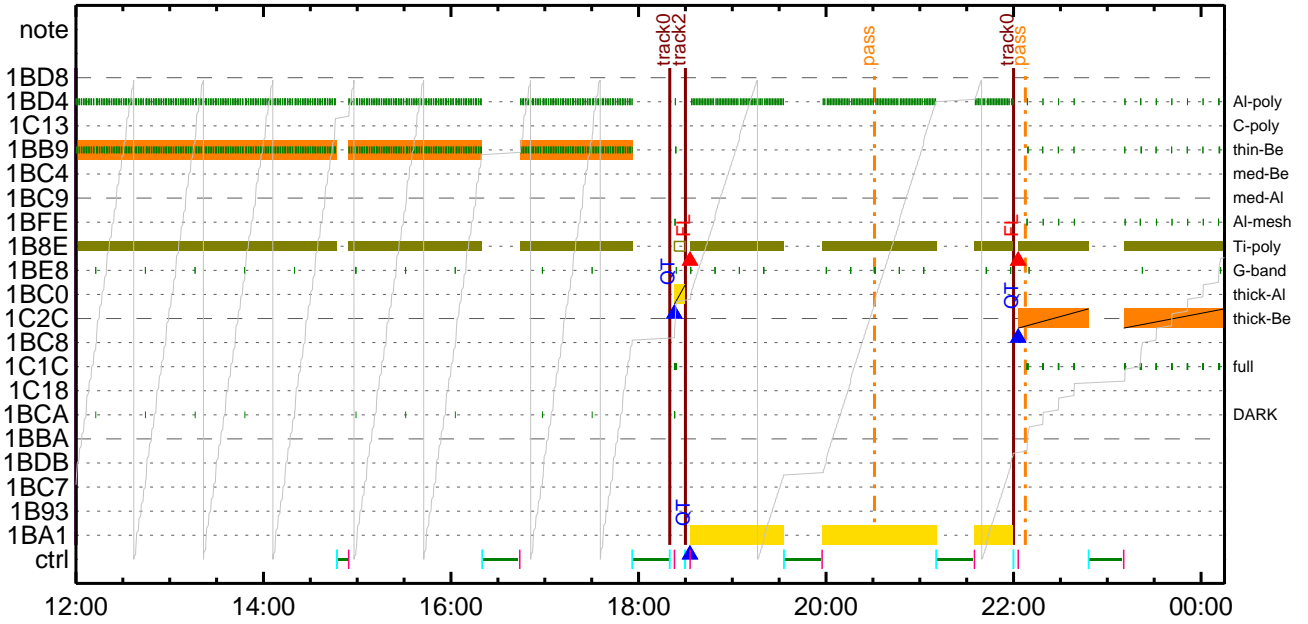
* * * * *

FLD Patrol												
Term	Pointing (x, y)		Comment									
03/23 18:30:18 - 03/24 06:04:18	Track (4.5, -90.0)	@ 03/23 18:30:00	# HOP370								
03/24 06:14:18 - 03/24 18:00:18	Track (934.8, 130.1)	@ 03/24 06:14:00	# AR12736								
03/24 18:10:18 - 03/25 06:06:18	Track (1.2, -419.9)	@ 03/24 18:10:00	# HOP370								
03/25 06:20:18 - 03/25 17:53:18	Track (876.3, 60.4)	@ 03/25 06:16:00	# AR12735								
03/25 18:03:48 - 03/26 05:38:18	Track (915.7, 47.9)	@ 03/25 18:03:30	# AR12735								
03/26 05:48:18 - 03/28 10:28:00	Fixed (942.7, 35.4)		# AR12735, fixed pointing								
AI-poly/Open	AI-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

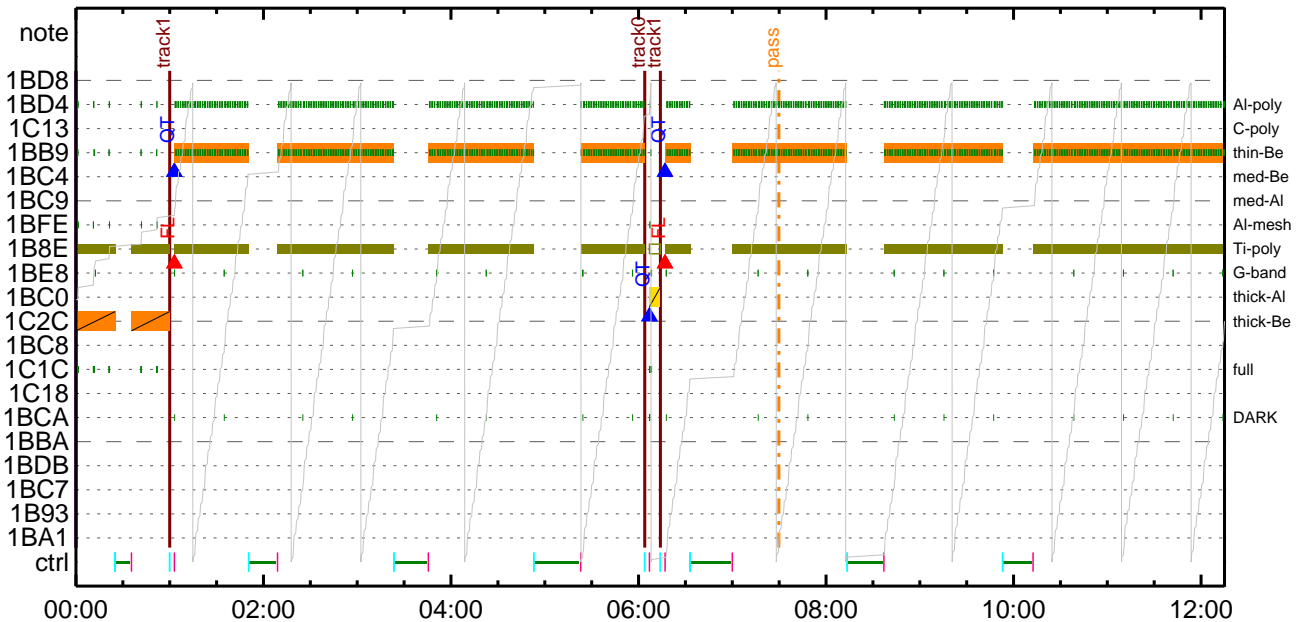
CMDI #0134 2019/03/23



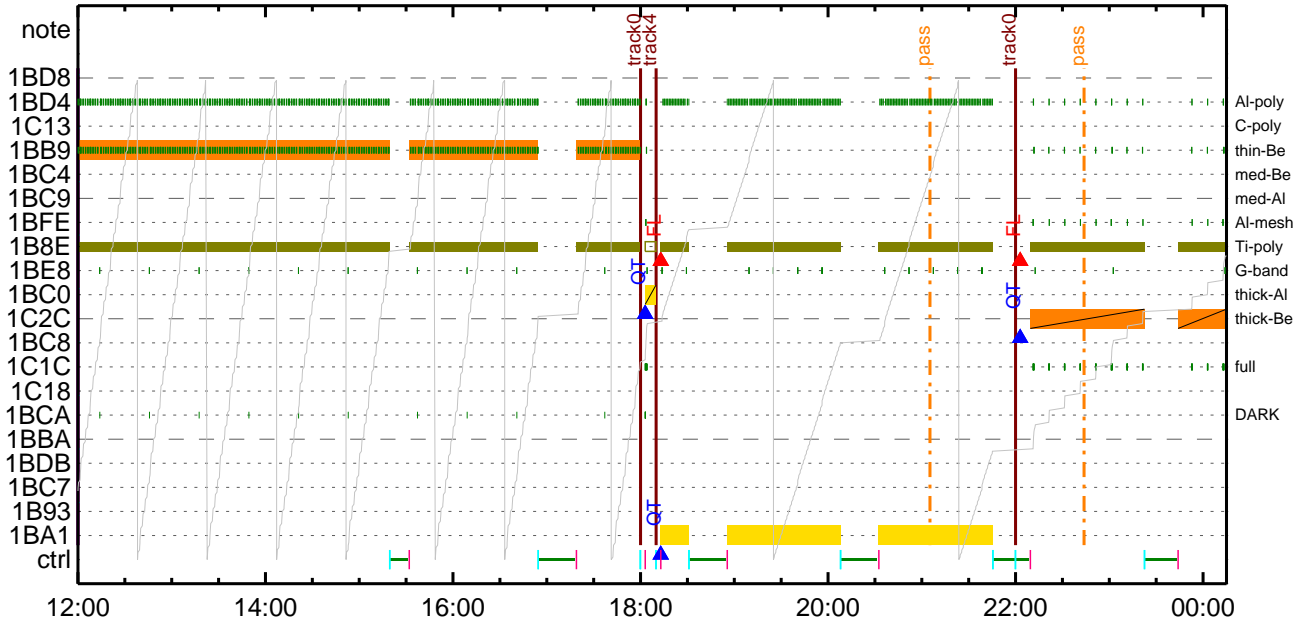
CMDI #0134 2019/03/23



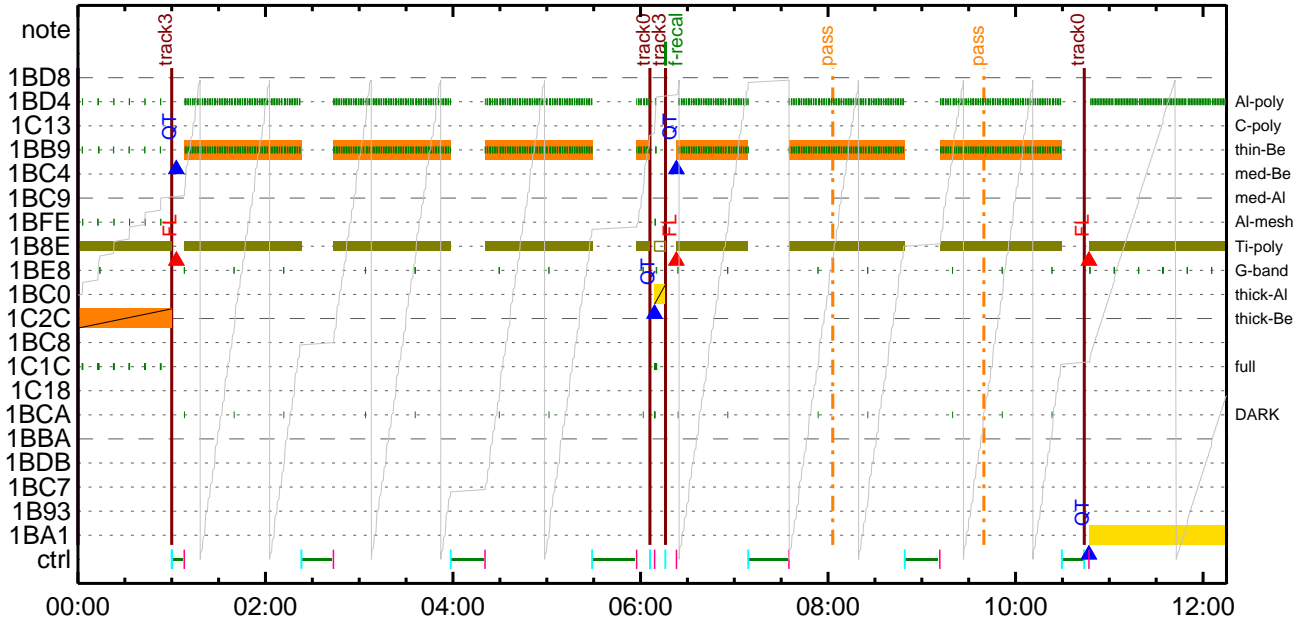
CMDI #0134 2019/03/24



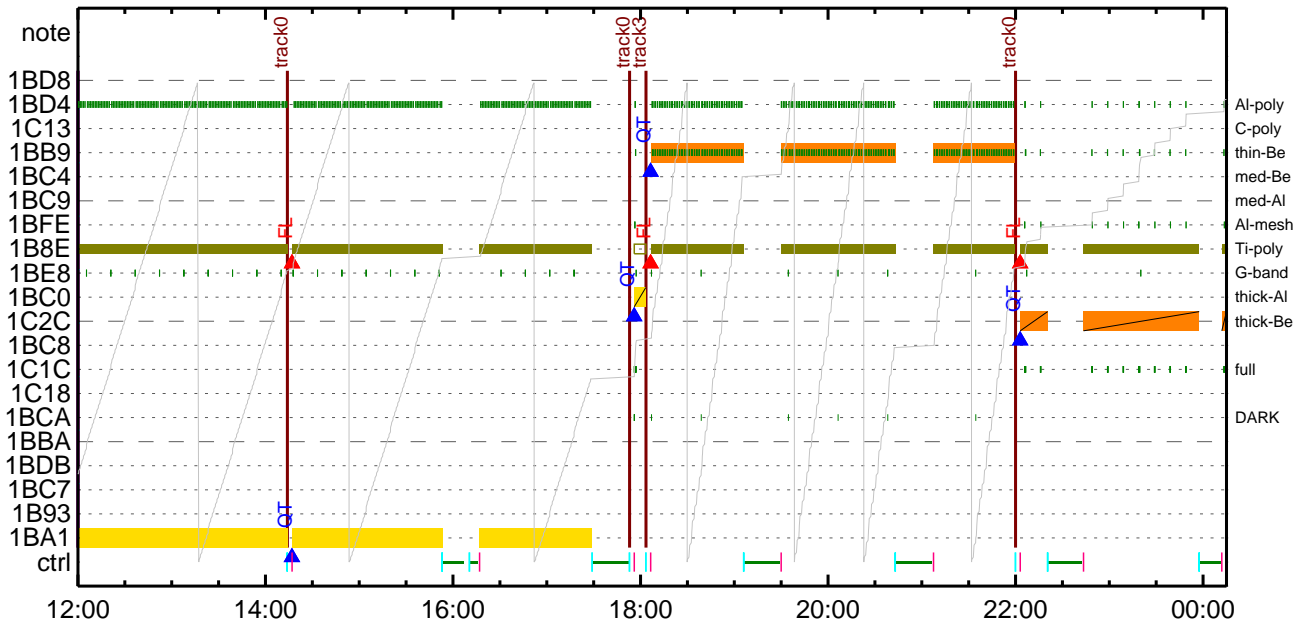
CMDI #0134 2019/03/24



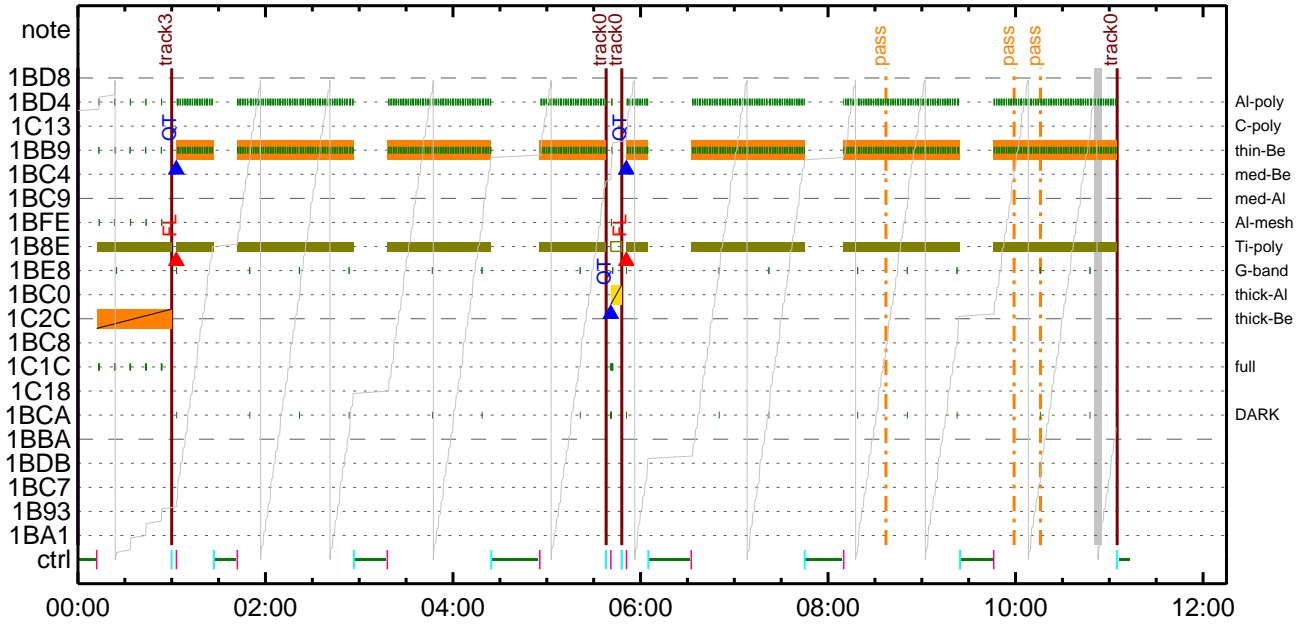
CMDI #0134 2019/03/25



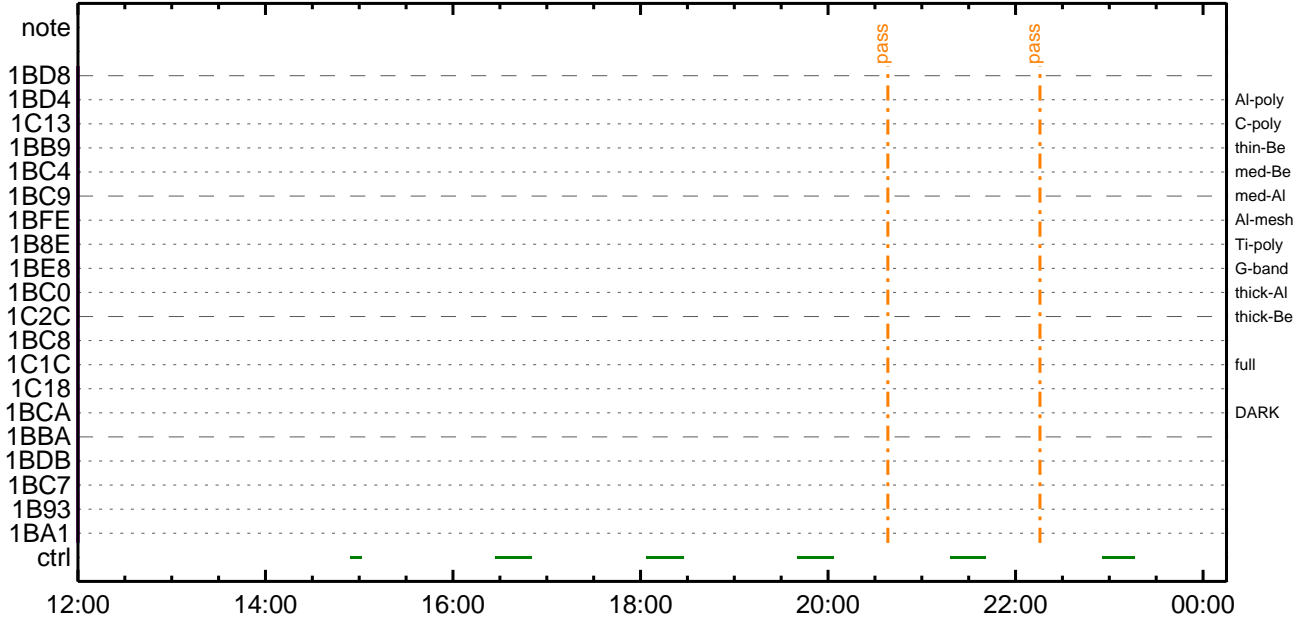
CMDI #0134 2019/03/25



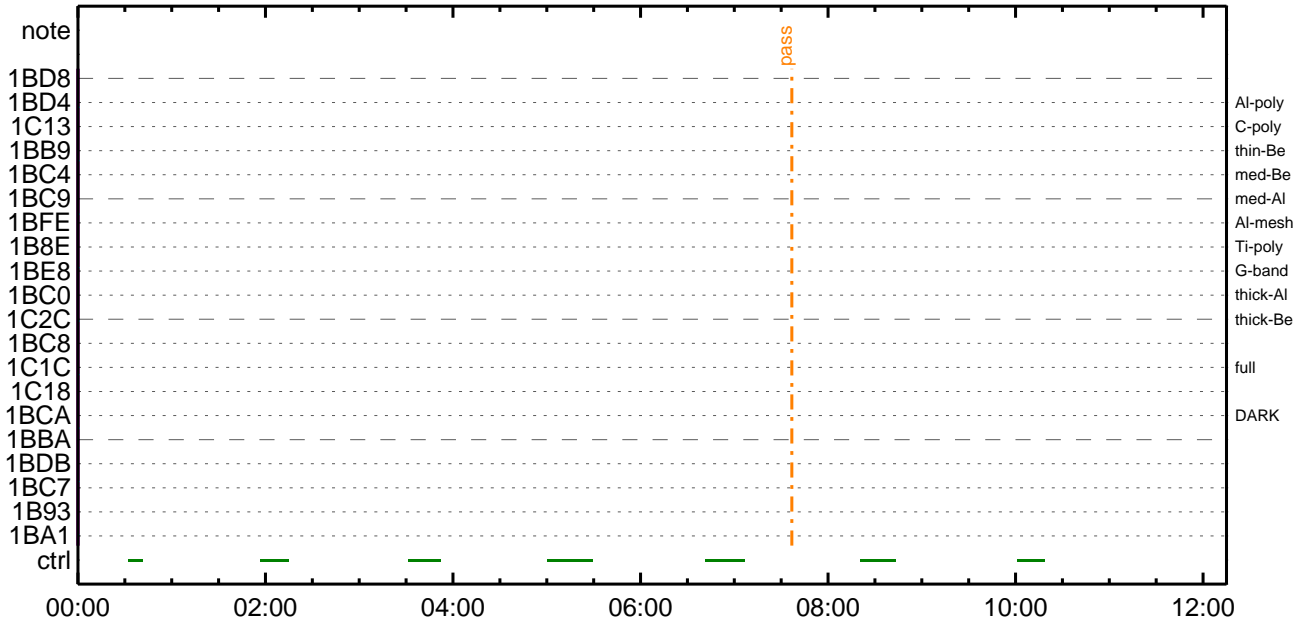
CMDI #0134 2019/03/26



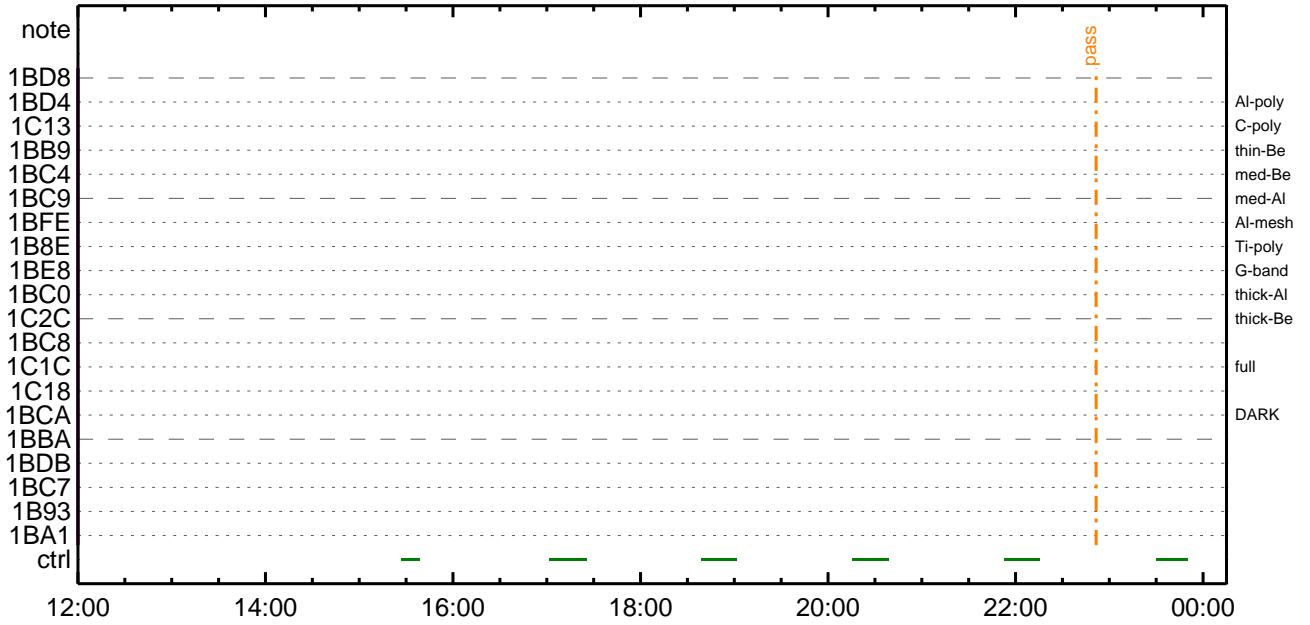
CMDI #0134 2019/03/26



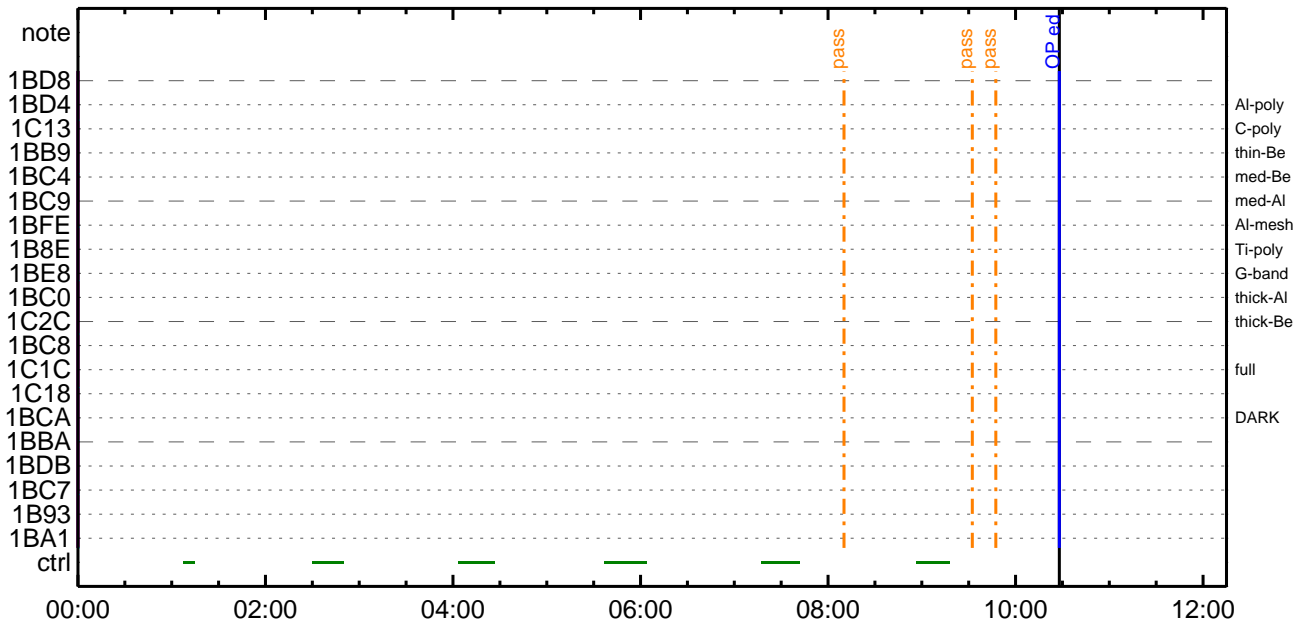
CMDI #0134 2019/03/27



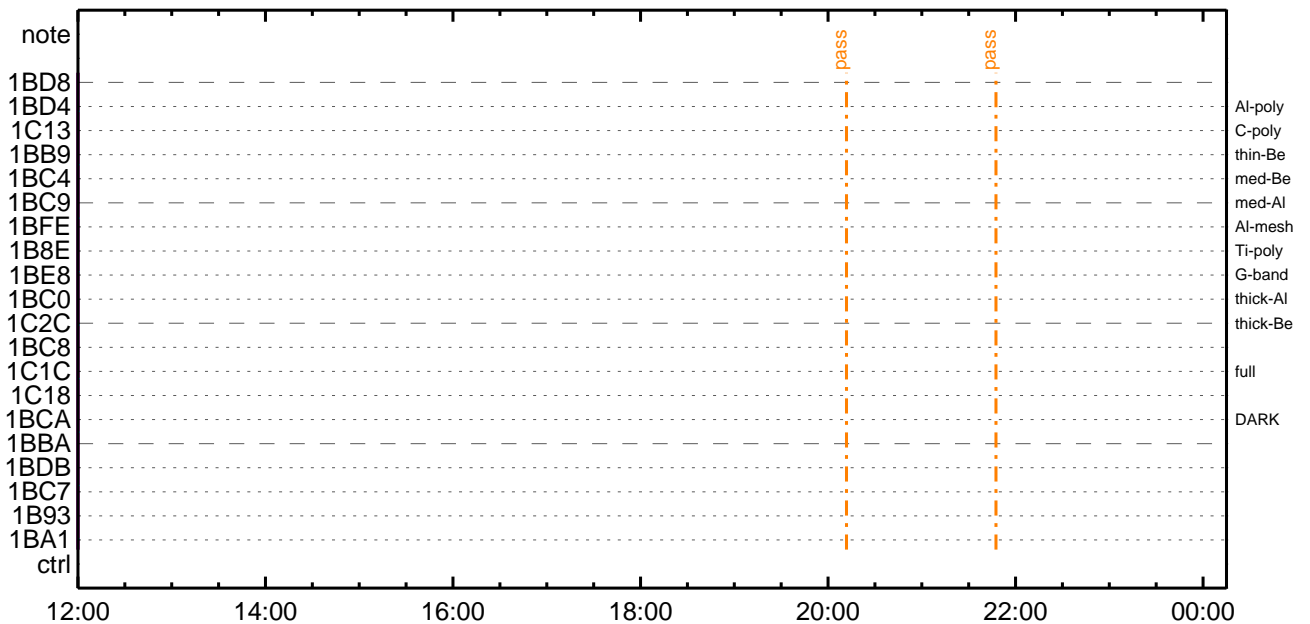
CMDI #0134 2019/03/27



CMDI #0134 2019/03/28



CMDI #0134 2019/03/28




```
0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-997:OP
0104 ( )
0105 S. OG og-997:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGñî¼Ê¹ç•è²îOKð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGñî¼Ê¹ç•è²îOKð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPñî¼Ê¹ç•è²îOKð³îÇ§
0165 C.
0166 C. ***** òÊ²¼òî¼Ã´¶Å°òÊË-ò°À÷¿@ (%âµ-YAYOXx½ªî»ð³îÇ§) *****
0167 C. DHUYâ;4YE;Ê¼Y½;Yî;4YE;Ëòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |§ OPOG UPLOADñ-Á÷¿@NGñî¼Ê¹ç;ç°Ê²¼òî¼TI-CMDÁ÷¿@ñî¼Ê¹Ôñ•ñÊññ³ñÊ;f
0180 C. ñÊñ¿;çSETñEDUMPNñÊ±°îYÑY¹ñÇ¹Ôñ|ñ³ñÊ;f
0181 C.
0182 C. TIY³Y½YOXx½ªî»ð³îÇ§(UT)
0183 +. TI 2019-03-23 10:43:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2019-03-23 10:43:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2019-03-23 10:43:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

0194 C.
0195 +. TI 2019-03-23 10:47:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼õîÄë%îíñõîîŸÄŸ§ŸÄŸ-¹àîŸ
0200 C. ¢¢[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. ¢¢[HK1_TI_CMD_NUM] EQ 4
0202 C. ¢¢[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. ¢¢[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TIîŸ°èŸÄŸÖŸ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0220 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ŸÄŸÖŸ×½ª î»õ³îÇ§
0226 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBLõîŸÈ¹ç•è²îOKõ³îÇ§
0229 C.
0230 C. DHUŸâ;¼ŸÉ;È¼Ÿ¼. Ÿi;¼ŸÈ;Èõðîãõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0234 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2019-03-23 10:47:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP []
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2019-03-23 10:47:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2019-03-23 10:47:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2019-03-23 10:47:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîãî»õ¼ŸõÈÃõ¹õèDCBC•x²è *****
0276 C. (¼ã°îŸÖŸÄŸÈŸŸÈŸáŸçŸèõÈ¼õõ¼Ã»Ÿõ¹õè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŸĐŸ¹•î Daily±çîñõÈ´õõ¹õèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŸÄŸ§ŸÄŸ-¼Ã»Ÿ;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-998 2019-03-23 12:21:09 85 33 SOLAR-B MAIN //
0001   C.
0002   . C. ***** AOS *****
0003   C.
0004   . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005   C.
0006   C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008   C.
0009   . C. ***** AOCs : Reload orbital element (send every contact) *****
0010   C.   Áí;È¿¿ÀB•µ°È»Í×ÁÇ¿ÍYçYÁY×YÍ;¼YÉ;ÈÈÈµ•íÉ;ÈBÈ¼°ÇÒB•B¿¼l¹çBÍ;çÀ®, ùB¹BÈBÞBçÁ+¿®B•BÈBDB³BÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012   C.
0013   C.
0014   . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015   C. Upload the Orbit Element and the Target Attitude
0016   C. RAM-ID:TARGET_ATT
0017   . S. RAM ram-150:TARGET_ATT
0018   ( )
0019   C.
0020   C.
0021   C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023   BC      (07 00 00 00 18 00)
0024   C.
0025   C.      <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026   C.
0027   C.
0028   C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030   BC      (04 0b f8)
0031   C.
0032   C. Wait for AOCSDUMP to end
0033   C.
0034   . C. Check the dump memory
0035   C.
0036   C.                                     Result = OK [ ]
0037   C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039   BC      (02 0a f8)
0040   C.
0041   C.                                     <A_***>[TLM STS] FMT = 2 [ ]
0042   C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_CHG_ENA
0047   BC      (20)
0048   . C. Verify EIS_MODE_CHG_FLG is ENA
0049 +. DC 07-FC EIS_MODE_MANU
0050   BC      (21 02)
0051   . C. Verify EIS in MANUAL mode
0052   . C. Estimated OBSTBL upload time is 27s
0053   C. *****
0054   C. EIS START OBSTBL LOAD
0055   C. *****
0056   . S. RAM ram-820:EIS_OBSTBL
0057   ( )
0058 +. DC 07-FC EIS_DUMP_OBSTBL
0059   BC      (07 07 07 00 00 70 00)
0060   C.
0061   C. Execute, after the success of OBSTBL upload.
0062   C. Set EIS TI-commands
0063 +. TI 2019-03-23 10:47:50.0
0064   DC 07-FC EIS_MODE_CHG_ENA
0065   BC      (20)
0066   . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0067   C. *****
0068   C. EIS END OBSTBL LOAD
0069   C. *****
0070   C.
0071   . C. ***** MDP `ûÁî¿í»ô¼Y¿ÈÁB¿¹BÈDCBC•x²è *****
0072   C. (¼á°ÍYÓYÁYÈYÞYÉYáYçYÈB¼¿B¼Á»Û¹Bè)
0073   . S. DC-BC dcbc-402:DCBC
0074   (MDP_known_event)
0075   C.
0076   C.
0077   . C. ***** YDÝ¹.Ï Daily¿¿ÏÑB`ØB¹BÈDCBC•x²è *****
0078   . S. DC-BC dcbc-153:DCBC
0079   (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080   C.
0081   C.
0082   . C. ;ãLOSÁY$YÁY~¼Á»Û;ã
0083   C.
0084   . C. ***** LOS *****
0085   C.
```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-287:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2019-03-23 10:47:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 06 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 07 80 80 20 20)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 08 80 80 20 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 09 80 80 08 20)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 0a 80 80 08 08)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0f 80 80 06 06)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 10 80 80 08 08)
0170 + DC 07-F0 MDP_XRT_FLD_ENA
0171 BC (d8)
0172 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0173 BC (c8)
0174 + DC 07-F0 MDP_XRT_ARS_DIS
0175 BC (d5)
0176 + DC 07-F0 MDP_XRT_AEC_RESET
0177 BC (d0)
0178 + DC 07-F0 MDP_XRT_FLD_RESET
0179 BC (da)
0180 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0181 BC (c4 0e)
0182 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0183 BC (c5 0d)
0184 . C. ----- Success Verify ? OK / NG ____
0185 C.
0186 C.
0187 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0188 C.
0189 +. DC 07-F0 MDP_XRT_MODE_OBSV
0190 BC (c2)
0191 +. TI 2019-03-23 10:47:02.0
0192 DC 07-F0 MDP_XRT_MODE_OBSV
0193 BC (c2)

```


Mar 23, 19 12:21

XRT_OGLIST_0134.chk

Page 1/11

*** OP Sequence for XRT ***

2019/03/23	10:57:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	10:57:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	10:57:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2019/03/23	10:58:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2019/03/23	10:58:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/03/23	10:58:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/03/23	10:58:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/03/23	10:58:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/03/23	10:58:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/23	10:58:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	10:58:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	10:58:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/23	10:58:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/23	11:00:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2019/03/23	11:00:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/03/23	11:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/23	11:07:30.0	XRT_Custom_430_OG [0x1ae]					
2019/03/23	11:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/23	14:47:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	14:47:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	14:47:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/23	14:47:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/23	14:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/23	14:53:30.0	XRT_Custom_430_OG [0x1ae]					
2019/03/23	14:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/23	16:20:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	16:20:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	16:20:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/23	16:20:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/23	16:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/23	16:43:00.0	XRT_Custom_430_OG [0x1ae]					
2019/03/23	16:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/23	17:56:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	17:56:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	17:56:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/03/23	17:56:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/03/23	17:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/03/23	18:19:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	18:19:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/03/23	18:19:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/03/23	18:20:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2019/03/23	18:20:18.0	XRT_FLD_DIS_409_OG [0x199]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2019/03/23	18:20:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2019/03/23	18:20:22.0	XRT_ARS_DIS_443_OG [0x1bb]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/03/23	18:22:58.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2019/03/23	18:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/03/23	18:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		

Saturday March 23, 2019

1/11

2019/03/23	18:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/23	18:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2019/03/23	18:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2019/03/23	18:30:18.0	XRT_FLD_ENA_425_OG [0x1a9]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/03/23	18:32:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/03/23	18:32:50.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/03/23	18:32:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/03/23	18:32:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/23	18:32:56.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2019/03/23	18:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2019/03/23	18:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/23	19:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/23	19:33:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/23	19:33:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/23	19:33:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/03/23	19:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/03/23	19:56:30.0	XRT_Custom_430_OG [0x1ae]							
2019/03/23	19:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/23	21:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/23	21:10:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/23	21:10:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/23	21:10:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/03/23	21:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/03/23	21:34:00.0	XRT_Custom_430_OG [0x1ae]							
2019/03/23	21:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/23	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/23	21:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/23	21:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2019/03/23	22:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2019/03/23	22:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/03/23	22:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/03/23	22:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/03/23	22:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/03/23	22:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/23	22:02:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2019/03/23	22:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2019/03/23	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/23	22:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/23	22:48:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/23	22:48:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/23	22:48:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/03/23	22:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/03/23	23:09:30.0	XRT_Custom_430_OG [0x1ae]							
2019/03/23	23:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/24	00:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/24	00:25:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/24	00:25:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/24	00:25:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

2019/03/24	00:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2019/03/24	00:34:30.0	XRT_Custom_430_OG [0x1ae]									
2019/03/24	00:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2019/03/24	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	00:59:56.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	00:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]									
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00						
2019/03/24	01:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]									
		AOCU_NM	5	02-76	01 00 00 00 00						
2019/03/24	01:00:18.0	XRT_FLD_ENA_411_OG [0x19b]									
		MDP_XRT_FLD_ENA	1	07-F0	d8						
2019/03/24	01:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]									
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8						
2019/03/24	01:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]									
		MDP_XRT_AEC_RESET	1	07-F0	d0						
2019/03/24	01:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]									
		MDP_XRT_ARS_DIS	1	07-F0	d5						
2019/03/24	01:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2019/03/24	01:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]									
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11						
2019/03/24	01:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]									
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d						
2019/03/24	01:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2019/03/24	01:50:30.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	01:50:32.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	01:50:34.0	XRT_FLD_RESET_415_OG [0x19f]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2019/03/24	01:50:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2019/03/24	01:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2019/03/24	02:08:00.0	XRT_Custom_430_OG [0x1ae]									
2019/03/24	02:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2019/03/24	03:23:30.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	03:23:32.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	03:23:34.0	XRT_FLD_RESET_415_OG [0x19f]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2019/03/24	03:23:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2019/03/24	03:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2019/03/24	03:44:30.0	XRT_Custom_430_OG [0x1ae]									
2019/03/24	03:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2019/03/24	04:53:00.0	XRT_CTRL_MANU_400_OG [0x190]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	04:53:02.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	04:53:04.0	XRT_FLD_RESET_415_OG [0x19f]									
		MDP_XRT_FLD_RESET	1	07-F0	da						
2019/03/24	04:53:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]									
		MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2019/03/24	04:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]									
		MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2019/03/24	05:22:00.0	XRT_Custom_430_OG [0x1ae]									
2019/03/24	05:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2019/03/24	06:03:54.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	06:03:56.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	06:03:58.0	XRT_FOCUS_POSITION_406_OG [0x196]									
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00						
2019/03/24	06:04:00.0	AOCS_Ore-point_Start_2_OG [0x098]									
		AOCU_NM	5	02-76	00 00 00 00 00						
2019/03/24	06:04:18.0	XRT_FLD_DIS_409_OG [0x199]									
		MDP_XRT_FLD_DIS	1	07-F0	d9						
2019/03/24	06:04:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]									
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9						
2019/03/24	06:04:22.0	XRT_ARS_DIS_443_OG [0x1bb]									
		MDP_XRT_ARS_DIS	1	07-F0	d5						
2019/03/24	06:06:58.0	XRT_QT_PROG_SET_404_OG [0x194]									
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b						
2019/03/24	06:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]									
		MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2019/03/24	06:13:54.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	06:13:56.0	XRT_CTRL_MANU_402_OG [0x192]									
		MDP_XRT_CTRL_MANU	1	07-F0	c1						
2019/03/24	06:13:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]									
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00						
2019/03/24	06:14:00.0	AOCS_Ore-point_Start_1_OG [0x097]									

2019/03/24	06:14:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01	00	00	00	00
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2019/03/24	06:14:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	d0				
2019/03/24	06:14:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d5				
2019/03/24	06:14:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	da				
2019/03/24	06:14:26.0	XRT_FLD_RESET_434_OG [0x1b2]		1	07-F0	da				
		MDP_XRT_FLD_RESET		2	07-F0	c4	11			
2019/03/24	06:16:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]		2	07-F0	c4	11			
		MDP_XRT_QT_PROG_SET		2	07-F0	c5	0d			
2019/03/24	06:16:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]		2	07-F0	c5	0d			
		MDP_XRT_FL_PROG_SET		1	07-F0	c0				
2019/03/24	06:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2019/03/24	06:33:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/24	06:33:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	da				
2019/03/24	06:33:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	e8				
2019/03/24	06:33:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e9				
2019/03/24	06:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	c0				
2019/03/24	06:59:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c1				
2019/03/24	07:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c1				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2019/03/24	08:13:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/24	08:13:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	da				
2019/03/24	08:13:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	e8				
2019/03/24	08:13:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e9				
2019/03/24	08:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	c0				
2019/03/24	08:36:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c1				
2019/03/24	08:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c1				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2019/03/24	09:53:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/24	09:53:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	da				
2019/03/24	09:53:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	e8				
2019/03/24	09:53:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e9				
2019/03/24	09:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	c0				
2019/03/24	10:11:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c1				
2019/03/24	10:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c1				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2019/03/24	15:19:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/24	15:19:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	da				
2019/03/24	15:19:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	e8				
2019/03/24	15:19:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e9				
2019/03/24	15:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	c0				
2019/03/24	15:31:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c1				
2019/03/24	15:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c1				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2019/03/24	16:54:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/24	16:54:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	da				
2019/03/24	16:54:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	e8				
2019/03/24	16:54:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e9				
2019/03/24	16:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	c0				
2019/03/24	17:18:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c1				
2019/03/24	17:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c1				
		MDP_XRT_CTRL_AUTO		1	07-F0	c1				
2019/03/24	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/24	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		4	07-F8	22	ff	aa	00	
2019/03/24	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]		4	07-F8	22	ff	aa	00	
		XRT_FOCUS_POSITION		1	07-F0	d9				
2019/03/24	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]		5	02-76	00	00	00	00	00
		AOCU_NM		1	07-F0	d9				
2019/03/24	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]		1	07-F0	d9				
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2019/03/24	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]		1	07-F0	d9				

2019/03/24	18:00:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/03/24	18:02:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2019/03/24	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/24	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2019/03/24	18:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04	00 00 00 00
2019/03/24	18:10:18.0	XRT_FLD_ENA_425_OG [0x1a9]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/03/24	18:12:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/03/24	18:12:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/03/24	18:12:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/03/24	18:12:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/24	18:12:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2019/03/24	18:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/03/24	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/24	18:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	18:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	18:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/24	18:31:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/24	18:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/24	18:54:30.0	XRT_Custom_430_OG [0x1ae]					
2019/03/24	18:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/24	20:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	20:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	20:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/24	20:08:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/24	20:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/24	20:31:30.0	XRT_Custom_430_OG [0x1ae]					
2019/03/24	20:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/24	21:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	21:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	21:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/24	21:45:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/24	21:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/24	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	21:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	21:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2019/03/24	22:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2019/03/24	22:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/03/24	22:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/03/24	22:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/03/24	22:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/03/24	22:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/24	22:02:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2019/03/24	22:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/03/24	22:08:30.0	XRT_Custom_430_OG [0x1ae]					
2019/03/24	22:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/24	23:22:30.0	XRT_CTRL_MANU_400_OG [0x190]					

2019/03/24	23:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	23:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/24	23:22:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/24	23:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/24	23:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/24	23:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2019/03/25	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0	
2019/03/25	00:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/25	00:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	01:00:00.5	AOCS_Ore-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2019/03/25	01:00:01.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	03 00 00 00 00	
2019/03/25	01:00:03.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1	
2019/03/25	01:00:05.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	01:00:07.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	01:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da	
2019/03/25	01:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/25	01:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/25	01:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/03/25	01:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/03/25	01:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	d0	
2019/03/25	01:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_ARS_RESET	1	07-F0	d0	
2019/03/25	01:03:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5	
2019/03/25	01:07:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/03/25	01:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_FLD_RESET_434_OG [0x1b2]	1	07-F0	da	
2019/03/25	02:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/25	02:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_QT_PROG_SET_416_OG [0x1a0]	2	07-F0	c4 11	
2019/03/25	02:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2019/03/25	02:23:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	XRT_FL_PROG_SET_440_OG [0x1b8]	2	07-F0	c5 0d	
2019/03/25	02:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2019/03/25	02:42:30.0	XRT_Custom_430_OG [0x1ae]	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9	
2019/03/25	02:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/25	03:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_430_OG [0x1ae]				
2019/03/25	03:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0	
2019/03/25	03:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/25	03:58:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	04:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	04:19:30.0	XRT_Custom_430_OG [0x1ae]	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da	
2019/03/25	04:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/25	05:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_PREFLR_STRT_428_OG [0x1ac]	1	07-F0	e8	
2019/03/25	05:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/25	05:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9	
2019/03/25	05:29:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/25	05:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	XRT_Custom_430_OG [0x1ae]				
2019/03/25	05:56:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0	
2019/03/25	05:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/25	06:05:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	06:05:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	06:05:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1	
2019/03/25	06:06:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	

2019/03/25	06:06:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2019/03/25	06:06:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2019/03/25	06:06:22.0	XRT_ARS_DIS_443_OG [0x1bb]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2019/03/25	06:08:58.0	XRT_QT_PROG_SET_404_OG [0x194]		2	07-F0	c4	0b			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0b			
2019/03/25	06:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2019/03/25	06:15:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/25	06:15:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/25	06:15:58.0	XRT_FOCUS_RECALIBRATE_417_OG [0x1a1]		2	07-F8	78	00			
		XRT_FOCUS_RECAL		2	07-F8	78	00			
2019/03/25	06:16:00.0	AOCS_Ore-point_Start_5_OG [0x09b]		5	02-76	03	00	00	00	00
		AOCU_NM		5	02-76	03	00	00	00	00
2019/03/25	06:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2019/03/25	06:20:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2019/03/25	06:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2019/03/25	06:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2019/03/25	06:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2019/03/25	06:20:26.0	XRT_FLD_RESET_434_OG [0x1b2]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2019/03/25	06:22:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]		2	07-F0	c4	11			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11			
2019/03/25	06:22:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]		2	07-F0	c5	0d			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0d			
2019/03/25	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2019/03/25	07:09:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/25	07:09:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/25	07:09:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2019/03/25	07:09:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2019/03/25	07:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2019/03/25	07:34:00.0	XRT_Custom_430_OG [0x1ae]								
2019/03/25	07:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2019/03/25	08:49:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/25	08:49:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/25	08:49:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2019/03/25	08:49:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2019/03/25	08:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2019/03/25	09:10:30.0	XRT_Custom_430_OG [0x1ae]								
2019/03/25	09:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2019/03/25	10:29:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/25	10:29:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/25	10:29:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2019/03/25	10:29:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2019/03/25	10:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2019/03/25	10:43:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/25	10:43:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/25	10:43:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2019/03/25	10:44:00.0	AOCS_ORe-point_Start_6_OG [0x09c]		5	02-76	00	c1	69	00	00
		AOCU_NM		5	02-76	00	c1	69	00	00
2019/03/25	10:44:18.0	XRT_FLD_ENA_425_OG [0x1a9]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2019/03/25	10:46:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2019/03/25	10:46:50.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2019/03/25	10:46:52.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2019/03/25	10:46:54.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2019/03/25	10:46:56.0	XRT_QT_PROG_SET_405_OG [0x195]		2	07-F0	c4	01			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	01			

Mar 23, 19 12:21

XRT_OGLIST_0134.chk

Page 8/11

2019/03/25	10:46:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/03/25	10:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/25	14:13:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	14:13:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	14:13:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2019/03/25	14:14:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	34 fd 00 00
2019/03/25	14:14:18.0	XRT_FLD_ENA_425_OG [0x1a9]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/03/25	14:16:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/03/25	14:16:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/03/25	14:16:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/03/25	14:16:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/25	14:16:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2019/03/25	14:16:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/03/25	14:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/25	15:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	15:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	15:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/25	15:53:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/25	15:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/25	16:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	16:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	16:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/25	16:10:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/25	16:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/25	16:16:00.0	XRT_Custom_430_OG [0x1ae]					
2019/03/25	16:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/25	17:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	17:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	17:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/25	17:29:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/25	17:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/25	17:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	17:52:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	17:52:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2019/03/25	17:53:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2019/03/25	17:53:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2019/03/25	17:53:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2019/03/25	17:53:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/03/25	17:55:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2019/03/25	17:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/25	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	18:03:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/25	18:03:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2019/03/25	18:03:30.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	03	00 00 00 00
2019/03/25	18:03:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/03/25	18:03:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/03/25	18:03:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	

Mar 23, 19 12:21

XRT_OGLIST_0134.chk

Page 9/11

2019/03/25	18:03:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/03/25	18:03:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/25	18:06:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2019/03/25	18:06:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/03/25	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/25	19:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/25	19:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/25	19:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/25	19:06:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/03/25	19:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/03/25	19:29:00.0	XRT_Custom_430_OG [0x1ae]							
2019/03/25	19:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/25	20:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/25	20:43:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/25	20:43:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/25	20:43:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/03/25	20:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/03/25	21:06:30.0	XRT_Custom_430_OG [0x1ae]							
2019/03/25	21:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/25	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/25	21:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/25	21:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2019/03/25	22:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2019/03/25	22:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/03/25	22:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/03/25	22:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/03/25	22:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/03/25	22:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/25	22:02:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2019/03/25	22:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/03/25	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/25	22:20:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/25	22:20:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/25	22:20:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/25	22:20:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/03/25	22:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/03/25	22:42:30.0	XRT_Custom_430_OG [0x1ae]							
2019/03/25	22:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/25	23:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/25	23:57:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/25	23:57:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/03/25	23:57:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/03/26	00:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/03/26	00:11:00.0	XRT_Custom_430_OG [0x1ae]							
2019/03/26	00:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/03/26	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/26	00:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/03/26	00:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2019/03/26	01:00:00.5	AOCS_ORe-point_Start_5_OG [0x09b]							

2019/03/26	01:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	00	00	00	00
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2019/03/26	01:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2019/03/26	01:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2019/03/26	01:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2019/03/26	01:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2019/03/26	01:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]		2	07-F0	c4	11			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11			
2019/03/26	01:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]		2	07-F0	c5	0d			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0d			
2019/03/26	01:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2019/03/26	01:27:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/26	01:27:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/26	01:27:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2019/03/26	01:27:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2019/03/26	01:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2019/03/26	01:41:00.0	XRT_Custom_430_OG [0x1ae]								
2019/03/26	01:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2019/03/26	02:56:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/26	02:56:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/26	02:56:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2019/03/26	02:56:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2019/03/26	02:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2019/03/26	03:17:00.0	XRT_Custom_430_OG [0x1ae]								
2019/03/26	03:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2019/03/26	04:24:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/26	04:24:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/26	04:24:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2019/03/26	04:24:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2019/03/26	04:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2019/03/26	04:54:30.0	XRT_Custom_430_OG [0x1ae]								
2019/03/26	04:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2019/03/26	05:37:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/26	05:37:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/26	05:37:58.0	XRT_FOCUS_POSITION_406_OG [0x196]		4	07-F8	22	ff	aa	00	
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2019/03/26	05:38:00.0	AOCS_Ore-point_Start_2_OG [0x098]		5	02-76	00	00	00	00	00
		AOCU_NM		5	02-76	00	00	00	00	00
2019/03/26	05:38:18.0	XRT_FLD_DIS_409_OG [0x199]		1	07-F0	d9				
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2019/03/26	05:38:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2019/03/26	05:38:22.0	XRT_ARS_DIS_443_OG [0x1bb]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2019/03/26	05:40:58.0	XRT_QT_PROG_SET_404_OG [0x194]		2	07-F0	c4	0b			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0b			
2019/03/26	05:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2019/03/26	05:47:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/26	05:47:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2019/03/26	05:47:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2019/03/26	05:48:00.0	AOCS_Ore-point_Start_8_OG [0x09e]		5	02-76	00	fc	de	ac	32
		AOCU_NM		5	02-76	00	fc	de	ac	32
2019/03/26	05:48:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2019/03/26	05:48:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2019/03/26	05:48:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2019/03/26	05:48:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2019/03/26	05:48:26.0	XRT_FLD_RESET_434_OG [0x1b2]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2019/03/26	05:50:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]								

Mar 23, 19 12:21

XRT_OGLIST_0134.chk

Page 11/11

2019/03/26	05:50:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/03/26	05:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/26	06:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/26	06:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/26	06:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/26	06:05:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/26	06:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/26	06:31:30.0	XRT_Custom_430_OG [0x1ae]					
2019/03/26	06:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/26	07:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/26	07:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/26	07:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/26	07:45:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/26	07:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/26	08:09:00.0	XRT_Custom_430_OG [0x1ae]					
2019/03/26	08:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/26	09:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/26	09:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/26	09:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/03/26	09:24:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/03/26	09:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/03/26	09:45:00.0	XRT_Custom_430_OG [0x1ae]					
2019/03/26	09:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/03/26	11:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/26	11:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/03/26	11:05:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00
						00	00
						00	00
						00	00