

XRT Timeline to be uploaded on 2019/04/11

Period: 2019/04/11 10:30:00 - 2019/04/16 11:15:00

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Normal mode

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XOB #1C1E: QS (Filter-Ratio with Al/poly and Al/mesh), 384FOV at 1064, 1048 with G-band (1ms/1ms leak) 60s cad - AEC0- HOP328												
Term	Pointing (x, y)						Comment					
04/11 10:53:30 - 04/11 14:19:54	Track (-439.6, 144.4) ^{@ 04/11 10:40:00}						# OP start + 10min, HOP 328 with ALMA					
04/11 20:13:00 - 04/12 05:47:54	Track (-359.4, 147.8) ^{@ 04/11 20:10:00}						HOP 328 with ALMA					
PROG= 11 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 76 120-time(s) 60.0sec												
└─ Open/Al-mesh Open/thick-Al close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 8.00s Obs 1x1 384x384 (1064, 1048) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1C37: CME watch - 4x4 - AEC 2 - Thin-Be - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 60s cad												
Term	Pointing (x, y)						Comment					
04/11 14:23:00 - 04/11 15:29:54	Track (-854.1, 268.9) ^{@ 04/11 14:20:00}						HOP 368 CLASP2 for QS					
04/11 18:53:00 - 04/11 19:59:54	Track (-837.5, 272.6) ^{@ 04/11 18:50:00}						HOP 368 CLASP2 for QS					
PROG= 04 Inf.-time(s)												
└─ Subr= 1 50-time(s) 60.0sec												
└─ Seqn= 83 1-time(s) 4.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 30 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1BEF: HOP368 CLASP-2 - Al/poly (Be-Thin) - 256x256 - 1x1 - 4sec cad - Gband - 348x384 - 1ms - AEC1												
Term	Pointing (x, y)						Comment					
04/11 15:33:00 - 04/11 18:49:54	Track (-616.4, 182.2) ^{@ 04/11 15:30:00}						HOP 368 CLASP2 for plage					
PROG= 12 Inf.-time(s)												
└─ Subr= 1 2-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Subr= 2 250-time(s) 4.0sec												
└─ Seqn= 65 1-time(s) 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 256x256 (1064, 1048) Q=95 1 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1C2A: Synoptic Q95 2x2 - Al/mesh(181/1024/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(181/1024/8192)												
Term	Pointing (x, y)						Comment					
04/11 20:03:00 - 04/11 20:09:54	Fixed (0.0, 0.0)						synoptic, shifted 2.5 min					
04/12 05:51:00 - 04/12 05:57:54	Fixed (0.0, 0.0)						synoptic, shifted -12.0 min					
04/12 17:53:00 - 04/12 17:59:54	Fixed (0.0, 0.0)						synoptic, shifted -10.0 min					
04/13 06:03:00 - 04/13 06:09:54	Fixed (0.0, 0.0)						synoptic					
PROG= 06 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 88 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 18 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 52 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 16.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 23 1-time(s) 2.0sec												

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C3A: HOP282 multi-thick filters (thin/med/thick-Be, med/thick-Al) w/G-band (33ms/45ms leak) wo/PFB, 600s												
Term	Pointing (x, y)		Comment									
04/12 06:01:00 - 04/12 17:49:54	Track (-273.4, 150.4) @ 04/12 05:58:00		track AR 12738									
04/12 18:03:00 - 04/13 05:59:54	Track (-164.5, 152.4) @ 04/12 18:00:00		track AR 12738									
04/13 06:13:00 - 04/13 09:53:00	Track (-52.1, 153.1) @ 04/13 06:10:00		track AR 12738									
PROG= 15 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 15-time(s) 600.0sec												
Seqn= 41 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=98	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	3	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=98	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512)												
Term	Pointing (x, y)		Comment									
04/11 10:53:30 - 04/11 14:19:54	Track (-439.6, 144.4) @ 04/11 10:40:00		# OP start + 10min, HOP 328 with ALMA									
04/11 14:23:00 - 04/11 15:29:54	Track (-854.1, 268.9) @ 04/11 14:20:00		HOP 368 CLASP2 for QS									
04/11 18:53:00 - 04/11 19:59:54	Track (-837.5, 272.6) @ 04/11 18:50:00		HOP 368 CLASP2 for QS									
04/11 20:13:00 - 04/12 05:47:54	Track (-359.4, 147.8) @ 04/11 20:10:00		HOP 328 with ALMA									
04/12 06:01:00 - 04/12 17:49:54	Track (-273.4, 150.4) @ 04/12 05:58:00		track AR 12738									
04/12 18:03:00 - 04/13 05:59:54	Track (-164.5, 152.4) @ 04/12 18:00:00		track AR 12738									
04/13 06:13:00 - 04/13 09:53:00	Track (-52.1, 153.1) @ 04/13 06:10:00		track AR 12738									
PROG= 13 30-time(s)												
Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

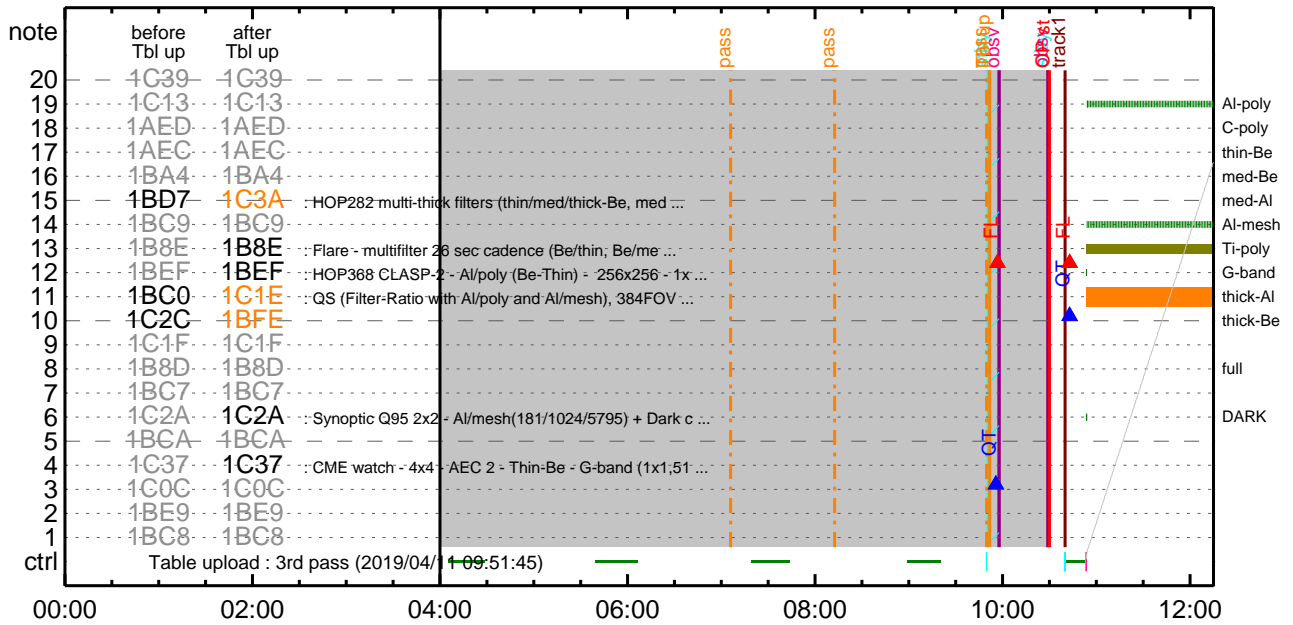
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Flare Detection

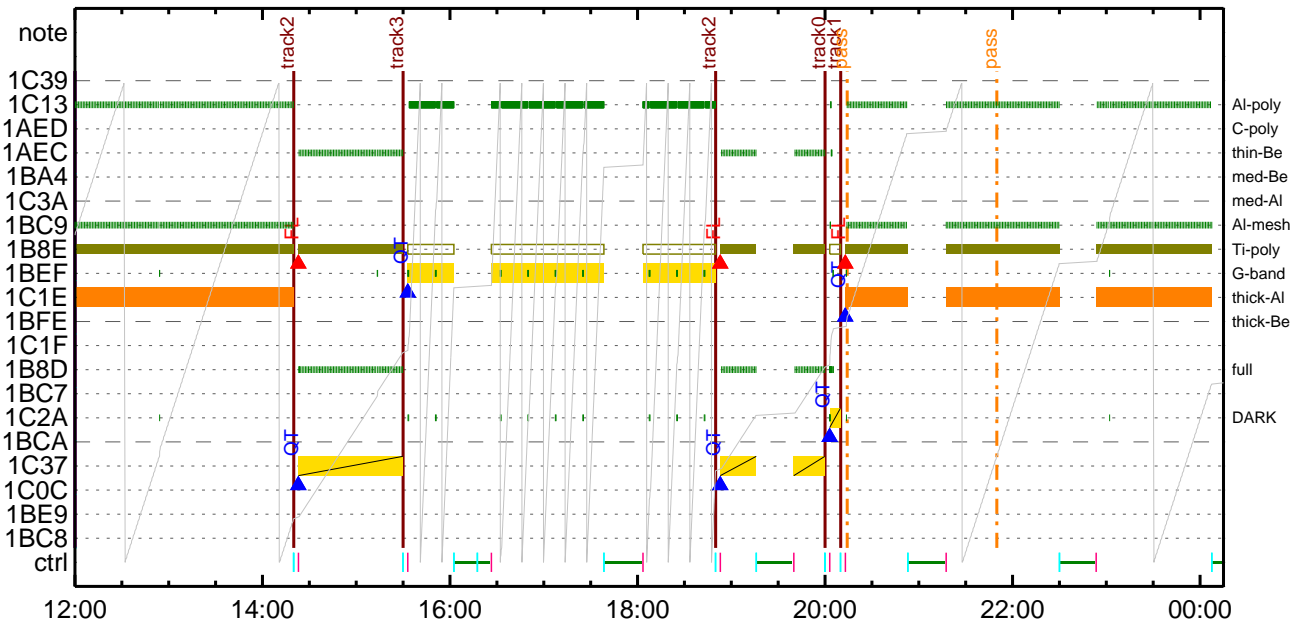
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FLD Patrol												
Term	Pointing (x, y)		Comment									
04/11 18:50:18 - 04/11 20:00:18	Track (-837.5, 272.6) @ 04/11 18:50:00		HOP 368 CLASP2 for QS									
04/11 20:10:18 - 04/12 05:48:18	Track (-359.4, 147.8) @ 04/11 20:10:00		HOP 328 with ALMA									
04/12 05:58:18 - 04/12 17:50:18	Track (-273.4, 150.4) @ 04/12 05:58:00		track AR 12738									
04/12 18:00:18 - 04/13 06:00:18	Track (-164.5, 152.4) @ 04/12 18:00:00		track AR 12738									
04/13 06:10:18 - 04/16 11:15:00	Track (-52.1, 153.1) @ 04/13 06:10:00		track AR 12738									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50			80sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

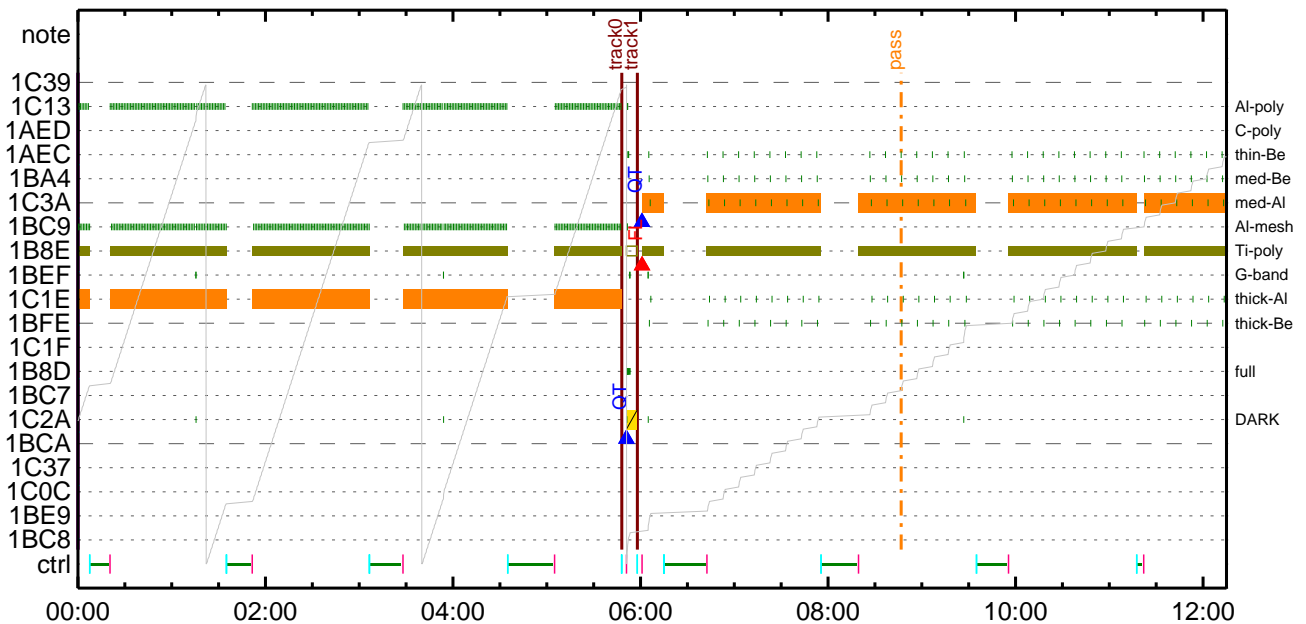
CMDI #0170 2019/04/11



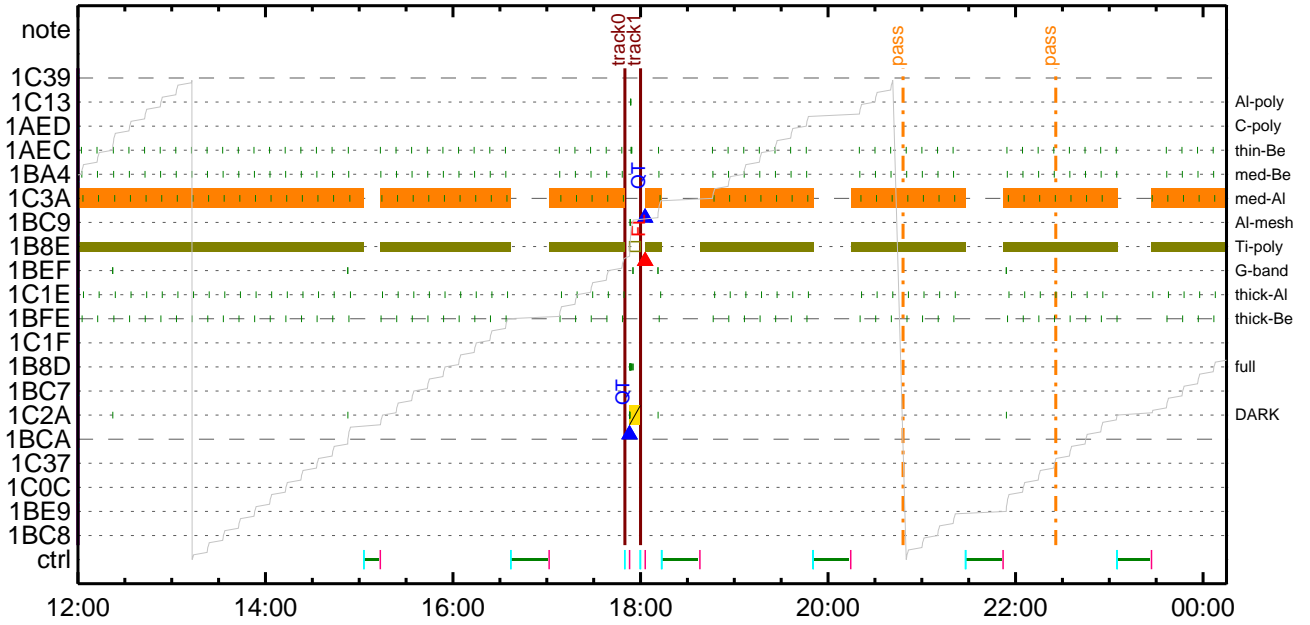
CMDI #0170 2019/04/11



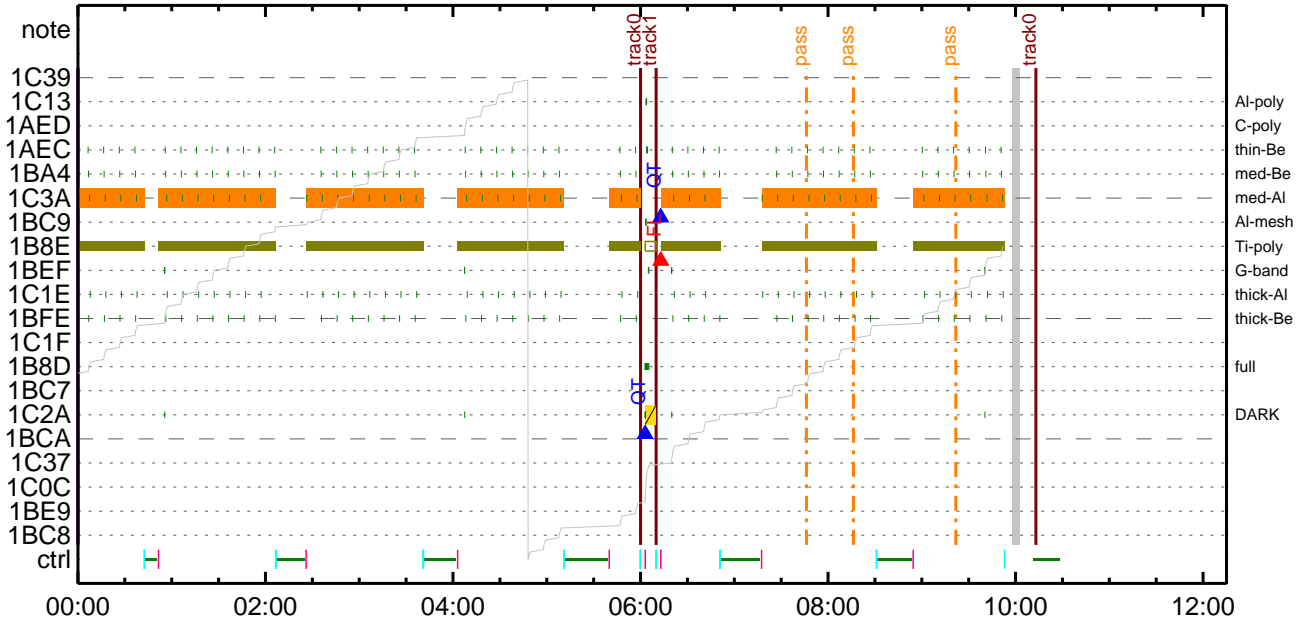
CMDI #0170 2019/04/12



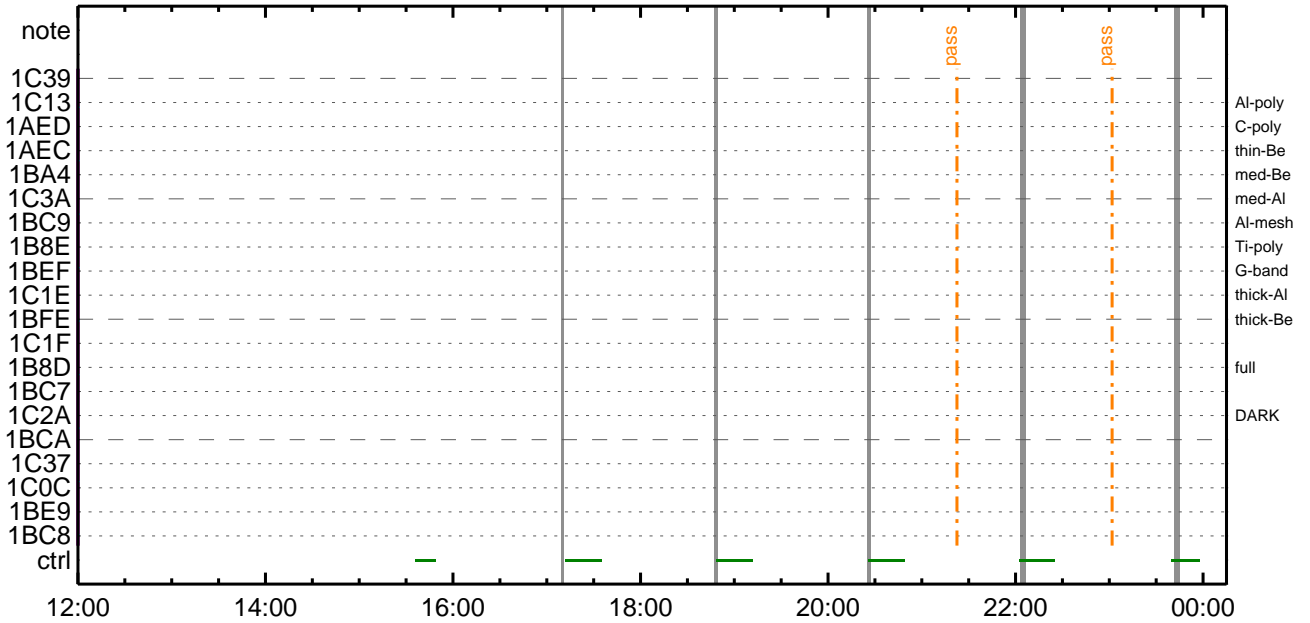
CMDI #0170 2019/04/12



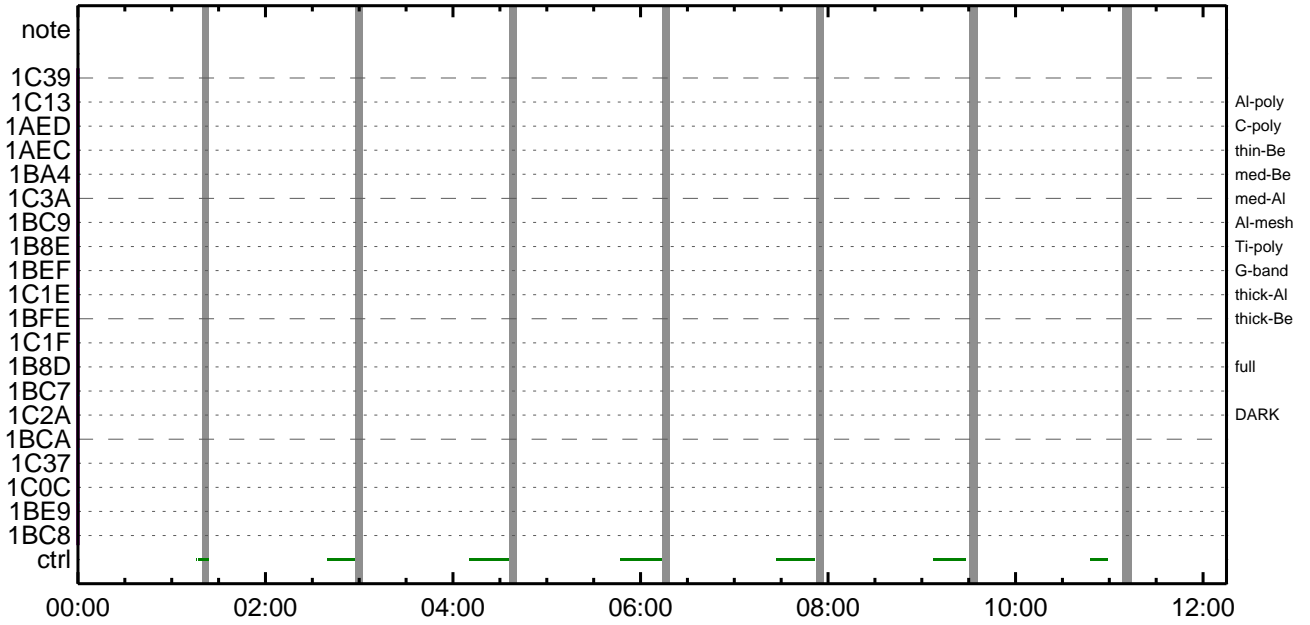
CMDI #0170 2019/04/13



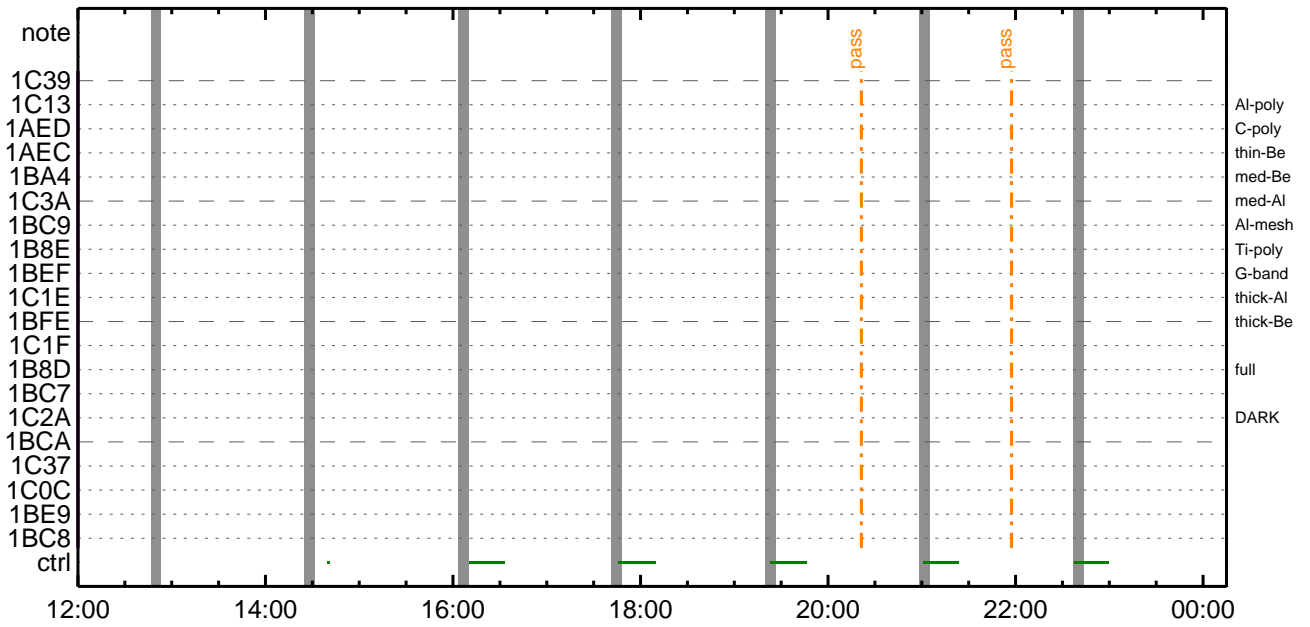
CMDI #0170 2019/04/13



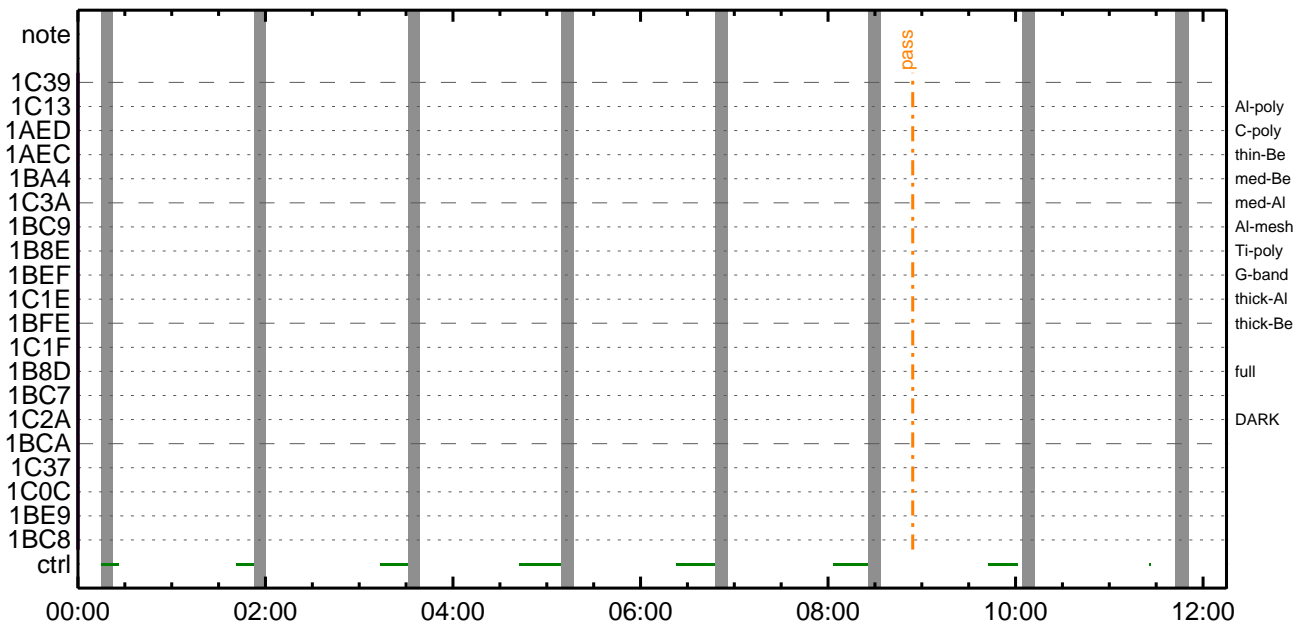
CMDI #0170 2019/04/14



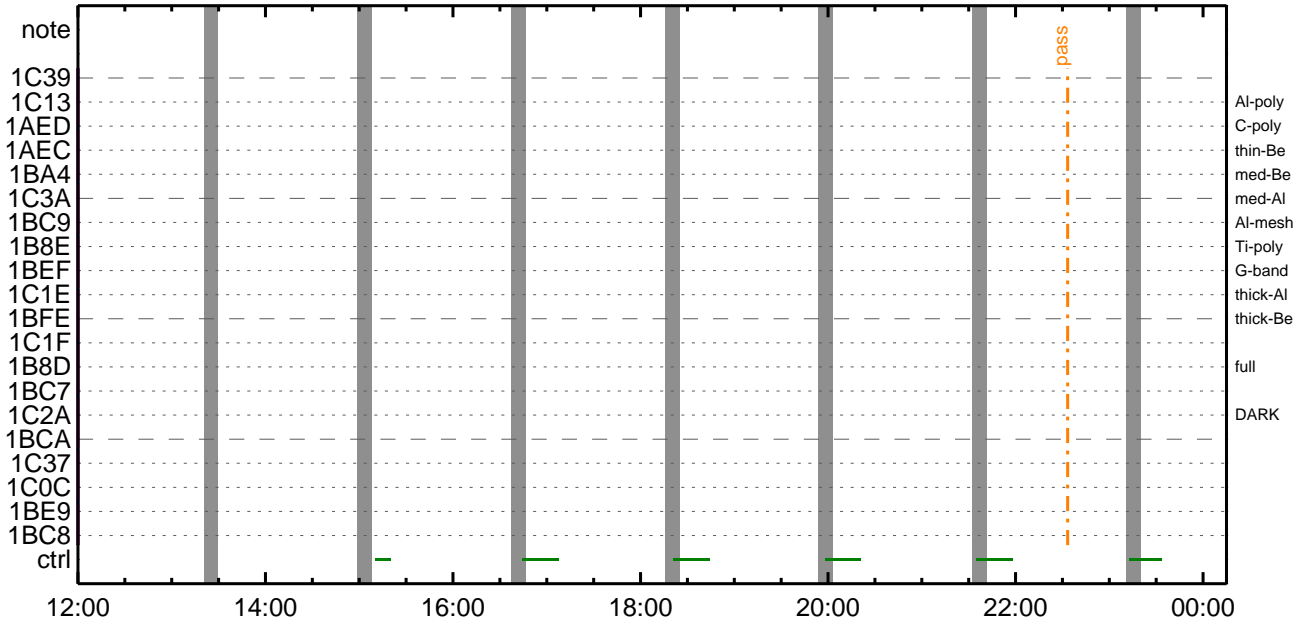
CMDI #0170 2019/04/14



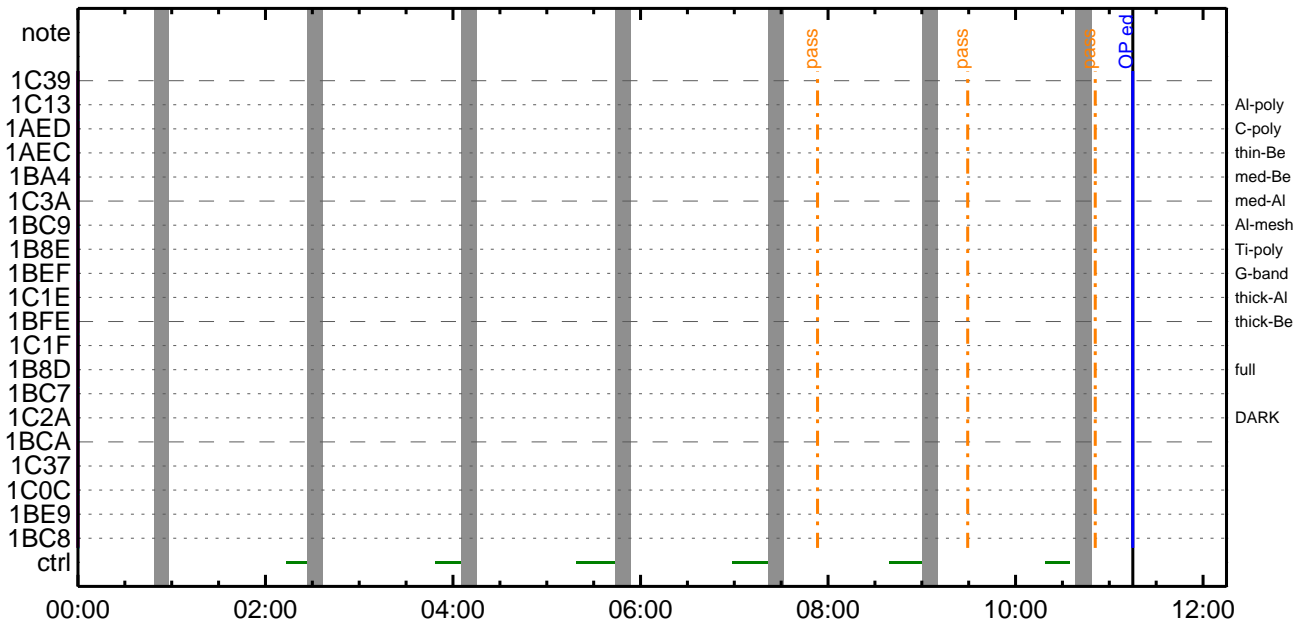
CMDI #0170 2019/04/15



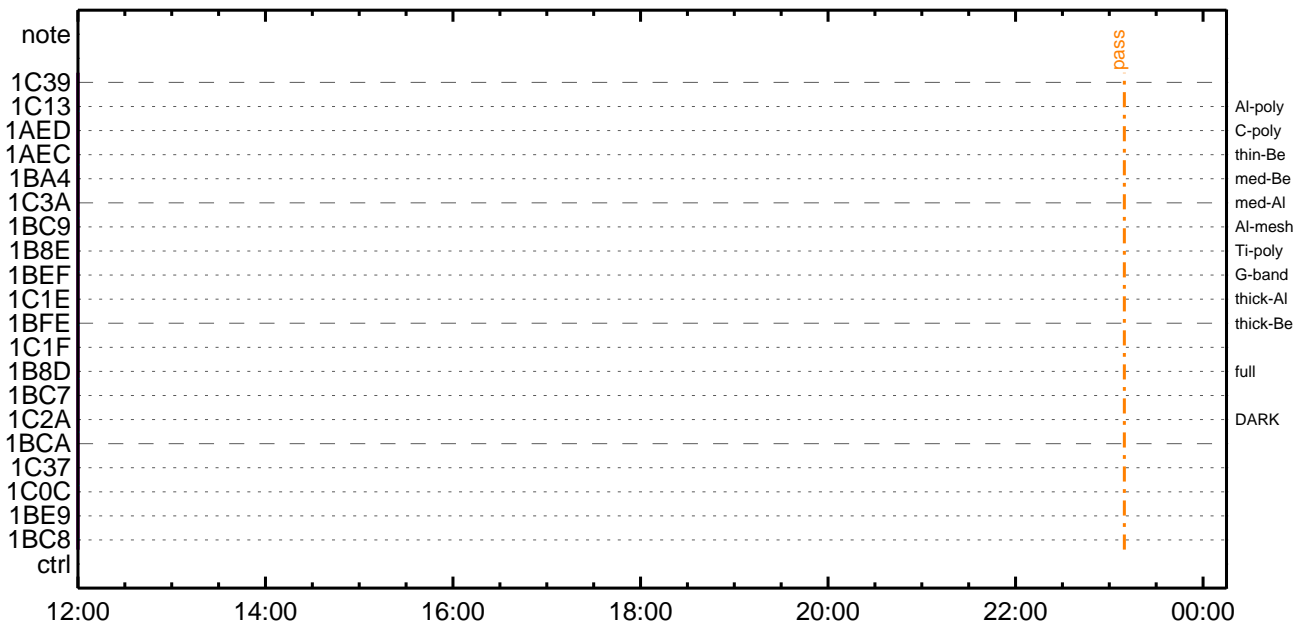
CMDI #0170 2019/04/15



CMDI #0170 2019/04/16



CMDI #0170 2019/04/16




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0096 C.                SET EDUMP I±°iYÑY¹aÇ¹Öa|a³aE;f
0097 C.
0098 C. TIY³YFYYÖYÉaòdÄDİ¿(UT)
0099 +. TI 2019-04-11 10:25:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2019-04-11 10:25:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2019-04-11 10:25:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2019-04-11 10:29:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. °E²¼aİÄè%îÍÑaİYÁY§YÄY-¹àÛ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]            EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]            EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]            EQ        0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]            EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]            EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]            EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]           EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]             EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]              EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]             EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]           EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]          EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]             EQ        EXEC
0140 C.
0141 C. YÄYÖY×½ªİ»òð³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]             EQ        NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKòð³İÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y½,¥i;¼YÈ;Èòðİá¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]              EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]             EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]           EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]          EQ        4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2019-04-11 10:29:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2019-04-11 10:29:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 C.                [ ] [HK1_TI_CMD_NUM]            EQ        2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2019-04-11 10:29:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 C.                [ ] [HK1_TI_CMD_NUM]            EQ        1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´úÄîaİ»ö¼YªEÄa¹aèDCBC•x²è *****
0181 C. (%á°îYÖYÄYÉYFYYÉYÄYÇYè²¼a¼ª»Ü¹aè)
0182 C. S. DC-BC dcbc-402:DCBC
0183 C. (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** YDY¹.İ Daily±¿İÑaÈ¹Ø¹aèDCBC•x²è *****
0187 C. S. DC-BC dcbc-153:DCBC
0188 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;ãLOS¥ÁY§YÄY-¼Ä»Ü;ä
0192 C.
0193 C. ***** LOS *****

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 85 83 04 04)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 85 83 02 02)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 80 80 20 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c 80 80 08 20)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0f 80 80 06 06)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 10 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_FLD_ENA
0145 BC (d8)
0146 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0147 BC (c8)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 + DC 07-F0 MDP_XRT_AEC_RESET
0151 BC (d0)
0152 + DC 07-F0 MDP_XRT_FLD_RESET
0153 BC (da)
0154 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0155 BC (c4 04)
0156 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0157 BC (c5 0d)
0158 . C. ----- Success Verify ? OK / NG ____
0159 C.
0160 C.
0161 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0162 C.
0163 +. DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 +. TI 2019-04-11 10:29:02.0
0166 DC 07-F0 MDP_XRT_MODE_OBSV
0167 BC (c2)
0168 . C. ----- Success Verify ? OK / NG ____
0169 C.
0170 C. ***** XRT END *****
0171 C.
0172 . C. ***** MDP `ûÃîñî»ò¼ÿñÈÃð¹ñèDCBC•x²è *****
0173 C. (¼á°îÿÓÿÃÿÈÿËÿËÿáÿçÿèñÈ¼ñ¼¼»Ûñ¹ñè)
0174 . S. DC-BC dcbc-402:DCBC
0175 (MDP_known_event)
0176 C.
0177 C.
0178 . C. ***** ÿÐÿ!•Ï Daily±;îññÈ`ð¹ñèDCBC•x²è *****
0179 . S. DC-BC dcbc-153:DCBC
0180 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0181 C.
0182 C.
0183 . C. ;ãLOSÿÁÿÿÿÿÿ¼»Û;ã
0184 C.
0185 . C. ***** LOS *****
0186 C.

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Apr 11, 19 11:50

XRT_OGLIST_0170.chk

Page 1/7

*** OP Sequence for XRT ***

2019/04/11	10:39:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/04/11	10:39:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/04/11	10:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2019/04/11	10:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2019/04/11	10:40:00.5	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/04/11	10:40:02.5	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/04/11	10:40:04.5	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/04/11	10:40:06.5	XRT_PREFLR_STRT_428_OG [0x1ac]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/04/11	10:40:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/04/11	10:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/04/11	10:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/04/11	10:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/04/11	10:40:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/04/11	10:42:56.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2019/04/11	10:42:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/04/11	10:43:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/04/11	10:52:30.0	XRT_Custom_430_OG [0x1ae]					
2019/04/11	10:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/04/11	14:19:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/04/11	14:19:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/04/11	14:19:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/04/11	14:20:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2019/04/11	14:20:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/04/11	14:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/04/11	14:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/04/11	14:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/04/11	14:20:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/04/11	14:22:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2019/04/11	14:22:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/04/11	14:23:00.5	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/04/11	15:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/04/11	15:29:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/04/11	15:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2019/04/11	15:30:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2019/04/11	15:30:18.0	XRT_FLD_DIS_409_OG [0x199]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2019/04/11	15:30:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2019/04/11	15:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/04/11	15:30:24.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/04/11	15:32:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2019/04/11	15:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/04/11	16:02:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/04/11	16:02:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/04/11	16:02:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/04/11	16:02:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/04/11	16:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/04/11	16:17:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		

Apr 11, 19 11:50

XRT_OGLIST_0170.chk

Page 2/7

2019/04/11	16:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	16:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/04/11	16:17:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/04/11	16:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/04/11	16:25:30.0	XRT_Custom_430_OG [0x1ae]					
2019/04/11	16:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/04/11	17:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	17:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	17:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/04/11	17:38:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/04/11	17:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/04/11	18:02:30.0	XRT_Custom_430_OG [0x1ae]					
2019/04/11	18:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/04/11	18:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	18:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	18:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/04/11	18:50:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 00 00 00 00	
2019/04/11	18:50:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/04/11	18:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/04/11	18:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/04/11	18:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/04/11	18:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/04/11	18:52:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2019/04/11	18:52:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2019/04/11	18:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/04/11	19:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	19:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	19:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/04/11	19:16:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/04/11	19:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/04/11	19:39:00.0	XRT_Custom_430_OG [0x1ae]					
2019/04/11	19:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/04/11	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	19:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/04/11	20:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00	
2019/04/11	20:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2019/04/11	20:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2019/04/11	20:00:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/04/11	20:02:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2019/04/11	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/04/11	20:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	20:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/04/11	20:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2019/04/11	20:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00	
2019/04/11	20:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/04/11	20:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/04/11	20:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	

Apr 11, 19 11:50

XRT_OGLIST_0170.chk

2019/04/11	20:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2019/04/11	20:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2019/04/11	20:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0b
2019/04/11	20:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 0d
2019/04/11	20:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2019/04/11	20:53:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/11	20:53:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/11	20:53:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2019/04/11	20:53:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2019/04/11	20:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2019/04/11	21:16:30.0	XRT_Custom_430_OG [0x1ae]		
2019/04/11	21:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2019/04/11	22:30:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/11	22:30:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/11	22:30:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2019/04/11	22:30:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2019/04/11	22:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2019/04/11	22:52:30.0	XRT_Custom_430_OG [0x1ae]		
2019/04/11	22:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2019/04/12	00:07:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/12	00:07:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/12	00:07:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2019/04/12	00:07:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2019/04/12	00:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2019/04/12	00:19:30.0	XRT_Custom_430_OG [0x1ae]		
2019/04/12	00:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2019/04/12	01:35:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/12	01:35:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/12	01:35:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2019/04/12	01:35:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2019/04/12	01:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2019/04/12	01:50:30.0	XRT_Custom_430_OG [0x1ae]		
2019/04/12	01:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2019/04/12	03:06:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/12	03:06:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/12	03:06:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2019/04/12	03:06:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2019/04/12	03:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2019/04/12	03:27:00.0	XRT_Custom_430_OG [0x1ae]		
2019/04/12	03:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2019/04/12	04:35:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/12	04:35:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/12	04:35:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2019/04/12	04:35:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2019/04/12	04:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2019/04/12	05:04:00.0	XRT_Custom_430_OG [0x1ae]		
2019/04/12	05:05:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2019/04/12	05:47:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/12	05:47:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2019/04/12	05:47:58.0	XRT_FOCUS_POSITION_406_OG [0x196]		

2019/04/12	05:48:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2019/04/12	05:48:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2019/04/12	05:48:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2019/04/12	05:48:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/04/12	05:50:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2019/04/12	05:51:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/04/12	05:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	05:57:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	05:57:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2019/04/12	05:58:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00	00	00
2019/04/12	05:58:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/04/12	05:58:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/04/12	05:58:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/04/12	05:58:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/04/12	05:58:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/04/12	06:00:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f		
2019/04/12	06:00:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2019/04/12	06:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/04/12	06:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	06:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	06:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/04/12	06:15:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/04/12	06:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/04/12	06:41:30.0	XRT_Custom_430_OG [0x1ae]							
2019/04/12	06:42:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/04/12	07:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	07:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	07:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/04/12	07:55:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/04/12	07:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/04/12	08:18:30.0	XRT_Custom_430_OG [0x1ae]							
2019/04/12	08:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/04/12	09:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	09:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	09:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/04/12	09:35:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/04/12	09:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/04/12	09:54:30.0	XRT_Custom_430_OG [0x1ae]							
2019/04/12	09:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/04/12	11:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	11:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	11:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/04/12	11:17:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/04/12	11:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/04/12	11:21:00.0	XRT_Custom_430_OG [0x1ae]							
2019/04/12	11:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/04/12	15:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/04/12	15:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

2019/04/12	15:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/12	15:03:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/12	15:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/12	15:12:30.0	XRT_Custom_430_OG [0x1ae]				
2019/04/12	15:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/12	16:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	16:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	16:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/12	16:37:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/12	16:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/12	17:00:30.0	XRT_Custom_430_OG [0x1ae]				
2019/04/12	17:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/12	17:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	17:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	17:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/04/12	17:50:00.0	AOCS_Or-e-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2019/04/12	17:50:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2019/04/12	17:50:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2019/04/12	17:50:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/04/12	17:52:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2019/04/12	17:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/12	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	17:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2019/04/12	18:00:00.0	AOCS_Or-e-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2019/04/12	18:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2019/04/12	18:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/04/12	18:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2019/04/12	18:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/04/12	18:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/12	18:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2019/04/12	18:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/04/12	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/12	18:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	18:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	18:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/12	18:13:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/12	18:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/12	18:37:00.0	XRT_Custom_430_OG [0x1ae]				
2019/04/12	18:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/12	19:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	19:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	19:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/12	19:50:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/12	19:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/12	20:13:30.0	XRT_Custom_430_OG [0x1ae]				
2019/04/12	20:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/12	21:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	21:28:02.0	XRT_CTRL_MANU_402_OG [0x192]				

Apr 11, 19 11:50

XRT_OGLIST_0170.chk

Page 6/7

2019/04/12	21:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	21:28:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/12	21:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/12	21:51:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/12	21:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/12	23:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	23:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/12	23:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/12	23:05:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/12	23:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/12	23:26:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/12	23:27:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/13	00:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	00:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	00:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/13	00:42:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/13	00:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/13	00:50:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/13	00:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/13	02:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	02:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	02:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/13	02:06:36.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/13	02:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/13	02:25:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/13	02:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/13	03:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	03:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	03:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/13	03:41:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/13	03:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/13	04:02:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/13	04:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/13	05:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	05:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	05:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/13	05:11:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/13	05:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/13	05:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/13	05:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/13	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	06:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/04/13	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00
2019/04/13	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2019/04/13	06:00:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2019/04/13	06:02:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/04/13	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2019/04/13	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

Apr 11, 19 11:50

XRT_OGLIST_0170.chk

Page 7/7

2019/04/13	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2019/04/13	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2019/04/13	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2019/04/13	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/04/13	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2019/04/13	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2019/04/13	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/13	06:12:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2019/04/13	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/04/13	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/13	06:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	06:51:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	06:51:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/13	06:51:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/13	06:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/13	07:16:30.0	XRT_Custom_430_OG [0x1ae]			
2019/04/13	07:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/13	08:31:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	08:31:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	08:31:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/04/13	08:31:06.0	XRT_PREFLR_STRT_428_OG [0x1ac]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/04/13	08:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/04/13	08:53:30.0	XRT_Custom_430_OG [0x1ae]			
2019/04/13	08:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/04/13	09:53:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/04/13	10:13:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00