

# XRT Timeline to be uploaded on 2019/07/11

Period: 2019/07/11 09:57:00 - 2019/07/16 11:46:00

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## Normal mode

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### XOB #1C53: Synoptic Q95 2x2 - Al/mesh(181/1024/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(256/2897/8192)

Term	Pointing (x, y)	Comment
07/11 20:46:30 - 07/11 21:42:30	Fixed ( -20.0, 11.0)	11/20 and synoptic
07/11 22:20:00 - 07/11 23:21:00	Fixed ( -20.0, 111.0)	12/20
07/11 23:54:30 - 07/12 00:59:00	Fixed ( -20.0, 211.0)	13/20
07/12 01:29:30 - 07/12 02:33:30	Fixed ( -20.0, 411.0)	15/20
07/12 03:07:30 - 07/12 04:07:00	Fixed ( -20.0, 611.0)	17/20
07/12 04:46:00 - 07/12 05:40:00	Fixed ( -20.0, 711.0)	18/20
07/12 06:24:30 - 07/12 07:20:30	Fixed ( -20.0, 811.0)	19/20
07/12 08:02:30 - 07/12 08:32:24	Fixed ( -20.0, 911.0)	20/20
07/12 08:35:30 - 07/12 08:42:24	Fixed ( 0.0, 0.0)	synoptic shifted manually
07/12 18:06:30 - 07/12 18:13:24	Fixed ( 0.0, 0.0)	synoptic, shifted 3.5 min
07/13 05:52:00 - 07/13 05:58:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic

**PROG= 16 1-time(s)**

Subr= 1	1-time(s)	2.0sec											
<b>Seqn= 5</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 88</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 44</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 52</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 23</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

### XOB #1BA4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms)

Term	Pointing (x, y)	Comment
07/12 08:45:30 - 07/12 17:23:00	Track ( 237.6, -538.5) <sup>Ⓢ 07/12 08:42:30</sup>	AR12744 and HOP375 (1430 - 1730UT)
07/12 18:16:30 - 07/13 01:34:30	Track ( 306.4, -537.2) <sup>Ⓢ 07/12 18:13:30</sup>	AR12744 obs
07/13 06:02:00 - 07/13 09:15:00	Track ( 387.9, -534.8) <sup>Ⓢ 07/13 05:59:00</sup>	Cont,

**PROG= 08 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec											
<b>Seqn= 92</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 42</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512	(1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 96</b>	<b>40-time(s)</b>	<b>2.0sec</b>											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

### XOB #1C51: HOP349 - 3-filter Synoptics (Al-mesh[128/1024/5795], Al-poly[256/4096/8192], thin-Be[2048/16384/32768] with 512x512 G-band+Leak(1064,1048)

Term	Pointing (x, y)	Comment
07/13 02:04:30 - 07/13 05:48:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic

**PROG= 18 Inf.-time(s)**

Subr= 1	1-time(s)	300.0sec											
<b>Seqn= 88</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 50</b>	<b>1-time(s)</b>	<b>2.0sec</b>											

Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 56</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 81</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
<b>Subr= 2</b>		<b>5-time(s)</b>	<b>420.0sec</b>									
<b>Seqn= 8</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 6</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 29</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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#### XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
07/12 08:45:30 - 07/12 17:23:00	Track ( 237.6, -538.5) <sup>Ⓢ 07/12 08:42:30</sup>	AR12744 and HOP375 (1430 - 1730UT)
07/12 18:16:30 - 07/13 01:34:30	Track ( 306.4, -537.2) <sup>Ⓢ 07/12 18:13:30</sup>	AR12744 obs
07/13 02:04:30 - 07/13 05:48:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic
07/13 06:02:00 - 07/13 09:15:00	Track ( 387.9, -534.8) <sup>Ⓢ 07/13 05:59:00</sup>	Cont,

#### PROG= 13 30-time(s)

<b>Subr= 1</b>		<b>20-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 11</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn=100</b>		<b>1-time(s)</b>	<b>10.0sec</b>									
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 10</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 87</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

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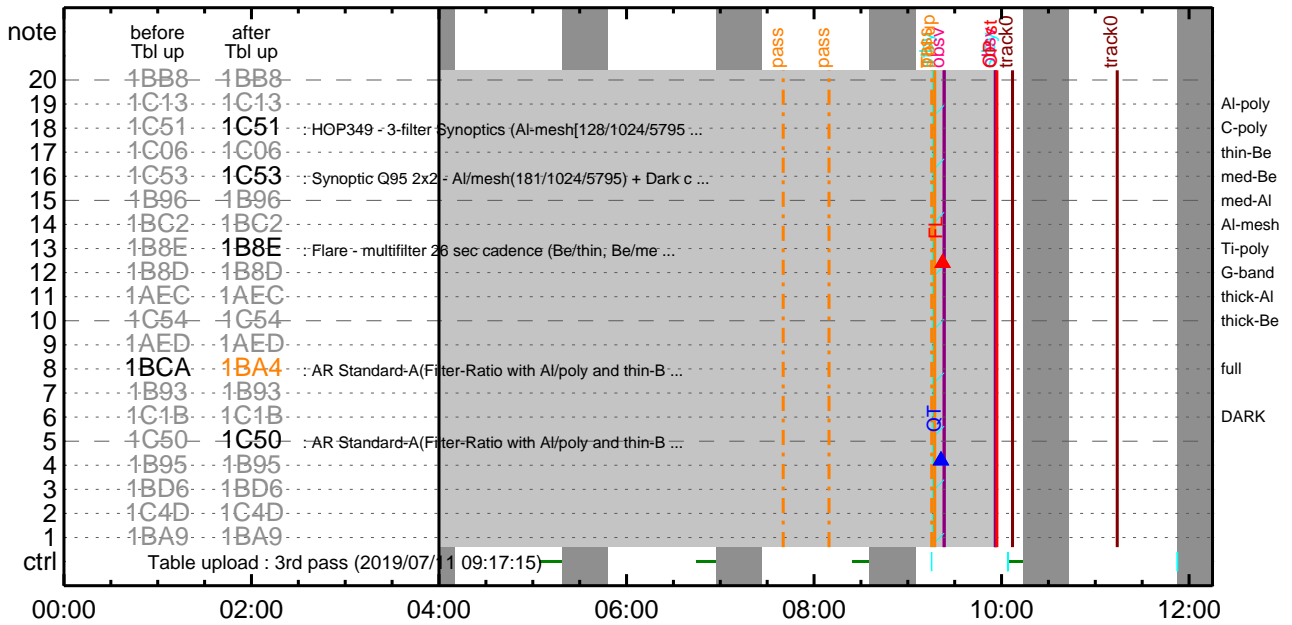
### Flare Detection

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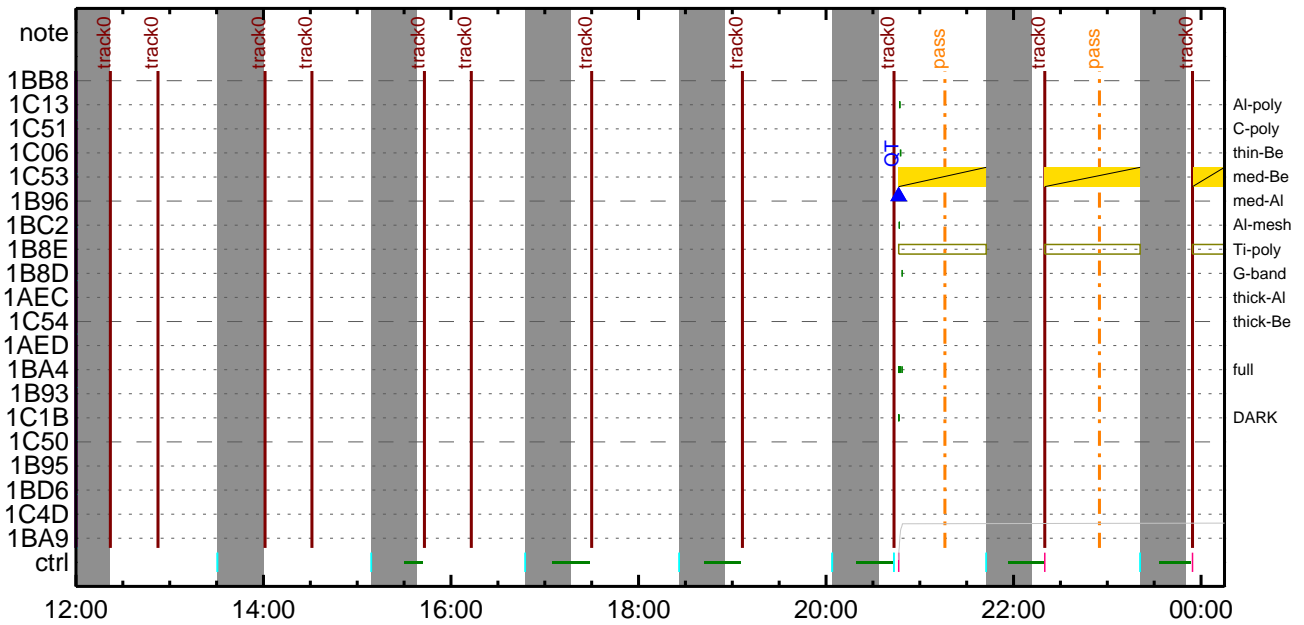
#### FLD Patrol

Term	Pointing (x, y)	Comment
07/12 08:42:48 - 07/12 18:03:48	Track ( 237.6, -538.5) <sup>Ⓢ 07/12 08:42:30</sup>	AR12744 and HOP375 (1430 - 1730UT)
07/12 18:13:48 - 07/13 05:49:18	Track ( 306.4, -537.2) <sup>Ⓢ 07/12 18:13:30</sup>	AR12744 obs
07/13 05:59:18 - 07/16 11:46:00	Track ( 387.9, -534.8) <sup>Ⓢ 07/13 05:59:00</sup>	Cont,
Al-poly/Open	Al-poly/Open	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

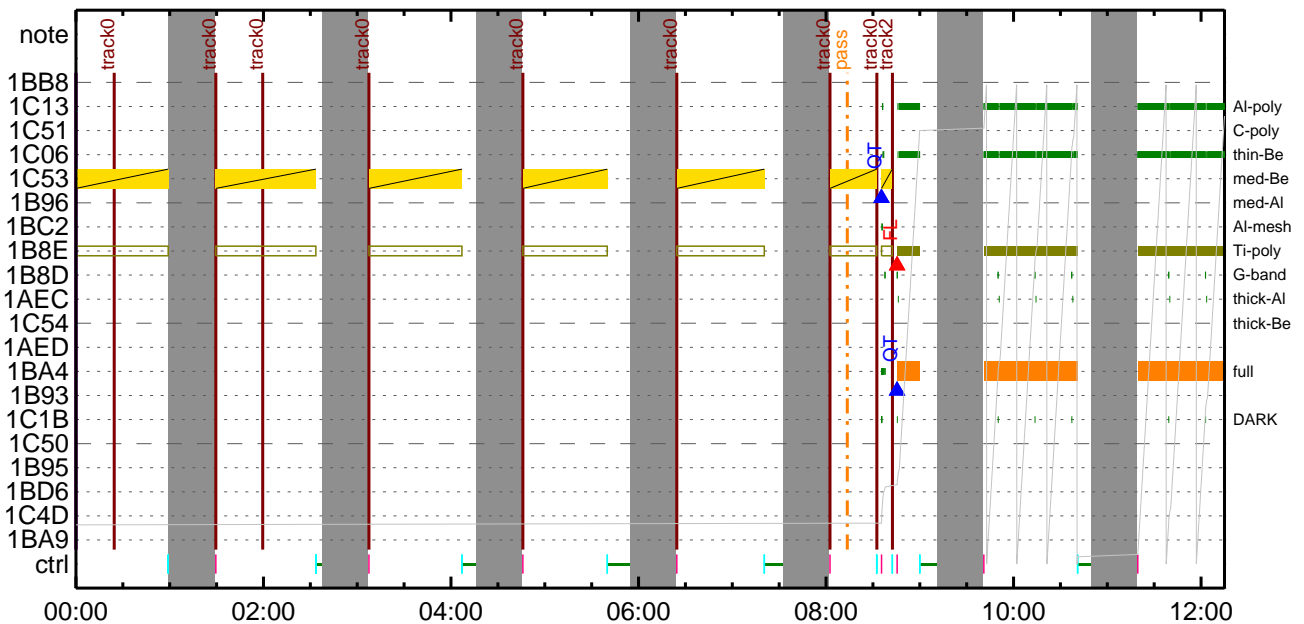
### CMDI #0343 2019/07/11



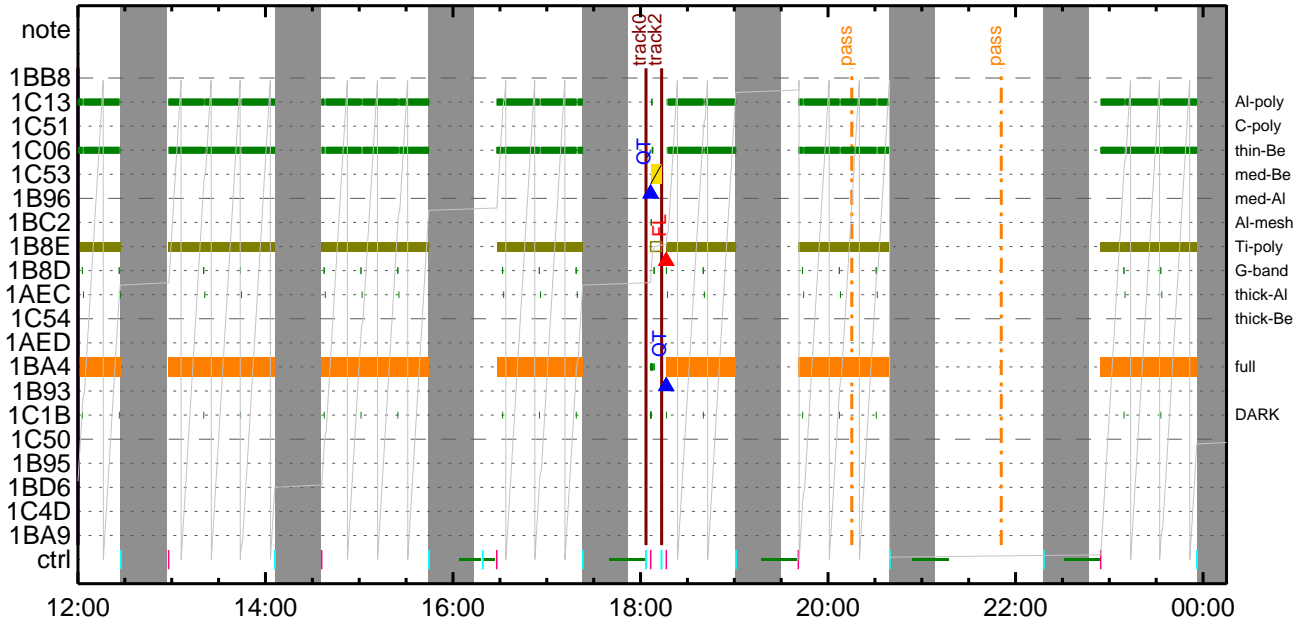
### CMDI #0343 2019/07/11



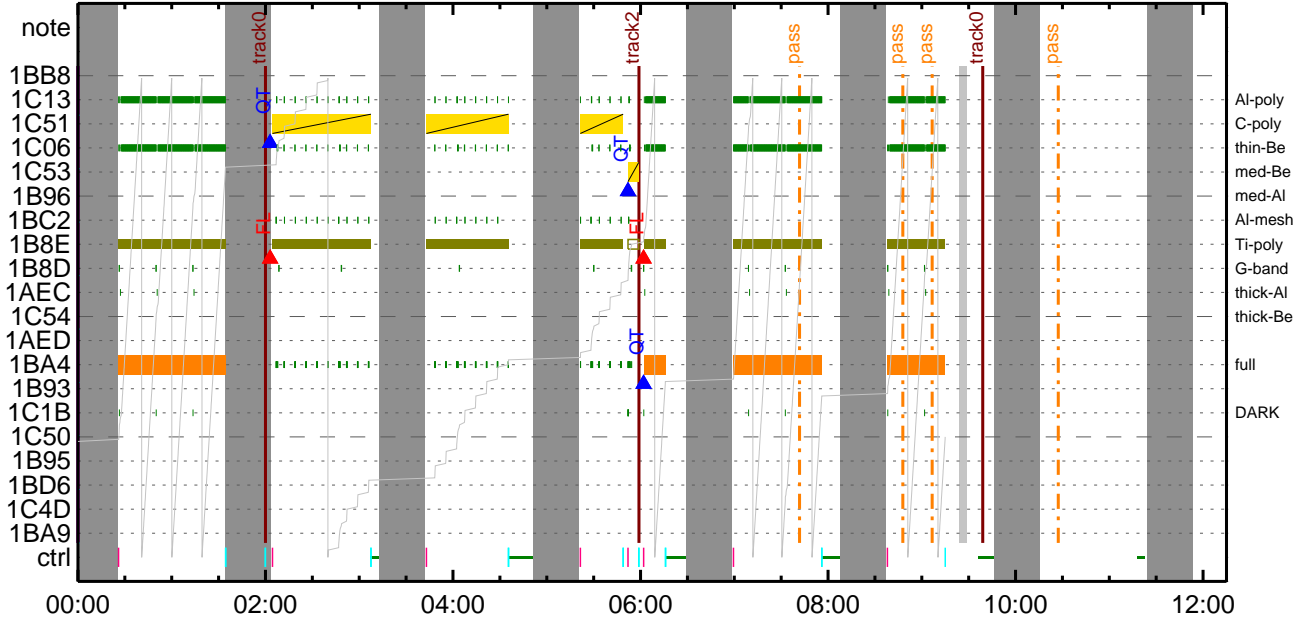
### CMDI #0343 2019/07/12



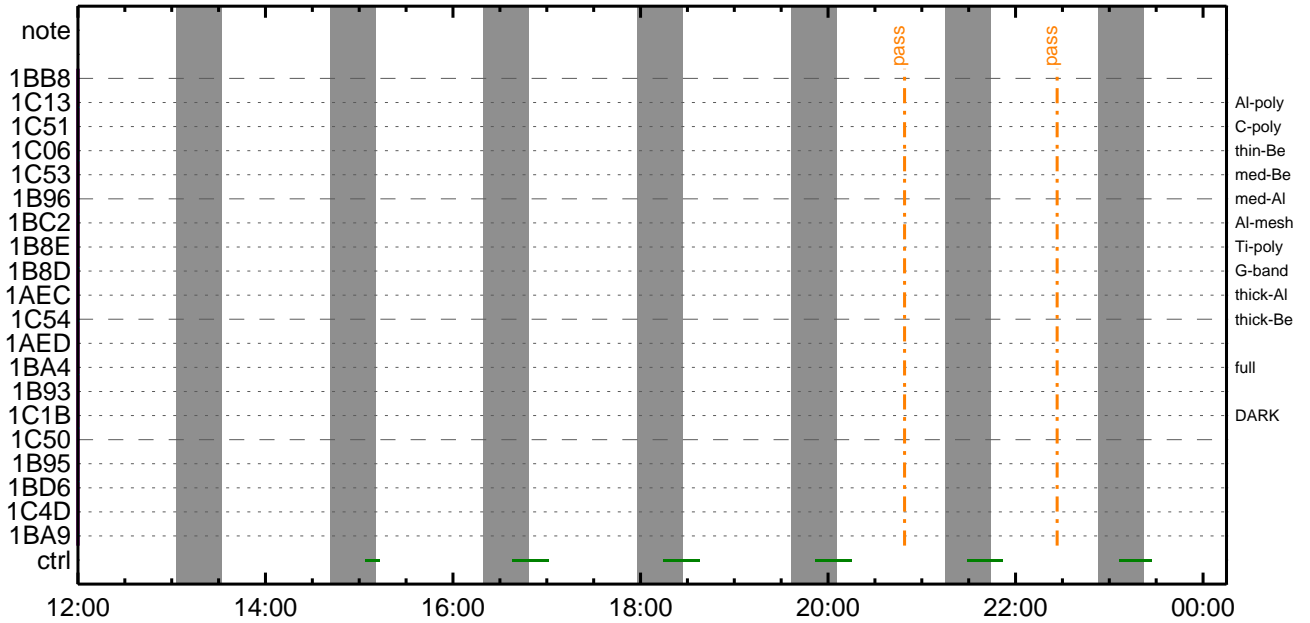
### CMDI #0343 2019/07/12



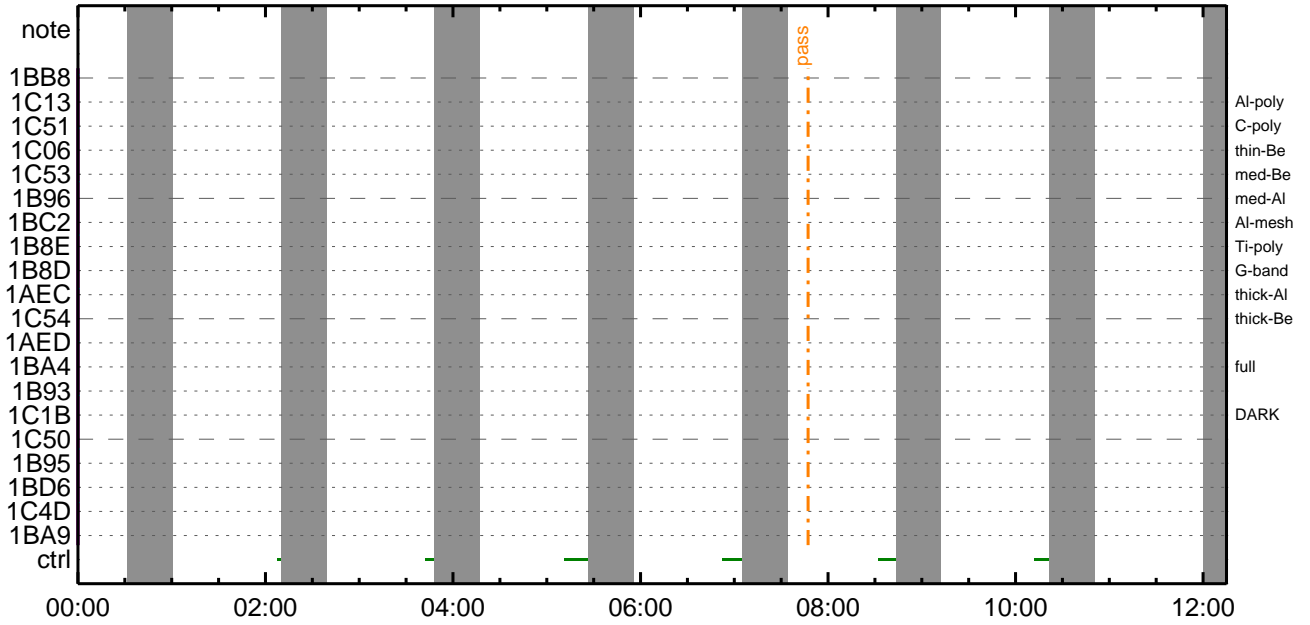
### CMDI #0343 2019/07/13



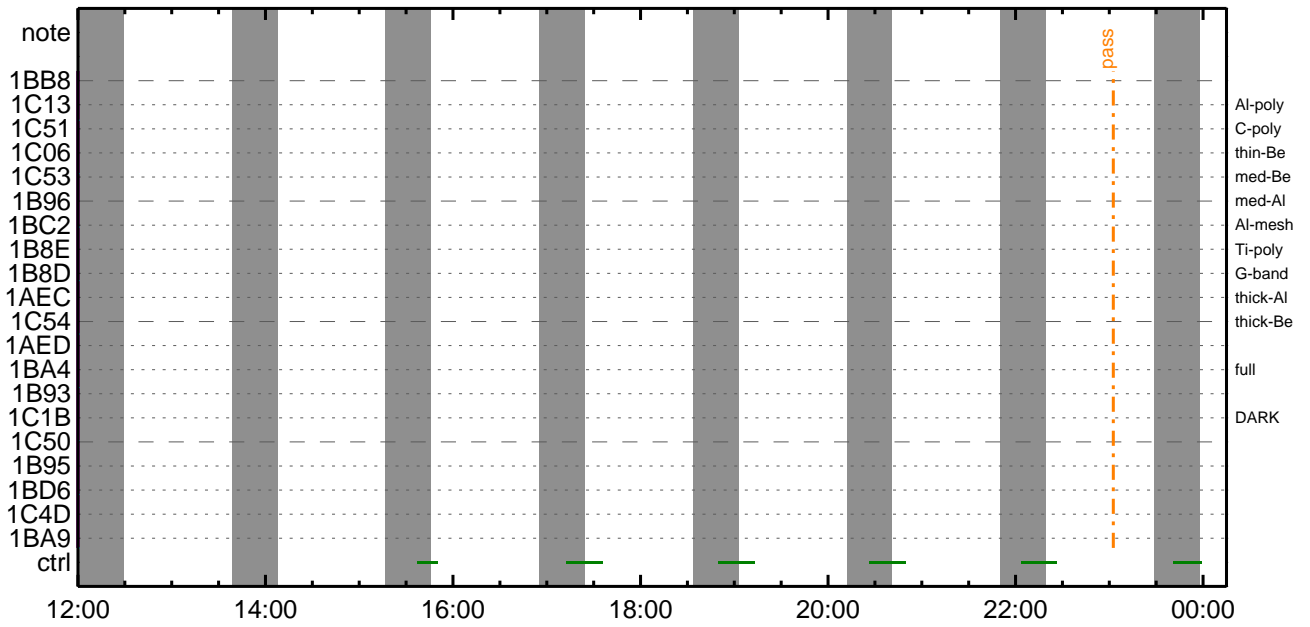
### CMDI #0343 2019/07/13



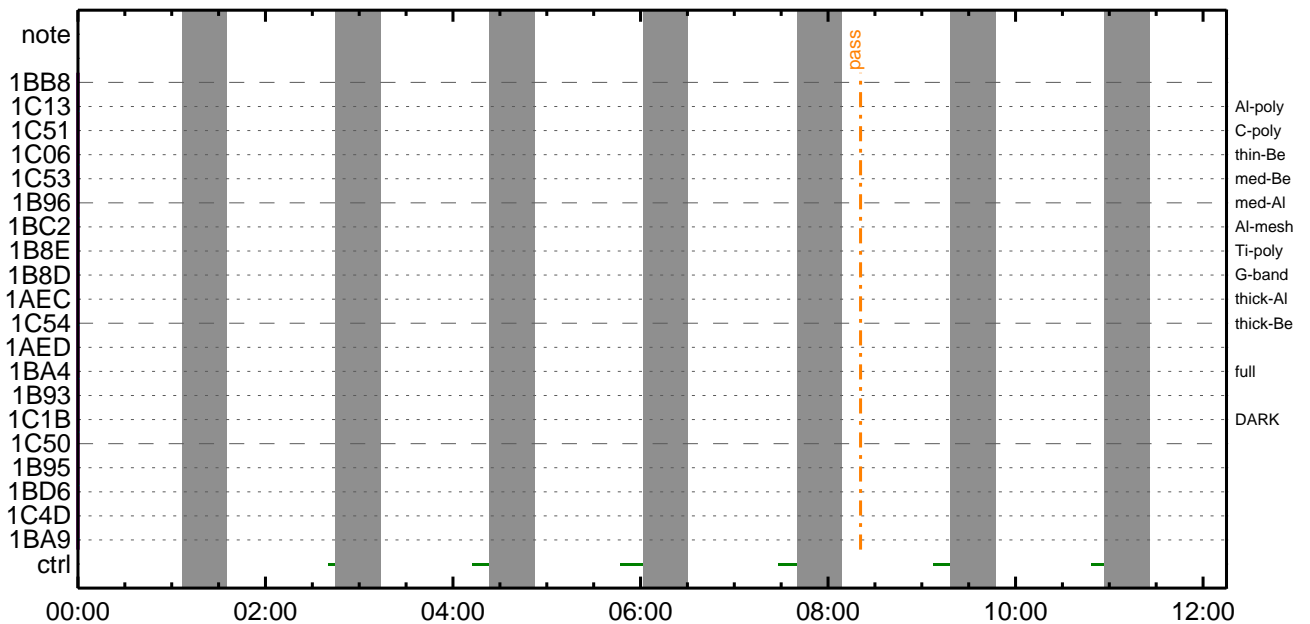
CMDI #0343 2019/07/14



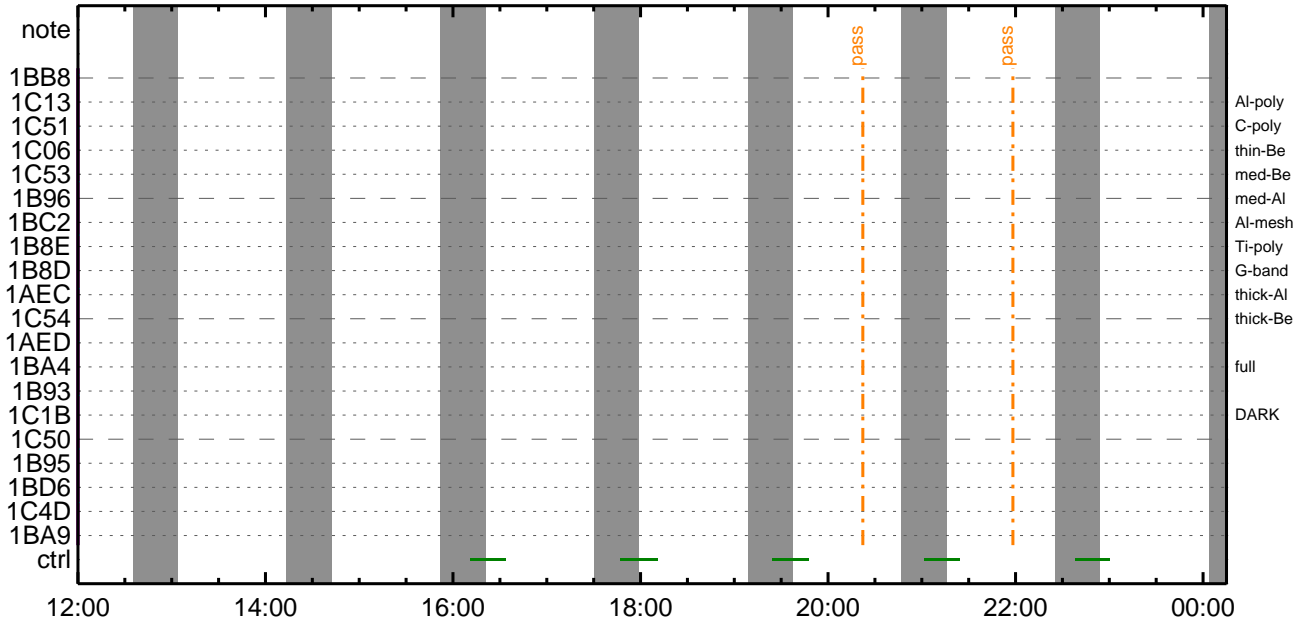
CMDI #0343 2019/07/14



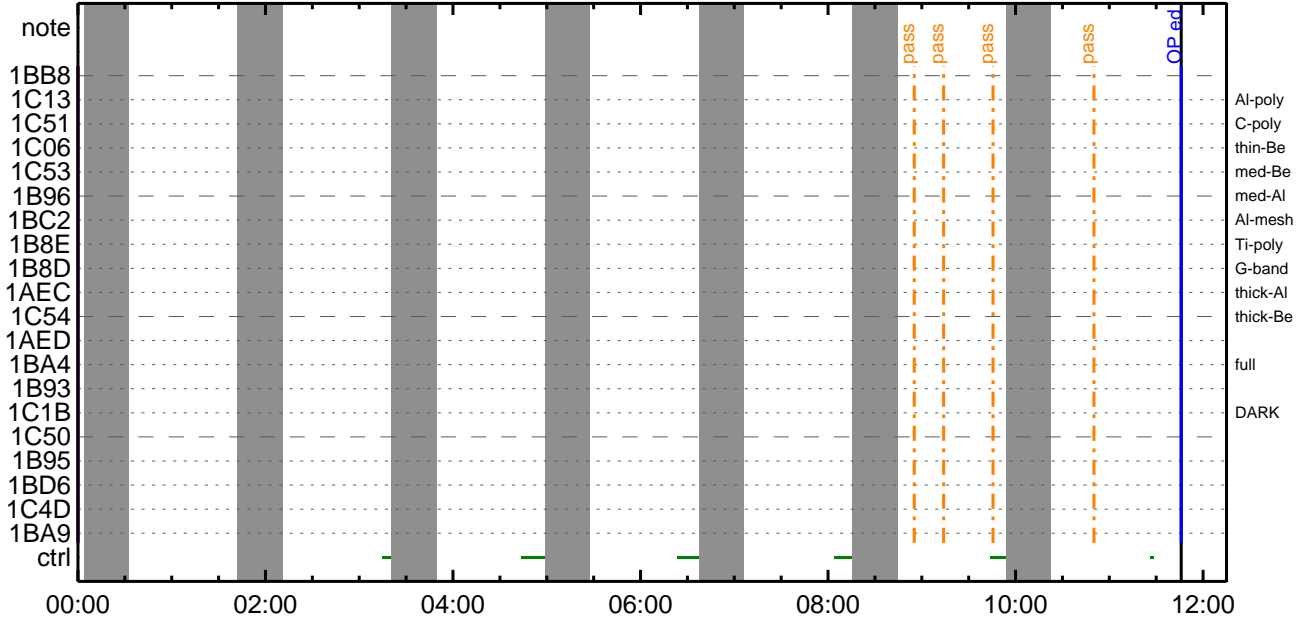
CMDI #0343 2019/07/15



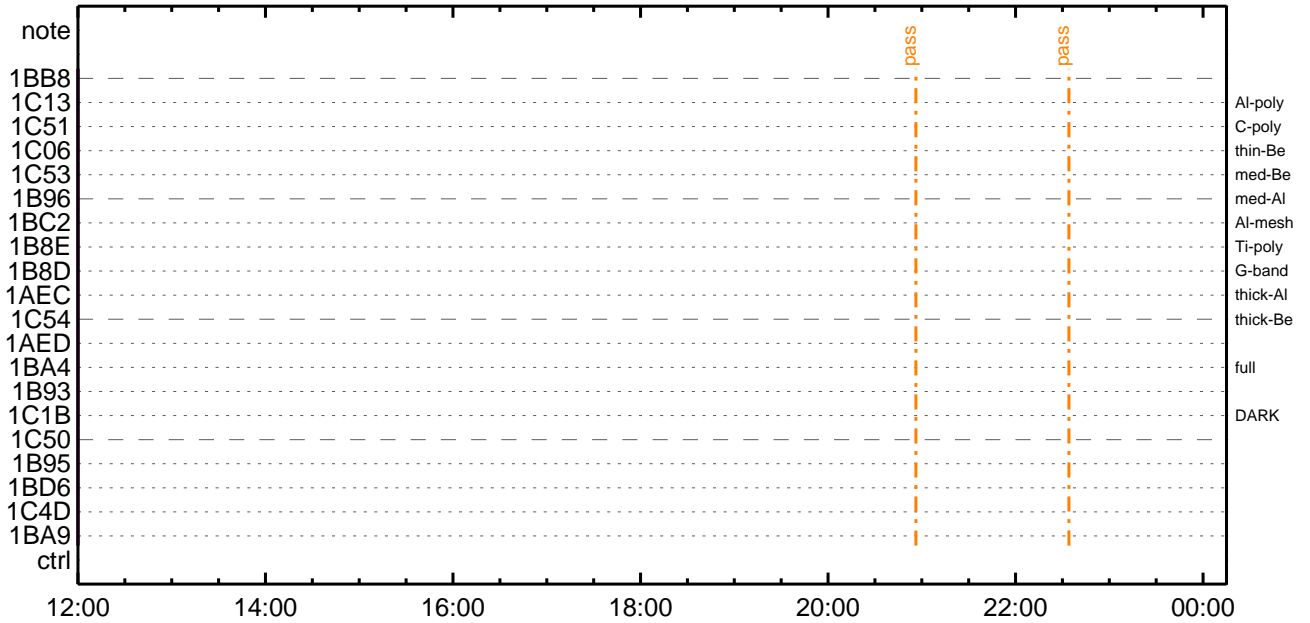
### CMDI #0343 2019/07/15



### CMDI #0343 2019/07/16



### CMDI #0343 2019/07/16





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-214:OP
0104 ( )
0105 S. OG og-214:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî¼Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ªî»ò³îÇ§²¼òî¼Ä´¶Á°òEÉ¬ò°Á÷¿@) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²¬Á÷¿@NG²î¼E¹ç;ç°E²¼òî¼TI-CMDÁ÷¿@²¼î¼Ä´¶Á°òEÉ¬ò°Á÷¿@²¼E¹ç;
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³YpYóYÉòòÄDî¿(UT)
0183 +. TI 2019-07-11 09:52:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2019-07-11 09:52:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2019-07-11 09:52:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.  
0195 +. TI 2019-07-11 09:56:59.5  
0196 DC 01-B2 DHU\_OP\_START  
0197 C.                            çç[HK1\_TI\_CMD\_NUM]                            EQ            1COUNTUP  
0198 C.  
0199 C. °Ê²¼αîÄë%îíñαîîŷÄŷ§ŷÄŷ-¹àîü  
0200 C.                            çç[HK1\_TI\_CMD\_ENA/DIS]                            EQ            ENA  
0201 C.                            çç[HK1\_TI\_CMD\_NUM]                            EQ            4  
0202 C.                            çç[HK1\_NEXT\_EXEC\_PIM]                            EQ            DHU  
0203 C.                            çç[HK1\_NEXT\_EXEC\_DC]                            EQ            0xB3  
0204 C.  
0205 C. \*\*\*\*\*  
0206 C. TIîî°èŷÄŷÖŷ×  
0207 C. \*\*\*\*\*  
0208 C.  
0209 C. TI\_TBL(0x03AB00-0x03AEFF;§ 1024byte)  
0210 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0211 BC                            (03 ab 03 01 02)  
0212 C.                            çç[HK1\_DMP\_TOP\_ADRS\_1]                            EQ            07  
0213 C.                            çç[HK1\_DMP\_TOP\_ADRS\_0]                            EQ            2B  
0214 C.                            çç[HK1\_DMP\_BLOCK\_NUM]                            EQ            3  
0215 C.                            çç[HK1\_DMP\_REPEAT\_NUM]                            EQ            0  
0216 C.                            çç[HK1\_DMA\_DMP\_PIM]                            EQ            DHU  
0217 +. DC 01-22 DHU\_MODE\_CHNG  
0218 BC                            (07 0b f8)  
0219 C.                            çç[HK1\_PKT\_FORM\_NO]                            EQ            7  
0220 C.                            çç[HK1\_PKT\_GEN\_TIME]                            EQ            0.25 s  
0221 C.                            çç[HK1\_S\_TLM\_BIT\_RATE]                            EQ            32k  
0222 C.                            çç[HK1\_X\_TLM\_BIT\_RATE]                            EQ            4M  
0223 C.                            çç[HK1\_DMP\_CHK\_FLG]                            EQ            EXEC  
0224 C.  
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§  
0226 C.                            çç[HK1\_DMP\_CHK\_FLG]                            EQ            NON  
0227 C.  
0228 C. RAM ID=TI\_TBLαîî¼Ê¹ç•è²îOKαò³îç§  
0229 C.  
0230 C. DHUŷâ;¼ŷÊ;Ê¼ŷ¼. ŷî;¼ŷÊ;Êαòîâα¹  
0231 +. DC 01-22 DHU\_MODE\_CHNG  
0232 BC                            (02 0a f8)  
0233 C.                            çç[HK1\_PKT\_FORM\_NO]                            EQ            2  
0234 C.                            çç[HK1\_PKT\_GEN\_TIME]                            EQ            0.5S  
0235 C.                            çç[HK1\_S\_TLM\_BIT\_RATE]                            EQ            32K  
0236 C.                            çç[HK1\_X\_TLM\_BIT\_RATE]                            EQ            4M  
0237 C.  
0238 C. \*\*\*\*\*  
0239 C. SOT TI command set  
0240 C. \*\*\*\*\*  
0241 C. Execute, after the success of OP upload.  
0242 +. TI 2019-07-11 09:56:16.0  
0243 DC 07-F0 MDP\_SOT\_MODE\_STBY  
0244 BC                            (41)  
0245 C. -----  
0246 C.       HK1\_TI\_CMD\_NUM                            = 1 CNTUP [ ]  
0247 C. -----  
0248 C. \*\*\*\*\* SOT END \*\*\*\*\*  
0249 C. Stop EIS observation and temporarily disable EIS mode changes  
0250 C.  
0251 C.  
0252 C. \*\*\*\*\* Start EIS operation (TI set) \*\*\*\*\*  
0253 C. Execute, after the success of OP upload.  
0254 C. Set EIS TI-commands  
0255 +. TI 2019-07-11 09:56:30.0  
0256 DC 07-FC EIS\_MODE\_MANU  
0257 BC                            (21 02)  
0258 +. TI 2019-07-11 09:56:40.0  
0259 DC 07-FC EIS\_MODE\_CHG\_DIS  
0260 BC                            (22)  
0261 C.                            [    ] [HK1\_TI\_CMD\_NUM]                            EQ            2 COUNTUP  
0262 C. \*\*\*\*\* End EIS operation (TI set) \*\*\*\*\*  
0263 C.  
0264 C.  
0265 C.  
0266 C. \*\*\*\*\* XRT START \*\*\*\*\*  
0267 C. Execute, after the success of OP upload.  
0268 +. TI 2019-07-11 09:56:00.0  
0269 DC 07-F0 MDP\_XRT\_MODE\_STBY  
0270 BC                            (c3)  
0271 C.                            [    ] [HK1\_TI\_CMD\_NUM]                            EQ            1COUNTUP  
0272 C.  
0273 C. \*\*\*\*\* XRT END \*\*\*\*\*  
0274 C.  
0275 C. \*\*\*\*\* MDP `ûÃîαî»ö¼ŷαÊÄα¹αèDCBC•x²è \*\*\*\*\*  
0276 C. (¼ª°îŷÖŷÄŷÊŷŷŷÄŷçŷèè¼αα¼Ä»Ûα¹αè)  
0277 C. S. DC-BC dcbc-402:DCBC  
0278 C. (MDP\_known\_event)  
0279 C.  
0280 C.  
0281 C. \*\*\*\*\* ŷÐŷ¹.î Daily±çîñαè'Øα¹αèDCBC•x²è \*\*\*\*\*  
0282 C. S. DC-BC dcbc-153:DCBC  
0283 C. (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0284 C.  
0285 C.  
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã  
0287 C.  
0288 C. \*\*\*\*\* LOS \*\*\*\*\*  
0289 C.





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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-286:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2019-07-11 09:56:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 20)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 08)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 08 20)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 85 83 08 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0f 80 80 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 10 80 80 08 08)
0172 + DC 07-F0 MDP_XRT_FLD_DIS
0173 BC (d9)
0174 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0175 BC (c9)
0176 + DC 07-F0 MDP_XRT_ARS_DIS
0177 BC (d5)
0178 + DC 07-F0 MDP_XRT_AEC_RESET
0179 BC (d0)
0180 + DC 07-F0 MDP_XRT_FLD_RESET
0181 BC (da)
0182 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0183 BC (c4 05)
0184 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0185 BC (c5 0d)
0186 . C. ----- Success Verify ? OK / NG ____
0187 C.
0188 C.
0189 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0190 C.
0191 +. DC 07-F0 MDP_XRT_MODE_OBSV
0192 BC (c2)
0193 +. TI 2019-07-11 09:56:02.0

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0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 . C. ----- Success Verify ? OK / NG ____
0197 C.
0198 C. ***** XRT END *****
0199 C.
0200 . C. ***** MDP `úÃîñî»ò¼ŷñÊÃĐñ¹ñèDCBC•x²è *****
0201 C. (¼á°îŷÓŷÃŷÈŷŦŷËŷáŷçŷèñÈ¼ñ¼Ã»Ûñ¹ñè)
0202 . S. DC-BC dcbc-402:DCBC
0203 (MDP_known_event)
0204 C.
0205 C.
0206 . C. ***** ŷĐŷ¹•İ Daily±¿İññÈ´Øñ¹ñèDCBC•x²è *****
0207 . S. DC-BC dcbc-153:DCBC
0208 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209 C.
0210 C.
0211 . C. ;ãLOSŷÁŷŦŷËŷ¹¼Ã»Ûñ¹ñè
0212 C.
0213 . C. ***** LOS *****
0214 C.
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\*\*\* OP Sequence for XRT \*\*\*

2019/07/11	10:04:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	10:04:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	10:04:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/07/11	10:04:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/07/11	10:07:00.0	AOCS_ORe-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 57 02 01	ca		
2019/07/11	10:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/07/11	11:14:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 4e 24 01	ca		
2019/07/11	11:52:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	11:52:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	11:52:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/07/11	11:52:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/07/11	11:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/07/11	12:22:00.0	AOCS_ORe-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 45 3f 01	ca		
2019/07/11	12:52:30.0	AOCS_ORe-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	00 3c 5a 01	ca		
2019/07/11	13:30:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	13:30:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	13:30:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/07/11	13:30:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/07/11	13:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/07/11	14:01:00.0	AOCS_ORe-point_Start_5_OG [0x09b]						
		AOCU_NM	5	02-76	00 33 74 01	ca		
2019/07/11	14:31:00.0	AOCS_ORe-point_Start_6_OG [0x09c]						
		AOCU_NM	5	02-76	00 2a 97 01	ca		
2019/07/11	15:09:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	15:09:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	15:09:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/07/11	15:09:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/07/11	15:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/07/11	15:43:00.0	AOCS_ORe-point_Start_7_OG [0x09d]						
		AOCU_NM	5	02-76	00 21 b2 01	ca		
2019/07/11	16:13:00.0	AOCS_ORe-point_Start_8_OG [0x09e]						
		AOCU_NM	5	02-76	00 18 cc 01	ca		
2019/07/11	16:47:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	16:47:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	16:47:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/07/11	16:47:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/07/11	16:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/07/11	17:30:00.0	AOCS_ORe-point_Start_9_OG [0x09f]						
		AOCU_NM	5	02-76	00 0f e7 01	ca		
2019/07/11	18:26:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	18:26:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	18:26:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/07/11	18:26:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/07/11	18:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/07/11	19:06:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]						
		AOCU_NM	5	02-76	00 07 02 01	ca		
2019/07/11	20:04:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	20:04:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/07/11	20:04:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/07/11	20:04:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/07/11	20:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/07/11	20:43:24.0	XRT_CTRL_MANU_402_OG [0x192]						

2019/07/11	20:43:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/11	20:43:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/11	20:43:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/07/11	20:43:48.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 ff 03 01 ca	
2019/07/11	20:43:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2019/07/11	20:43:52.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2019/07/11	20:46:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/07/11	20:46:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2019/07/11	21:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/07/11	21:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/11	21:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/11	21:42:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/07/11	21:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/07/11	22:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/07/11	22:20:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	XRT_Custom_430_OG [0x1ae]				
2019/07/11	22:20:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	AOCU_NM	5	02-76	00 f6 25 01 ca	
2019/07/11	23:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/07/11	23:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/11	23:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/11	23:21:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/07/11	23:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/07/11	23:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/07/11	23:54:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]	XRT_Custom_430_OG [0x1ae]				
2019/07/11	23:54:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	AOCU_NM	5	02-76	00 ed 40 01 ca	
2019/07/12	00:24:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/07/12	00:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 e4 5b 01 ca	
2019/07/12	00:59:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/12	00:59:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/12	00:59:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/07/12	01:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/07/12	01:28:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/07/12	01:29:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]	XRT_Custom_430_OG [0x1ae]				
2019/07/12	01:29:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	AOCU_NM	5	02-76	00 db 75 01 ca	
2019/07/12	01:59:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/07/12	02:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 d2 98 01 ca	
2019/07/12	02:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/12	02:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/12	02:33:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/07/12	02:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/07/12	03:06:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/07/12	03:07:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]	XRT_Custom_430_OG [0x1ae]				
2019/07/12	03:07:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	AOCU_NM	5	02-76	00 c9 b3 01 ca	
2019/07/12	04:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/07/12	04:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/12	04:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/07/12	04:07:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/07/12	04:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/07/12	04:45:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/07/12	04:46:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]	XRT_Custom_430_OG [0x1ae]				

2019/07/12	04:46:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	AOCU_NM	5	02-76	00	c0	cd	01	ca
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2019/07/12	05:40:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	05:40:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	05:40:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0		da			
2019/07/12	05:40:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]								
			MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2019/07/12	05:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2019/07/12	06:23:30.0	XRT_Custom_430_OG [0x1ae]								
2019/07/12	06:24:30.0	AOCS_Ore-point_Start_19_OG [0x0a9]								
			AOCU_NM	5	02-76	00	b7	e8	01	ca
2019/07/12	06:24:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2019/07/12	07:20:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	07:20:32.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	07:20:34.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0		da			
2019/07/12	07:20:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]								
			MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2019/07/12	07:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2019/07/12	08:01:30.0	XRT_Custom_430_OG [0x1ae]								
2019/07/12	08:02:30.0	AOCS_Ore-point_Start_20_OG [0x0aa]								
			AOCU_NM	5	02-76	00	af	03	01	ca
2019/07/12	08:02:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2019/07/12	08:32:24.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	08:32:26.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	08:32:28.0	XRT_FOCUS_POSITION_406_OG [0x196]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2019/07/12	08:32:30.0	AOCS_Ore-point_Start_21_OG [0x0ab]								
			AOCU_NM	5	02-76	00	00	00	00	00
2019/07/12	08:32:48.0	XRT_FLD_DIS_409_OG [0x199]								
			MDP_XRT_FLD_DIS	1	07-F0		d9			
2019/07/12	08:32:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2019/07/12	08:32:52.0	XRT_ARS_DIS_443_OG [0x1bb]								
			MDP_XRT_ARS_DIS	1	07-F0		d5			
2019/07/12	08:35:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]								
			MDP_XRT_QT_PROG_SET	2	07-F0		c4	10		
2019/07/12	08:35:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2019/07/12	08:42:24.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	08:42:26.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	08:42:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2019/07/12	08:42:30.0	AOCS_Ore-point_Start_22_OG [0x0ac]								
			AOCU_NM	5	02-76	02	03	02	01	ca
2019/07/12	08:42:48.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0		d8			
2019/07/12	08:42:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2019/07/12	08:42:52.0	XRT_AEC_RESET_448_OG [0x1c0]								
			MDP_XRT_AEC_RESET	1	07-F0		d0			
2019/07/12	08:42:54.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0		d5			
2019/07/12	08:42:56.0	XRT_FLD_RESET_434_OG [0x1b2]								
			MDP_XRT_FLD_RESET	1	07-F0		da			
2019/07/12	08:45:26.0	XRT_QT_PROG_SET_407_OG [0x197]								
			MDP_XRT_QT_PROG_SET	2	07-F0		c4	08		
2019/07/12	08:45:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]								
			MDP_XRT_FL_PROG_SET	2	07-F0		c5	0d		
2019/07/12	08:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2019/07/12	09:00:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	09:00:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	09:00:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0		da			
2019/07/12	09:00:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]								
			MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2019/07/12	09:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2019/07/12	09:40:00.0	XRT_Custom_430_OG [0x1ae]								
2019/07/12	09:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2019/07/12	10:41:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	10:41:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0		c1			
2019/07/12	10:41:04.0	XRT_FLD_RESET_415_OG [0x19f]								



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2019/07/12	10:41:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	10:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/12	11:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/12	11:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/07/12	12:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	12:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	12:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	12:27:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/12	12:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/12	12:57:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/07/12	12:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	14:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	14:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	14:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	14:06:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/12	14:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/12	14:35:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/07/12	14:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	15:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	15:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	15:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	15:44:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/12	15:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/12	16:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	16:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	16:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	16:19:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/12	16:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/12	16:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/07/12	16:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	17:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	17:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	17:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	17:23:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/12	17:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/12	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	18:03:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	18:03:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/07/12	18:03:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 00 00 00 00
2019/07/12	18:03:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2019/07/12	18:03:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2019/07/12	18:03:52.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/07/12	18:06:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2019/07/12	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/07/12	18:13:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	18:13:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	18:13:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2019/07/12	18:13:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	02 03 02 01 ca

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2019/07/12	18:13:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2019/07/12	18:13:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/07/12	18:13:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2019/07/12	18:13:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2019/07/12	18:13:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	18:16:26.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2019/07/12	18:16:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/07/12	18:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/07/12	19:01:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	19:01:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	19:01:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	19:01:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/12	19:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/12	19:40:00.0	XRT_Custom_430_OG [0x1ae]			
2019/07/12	19:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/07/12	20:39:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	20:39:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	20:39:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	20:39:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/12	20:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/12	22:18:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	22:18:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	22:18:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	22:18:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/12	22:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/12	22:53:30.0	XRT_Custom_430_OG [0x1ae]			
2019/07/12	22:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/07/12	23:56:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	23:56:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/12	23:56:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/12	23:56:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/12	23:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/13	00:25:00.0	XRT_Custom_430_OG [0x1ae]			
2019/07/13	00:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/07/13	01:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/13	01:34:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/13	01:34:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/07/13	01:34:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/07/13	01:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/13	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/13	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/13	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/07/13	02:00:00.0	AOCS_Ore-point_Start_21_OG [0x0ab]			
		AOCU_NM	5	02-76	00 00 00 00 00
2019/07/13	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2019/07/13	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/07/13	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2019/07/13	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2019/07/13	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da

2019/07/13	02:02:56.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2019/07/13	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/07/13	02:03:30.0	XRT_Custom_430_OG [0x1ae]							
2019/07/13	02:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/07/13	03:07:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	03:07:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	03:07:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/07/13	03:07:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/07/13	03:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/07/13	03:42:00.0	XRT_Custom_430_OG [0x1ae]							
2019/07/13	03:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/07/13	04:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	04:35:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	04:35:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/07/13	04:35:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/07/13	04:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/07/13	05:20:30.0	XRT_Custom_430_OG [0x1ae]							
2019/07/13	05:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/07/13	05:48:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	05:48:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	05:48:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2019/07/13	05:49:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2019/07/13	05:49:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2019/07/13	05:49:22.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/07/13	05:51:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2019/07/13	05:52:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/07/13	05:58:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	05:58:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	05:58:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2019/07/13	05:59:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	02	03 02 01 ca			
2019/07/13	05:59:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/07/13	05:59:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/07/13	05:59:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/07/13	05:59:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/07/13	05:59:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/07/13	06:01:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2019/07/13	06:01:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/07/13	06:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/07/13	06:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	06:16:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	06:16:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/07/13	06:16:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/07/13	06:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/07/13	06:58:30.0	XRT_Custom_430_OG [0x1ae]							
2019/07/13	06:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/07/13	07:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	07:56:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/07/13	07:56:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/07/13	07:56:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							

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2019/07/13	07:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2019/07/13	08:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/07/13	08:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/07/13	09:15:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/07/13	09:39:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 00 00 00 00