

XRT Timeline to be uploaded on 2019/09/03

Period: 2019/09/03 11:05:00 - 2019/09/07 10:10:00

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Normal mode

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XOB #1C5D: CME watch - 4x4 - AEC 2/3 - Al-poly - AEC2 - Be-thin - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
09/03 11:18:00 - 09/03 13:59:54	Track (-55.2, 54.9) @ 09/03 11:15:00	# OP start + 10min. HOP 384 AR observations.
09/03 20:12:15 - 09/04 00:59:54	Fixed (0.0, 822.0)	# HOP 385 (N pole)
09/04 03:30:00 - 09/04 06:17:00	Fixed (0.0, 0.0)	# HOP 366 (PSP) + HOP 349 + synoptic.
09/04 16:03:00 - 09/04 20:59:54	Fixed (0.0, 822.0)	# HOP 385 (N pole).
09/05 00:03:00 - 09/05 02:59:54	Track (273.8, 59.6) @ 09/05 00:00:00	# AR obs.
09/05 07:19:30 - 09/05 10:13:00	Track (333.8, 62.1) @ 09/05 07:00:00	# HOP 384 AR obs.

PROG= 01 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec
Seqn= 30 1-time(s) 2.0sec
Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 10-time(s) 360.0sec
Seqn= 6 1-time(s) 2.0sec
Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 83 1-time(s) 2.0sec
thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1BE9: HOP81/206 2-filter - Al/poly 16s, Al/mesh 12s 30s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
09/03 14:03:00 - 09/03 19:59:54	Fixed (-22.0, 861.0)	# HOP 206 (N pole).

PROG= 12 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec
Seqn= 16 2-time(s) 2.0sec
Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec
Seqn= 90 1-time(s) 30.0sec
Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Subr= 3 60-time(s) 2.0sec
Seqn= 39 1-time(s) 30.0sec
Open/Al-mesh Open/Al-mesh close Safe Norm 11.3s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Al-poly/Open Al-poly/Open close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1C55: Synoptic Q95 2x2 - Al/mesh(512/2048/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(512/5795/1157)

Term	Pointing (x, y)	Comment
09/03 20:03:00 - 09/03 20:09:09	Fixed (0.0, 0.0)	synoptic, shifted
09/04 06:49:00 - 09/04 06:59:54	Fixed (0.0, 0.0)	# HOP 366 (PSP) + HOP 349 + synoptic.
09/04 15:53:00 - 09/04 15:59:54	Fixed (0.0, 0.0)	synoptic, shifted.
09/04 21:03:00 - 09/04 21:09:54	Fixed (0.0, 0.0)	# HOP 366 (PSP)
09/05 06:46:00 - 09/05 06:53:00	Fixed (0.0, 0.0)	# HOP 366 + HOP 349 + synoptic

PROG= 07 1-time(s)

Subr= 1 1-time(s) 2.0sec
Seqn= 5 1-time(s) 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 78 1-time(s) 2.0sec
Open/Al-mesh Open/Al-mesh close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 91 1-time(s) 2.0sec
Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open Al-poly/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 52 1-time(s) 2.0sec
thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close Safe Norm 16.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 23 1-time(s) 2.0sec
Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1C4D: HOP349 - 3-filter Synoptics (Al-mesh[128/1024/5795], Al-poly[256/4096/8192], thin-Be[2048/16384/32768] with 512x512 G-band+Leak(1064,1048)

Term	Pointing (x, y)	Comment
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09/04 01:03:00 - 09/04 03:08:00 Fixed (0.0, 0.0) # HOP 366 (PSP) + HOP 349 + synoptic.
 09/04 21:13:00 - 09/04 23:59:54 Fixed (0.0, 0.0) # HOP 366 (PSP)
 09/05 03:03:00 - 09/05 06:42:54 Fixed (0.0, 0.0) # HOP 366 + HOP 349 + synoptic

PROG= 17 Inf.-time(s)

Subr= 1		1-time(s)		300.0sec											
Seqn= 88		1-time(s)		2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 50		1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 56		1-time(s)		2.0sec											
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 81		1-time(s)		2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512	(1064, 1048)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512	(1064, 1048)	Q=95	0	0	2.0sec	
Subr= 2		6-time(s)		450.0sec											
Seqn= 8		1-time(s)		2.0sec											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec	
Seqn= 6		1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec	
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec	
Seqn= 29		1-time(s)		2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	2	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

XOB #1BD7: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 900s cad (G-band/Leak last)

Term	Pointing (x, y)	Comment
09/04 07:03:00 - 09/04 15:49:54	Track (123.4, 55.6) @ 09/04 07:00:00	# HOP 384 AR (officially 7-14UT).

PROG= 08 Inf.-time(s)

Subr= 1		8-time(s)		900.0sec											
Seqn= 8		1-time(s)		2.0sec											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec	
Seqn= 6		1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	3	0	2.0sec	
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048	(1024, 1024)	DPCM	2	0	2.0sec	
Subr= 2		1-time(s)		2.0sec											
Seqn= 30		1-time(s)		2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512	(1024, 1024)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512	(1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
09/03 11:18:00 - 09/03 13:59:54	Track (-55.2, 54.9) @ 09/03 11:15:00	# OP start + 10min. HOP 384 AR observations.
09/03 14:03:00 - 09/03 19:59:54	Fixed (-22.0, 861.0)	# HOP 206 (N pole).
09/03 20:12:15 - 09/04 00:59:54	Fixed (0.0, 822.0)	# HOP 385 (N pole).
09/04 01:03:00 - 09/04 03:08:00	Fixed (0.0, 0.0)	# HOP 366 (PSP) + HOP 349 + synoptic.
09/04 03:30:00 - 09/04 06:17:00	Fixed (0.0, 0.0)	# HOP 366 (PSP) + HOP 349 + synoptic.
09/04 07:03:00 - 09/04 15:49:54	Track (123.4, 55.6) @ 09/04 07:00:00	# HOP 384 AR (officially 7-14UT).
09/04 16:03:00 - 09/04 20:59:54	Fixed (0.0, 822.0)	# HOP 385 (N pole).
09/04 21:13:00 - 09/04 23:59:54	Fixed (0.0, 0.0)	# HOP 366 (PSP)
09/05 00:03:00 - 09/05 02:59:54	Track (273.8, 59.6) @ 09/05 00:00:00	# AR obs.
09/05 03:03:00 - 09/05 06:42:54	Fixed (0.0, 0.0)	# HOP 366 + HOP 349 + synoptic
09/05 07:19:30 - 09/05 10:13:00	Track (333.8, 62.1) @ 09/05 07:00:00	# HOP 384 AR obs.

PROG= 13 30-time(s)

Subr= 1		20-time(s)		2.0sec											
Seqn= 11		1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec	
Seqn=100		1-time(s)		10.0sec											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384	(1024, 1024)	Q=95	2	0	2.0sec	
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec	
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 11		1-time(s)		2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec	

Seqn= 87	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

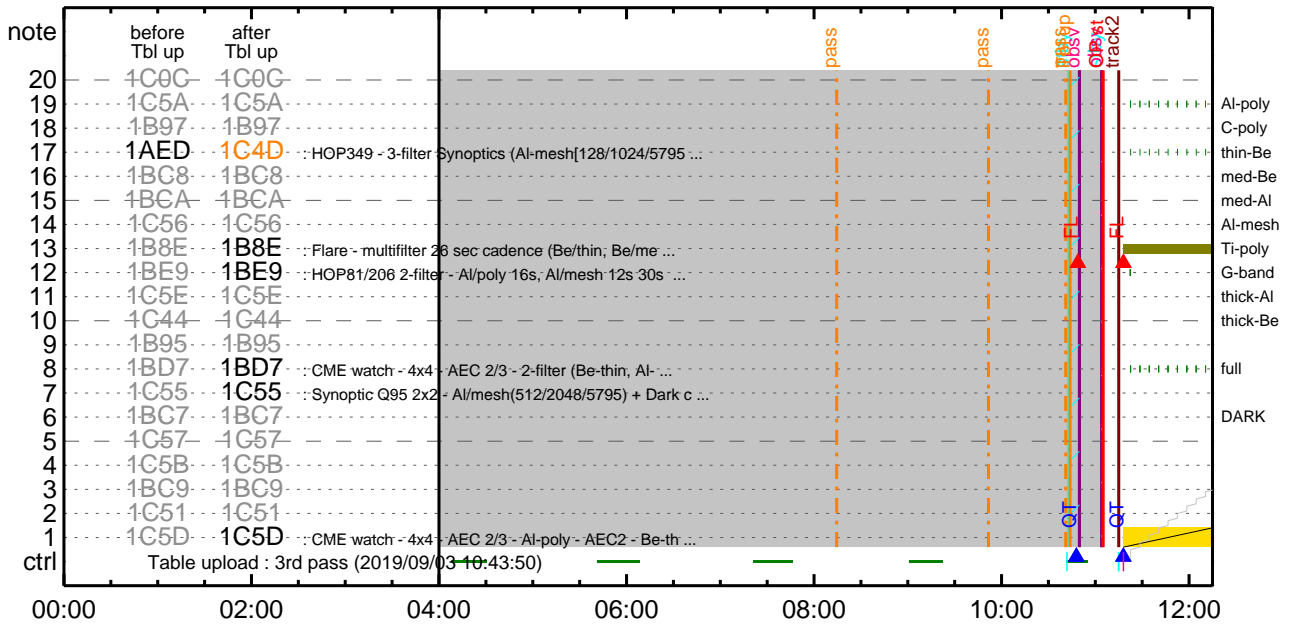
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Flare Detection

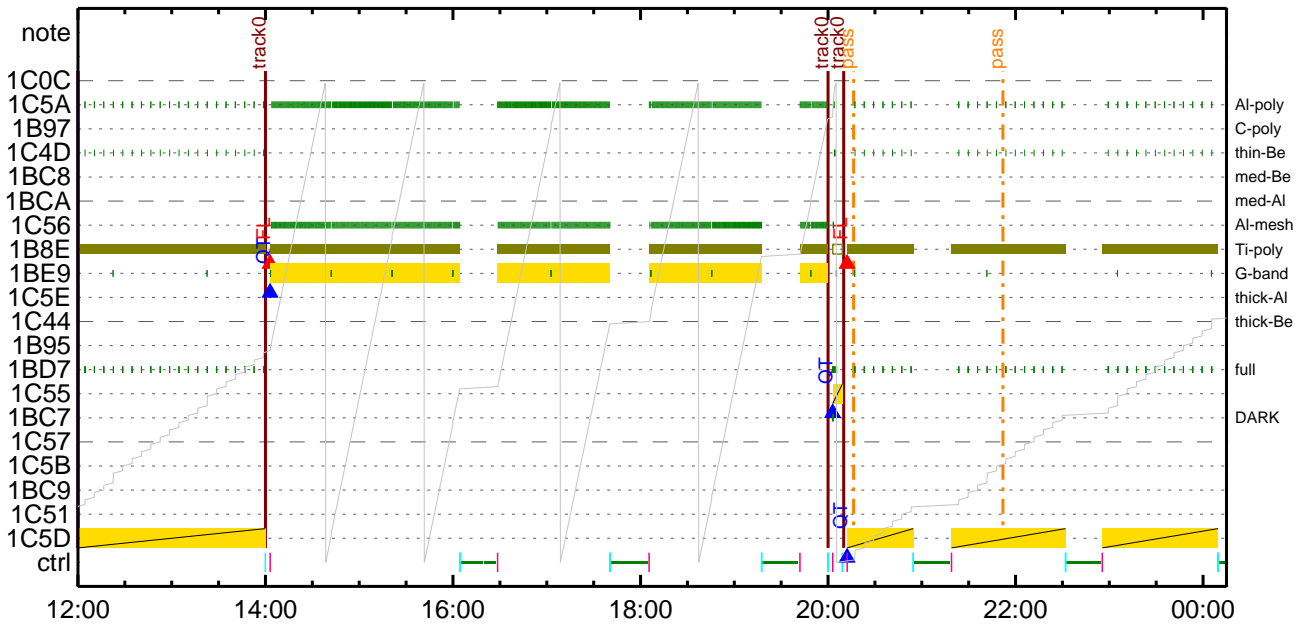
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FLD Patrol													
Term	Pointing (x, y)						Comment						
09/03 20:09:33 - 09/04 06:46:18	Fixed (0.0,	0.0)	synoptic, shifted									
09/04 07:00:18 - 09/04 15:50:18	Track (123.4,	55.6)	# HOP 384 AR (officially 7-14UT).									
09/04 16:00:18 - 09/04 21:00:18	Fixed (0.0,	822.0)	# HOP 385 (N pole).									
09/04 21:10:18 - 09/05 06:43:18	Fixed (0.0,	0.0)	# HOP 366 (PSP)									
09/05 07:00:18 - 09/07 10:10:00	Track (333.8,	62.1)	# HOP 384 AR obs.									
AI-poly/Open	AI-poly/Open	close	Safe	Norm	8ms	Obs	8x8	Q=50			80sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

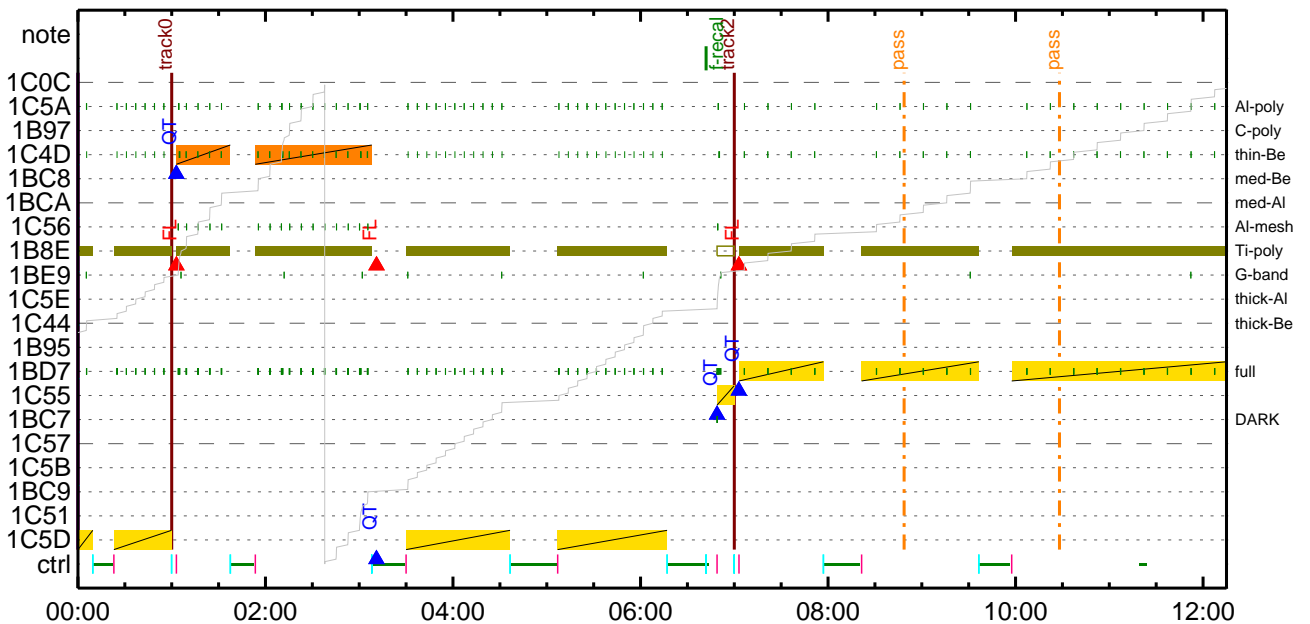
CMDI #0452 2019/09/03



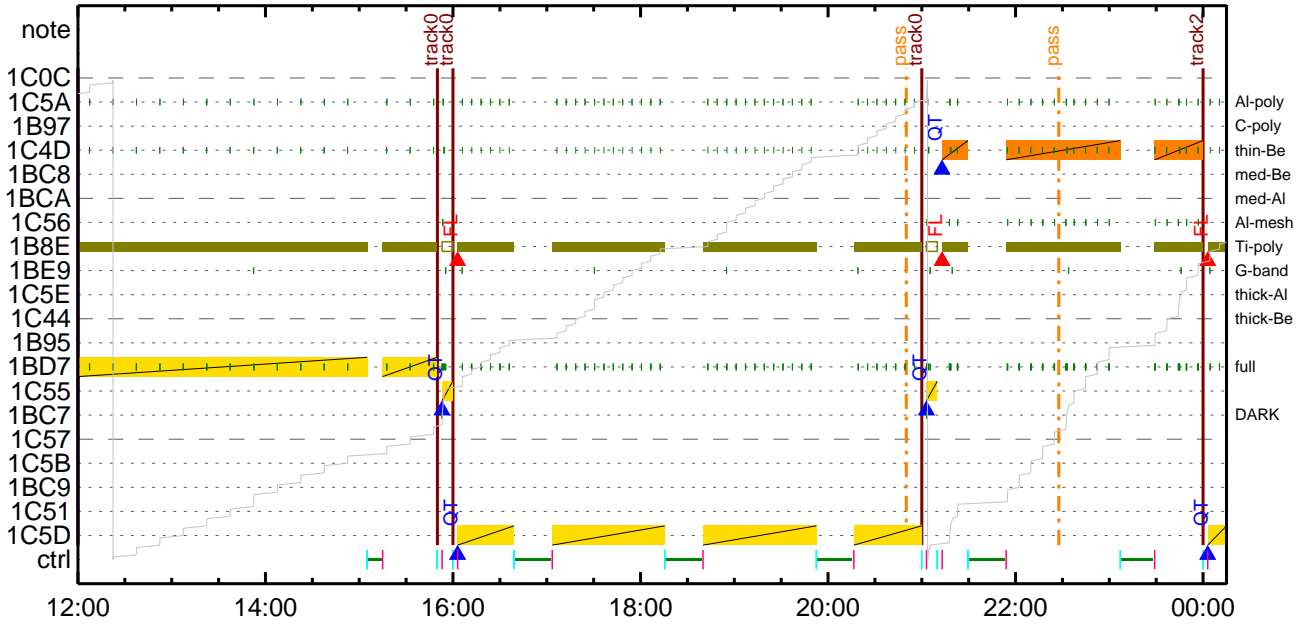
CMDI #0452 2019/09/03



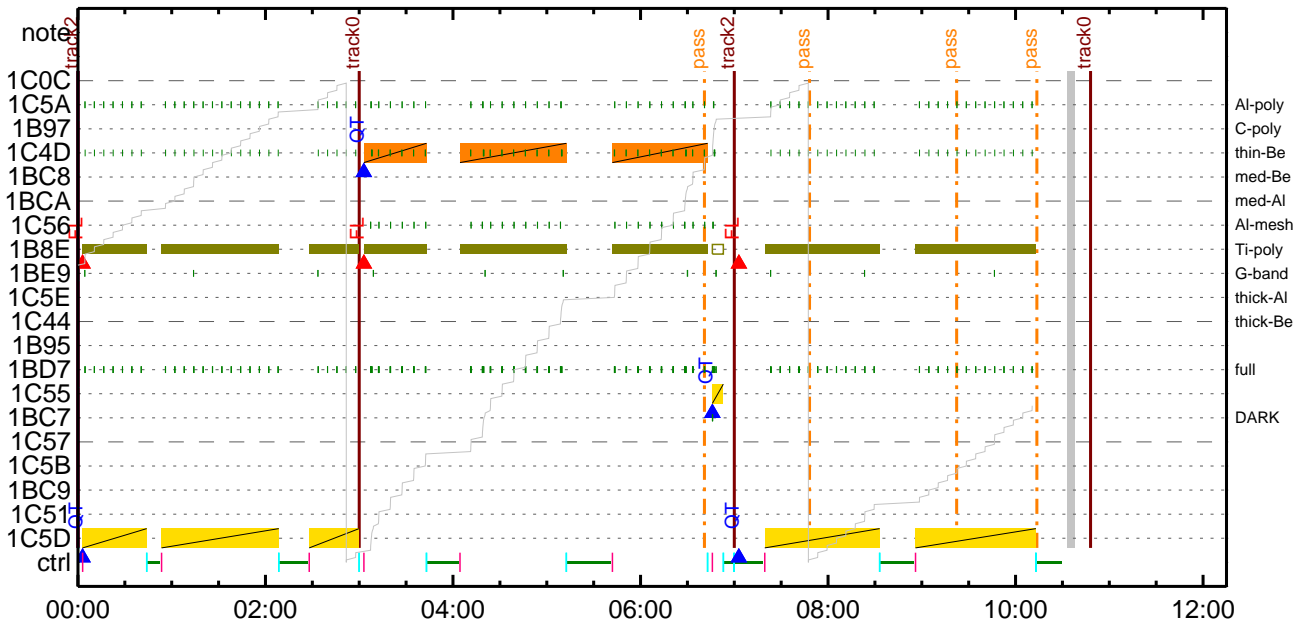
CMDI #0452 2019/09/04



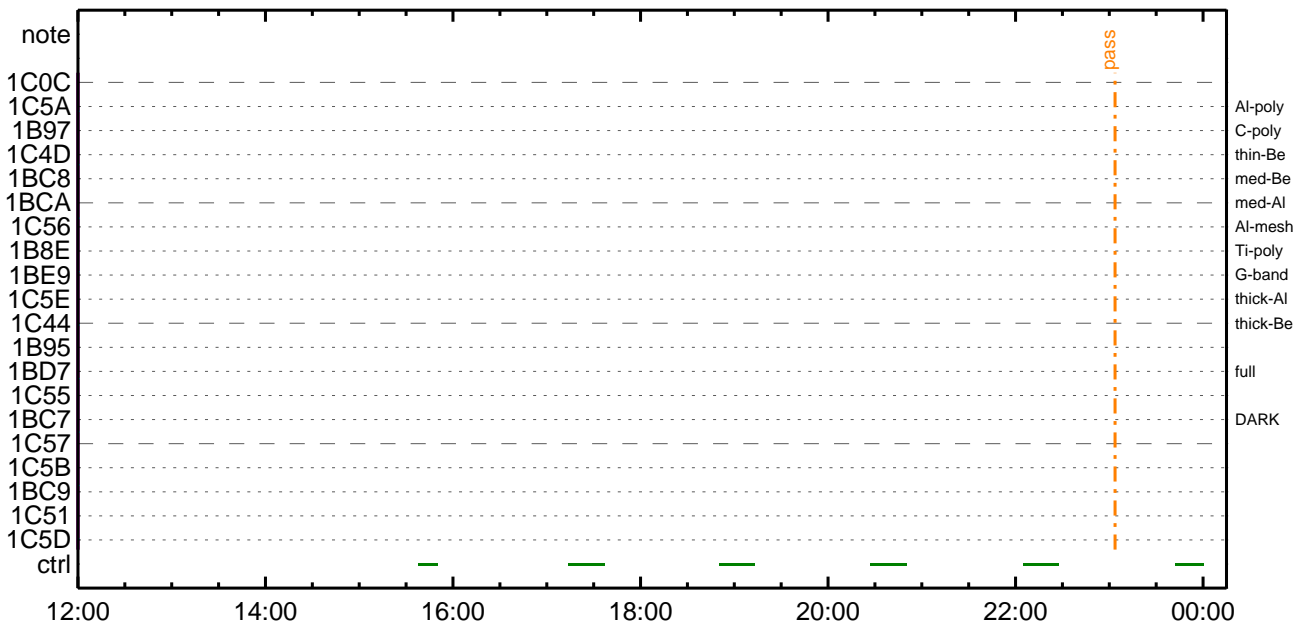
CMDI #0452 2019/09/04



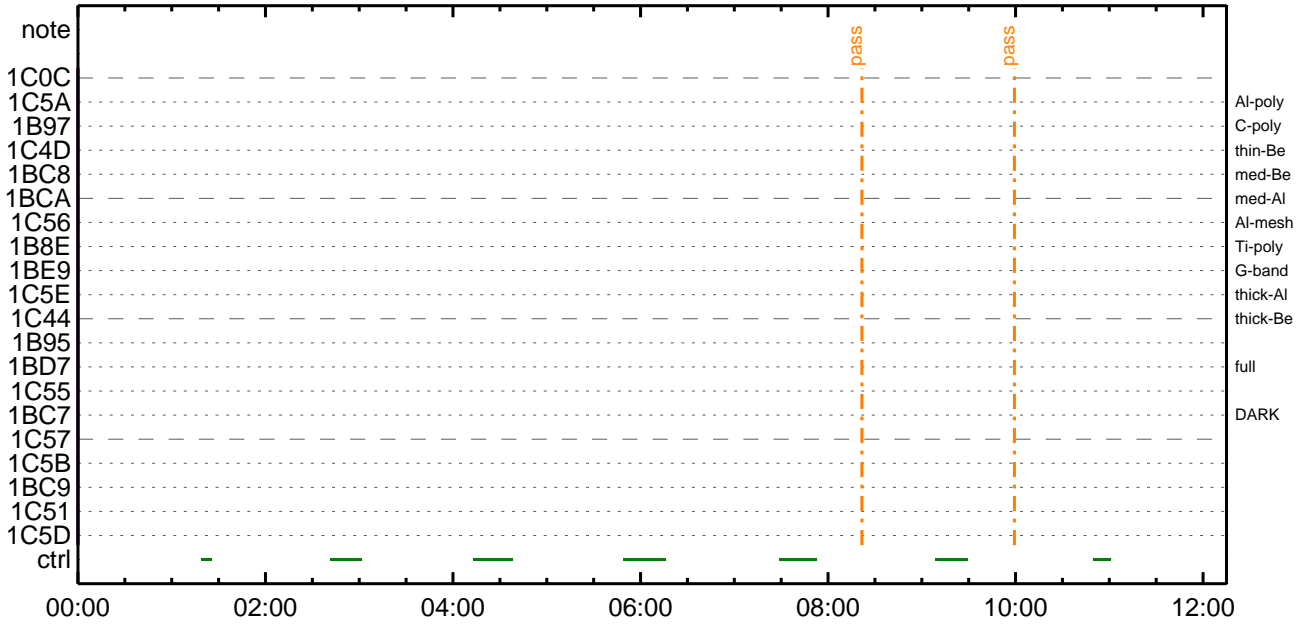
CMDI #0452 2019/09/05



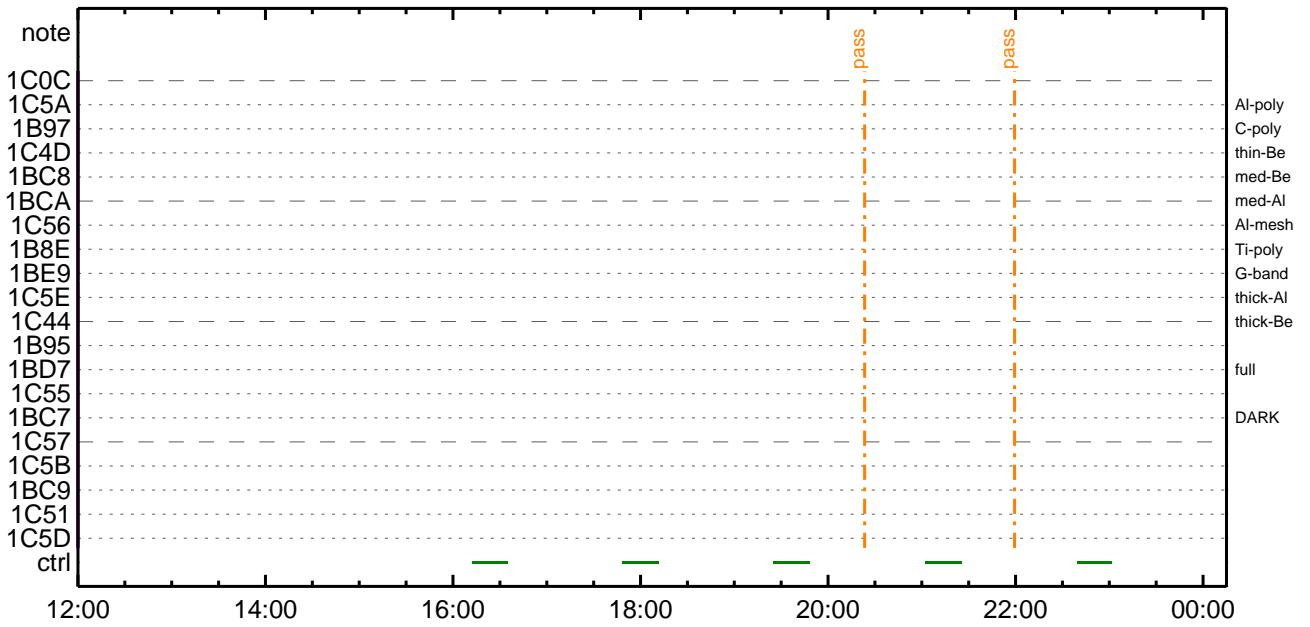
CMDI #0452 2019/09/05



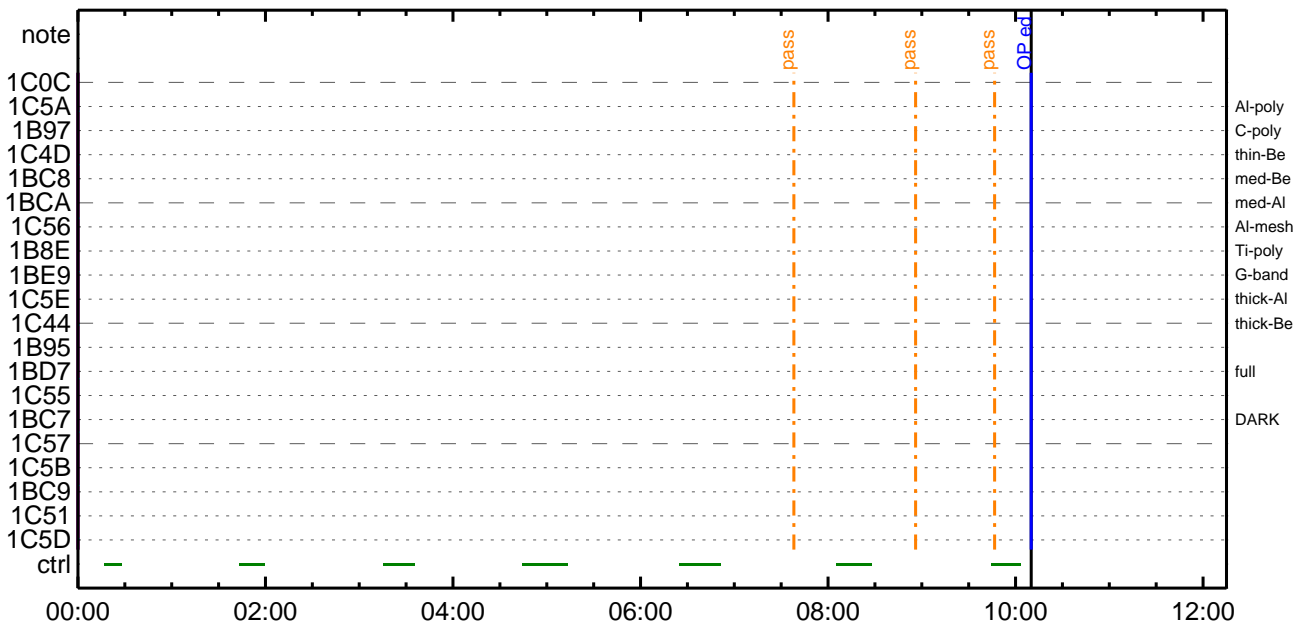
CMDI #0452 2019/09/06



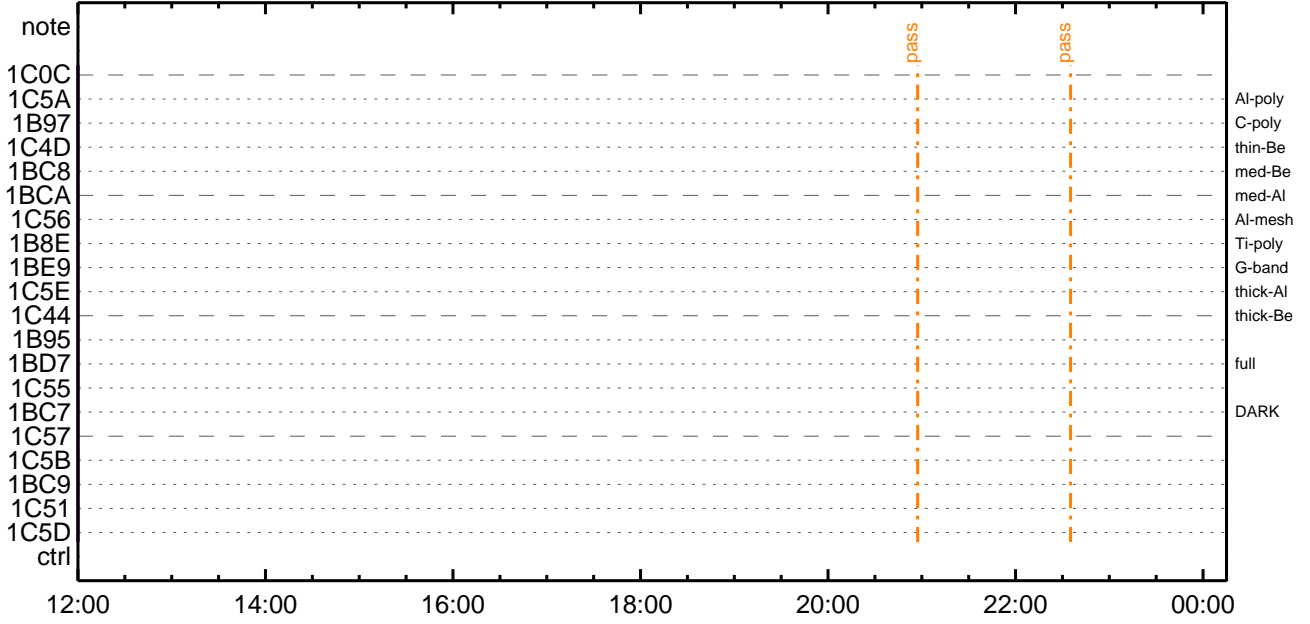
CMDI #0452 2019/09/06



CMDI #0452 2019/09/07



CMDI #0452 2019/09/07




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0096 . C.
0097 . C.
0098 . C. *****
0099 . C. OP/OGY1;4YE;|YAYOYx
0100 . C. *****
0101 . C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-315:OP
0104 . C. ( )
0105 . S. OG og-315:OG
0106 . C. ( )
0107 . C.
0108 . C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 . C. ðç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 . C. ðç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 . C. ðç[HK1_DMP_BLOCK_NUM] EQ 127
0115 . C. ðç[HK1_DMP_REPEAT_NUM] EQ 0
0116 . C. ðç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 . C. ðç[HK1_PKT_FORM_NO] EQ 7
0120 . C. ðç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 . C. ðç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 . C. ðç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 . C. ðç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAYOYx½ª î»ð³ ÎÇ§
0125 . C. ðç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGαî¾È¹ç•è² ÎOKαð³ ÎÇ§
0127 . C.
0128 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 . C. ðç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 . C. ðç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 . C. ðç[HK1_DMP_BLOCK_NUM] EQ 127
0134 . C. ðç[HK1_DMP_REPEAT_NUM] EQ 0
0135 . C. ðç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 . C. ðç[HK1_PKT_FORM_NO] EQ 7
0139 . C. ðç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 . C. ðç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 . C. ðç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 . C. ðç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAYOYx½ª î»ð³ ÎÇ§
0144 . C. ðç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGαî¾È¹ç•è² ÎOKαð³ ÎÇ§
0146 . C.
0147 . C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 . C. ðç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 . C. ðç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 . C. ðç[HK1_DMP_BLOCK_NUM] EQ 65
0153 . C. ðç[HK1_DMP_REPEAT_NUM] EQ 0
0154 . C. ðç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 . C. ðç[HK1_PKT_FORM_NO] EQ 7
0158 . C. ðç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 . C. ðç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 . C. ðç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 . C. ðç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAYOYx½ª î»ð³ ÎÇ§
0163 . C. ðç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OPαî¾È¹ç•è² ÎOKαð³ ÎÇ§
0165 . C.
0166 . C. ***** °È²¼α Î¾Ä´ ¶Á°ÈÈ¬α °Ä÷¿® (¼âµ-YAYOYx½ª è½çαðÄÔÃæαÇ¼ªα° ðè¼i¹çαçαâ) *****
0167 . C. DHUYâ;4YE;È½Y½;Yi;4YE;Èðîãα¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 . C. ðç[HK1_PKT_FORM_NO] EQ 2
0171 . C. ðç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 . C. ðç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 . C. ðç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 . C.
0175 . C. *****
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)
0177 . C. *****
0178 . C.
0179 . C. NOTICE ; § OPOG UPLOADα-Á÷¿@NGUî¾i¹ç;ç°È²¼α îTI-CMDÁ÷¿α î¾Á¹Ôα•αÈααα³ ðÈ; f
0180 . C. αßα¿;çSETαÈDUMPAîÆ±°iYNY¹αç¹Ôα|α³ ðÈ; f
0181 . C.
0182 . C. TIY³YßYÖYÈððÄDî¿¿ (UT)
0183 +. TI 2019-09-03 11:00:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 . C. ðç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 . C.
0187 +. TI 2019-09-03 11:00:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 . C. ðç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 . C.
0191 +. TI 2019-09-03 11:00:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 . C. ðç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

```

0194 C.
0195 +. TI 2019-09-03 11:04:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.        φφ[HK1_TI_CMD_NUM]             EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αİÄē%îîñαîŷĂŷ§ŷĂŷ⁻¹àîÛ
0200 C.        φφ[HK1_TI_CMD_ENA/DIS]         EQ      ENA
0201 C.        φφ[HK1_TI_CMD_NUM]             EQ      4
0202 C.        φφ[HK1_NEXT_EXEC_PIM]         EQ      DHU
0203 C.        φφ[HK1_NEXT_EXEC_DC]         EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷĂŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.        φφ[HK1_DMP_TOP_ADRS_1]       EQ      07
0213 C.        φφ[HK1_DMP_TOP_ADRS_0]       EQ      2B
0214 C.        φφ[HK1_DMP_BLOCK_NUM]        EQ      3
0215 C.        φφ[HK1_DMP_REPEAT_NUM]      EQ      0
0216 C.        φφ[HK1_DMA_DMP_PIM]         EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.        φφ[HK1_PKT_FORM_NO]         EQ      7
0220 C.        φφ[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.        φφ[HK1_S_TLM_BIT_RATE]      EQ      32k
0222 C.        φφ[HK1_X_TLM_BIT_RATE]      EQ      4M
0223 C.        φφ[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ŷĂŷÖŷ×½ªî»αò³îç§
0226 C.        φφ[HK1_DMP_CHK_FLG]         EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•ē²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÊ;È¼ŷ¼. ŷî;¼ŷÈ;Èαòîâα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.        φφ[HK1_PKT_FORM_NO]         EQ      2
0234 C.        φφ[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.        φφ[HK1_S_TLM_BIT_RATE]      EQ      32K
0236 C.        φφ[HK1_X_TLM_BIT_RATE]      EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2019-09-03 11:04:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.      HK1_TI_CMD_NUM           = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2019-09-03 11:04:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2019-09-03 11:04:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]       EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2019-09-03 11:04:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]       EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `ûÃîαİ»ö¼ŷ¼αÉÄα¹αēDCBC•x²è *****
0276 C. (¼â°îŷÖŷĂŷÈŷŷŷÈŷáŷçŷèòÈ¼αα¼Ă»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•İ Daily±çİñòÈ'Øα¹αēDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ăLOSŷĂŷ§ŷĂŷ⁻¹¼Ă»Û;ă
0287 C.
0288 C. ***** LOS *****
0289 C.

```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 5475221.0 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 24s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2019-09-03 11:04:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÃîî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0165 C. (¼á°î¼ÝðËÄð¹ñèDCBC•x²è)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¼D¼¹•î Daily±¼îñè'ø¹ñèDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOS¼Á¼S¼Ä¼-¼Á»Ü;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-317 2019-09-03 12:02:01 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁ•µ°È»Í×ÁÇ¿ÍY¿Y×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈÈ¿°ÇÓã•¿¿¼í¹¿ãÍ;çÁ®, ù¿¹ãÈ¿ãÇÁ+¿®ã•¿Èãã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-289:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2019-09-03 11:04:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 06 80 80 08 08)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 07 80 80 20 20)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 08 85 83 06 06)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 09 80 80 20 08)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 0a 80 80 08 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0b 85 83 08 08)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 + DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 + DC 07-F0 MDP_XRT_ARS_DIS
0093 BC (d5)
0094 + DC 07-F0 MDP_XRT_AEC_RESET
0095 BC (d0)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 01)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 0d)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2019-09-03 11:04:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP 'úÃîâî»ö¼ÝðĒÂÐâ¹æDCBC•x²è *****
0117 C. (¼â°îŸÓŸÁŸĒŸŒŸŦŸáŸçŸèâĒ¼â¼Ā»Ūâ¹æè)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ŸÐŸ¹•İ Daily±¿İÑâĒ'Øâ¹æDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSŸÁŸŒŸĀŸ⁻¼Ā»Ū;ã
0128 C.
0129 . C. ***** LOS *****
0130 C.
```

*** OP Sequence for XRT ***

```

2019/09/03 11:14:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 11:14:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 11:14:58.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2019/09/03 11:15:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 02 00 00 00 00
2019/09/03 11:15:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2019/09/03 11:15:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2019/09/03 11:15:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2019/09/03 11:15:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2019/09/03 11:15:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/09/03 11:17:56.0 XRT_QT_PROG_SET_431_OG [0x1af]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2019/09/03 11:17:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2019/09/03 11:18:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/09/03 13:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 13:59:56.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2019/09/03 14:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 b3 75 01 f3
2019/09/03 14:00:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2019/09/03 14:00:18.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2019/09/03 14:00:20.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2019/09/03 14:00:22.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2019/09/03 14:00:24.0 XRT_FLD_RESET_407_OG [0x197]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/09/03 14:02:56.0 XRT_QT_PROG_SET_405_OG [0x195]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0c
2019/09/03 14:02:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2019/09/03 14:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/09/03 16:04:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 16:04:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 16:04:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/09/03 16:04:36.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2019/09/03 16:07:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2019/09/03 16:27:30.0 XRT_Custom_430_OG [0x1ae]
2019/09/03 16:28:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/09/03 17:40:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 17:40:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 17:40:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/09/03 17:40:36.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2019/09/03 17:43:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2019/09/03 18:04:30.0 XRT_Custom_430_OG [0x1ae]
2019/09/03 18:05:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/09/03 19:17:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 19:17:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 19:17:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/09/03 19:17:36.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2019/09/03 19:20:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2019/09/03 19:41:00.0 XRT_Custom_430_OG [0x1ae]
2019/09/03 19:42:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/09/03 19:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 19:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/03 19:59:58.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00

```

2019/09/03	20:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	00	00	00	00	00	
2019/09/03	20:00:18.0	XRT_FLD_DIS_409_OG [0x199]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2019/09/03	20:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2019/09/03	20:00:22.0	XRT_ARS_DIS_443_OG [0x1bb]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2019/09/03	20:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07				
2019/09/03	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2019/09/03	20:09:09.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2019/09/03	20:09:11.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2019/09/03	20:09:13.0	XRT_FOCUS_POSITION_406_OG [0x196]								
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2019/09/03	20:09:33.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2019/09/03	20:09:35.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2019/09/03	20:09:37.5	XRT_AEC_RESET_448_OG [0x1c0]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2019/09/03	20:09:39.5	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2019/09/03	20:09:41.5	XRT_FLD_RESET_434_OG [0x1b2]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2019/09/03	20:10:00.0	AOCS_OrE-point_Start_4_OG [0x09a]								
		AOCU_NM	5	02-76	00	b6	f2	00	00	
2019/09/03	20:12:11.5	XRT_QT_PROG_SET_431_OG [0x1af]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01				
2019/09/03	20:12:13.5	XRT_FL_PROG_SET_440_OG [0x1b8]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d				
2019/09/03	20:12:15.5	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2019/09/03	20:54:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2019/09/03	20:54:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2019/09/03	20:54:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2019/09/03	20:54:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2019/09/03	20:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2019/09/03	21:18:00.0	XRT_Custom_430_OG [0x1ae]								
2019/09/03	21:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2019/09/03	22:32:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2019/09/03	22:32:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2019/09/03	22:32:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2019/09/03	22:32:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2019/09/03	22:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2019/09/03	22:54:30.0	XRT_Custom_430_OG [0x1ae]								
2019/09/03	22:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2019/09/04	00:09:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2019/09/04	00:09:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2019/09/04	00:09:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2019/09/04	00:09:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2019/09/04	00:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2019/09/04	00:22:00.0	XRT_Custom_430_OG [0x1ae]								
2019/09/04	00:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2019/09/04	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2019/09/04	00:59:56.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2019/09/04	00:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]								
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2019/09/04	01:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	00	00	00	00	00	
2019/09/04	01:00:18.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2019/09/04	01:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2019/09/04	01:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2019/09/04	01:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2019/09/04	01:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]								
		MDP_XRT_FLD_RESET	1	07-F0	da					

2019/09/04	01:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2019/09/04	01:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/09/04	01:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/04	01:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	01:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	01:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/04	01:37:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/04	01:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/04	01:52:30.0	XRT_Custom_430_OG [0x1ae]					
2019/09/04	01:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/04	03:08:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	03:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	03:08:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2019/09/04	03:08:24.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/09/04	03:08:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/09/04	03:08:28.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/09/04	03:08:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/04	03:08:32.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/04	03:11:02.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2019/09/04	03:11:04.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/09/04	03:29:00.0	XRT_Custom_430_OG [0x1ae]					
2019/09/04	03:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/04	04:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	04:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	04:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/04	04:36:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/04	04:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/04	05:06:00.0	XRT_Custom_430_OG [0x1ae]					
2019/09/04	05:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/04	06:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	06:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	06:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/04	06:17:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/04	06:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/04	06:41:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	06:41:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	06:41:58.0	XRT_FOCUS_RECALIBRATE_403_OG [0x193]	XRT_FOCUS_RECAL	2	07-F8	78	00
2019/09/04	06:45:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2019/09/04	06:46:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2019/09/04	06:46:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2019/09/04	06:46:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/04	06:48:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2019/09/04	06:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/04	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	06:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/04	06:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2019/09/04	07:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00	00
2019/09/04	07:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	

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2019/09/04	07:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/09/04	07:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/09/04	07:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/04	07:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/04	07:02:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08	
2019/09/04	07:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	
2019/09/04	07:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/04	07:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	07:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	07:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/04	07:57:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/04	08:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/04	08:20:30.0	XRT_Custom_430_OG [0x1ae]						
2019/09/04	08:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/04	09:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	09:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	09:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/04	09:36:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/04	09:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/04	09:56:31.0	XRT_Custom_430_OG [0x1ae]						
2019/09/04	09:57:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/04	15:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	15:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	15:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/04	15:05:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/04	15:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/04	15:14:00.0	XRT_Custom_430_OG [0x1ae]						
2019/09/04	15:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/04	15:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	15:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	15:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2019/09/04	15:50:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00	
2019/09/04	15:50:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2019/09/04	15:50:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2019/09/04	15:50:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/04	15:52:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07	
2019/09/04	15:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/04	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	15:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	15:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2019/09/04	16:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	b6 f2 00 00	
2019/09/04	16:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/09/04	16:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/09/04	16:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/09/04	16:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/04	16:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/04	16:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01	
2019/09/04	16:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d	

2019/09/04	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/04	16:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	16:39:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	16:39:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/04	16:39:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/04	16:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/04	17:02:30.0	XRT_Custom_430_OG [0x1ae]							
2019/09/04	17:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/04	18:15:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	18:15:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	18:15:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/04	18:15:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/04	18:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/04	18:39:00.5	XRT_Custom_430_OG [0x1ae]							
2019/09/04	18:40:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/04	19:52:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	19:52:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	19:52:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/04	19:52:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/04	19:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/04	20:15:30.0	XRT_Custom_430_OG [0x1ae]							
2019/09/04	20:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/04	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	20:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	20:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2019/09/04	21:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2019/09/04	21:00:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2019/09/04	21:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2019/09/04	21:00:22.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/09/04	21:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2019/09/04	21:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/04	21:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	21:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	21:09:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2019/09/04	21:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/09/04	21:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/09/04	21:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/09/04	21:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/09/04	21:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/04	21:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2019/09/04	21:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2019/09/04	21:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/04	21:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	21:29:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/04	21:29:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/04	21:29:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/04	21:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/04	21:53:00.0	XRT_Custom_430_OG [0x1ae]							
2019/09/04	21:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

2019/09/04	23:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/04	23:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	23:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	23:07:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/04	23:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/04	23:28:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/04	23:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/04	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/04	23:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/05	00:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/09/05	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00		
2019/09/05	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/09/05	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/09/05	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/09/05	00:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/05	00:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/05	00:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2019/09/05	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/09/05	00:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/05	00:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/05	00:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/05	00:44:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/05	00:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/05	00:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/05	00:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/05	02:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/05	02:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/05	02:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/05	02:08:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/05	02:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/05	02:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/05	02:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/05	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/05	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/05	02:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/05	03:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/09/05	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00		
2019/09/05	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/09/05	03:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/09/05	03:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/09/05	03:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/05	03:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/05	03:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2019/09/05	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/09/05	03:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/05	03:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/05	03:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

2019/09/05	03:43:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/05	03:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/05	04:03:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/05	04:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/05	05:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	05:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	05:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/05	05:12:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/05	05:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/05	05:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/05	05:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	06:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	06:42:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	06:42:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/09/05	06:43:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2019/09/05	06:43:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2019/09/05	06:43:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/05	06:45:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2019/09/05	06:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/05	06:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	06:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	06:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/05	06:53:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/05	06:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/05	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	06:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	06:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/09/05	07:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	ACOU_NM	5	02-76	02 00 00 00 00	
2019/09/05	07:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/09/05	07:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/09/05	07:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/09/05	07:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/05	07:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/05	07:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2019/09/05	07:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2019/09/05	07:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/05	07:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	08:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	08:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	08:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/05	08:33:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/05	08:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/05	08:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/05	08:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	10:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	10:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/05	10:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/05	10:13:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2019/09/05	10:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2019/09/05	10:48:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76 00 00 00 00 00	