

XRT Timeline to be uploaded on 2019/09/07

Period: 2019/09/07 10:10:00 - 2019/09/12 11:27:00

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Normal mode

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XOB #1C5D: CME watch - 4x4 - AEC 2/3 - Al-poly - AEC2 - Be-thin - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
09/07 10:23:00 - 09/07 10:59:54	Track (415.0, 151.8) ^{Ⓢ 09/07 10:20:00}	# OP start + 10min, HOP384
09/07 22:18:00 - 09/08 02:00:00	Fixed (0.0, 0.0)	HOP349, HOP366 and synoptic
09/08 06:14:07 - 09/08 07:00:30	Fixed (0.0, 0.0)	HOP349, HOP366 and synoptic
09/08 07:25:40 - 09/08 09:59:54	Track (569.4, 164.1) ^{Ⓢ 09/08 07:10:00}	HOP384
09/08 22:48:00 - 09/09 01:59:54	Track (670.4, 175.8) ^{Ⓢ 09/08 22:45:00}	New region
09/09 05:42:30 - 09/09 08:29:54	Track (710.5, 181.5) ^{Ⓢ 09/09 05:39:30}	HOP384
09/09 15:03:00 - 09/09 15:44:54	Fixed (-22.0, -39.0)	HOP312
09/10 06:15:30 - 09/10 10:59:05	Track (826.5, 204.4) ^{Ⓢ 09/10 06:13:30}	HOP384

PROG= 01 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 30	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 2	10-time(s)	360.0sec										
└─ Seqn= 6	1-time(s)	2.0sec										
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
└─ Seqn= 83	1-time(s)	2.0sec										
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C09: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
09/07 11:03:00 - 09/07 17:28:54	Fixed (-22.0, -939.0)	HOP81 on S-pole
09/07 17:42:00 - 09/07 21:37:00	Fixed (0.0, 822.0)	HOP385
09/08 10:03:00 - 09/08 17:59:54	Fixed (-22.0, 896.0)	HOP81 on N-pole
09/08 18:13:00 - 09/08 22:12:00	Fixed (0.0, 822.0)	HOP385
09/09 15:58:00 - 09/09 20:29:54	Fixed (0.0, 822.0)	HOP385

PROG= 16 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 16	2-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 90	1-time(s)	30.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3	60-time(s)	60.0sec										
└─ Seqn= 57	1-time(s)	30.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C55: Synoptic Q95 2x2 - Al/mesh(512/2048/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(512/5795/1157)

Term	Pointing (x, y)	Comment
09/07 17:32:00 - 09/07 17:38:54	Fixed (0.0, 0.0)	synoptic, shifted manually
09/08 06:02:06 - 09/08 06:11:01	Fixed (0.0, 0.0)	HOP349, HOP366 and synoptic
09/08 18:03:00 - 09/08 18:09:54	Fixed (0.0, 0.0)	synoptic
09/09 15:48:00 - 09/09 15:54:54	Fixed (0.0, 0.0)	synoptic, shifted manually
09/10 03:18:00 - 09/10 03:22:30	Fixed (0.0, 0.0)	HOP349 and synoptic

PROG= 07 1-time(s)

Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 5	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 78	1-time(s)	2.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 91	1-time(s)	2.0sec										
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 52	1-time(s)	2.0sec										
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 23	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec

Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C44: HOP349 - 3-filter Synoptics (Al-mesh[128/1024/5795], Al-poly[256/4096/8192], thin-Be[2048/16384/32768] with 512x512 G-band+Leak(1064,1048)

Term	Pointing (x, y)	Comment
09/08 02:03:06 - 09/08 05:59:00	Fixed (0.0, 0.0)	HOP349, HOP366 and synoptic
09/09 02:03:00 - 09/09 05:34:54	Fixed (0.0, 0.0)	HOP349 and synoptic
09/10 03:42:36 - 09/10 06:12:24	Fixed (0.0, 0.0)	HOP349 and synoptic

PROG= 10 Inf.-time(s)

Subr= 1 1-time(s) 300.0sec												
Seqn= 88 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 50 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 56 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 81 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
Subr= 2 15-time(s) 180.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C56: Synoptic 7 Filter w/ Al-mesh(512/2048/5795), Al-poly(512/5795/11571), Thin-Be(3897/16384/32768) - Thick-Be(65536), Al-poly+Ti-poly(4096/2314)

Term	Pointing (x, y)	Comment
09/09 05:38:00 - 09/09 05:39:24	Fixed (0.0, 0.0)	HOP349 and synoptic

PROG= 14 1-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 78 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 91 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 52 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 46 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17 1-time(s) 2.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 97 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1B93: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 30s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
09/09 08:33:00 - 09/09 14:47:00	Fixed (-22.0, 861.0)	HOP206 on N-pole

PROG= 06 Inf.-time(s)												
Subr= 1	1-time(s)	2.0sec										
└ Seqn= 16	2-time(s)	2.0sec										
└ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
└ Seqn= 90	1-time(s)	30.0sec										
└ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3	60-time(s)	2.0sec										
└ Seqn= 57	1-time(s)	30.0sec										
└ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└ Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C5A: HOP336 1-filter - Al/poly -384x384, 1024ms and 8192s, 60s-cadence, G-band - 384x384 1ms												
Term	Pointing (x, y)							Comment				
09/09 20:33:00 - 09/09 23:59:54	Track (-30.5, 601.0) @ 09/09 20:30:00							HOP336 N hemisphere				
09/10 00:03:00 - 09/10 03:14:54	Track (-31.7, -704.0) @ 09/10 00:00:00							HOP336 S hemisphere				

PROG= 19 Inf.-time(s)												
Subr= 1	1-time(s)	2.0sec										
└ Seqn= 16	2-time(s)	2.0sec										
└ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
└ Seqn= 90	1-time(s)	30.0sec										
└ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3	15-time(s)	2.0sec										
└ Seqn= 72	1-time(s)	60.0sec										
└ Al-poly/Open	Al-poly/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└ Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)							Comment				
09/07 10:23:00 - 09/07 10:59:54	Track (415.0, 151.8) @ 09/07 10:20:00							# OP start + 10min, HOP384				
09/07 11:03:00 - 09/07 17:28:54	Fixed (-22.0, -939.0)							HOP81 on S-pole				
09/07 17:42:00 - 09/07 21:37:00	Fixed (0.0, 822.0)							HOP385				
09/07 22:18:00 - 09/08 02:00:00	Fixed (0.0, 0.0)							HOP349, HOP366 and synoptic				
09/08 02:03:06 - 09/08 05:59:00	Fixed (0.0, 0.0)							HOP349, HOP366 and synoptic				
09/08 06:14:07 - 09/08 07:00:30	Fixed (0.0, 0.0)							HOP349, HOP366 and synoptic				
09/08 07:25:40 - 09/08 09:59:54	Track (569.4, 164.1) @ 09/08 07:10:00							HOP384				
09/08 10:03:00 - 09/08 17:59:54	Fixed (-22.0, 896.0)							HOP81 on N-pole				
09/08 18:13:00 - 09/08 22:12:00	Fixed (0.0, 822.0)							HOP385				
09/08 22:48:00 - 09/09 01:59:54	Track (670.4, 175.8) @ 09/08 22:45:00							New region				
09/09 02:03:00 - 09/09 05:34:54	Fixed (0.0, 0.0)							HOP349 and synoptic				
09/09 05:42:30 - 09/09 08:29:54	Track (710.5, 181.5) @ 09/09 05:39:30							HOP384				
09/09 08:33:00 - 09/09 14:47:00	Fixed (-22.0, 861.0)							HOP206 on N-pole				
09/09 15:03:00 - 09/09 15:44:54	Fixed (-22.0, -39.0)							HOP312				
09/09 15:58:00 - 09/09 20:29:54	Fixed (0.0, 822.0)							HOP385				
09/09 20:33:00 - 09/09 23:59:54	Track (-30.5, 601.0) @ 09/09 20:30:00							HOP336 N hemisphere				
09/10 00:03:00 - 09/10 03:14:54	Track (-31.7, -704.0) @ 09/10 00:00:00							HOP336 S hemisphere				
09/10 03:42:36 - 09/10 06:12:24	Fixed (0.0, 0.0)							HOP349 and synoptic				
09/10 06:15:30 - 09/10 10:59:05	Track (826.5, 204.4) @ 09/10 06:13:30							HOP384				

PROG= 13 30-time(s)												
Subr= 1	20-time(s)	2.0sec										
└ Seqn= 11	1-time(s)	2.0sec										
└ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└ Seqn=100	1-time(s)	10.0sec										
└ thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
└ med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└ Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
└ Seqn= 10	1-time(s)	2.0sec										
└ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└ Seqn= 11	1-time(s)	2.0sec										
└ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└ Seqn= 87	1-time(s)	2.0sec										
└ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

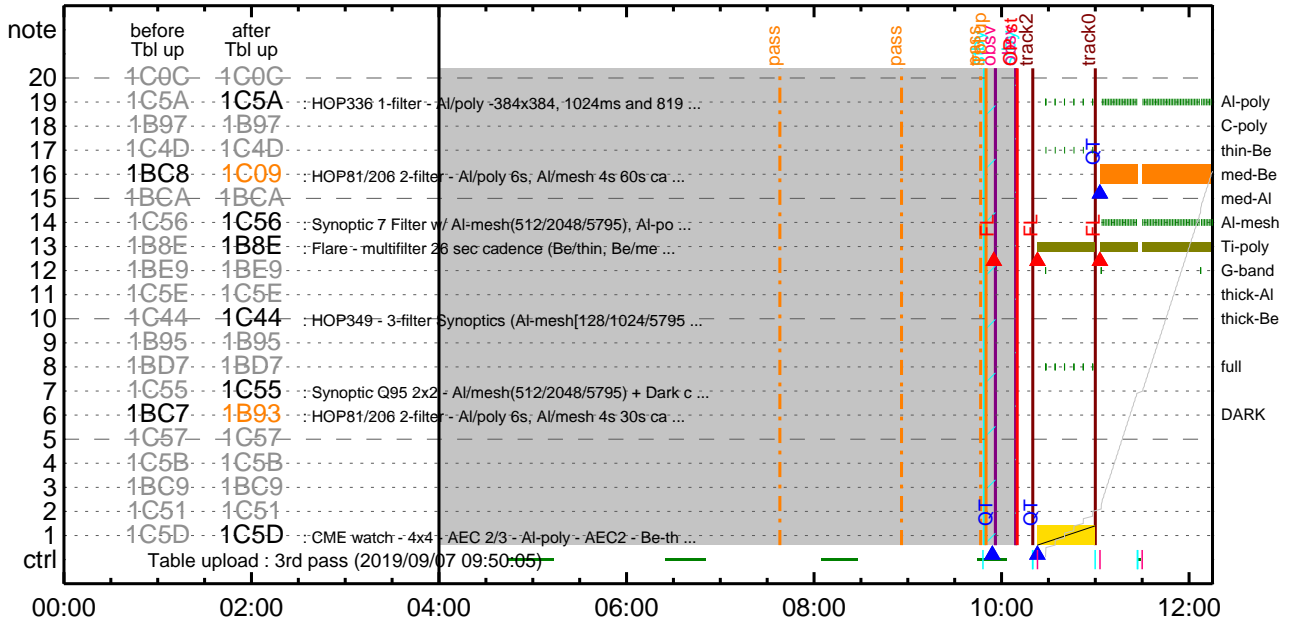
* * * * *

Flare Detection

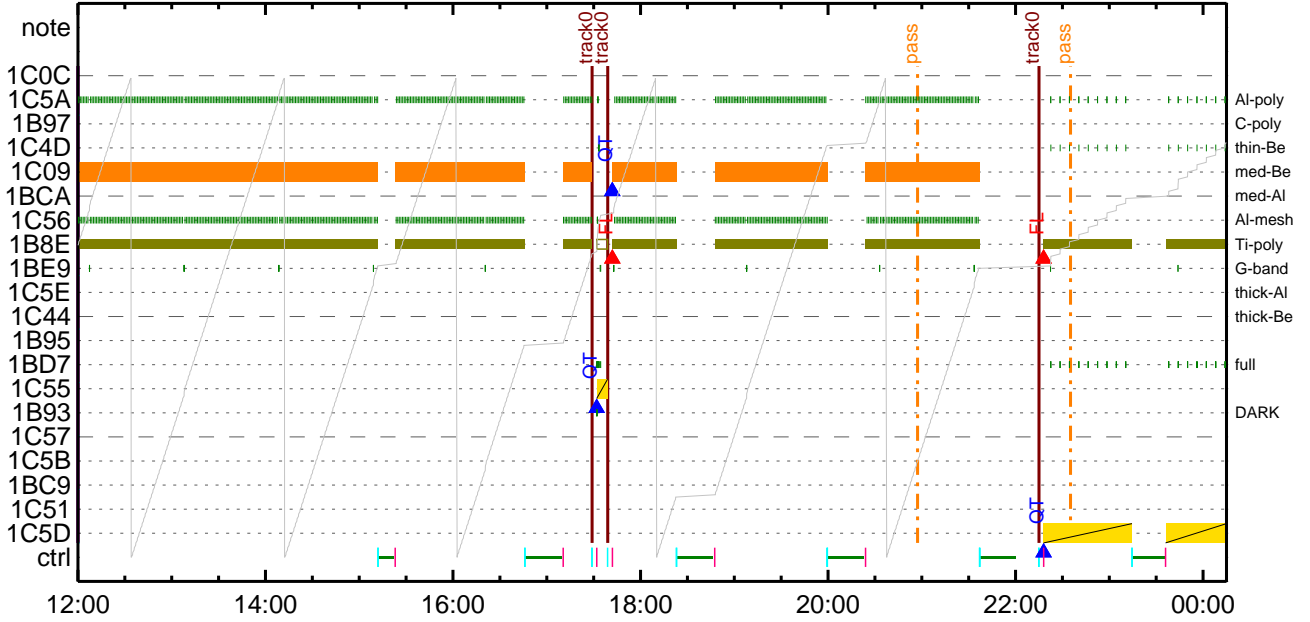
* * * * *

FLD Patrol												
Term		Pointing (x, y)					Comment					
09/07 17:39:16 - 09/08 05:59:24		Fixed (0.0, 822.0)					HOP385					
09/08 06:11:25 - 09/08 18:00:18		Fixed (0.0, 0.0)					HOP349, HOP366 and synoptic					
09/08 18:10:16 - 09/09 05:35:18		Fixed (0.0, 822.0)					HOP385					
09/09 05:39:48 - 09/09 15:45:18		Track (710.5, 181.5) @ 09/09 05:39:30					HOP384					
09/09 15:55:16 - 09/10 03:15:18		Fixed (0.0, 822.0)					HOP385					
09/10 03:39:54 - 09/12 11:27:00		Fixed (0.0, 0.0)					HOP349 and synoptic					
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8	Q=50			80sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp. AEC Buffer Interval	

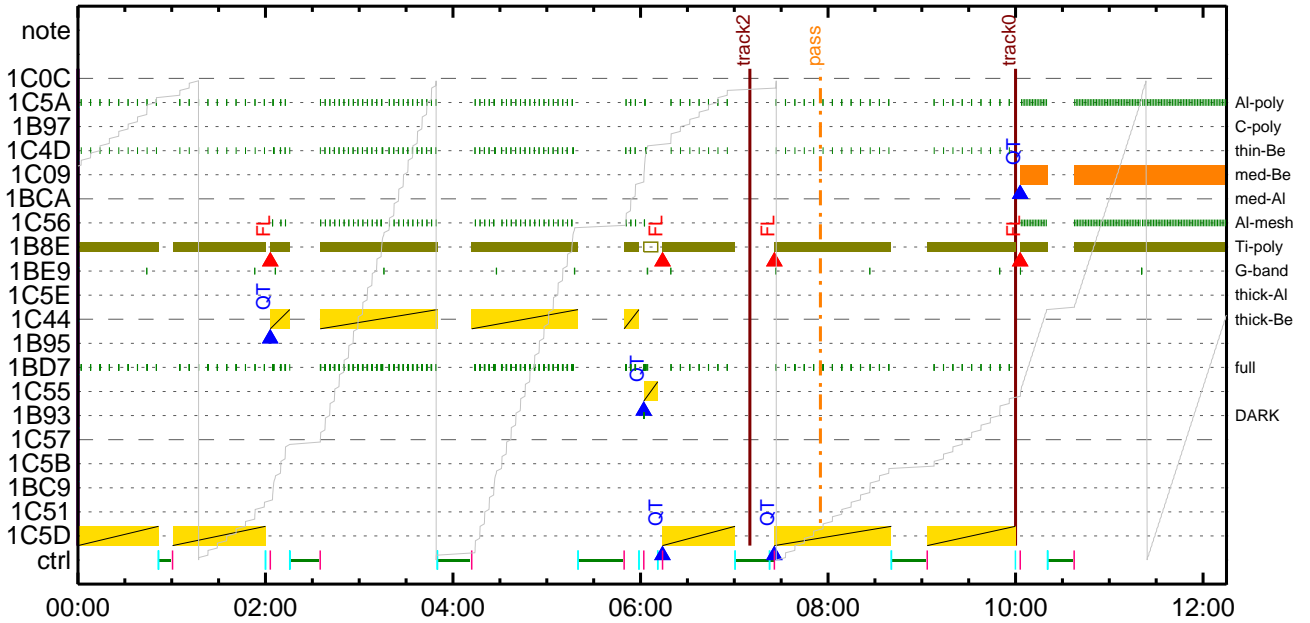
CMDI #0460 2019/09/07



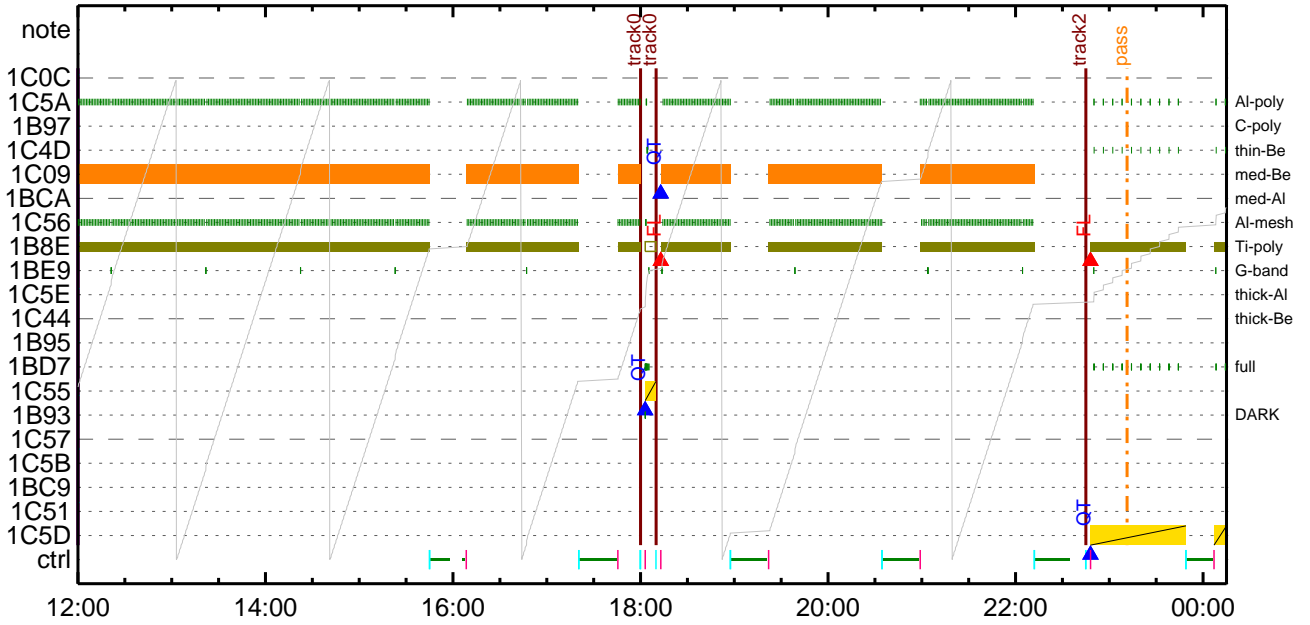
CMDI #0460 2019/09/07



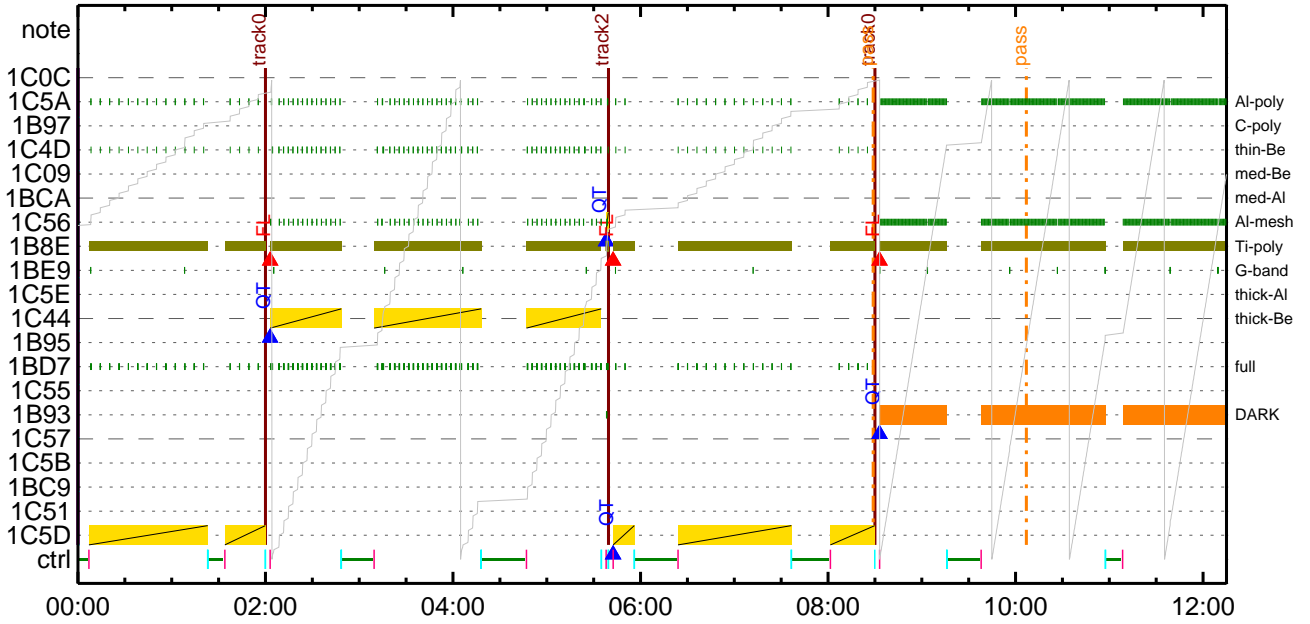
CMDI #0460 2019/09/08



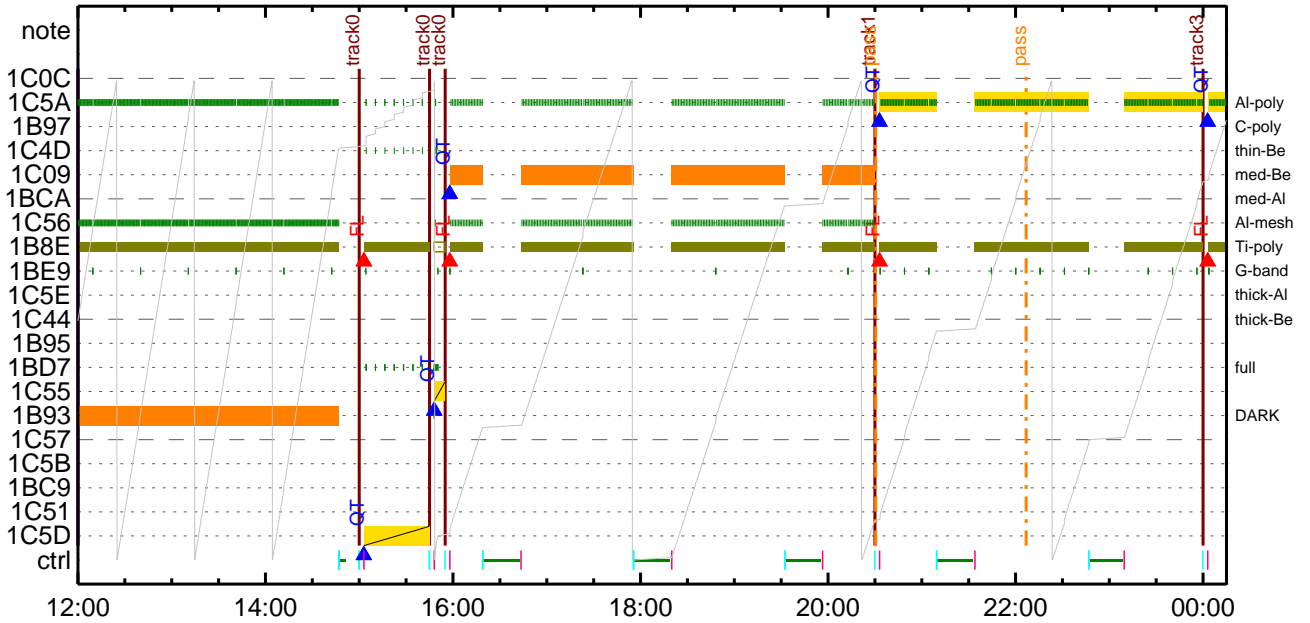
CMDI #0460 2019/09/08



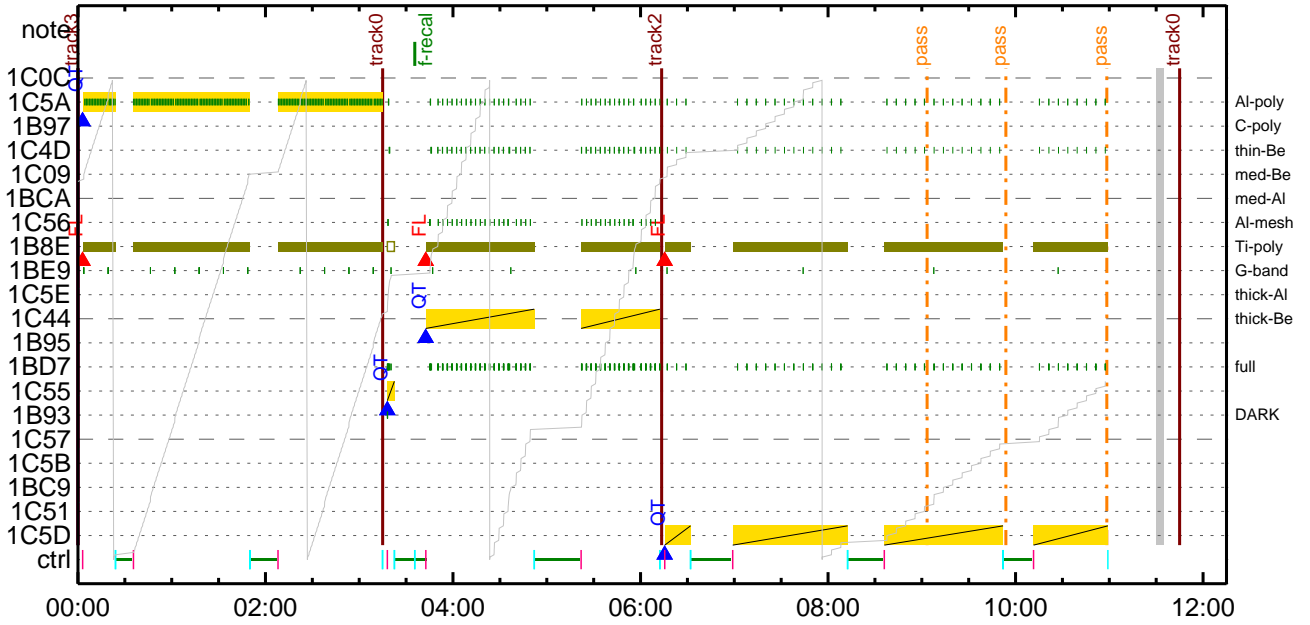
CMDI #0460 2019/09/09



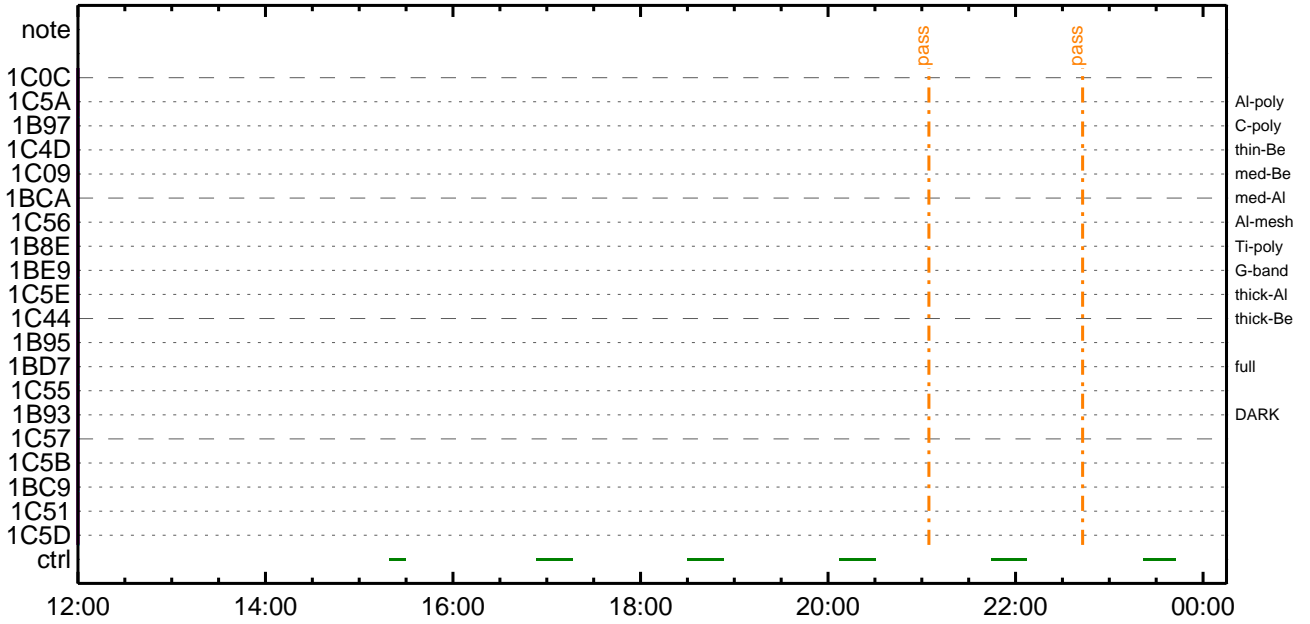
CMDI #0460 2019/09/09



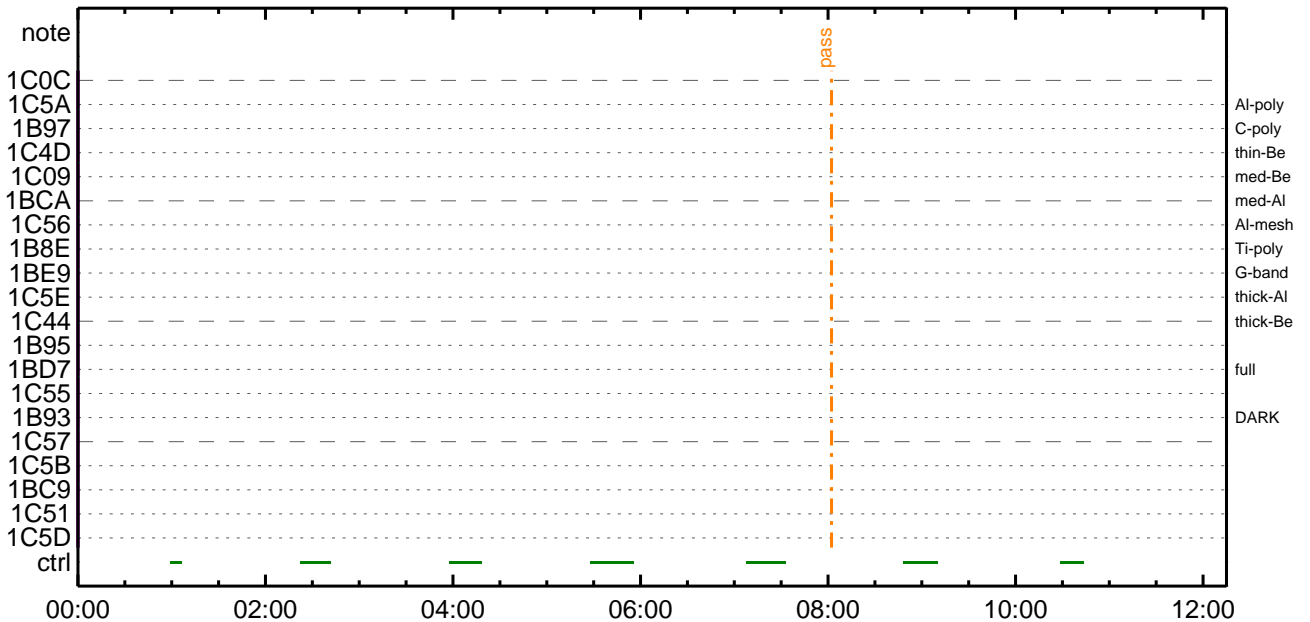
CMDI #0460 2019/09/10



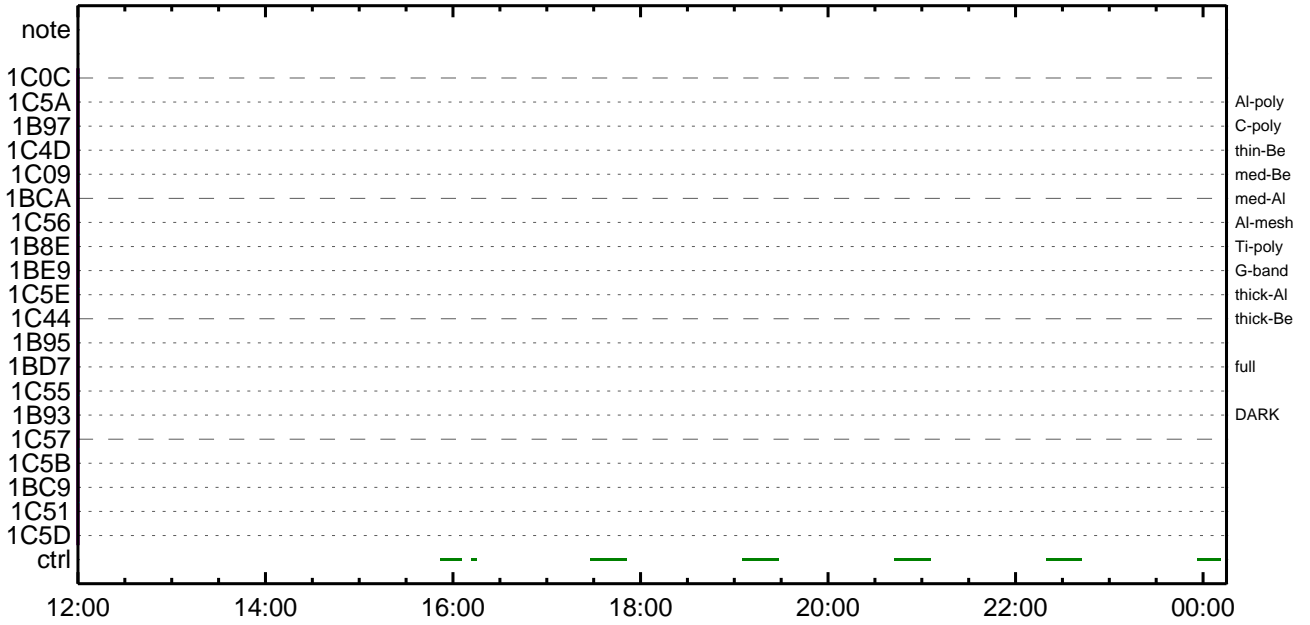
CMDI #0460 2019/09/10



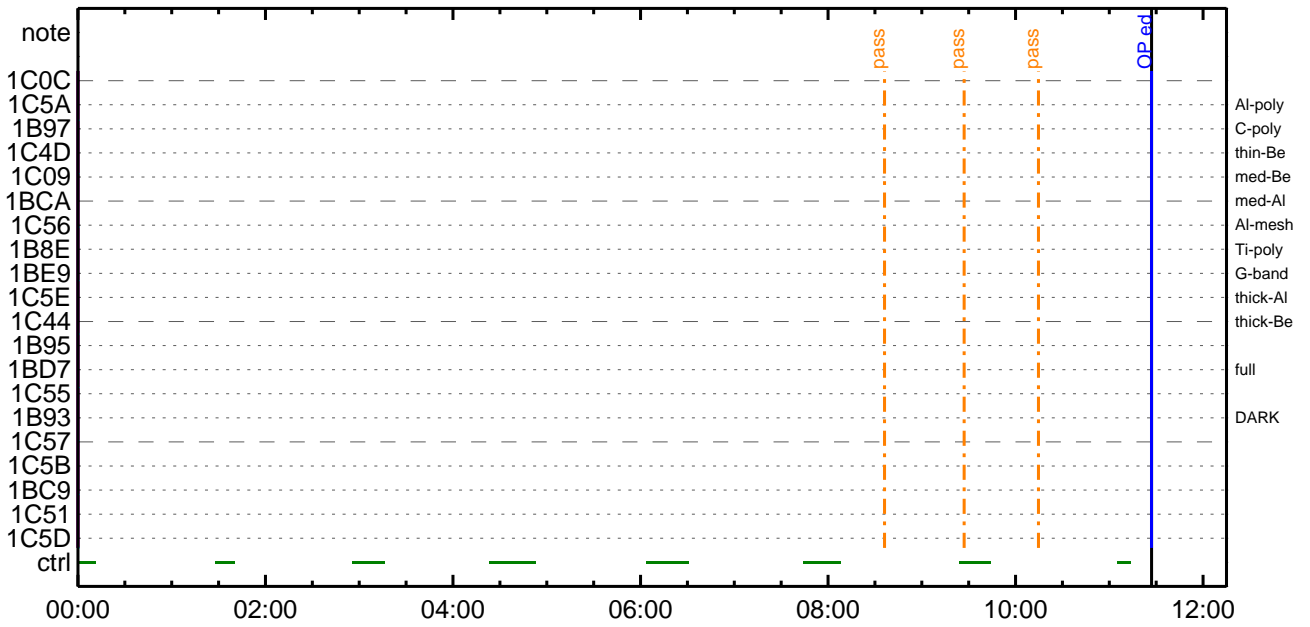
CMDI #0460 2019/09/11



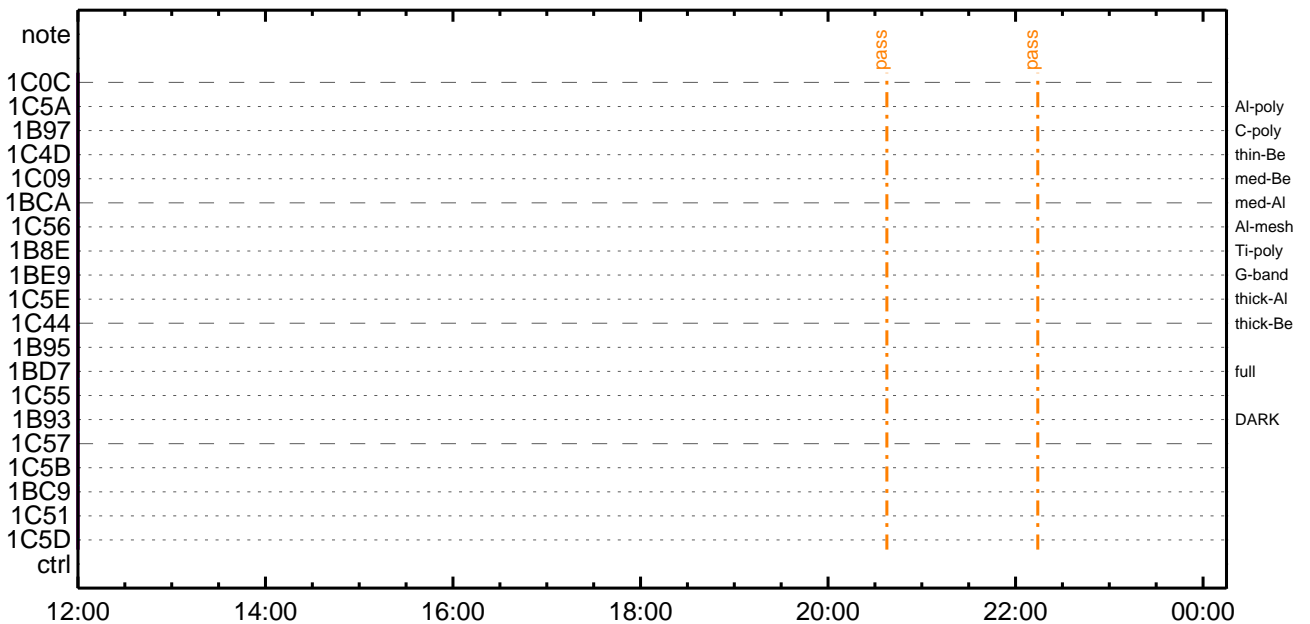
CMDI #0460 2019/09/11



CMDI #0460 2019/09/12



CMDI #0460 2019/09/12



(a) Spacecraft Operation Procedure (real-commands)

```
main-325 2019-09-07 12:34:07 205 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005  C.
0006  C. YÀY£;¼Y³YFÿ6YÉÄ+¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCs : Reload orbital element (send every contact) *****
0010  C.  Áí;Èð¿ÒÁð·μ°Ð»Í×ÁÇÒÍYçYÁY×Yí;¼YÉ;ÈÈè¼·íÉ;ÈðÈ¼°ÇÒð·Ò¿¼ì¹ÇÒÍ;çÄ®, ùÒ¹ÒÈðÒçÄ+¿®ð·ÒÈðÒÒ³ÒÈ;f
0011  +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  . C. *****
0015  C. OP/OGYí;¼YÉ;|YÁY6Y×
0016  C. *****
0017  C.
0018  . C. ;ãOP/OGYí;¼YÉ;ä
0019  . S. OP      op-325:OP
0020  ( )
0021  . S. OG      og-325:OG
0022  ( )
0023  C.
0024  . C. ;ãNMOG&OPí°èYÁY6Y×;ä
0025  C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027  BC      (20 00 7f 01 02)
0028  C.      çç[HK1_DMP_TOP_ADRS_1]           EQ      40
0029  C.      çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0030  C.      çç[HK1_DMP_BLOCK_NUM]            EQ     127
0031  C.      çç[HK1_DMP_REPEAT_NUM]          EQ      0
0032  C.      çç[HK1_DMA_DMP_PIM]              EQ     DHU
0033  +. DC 01-22 DHU_MODE_CHNG
0034  BC      (07 0b f8)
0035  C.      çç[HK1_PKT_FORM_NO]              EQ      7
0036  C.      çç[HK1_PKT_GEN_TIME]             EQ     0.25 s
0037  C.      çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0038  C.      çç[HK1_X_TLM_BIT_RATE]          EQ     4M
0039  C.      çç[HK1_DMP_CHK_FLG]              EQ     EXEC
0040  . C. YÀY6Y×½ª î»ðð³ îÇ§
0041  C.      çç[HK1_DMP_CHK_FLG]              EQ     NON
0042  . C. RAM ID=NMOGðí¼È¹ç·è² îOKðð³ îÇ§
0043  C.
0044  C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046  BC      (20 80 7f 01 02)
0047  C.      çç[HK1_DMP_TOP_ADRS_1]           EQ     41
0048  C.      çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0049  C.      çç[HK1_DMP_BLOCK_NUM]            EQ     127
0050  C.      çç[HK1_DMP_REPEAT_NUM]          EQ      0
0051  C.      çç[HK1_DMA_DMP_PIM]              EQ     DHU
0052  +. DC 01-22 DHU_MODE_CHNG
0053  BC      (07 0b f8)
0054  C.      çç[HK1_PKT_FORM_NO]              EQ      7
0055  C.      çç[HK1_PKT_GEN_TIME]             EQ     0.25 s
0056  C.      çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0057  C.      çç[HK1_X_TLM_BIT_RATE]          EQ     4M
0058  C.      çç[HK1_DMP_CHK_FLG]              EQ     EXEC
0059  . C. YÀY6Y×½ª î»ðð³ îÇ§
0060  C.      çç[HK1_DMP_CHK_FLG]              EQ     NON
0061  . C. RAM ID=NMOGðí¼È¹ç·è² îOKðð³ îÇ§
0062  C.
0063  C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065  BC      (21 00 41 01 02)
0066  C.      çç[HK1_DMP_TOP_ADRS_1]           EQ     42
0067  C.      çç[HK1_DMP_TOP_ADRS_0]           EQ      0
0068  C.      çç[HK1_DMP_BLOCK_NUM]            EQ     65
0069  C.      çç[HK1_DMP_REPEAT_NUM]          EQ      0
0070  C.      çç[HK1_DMA_DMP_PIM]              EQ     DHU
0071  +. DC 01-22 DHU_MODE_CHNG
0072  BC      (07 0b f8)
0073  C.      çç[HK1_PKT_FORM_NO]              EQ      7
0074  C.      çç[HK1_PKT_GEN_TIME]             EQ     0.25 s
0075  C.      çç[HK1_S_TLM_BIT_RATE]          EQ     32k
0076  C.      çç[HK1_X_TLM_BIT_RATE]          EQ     4M
0077  C.      çç[HK1_DMP_CHK_FLG]              EQ     EXEC
0078  . C. YÀY6Y×½ª î»ðð³ îÇ§
0079  C.      çç[HK1_DMP_CHK_FLG]              EQ     NON
0080  . C. RAM ID=NMOG,RAM ID=OPðí¼È¹ç·è² îOKðð³ îÇ§
0081  C.
0082  . C. ***** 0È²¼Òí¼Ä´ ¶¼°ÒÈÈ-Ò°Ä+¿® (¼áμ-YÁY6Y×¼È½ÇÒðÄÓÃæç¾ª°Ò¼Ò¿¼ì¹ÇÒÇÒâ) *****
0083  C. DHUã;¼YÉ;È¼Y¾, Yí;¼YÉ;ÈðÒÍá¹
0084  +. DC 01-22 DHU_MODE_CHNG
0085  BC      (02 0a f8)
0086  C.      çç[HK1_PKT_FORM_NO]              EQ      2
0087  C.      çç[HK1_PKT_GEN_TIME]             EQ     0.5S
0088  C.      çç[HK1_S_TLM_BIT_RATE]          EQ     32K
0089  C.      çç[HK1_X_TLM_BIT_RATE]          EQ     4M
0090  C.
0091  . C. *****
0092  C. TI-CMD SET (OPOG STOP/COPY/START)
0093  C. *****
0094  C.
0095  . C. NOTICE  |§ OPOG UPLOADÒ-Ä+¿®NGðí¼ì¹ç;ç°È²¼Òí¼TI-CMDÄ+¿®¼Ä¹ÔÒ·ÒÈðÒÒ³ÒÈ;f
```

```

0096 C.          0300; SET EDUMP 1000; 1000; 1000;
0097 C.
0098 C. TI 2019-09-07 10:05:00.0
0099 +. TI 2019-09-07 10:05:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2019-09-07 10:05:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2019-09-07 10:05:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2019-09-07 10:09:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.          0300; SET EDUMP 1000; 1000; 1000;
0116 C.          [HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C.          [HK1_TI_CMD_NUM] EQ 4
0118 C.          [HK1_NEXT_EXEC_PIM] EQ DHU
0119 C.          [HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 C. *****
0122 C. TI 2019-09-07 10:09:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.          [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.          [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.          [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.          [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.          [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.          [HK1_PKT_FORM_NO] EQ 7
0136 C.          [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.          [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.          [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.          [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C.          0300; SET EDUMP 1000; 1000; 1000;
0142 C.          [HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 C. RAM ID=TI_TBL 1000; 1000; 1000;
0145 C.
0146 C. DHU 2019-09-07 10:09:59.5
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.          [HK1_PKT_FORM_NO] EQ 2
0150 C.          [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.          [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.          [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2019-09-07 10:09:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2019-09-07 10:09:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2019-09-07 10:09:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 C.          [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2019-09-07 10:09:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 C.          [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP 2019-09-07 10:09:00.0 *****
0192 C. (2019-09-07 10:09:00.0)
0193 S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 42s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2019-09-07 10:09:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ũÃîũî»ö¼ÝñÊÃðñ¹ñèDCBC•x²è *****
0156 C. (¼ã°î¼ÝñÊÃðñ¹ñèDCBC•x²è)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¼D¼¹•î Daily±¿îññÊ'øñ¹ñèDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¼ÝñÊÃðñ¹ñè¼Ã»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-327 2019-09-07 12:34:07 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁ•µ°È»Í×ÁÇ¿ÍY¿Y×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈÈ¼°ÇÓã•¿¿¼í¹¿ãÍ;çÀ®, ù¿¹ãÈãÈãÇÁ+¿®ã•¿Èãã¿ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-289:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2019-09-07 10:09:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 06 80 80 08 08)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 07 80 80 20 20)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 08 85 83 06 06)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 09 80 80 20 08)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 0a 80 80 08 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0b 85 83 08 08)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 + DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 + DC 07-F0 MDP_XRT_ARS_DIS
0093 BC (d5)
0094 + DC 07-F0 MDP_XRT_AEC_RESET
0095 BC (d0)
```

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0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 01)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 0d)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2019-09-07 10:09:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP 'úÃÎÏ»ö¼ÝðËÂĐ¹ñèDCBC•x²è *****
0117 C. (¾ãºÏ¥Ó¥Ã¥Ê¥Ï¥Ë¥Á¥Ç¥È¼¼¼¼»Û¹¼è)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ¥Ð¥¹•Ï Daily±¿ÏÑñË'Ø¹ñèDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOS¥Á¥§¥Ã¥¬¼Å»Û;ä
0128 C.
0129 . C. ***** LOS *****
0130 C.

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*** OP Sequence for XRT ***

2019/09/07	10:19:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	10:19:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	10:19:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/09/07	10:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2019/09/07	10:20:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/09/07	10:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/09/07	10:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/09/07	10:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/07	10:20:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/07	10:22:56.0	XRT_QT_PROG_SET_431_OG [0x1af]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2019/09/07	10:22:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/09/07	10:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/07	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	10:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2019/09/07	11:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 53 74 01 f3		
2019/09/07	11:00:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/09/07	11:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/09/07	11:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/09/07	11:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/07	11:00:24.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/07	11:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2019/09/07	11:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/09/07	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/07	11:27:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	11:27:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	11:27:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/07	11:27:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/07	11:29:00.0	XRT_Custom_430_OG [0x1ae]					
2019/09/07	11:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/07	11:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/07	15:12:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	15:12:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	15:12:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/07	15:12:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/07	15:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/07	15:22:00.0	XRT_Custom_430_OG [0x1ae]					
2019/09/07	15:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/07	16:46:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	16:46:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	16:46:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/07	16:46:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/07	16:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/07	17:09:30.0	XRT_Custom_430_OG [0x1ae]					
2019/09/07	17:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/07	17:28:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	17:28:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/07	17:28:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		

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2019/09/07	17:29:00.0	AOCS_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00	00	00	00	00
2019/09/07	17:29:18.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9				
2019/09/07	17:29:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2019/09/07	17:29:22.0	XRT_ARS_DIS_443_OG [0x1bb] MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/09/07	17:31:58.0	XRT_QT_PROG_SET_446_OG [0x1be] MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2019/09/07	17:32:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/07	17:38:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/07	17:38:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2019/09/07	17:39:00.0	AOCS_OrE-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00	b6	f2	00	00
2019/09/07	17:39:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/09/07	17:39:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/09/07	17:39:20.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/09/07	17:39:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/09/07	17:39:24.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/07	17:41:56.0	XRT_QT_PROG_SET_425_OG [0x1a9] MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2019/09/07	17:41:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/09/07	17:42:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/07	18:23:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/07	18:23:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/07	18:23:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/07	18:23:06.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/07	18:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/07	18:46:30.0	XRT_Custom_430_OG [0x1ae]							
2019/09/07	18:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/07	19:59:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/07	19:59:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/07	19:59:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/07	19:59:36.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/07	20:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/07	20:23:00.0	XRT_Custom_430_OG [0x1ae]							
2019/09/07	20:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/07	21:37:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/07	21:37:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/07	21:37:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/07	21:37:06.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/07	21:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/07	22:14:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/07	22:14:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/07	22:14:58.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2019/09/07	22:15:00.0	AOCS_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00	00	00	00	00
2019/09/07	22:15:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/09/07	22:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/09/07	22:15:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/09/07	22:15:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/09/07	22:15:26.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/07	22:17:56.0	XRT_QT_PROG_SET_431_OG [0x1af] MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2019/09/07	22:17:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/09/07	22:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

2019/09/07	23:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/07	23:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/07	23:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/07	23:14:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/07	23:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/07	23:35:00.0	XRT_Custom_430_OG [0x1ae]					
2019/09/07	23:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	00:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	00:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	00:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	00:51:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	00:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/08	00:59:30.0	XRT_Custom_430_OG [0x1ae]					
2019/09/08	01:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	02:00:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	02:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	02:00:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/09/08	02:00:24.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/09/08	02:00:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/09/08	02:00:28.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/09/08	02:00:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/08	02:00:32.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	02:03:02.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2019/09/08	02:03:04.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2019/09/08	02:03:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	02:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	02:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	02:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	02:15:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	02:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/08	02:34:00.0	XRT_Custom_430_OG [0x1ae]					
2019/09/08	02:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	03:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	03:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	03:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	03:50:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	03:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/08	04:11:00.0	XRT_Custom_430_OG [0x1ae]					
2019/09/08	04:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	05:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	05:20:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	05:20:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	05:20:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	05:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/08	05:48:30.0	XRT_Custom_430_OG [0x1ae]					
2019/09/08	05:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	05:59:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	05:59:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	05:59:04.0	XRT_FOCUS_POSITION_406_OG [0x196]					

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2019/09/08	05:59:24.0	XRT_FLD_DIS_409_OG [0x199]	4	07-F8	22	ff	aa	00
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2019/09/08	05:59:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	1	07-F0	c9			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2019/09/08	05:59:28.0	XRT_ARS_DIS_443_OG [0x1bb]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/09/08	06:02:04.0	XRT_QT_PROG_SET_446_OG [0x1be]	2	07-F0	c4	07		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2019/09/08	06:02:06.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/09/08	06:11:01.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/09/08	06:11:03.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/09/08	06:11:05.0	XRT_FOCUS_POSITION_406_OG [0x196]	4	07-F8	22	ff	aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2019/09/08	06:11:25.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8			
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/09/08	06:11:27.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/09/08	06:11:29.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/09/08	06:11:31.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/09/08	06:11:33.0	XRT_FLD_RESET_434_OG [0x1b2]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/09/08	06:14:03.0	XRT_QT_PROG_SET_431_OG [0x1af]	2	07-F0	c4	01		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2019/09/08	06:14:05.0	XRT_FL_PROG_SET_440_OG [0x1b8]	2	07-F0	c5	0d		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2019/09/08	06:14:07.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/09/08	07:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/09/08	07:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/09/08	07:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/09/08	07:00:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/09/08	07:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/09/08	07:10:00.0	AOCs_OrE-point_Start_1_OG [0x097]	5	02-76	02	00	00	00
		AOCU_NM	5	02-76	02	00	00	00
2019/09/08	07:22:34.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/09/08	07:22:36.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/09/08	07:22:38.0	XRT_FOCUS_POSITION_406_OG [0x196]	4	07-F8	22	ff	aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2019/09/08	07:22:58.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8			
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/09/08	07:23:00.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/09/08	07:23:02.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/09/08	07:23:04.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/09/08	07:23:06.0	XRT_FLD_RESET_434_OG [0x1b2]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/09/08	07:25:36.0	XRT_QT_PROG_SET_431_OG [0x1af]	2	07-F0	c4	01		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01		
2019/09/08	07:25:38.0	XRT_FL_PROG_SET_440_OG [0x1b8]	2	07-F0	c5	0d		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2019/09/08	07:25:40.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/09/08	08:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/09/08	08:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/09/08	08:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/09/08	08:40:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/09/08	08:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/09/08	09:02:30.0	XRT_Custom_430_OG [0x1ae]						
2019/09/08	09:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/09/08	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/09/08	09:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	4	07-F8	22	fe	97	00
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2019/09/08	10:00:00.0	AOCs_OrE-point_Start_5_OG [0x09b]	5	02-76	00	b0	5b	01
		AOCU_NM	5	02-76	00	b0	5b	01
2019/09/08	10:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8			
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/09/08	10:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/09/08	10:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/09/08	10:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]						

2019/09/08	10:00:24.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/08	10:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	10:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
2019/09/08	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/09/08	10:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	10:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	10:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	10:20:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	10:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	10:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/08	10:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0	
2019/09/08	15:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	15:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	15:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	15:45:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	15:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	16:07:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/08	16:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0	
2019/09/08	17:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	17:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	17:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	17:20:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	17:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	17:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/08	17:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0	
2019/09/08	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	18:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2019/09/08	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00	00 00 00 00
2019/09/08	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2019/09/08	18:00:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2019/09/08	18:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/08	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2019/09/08	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	18:10:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2019/09/08	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	b6 f2 00 00
2019/09/08	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/09/08	18:10:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/09/08	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/09/08	18:10:24.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/08	18:12:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	18:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
2019/09/08	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/09/08	18:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	18:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	18:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2019/09/08	18:57:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	19:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	19:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/08	19:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	20:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	20:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	20:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	20:34:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	20:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/08	20:58:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	20:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	22:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	22:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	22:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	22:12:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	22:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/08	22:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	22:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	22:44:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/09/08	22:45:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00	
2019/09/08	22:45:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/09/08	22:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/09/08	22:45:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/09/08	22:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/08	22:45:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	22:47:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2019/09/08	22:47:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2019/09/08	22:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/08	23:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	23:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/08	23:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/08	23:49:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/08	23:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/09	00:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/09	00:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/09	01:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/09	01:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/09	01:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/09	01:23:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/09	01:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/09	01:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/09	01:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/09	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/09	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/09	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/09/09	02:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2019/09/09	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/09/09	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	

2019/09/09	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/09/09	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/09/09	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/09	02:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2019/09/09	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/09/09	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/09	02:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	02:48:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	02:48:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/09	02:48:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/09	02:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/09	03:08:30.0	XRT_Custom_430_OG [0x1ae]							
2019/09/09	03:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/09	04:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	04:18:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	04:18:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/09	04:18:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/09	04:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/09	04:46:00.0	XRT_Custom_430_OG [0x1ae]							
2019/09/09	04:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/09	05:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	05:34:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	05:34:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2019/09/09	05:35:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2019/09/09	05:35:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2019/09/09	05:35:22.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/09/09	05:37:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2019/09/09	05:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/09	05:39:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	05:39:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	05:39:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2019/09/09	05:39:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2019/09/09	05:39:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/09/09	05:39:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/09/09	05:39:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/09/09	05:39:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/09/09	05:39:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/09	05:42:26.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2019/09/09	05:42:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/09/09	05:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/09	05:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	05:56:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	05:56:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/09	05:56:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/09	05:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/09	06:23:00.0	XRT_Custom_430_OG [0x1ae]							
2019/09/09	06:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/09	07:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

2019/09/09	07:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/09	07:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/09	07:36:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/09	07:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/09	08:00:30.0	XRT_Custom_430_OG [0x1ae]						
2019/09/09	08:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/09	08:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/09	08:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2019/09/09	08:30:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 b3 75 01 f3		
2019/09/09	08:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/09/09	08:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/09/09	08:30:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/09/09	08:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/09	08:30:24.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/09	08:32:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2019/09/09	08:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/09/09	08:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/09	09:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/09	09:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/09	09:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/09	09:16:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/09	09:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/09	09:37:00.0	XRT_Custom_430_OG [0x1ae]						
2019/09/09	09:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/09	10:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/09	10:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/09	10:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/09	10:57:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/09	11:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/09	11:07:30.0	XRT_Custom_430_OG [0x1ae]						
2019/09/09	11:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/09	14:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/09	14:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/09	14:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/09	14:47:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/09	14:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/09	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/09	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/09	14:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/09/09	15:00:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 03 74 01 f3		
2019/09/09	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/09/09	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/09/09	15:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/09/09	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/09	15:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/09	15:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2019/09/09	15:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/09/09	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		

2019/09/09	15:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/09	15:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/09	15:44:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/09/09	15:45:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00
2019/09/09	15:45:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2019/09/09	15:45:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2019/09/09	15:45:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/09/09	15:47:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2019/09/09	15:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/09/09	15:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/09	15:54:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2019/09/09	15:55:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 b6 f2 00 00
2019/09/09	15:55:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2019/09/09	15:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/09/09	15:55:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2019/09/09	15:55:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/09/09	15:55:24.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/09/09	15:57:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2019/09/09	15:57:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/09/09	15:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/09/09	16:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/09	16:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/09	16:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/09/09	16:19:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/09/09	16:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/09/09	16:42:30.0	XRT_Custom_430_OG [0x1ae]				
2019/09/09	16:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/09/09	17:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/09	17:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/09	17:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/09/09	17:55:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/09/09	17:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/09/09	18:19:00.0	XRT_Custom_430_OG [0x1ae]				
2019/09/09	18:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/09/09	19:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/09	19:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/09	19:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/09/09	19:32:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/09/09	19:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/09/09	19:55:30.0	XRT_Custom_430_OG [0x1ae]				
2019/09/09	19:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/09/09	20:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/09	20:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2019/09/09	20:30:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	01 00 00 00 00
2019/09/09	20:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2019/09/09	20:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/09/09	20:30:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2019/09/09	20:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5

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2019/09/09	20:30:24.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/09	20:32:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2019/09/09	20:32:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/09/09	20:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/09	21:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	21:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	21:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/09	21:09:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/09	21:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/09	21:33:00.0	XRT_Custom_430_OG [0x1ae]								
2019/09/09	21:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/09	22:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	22:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	22:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/09	22:47:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/09	22:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/09	23:08:30.0	XRT_Custom_430_OG [0x1ae]								
2019/09/09	23:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/09	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/09	23:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2019/09/10	00:00:00.0	AOCS_Ore-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	03	00	00	00	00
2019/09/10	00:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/09/10	00:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/09/10	00:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/09/10	00:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/09/10	00:00:24.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/10	00:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2019/09/10	00:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/09/10	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/10	00:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/10	00:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/10	00:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/10	00:24:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/10	00:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/10	00:34:30.0	XRT_Custom_430_OG [0x1ae]								
2019/09/10	00:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/10	01:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/10	01:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/10	01:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/10	01:50:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/10	01:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/10	02:07:00.0	XRT_Custom_430_OG [0x1ae]								
2019/09/10	02:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/10	03:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/10	03:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/10	03:14:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2019/09/10	03:15:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2019/09/10	03:15:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2019/09/10	03:15:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]								

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2019/09/10	03:15:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/10	03:17:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2019/09/10	03:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/10	03:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/10	03:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/10	03:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/10	03:22:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/10	03:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/10	03:35:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/10	03:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/10	03:35:34.0	XRT_FOCUS_RECALIBRATE_403_OG [0x193]	XRT_FOCUS_RECAL	2	07-F8	78	00
2019/09/10	03:39:34.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2019/09/10	03:39:54.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/09/10	03:39:56.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/09/10	03:39:58.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/09/10	03:40:00.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/10	03:40:02.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/10	03:42:32.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2019/09/10	03:42:34.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/09/10	03:42:36.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/10	04:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/10	04:52:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/10	04:52:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/10	04:52:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/10	04:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/10	05:21:00.0	XRT_Custom_430_OG [0x1ae]					
2019/09/10	05:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/10	06:12:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/10	06:12:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/10	06:12:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2019/09/10	06:12:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/09/10	06:12:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/09/10	06:12:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/09/10	06:12:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/09/10	06:12:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/10	06:13:30.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00 00 00 00
2019/09/10	06:15:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2019/09/10	06:15:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/09/10	06:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/10	06:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/10	06:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/09/10	06:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/09/10	06:32:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/09/10	06:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/09/10	06:58:00.0	XRT_Custom_430_OG [0x1ae]					
2019/09/10	06:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/09/10	08:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2019/09/10	08:12:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/10	08:12:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/10	08:12:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/10	08:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/10	08:35:00.0	XRT_Custom_430_OG [0x1ae]							
2019/09/10	08:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/10	09:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/10	09:52:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/10	09:52:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/09/10	09:52:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/09/10	09:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/09/10	10:10:30.0	XRT_Custom_430_OG [0x1ae]							
2019/09/10	10:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/09/10	10:59:05.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/09/10	11:45:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				