

XRT Timeline to be uploaded on 2019/09/19

Period: 2019/09/19 11:11:00 - 2019/09/24 10:20:00

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Normal mode

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XOB #1C5D: CME watch - 4x4 - AEC 2/3 - Al-poly - AEC2 - Be-thin - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
09/19 11:24:00 - 09/19 14:59:54	Track (37.6, 230.4) ^{® 09/19 11:21:00}	# OP start + 10min EIS BP obs.
09/19 15:03:00 - 09/19 23:44:00	Fixed (-22.0, 896.0)	# HOP 379 @NPCH

PROG= 01 Inf.-time(s)													
Subr=	1	1-time(s)	2.0sec										
Seqn=	30	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr=	2	10-time(s)	360.0sec										
Seqn=	6	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn=	83	1-time(s)	2.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C56: Synoptic 7 Filter w/ Al-mesh(512/2048/5795), Al-poly(512/5795/11571), Thin-Be(3897/16384/32768) - Thick-Be(65536), Al-poly+Ti-poly(4096/2314)

Term	Pointing (x, y)	Comment
09/20 00:13:00 - 09/20 00:19:54	Fixed (0.0, 0.0)	# Synoptic
09/20 06:22:36 - 09/20 06:29:00	Fixed (0.0, 0.0)	# HOP 349

PROG= 14 1-time(s)													
Subr=	1	1-time(s)	2.0sec										
Seqn=	5	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn=	78	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn=	91	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn=	52	1-time(s)	2.0sec										
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn=	23	1-time(s)	4.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr=	2	1-time(s)	2.0sec										
Seqn=	46	1-time(s)	2.0sec										
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn=	17	1-time(s)	2.0sec										
	med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn=	97	1-time(s)	2.0sec										
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BB5: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 120s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
09/20 00:23:00 - 09/20 01:59:54	Track (147.1, 232.0) ^{® 09/20 00:20:00}	# EIS BP obs.

PROG= 15 Inf.-time(s)													
Subr=	1	1-time(s)	2.0sec										
Seqn=	30	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr=	2	20-time(s)	120.0sec										
Seqn=	8	1-time(s)	2.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn=	6	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C1F: HOP349 - 3-filter Synoptics (Al-mesh[512/2048/5795], Al-poly[512/4096/8192], thin-Be[3897/16384/32768] with 512x512 G-band+Leak - 50 min

Term	Pointing (x, y)	Comment
09/20 02:03:00 - 09/20 05:50:30	Fixed (0.0, 0.0)	# HOP 349
PROG= 03 Inf.-time(s)		
Subr= 1 1-time(s) 600.0sec		
Seqn= 78 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 82 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 52 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 16.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 4-time(s) 600.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 29 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
09/19 11:24:00 - 09/19 14:59:54	Track (37.6, 230.4) @ 09/19 11:21:00	# OP start + 10min EIS BP obs.
09/19 15:03:00 - 09/19 23:44:00	Fixed (-22.0, 896.0)	# HOP 379 @NPCH
09/20 00:23:00 - 09/20 01:59:54	Track (147.1, 232.0) @ 09/20 00:20:00	# EIS BP obs.
PROG= 13 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 87 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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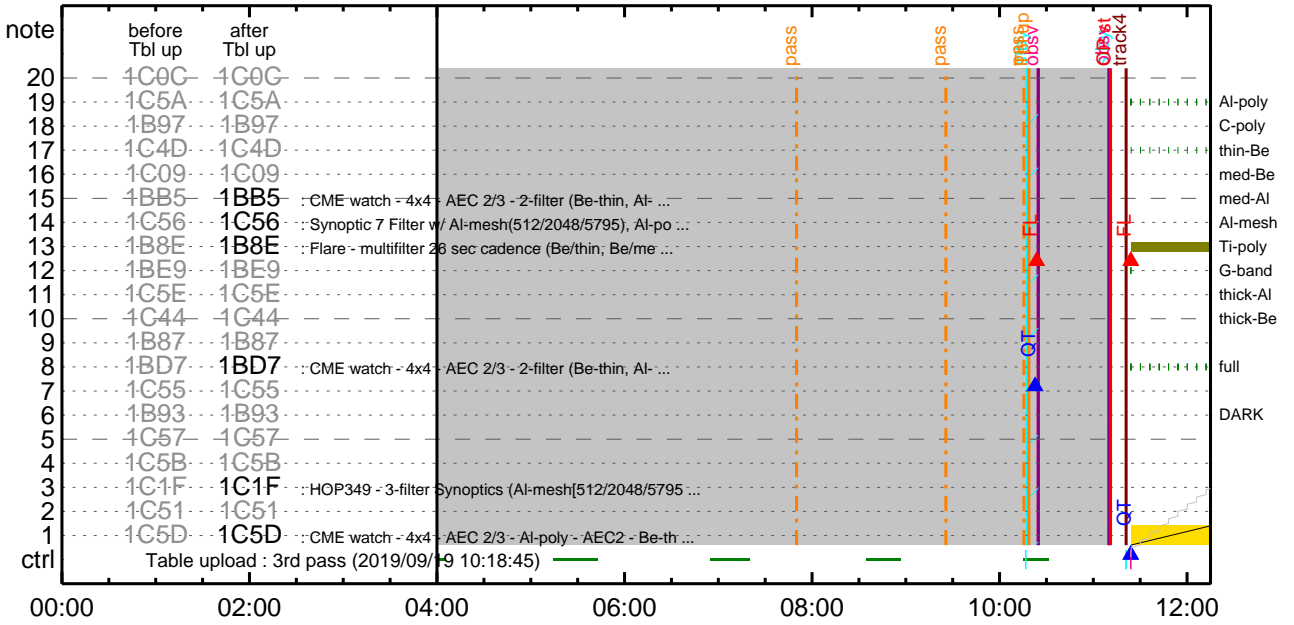
Flare Detection

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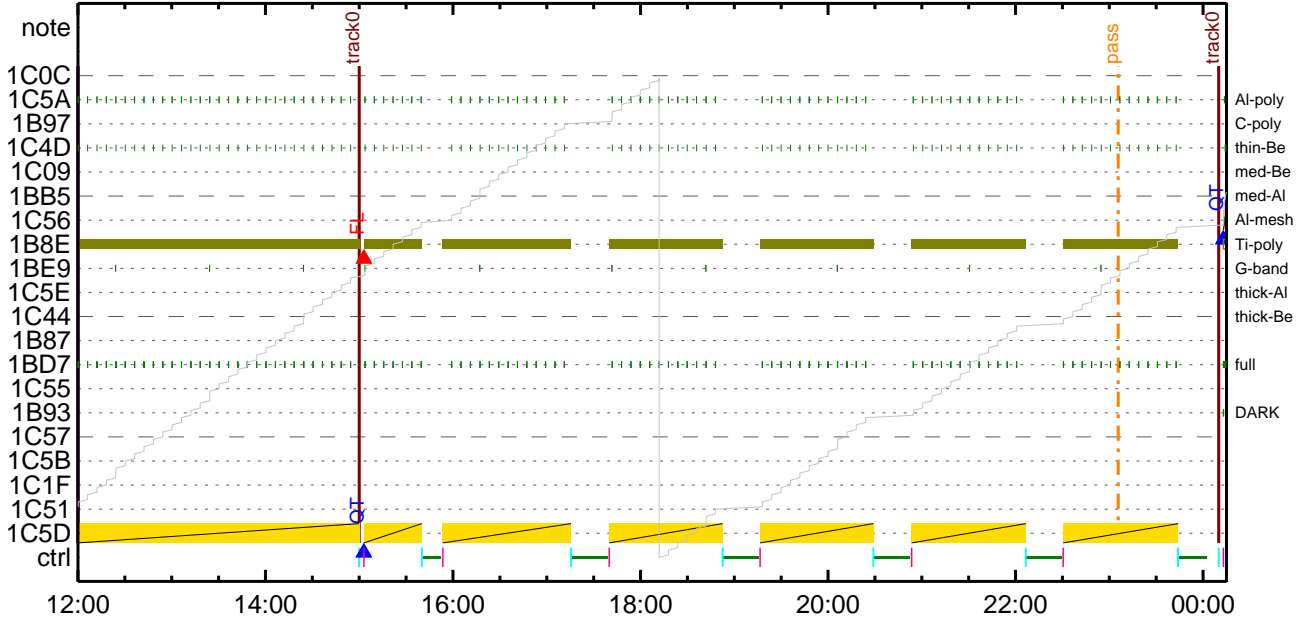
FLD Patrol

Term	Pointing (x, y)	Comment
09/20 00:20:18 - 09/20 02:00:18	Track (147.1, 232.0) @ 09/20 00:20:00	# EIS BP obs.
Al-poly/Open	Al-poly/Open close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

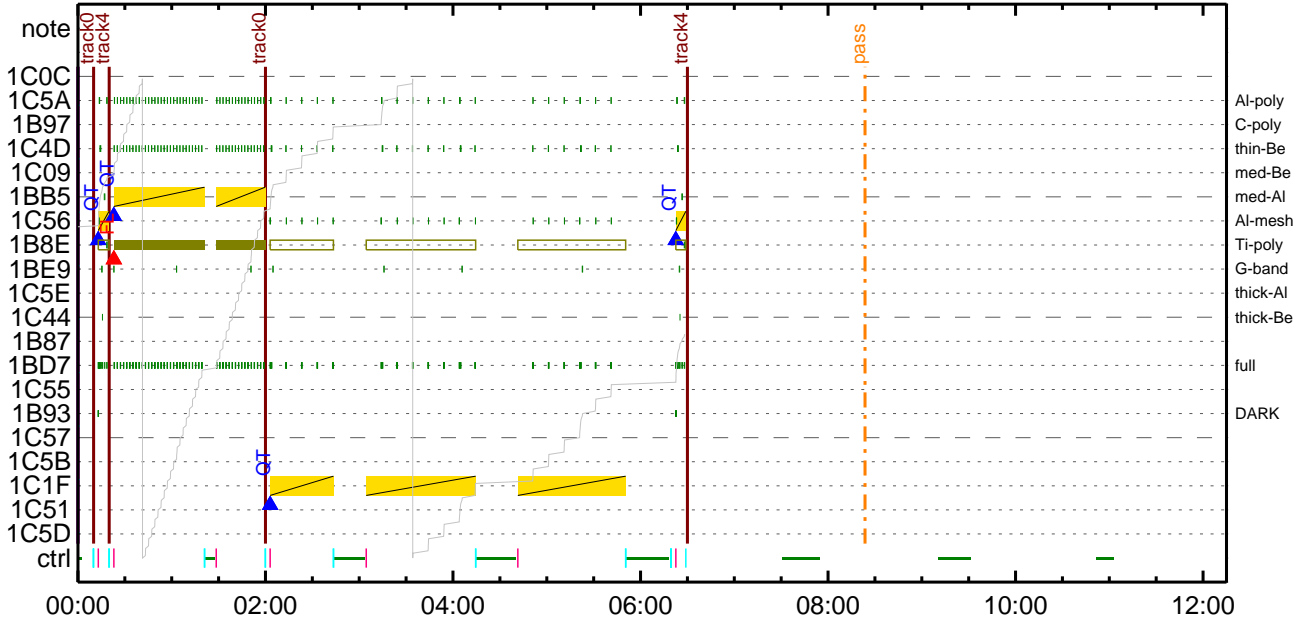
CMDI #0484 2019/09/19



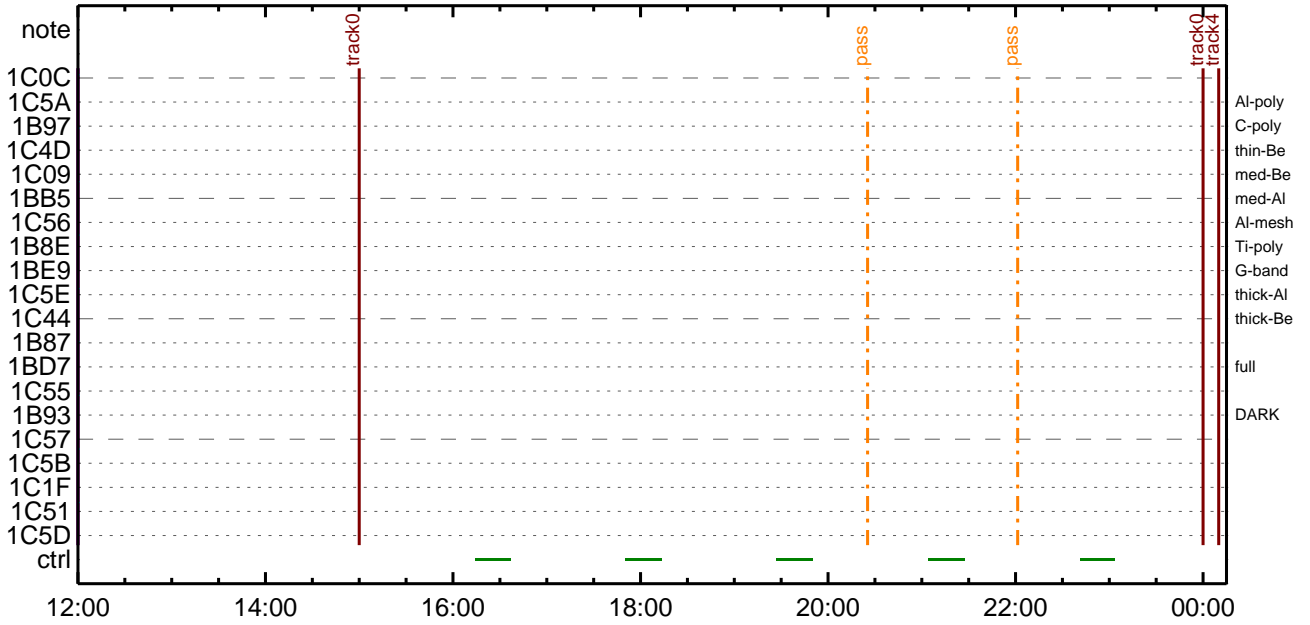
CMDI #0484 2019/09/19



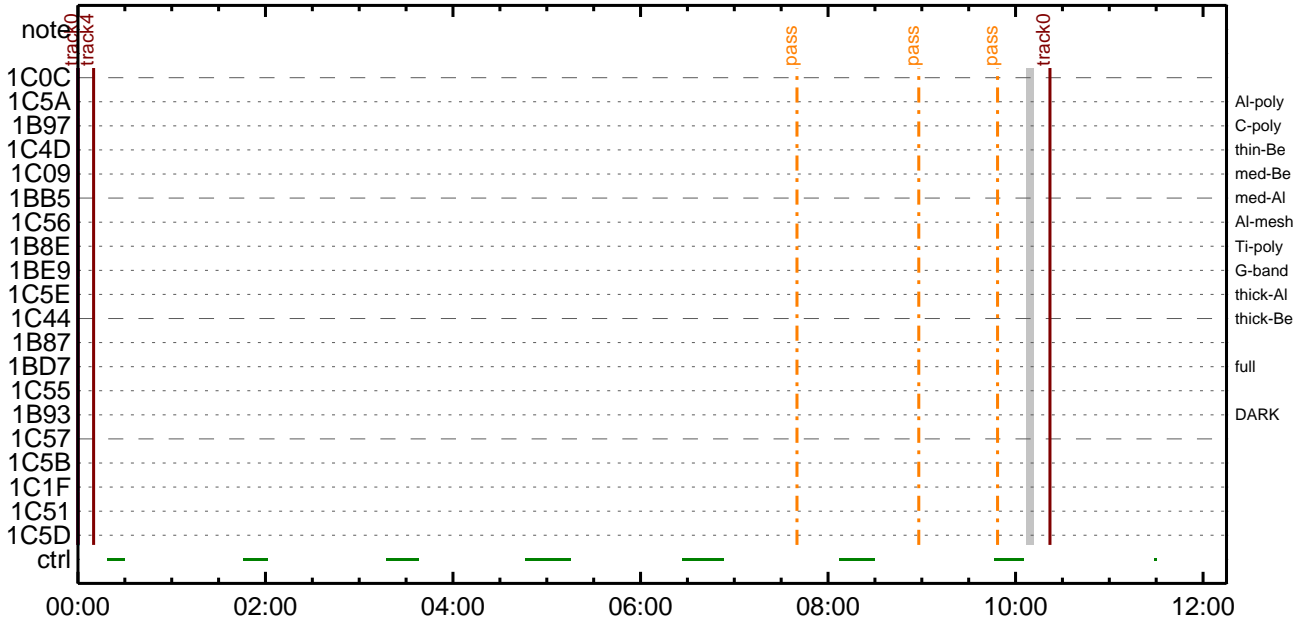
CMDI #0484 2019/09/20



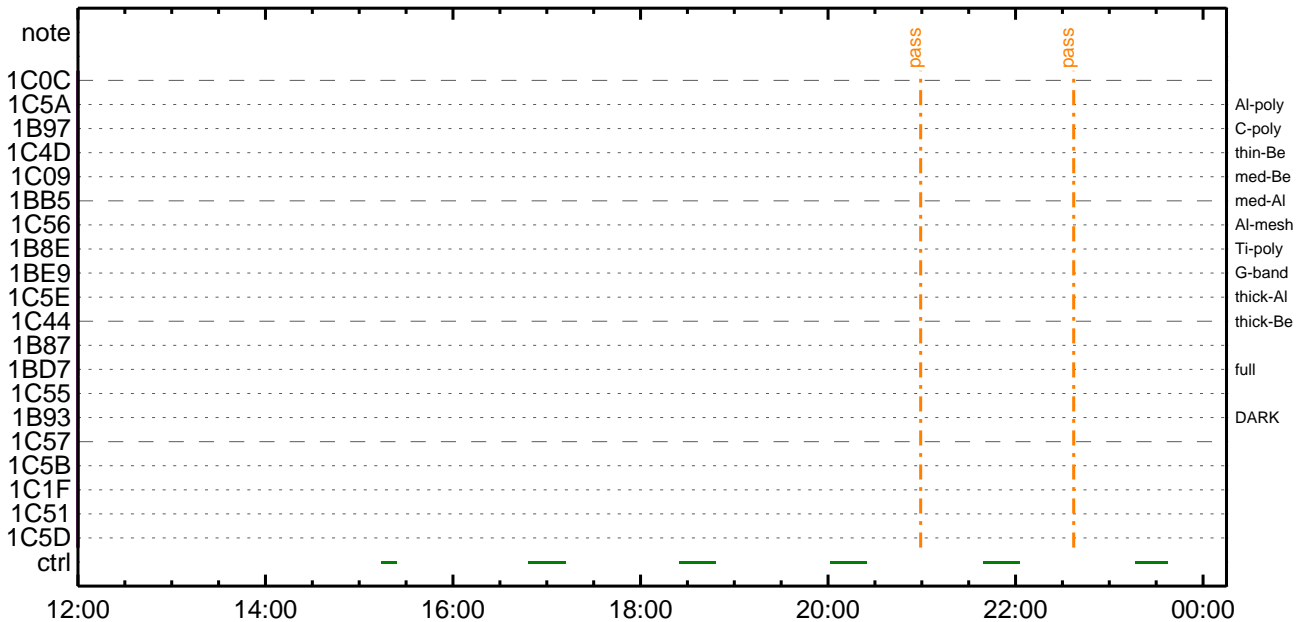
CMDI #0484 2019/09/20



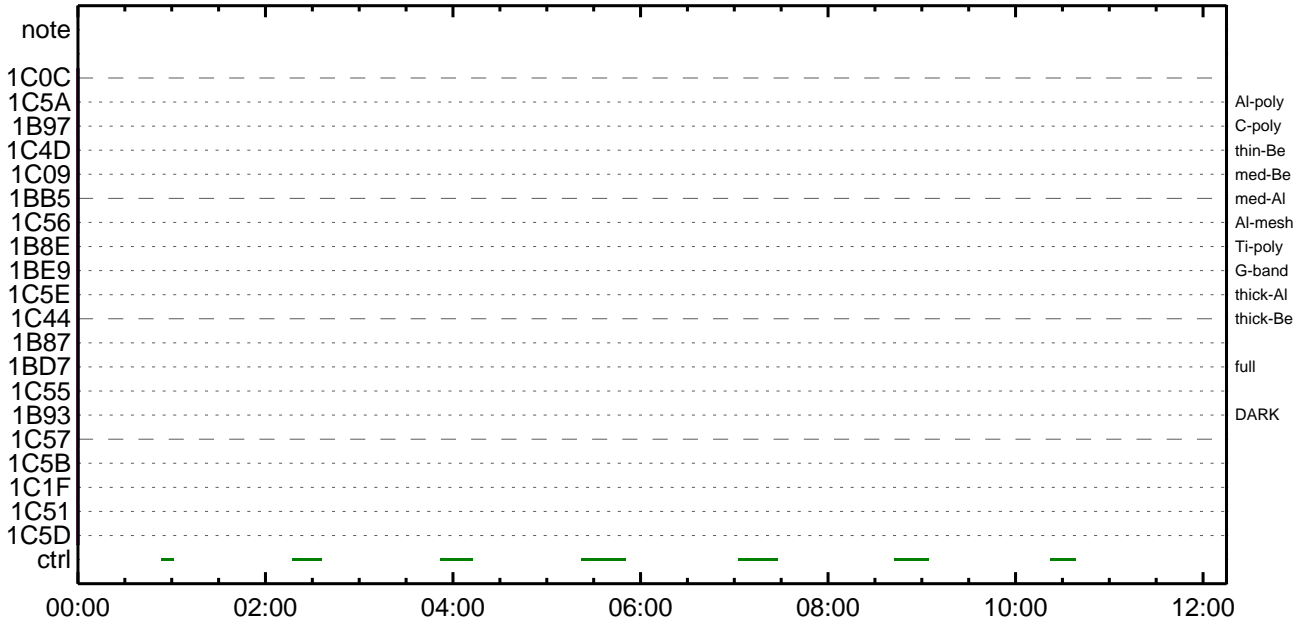
CMDI #0484 2019/09/21



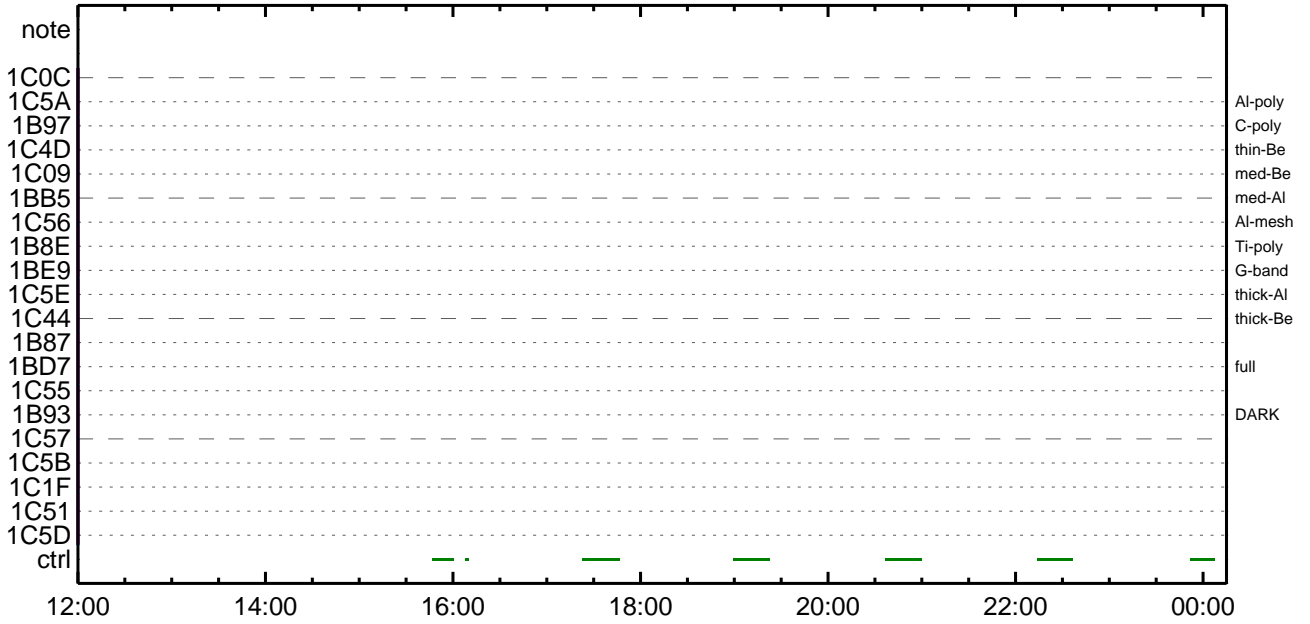
CMDI #0484 2019/09/21



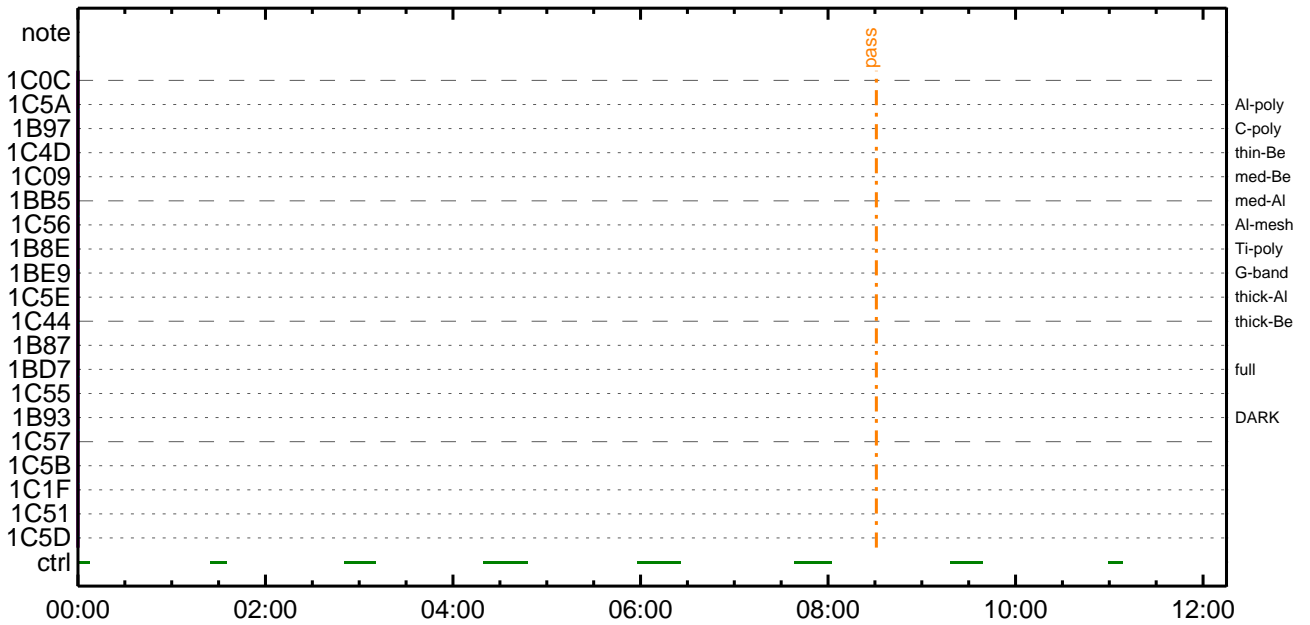
CMDI #0484 2019/09/22



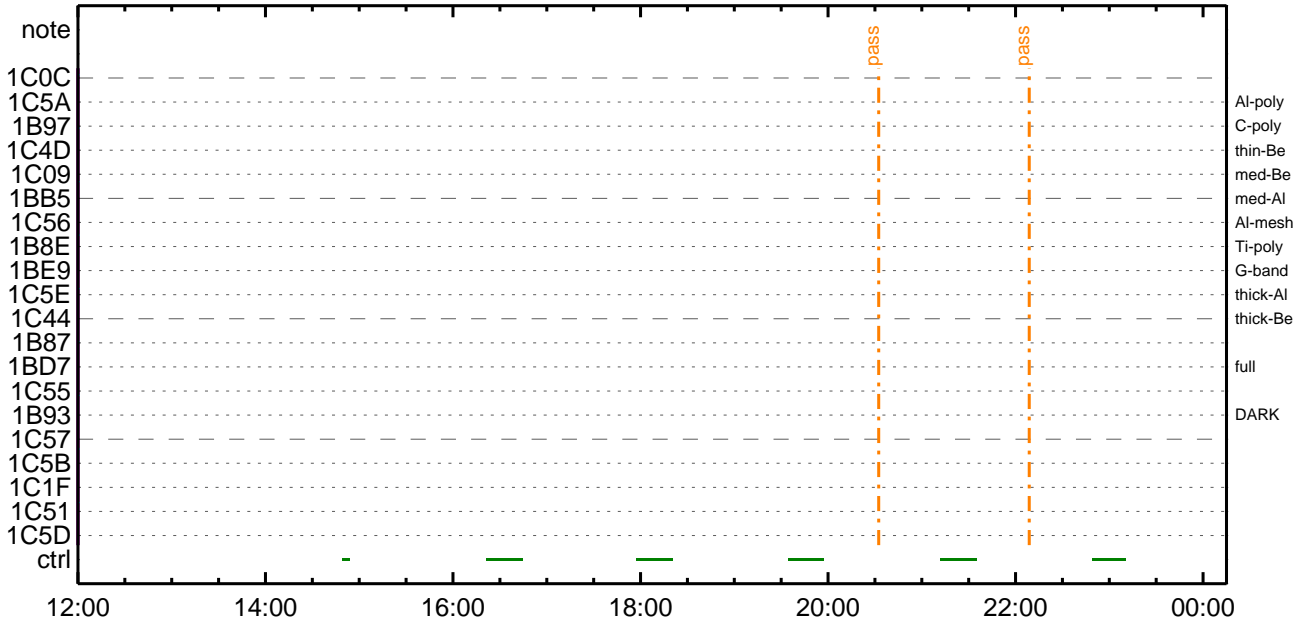
CMDI #0484 2019/09/22



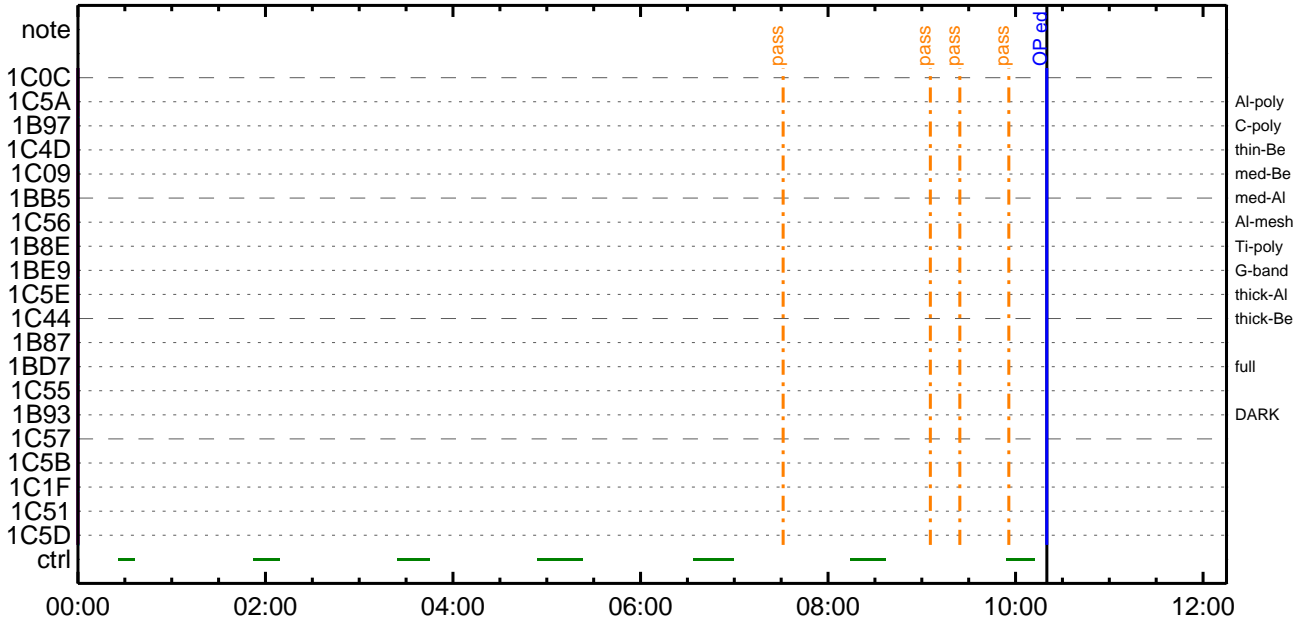
CMDI #0484 2019/09/23



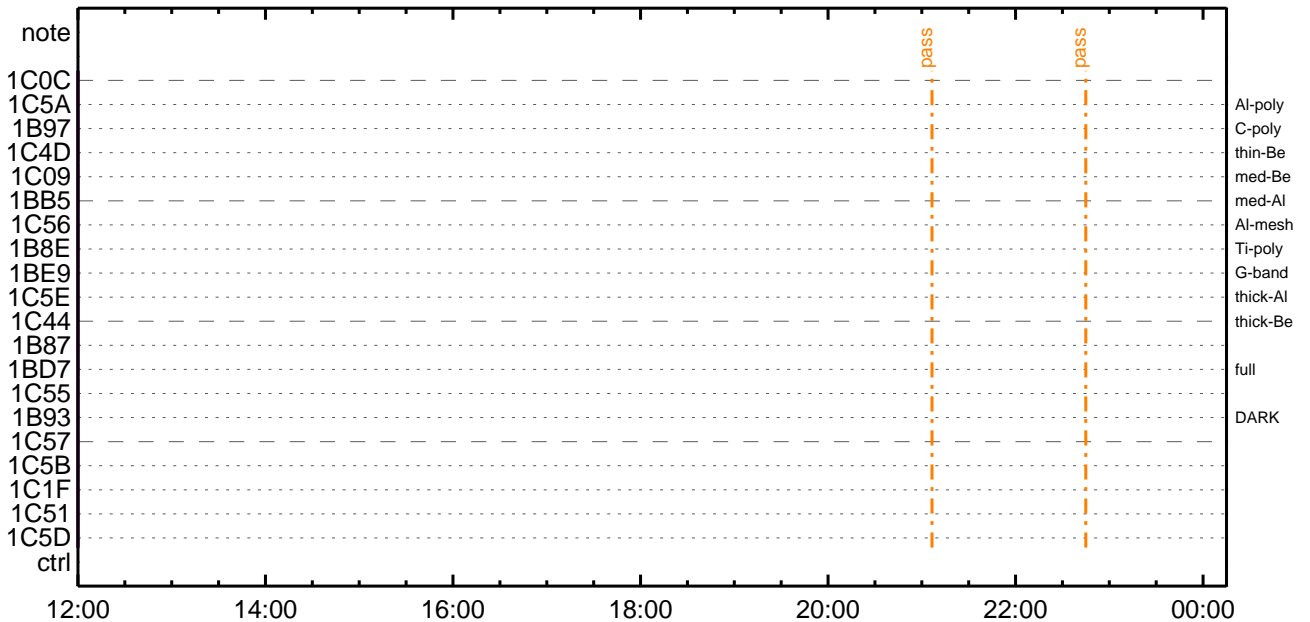
CMDI #0484 2019/09/23



CMDI #0484 2019/09/24



CMDI #0484 2019/09/24




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;1;YAYOX
0100 C. *****
0101 C.
0102 . C. ;aOP/OGY1;4YE;a
0103 . S. OP   op-346:OP
0104 C. ( )
0105 . S. OG   og-346:OG
0106 C. ( )
0107 C.
0108 . C. ;aNMOG&OPi°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC      (20 00 7f 01 02)
0112 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ      40
0113 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0114 C.      çç[HK1_DMP_BLOCK_NUM]      EQ     127
0115 C.      çç[HK1_DMP_REPEAT_NUM]     EQ      0
0116 C.      çç[HK1_DMA_DMP_PIM]        EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC      (07 0b f8)
0119 C.      çç[HK1_PKT_FORM_NO]        EQ      7
0120 C.      çç[HK1_PKT_GEN_TIME]       EQ     0.25 s
0121 C.      çç[HK1_S_TLM_BIT_RATE]    EQ     32k
0122 C.      çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0123 C.      çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0124 . C.  YAYOXx½ªi»ò³içS
0125 C.      çç[HK1_DMP_CHK_FLG]      EQ     NON
0126 . C. RAM ID=NMOG□i¼E¹ç•è²iOKò³içS
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC      (20 80 7f 01 02)
0131 C.      çç[HK1_DMP_TOP_ADRS_1]    EQ     41
0132 C.      çç[HK1_DMP_TOP_ADRS_0]    EQ      0
0133 C.      çç[HK1_DMP_BLOCK_NUM]    EQ     127
0134 C.      çç[HK1_DMP_REPEAT_NUM]   EQ      0
0135 C.      çç[HK1_DMA_DMP_PIM]      EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC      (07 0b f8)
0138 C.      çç[HK1_PKT_FORM_NO]      EQ      7
0139 C.      çç[HK1_PKT_GEN_TIME]     EQ     0.25 s
0140 C.      çç[HK1_S_TLM_BIT_RATE]   EQ     32k
0141 C.      çç[HK1_X_TLM_BIT_RATE]   EQ      4M
0142 C.      çç[HK1_DMP_CHK_FLG]     EQ     EXEC
0143 . C.  YAYOXx½ªi»ò³içS
0144 C.      çç[HK1_DMP_CHK_FLG]     EQ     NON
0145 . C. RAM ID=NMOG□i¼E¹ç•è²iOKò³içS
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC      (21 00 41 01 02)
0150 C.      çç[HK1_DMP_TOP_ADRS_1]    EQ     42
0151 C.      çç[HK1_DMP_TOP_ADRS_0]    EQ      0
0152 C.      çç[HK1_DMP_BLOCK_NUM]    EQ     65
0153 C.      çç[HK1_DMP_REPEAT_NUM]   EQ      0
0154 C.      çç[HK1_DMA_DMP_PIM]      EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC      (07 0b f8)
0157 C.      çç[HK1_PKT_FORM_NO]      EQ      7
0158 C.      çç[HK1_PKT_GEN_TIME]     EQ     0.25 s
0159 C.      çç[HK1_S_TLM_BIT_RATE]   EQ     32k
0160 C.      çç[HK1_X_TLM_BIT_RATE]   EQ      4M
0161 C.      çç[HK1_DMP_CHK_FLG]     EQ     EXEC
0162 . C.  YAYOXx½ªi»ò³içS
0163 C.      çç[HK1_DMP_CHK_FLG]     EQ     NON
0164 . C. RAM ID=NMOG, RAM ID=OP□i¼E¹ç•è²iOKò³içS
0165 C.
0166 . C. ***** °E²¼□i¼A´¶i°EÊ-□°A÷z@ (¼âµ-YAYOXx½ê¼ç□ðÅÖæ□ç¼ª°□ê¼i¹ç□çâ) *****
0167 C. DHUYâ;¼YE;E¼Y½;Yi;¼YE;E□ðiâ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC      (02 0a f8)
0170 C.      çç[HK1_PKT_FORM_NO]      EQ      2
0171 C.      çç[HK1_PKT_GEN_TIME]     EQ     0.5S
0172 C.      çç[HK1_S_TLM_BIT_RATE]   EQ     32K
0173 C.      çç[HK1_X_TLM_BIT_RATE]   EQ      4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE |§ OPOG UPLOAD□-A÷z@NG□i¼i¹ç;ç°E²¼□i¼TI-CMDÁ÷z@□i¼A¹Ô□•□E□□□³□E;f
0180 C.      □P□z;çSET□EDUMP□i¼±°i¼Y¹□ç¹Ô□|□³□E;f
0181 C.
0182 . C. TIY³YpYOXE□ðÂDîç(UT)
0183 +. TI 2019-09-19 11:06:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.      çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
0186 C.
0187 +. TI 2019-09-19 11:06:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.      çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
0190 C.
0191 +. TI 2019-09-19 11:06:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.      çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
```



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0194 C.
0195 +. TI 2019-09-19 11:10:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄè%îíñαîŷÄŷ§ŷÄŷ-¹àîŰ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]            EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]          EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]          EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]           EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]         EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]            EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]             EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]           EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]         EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]           EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]           EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷÈ¹ç•è²îOKαò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]             EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]           EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]         EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2019-09-19 11:10:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2019-09-19 11:10:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]           EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2019-09-19 11:10:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]           EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîαî»ò¼ŷαÈÄα¹αèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷâŷçŷèαÈ¼αα¼Ä»Űα¹αè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îñαÈ¹Øα¹αèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. îãLOSŷÄŷŷŷÄŷ-¼Ä»Ű;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

```



```

0096   C.
0097   C.
0098   . C. **** AOCs Commands (Tracking Curve Upload) ****
0099   C. Upload the Orbit Element and the Target Attitude
0100   C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102   ( )
0103   C.
0104   C.
0105   C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107   BC      (07 00 00 00 18 00)
0108   C.
0109   C.      <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110   C.
0111   C.
0112   C. Change the TLMFormatNo for the AOCs Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114   BC      (04 0b f8)
0115   C.
0116   C. Wait for AOCSDUMP to end
0117   C.
0118   . C. Check the dump memory
0119   C.
0120   C.                                     Result = OK [ ]
0121   C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123   BC      (02 0a f8)
0124   C.
0125   C.                                     <A_***>[TLM STS] FMT = 2 [ ]
0126   C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_CHG_ENA
0131   BC      (20)
0132   . C. Verify EIS_MODE_CHG_FLG is ENA
0133   +. DC 07-FC EIS_MODE_MANU
0134   BC      (21 02)
0135   . C. Verify EIS in MANUAL mode
0136   . C. Estimated OBSTBL upload time is 21s
0137   C. *****
0138   C. EIS START OBSTBL LOAD
0139   C. *****
0140   . S. RAM   ram-820:EIS_OBSTBL
0141   ( )
0142   +. DC 07-FC EIS_DUMP_OBSTBL
0143   BC      (07 07 07 00 00 70 00)
0144   C.
0145   C. Execute, after the success of OBSTBL upload.
0146   C. Set EIS TI-commands
0147   +. TI 2019-09-19 11:10:50.0
0148   DC 07-FC EIS_MODE_CHG_ENA
0149   BC      (20)
0150   . C.                                     [ ] [HK1_TI_CMD_NUM]    EQ    1 COUNTUP
0151   C. *****
0152   C. EIS END OBSTBL LOAD
0153   C. *****
0154   C.
0155   . C. ***** MDP  ´ûÄîñî»ö¼ÝñÉÁÐñ¹ñèDCBC•x²è *****
0156   C. (¼áºï¥ÖÄ¥É¥Þ¥É¥Á¥ç¥èñÉ%¼ñ¼Ä»Üñ¹ñè)
0157   . S. DC-BC dcbc-402:DCBC
0158   (MDP_known_event)
0159   C.
0160   C.
0161   . C. ***** ¥Ð¥¹•ï Daily±çínñÉ´Øñ¹ñèDCBC•x²è *****
0162   . S. DC-BC dcbc-153:DCBC
0163   (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164   C.
0165   C.
0166   . C. ;ãLOS¥Á¥$¥Ä¥¹¼Ä»Ü;ã
0167   C.
0168   . C. ***** LOS *****
0169   C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-348 2019-09-19 12:44:34 94 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÒø•ø¿¼í¹çøÍ;çÀ®, ùø¹øÈøÈøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 06 80 80 20 20)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 07 80 80 08 08)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 08 80 80 20 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 09 80 80 08 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 0f 80 80 06 06)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 10 80 80 08 08)
0052 + DC 07-F0 MDP_XRT_FLD_ENA
0053 BC (d8)
0054 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0055 BC (c8)
0056 + DC 07-F0 MDP_XRT_ARS_DIS
0057 BC (d5)
0058 + DC 07-F0 MDP_XRT_AEC_RESET
0059 BC (d0)
0060 + DC 07-F0 MDP_XRT_FLD_RESET
0061 BC (da)
0062 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0063 BC (c4 08)
0064 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0065 BC (c5 0d)
0066 . C. ----- Success Verify ? OK / NG ____
0067 C.
0068 C.
0069 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0070 C.
0071 +. DC 07-F0 MDP_XRT_MODE_OBSV
0072 BC (c2)
0073 +. TI 2019-09-19 11:10:02.0
0074 DC 07-F0 MDP_XRT_MODE_OBSV
0075 BC (c2)
0076 . C. ----- Success Verify ? OK / NG ____
0077 C.
0078 C. ***** XRT END *****
0079 C.
0080 . C. ***** MDP `ûÁÍøÍ»ò¼YøÈÁøø¹øÈDCBC•×²è *****
0081 C. (¼á°íYóYÁYÈYbYÈYáYçYèøÈ¼øø¼Á»Ûø¹øÈ)
0082 . S. DC-BC dcbc-402:DCBC
0083 (MDP_known_event)
0084 C.
0085 C.
0086 . C. ***** YD¥¹•İ Daily±¿ÍÑøÈ´Øø¹øÈDCBC•×²è *****
0087 . S. DC-BC dcbc-153:DCBC
0088 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089 C.
0090 C.
0091 . C. ;ãLOS¥ÁY$YÁY-¼Á»Û;ã
0092 C.
0093 . C. ***** LOS *****
0094 C.
```

*** OP Sequence for XRT ***

```

2019/09/19 11:20:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 11:20:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 11:20:58.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2019/09/19 11:21:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 04 00 00 00 00
2019/09/19 11:21:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2019/09/19 11:21:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2019/09/19 11:21:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2019/09/19 11:21:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2019/09/19 11:21:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/09/19 11:23:56.0 XRT_QT_PROG_SET_431_OG [0x1af]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2019/09/19 11:23:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2019/09/19 11:24:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/09/19 14:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 14:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 14:59:58.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2019/09/19 15:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 b0 5b 01 f3
2019/09/19 15:00:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2019/09/19 15:00:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2019/09/19 15:00:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2019/09/19 15:00:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2019/09/19 15:00:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/09/19 15:02:56.0 XRT_QT_PROG_SET_431_OG [0x1af]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2019/09/19 15:02:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2019/09/19 15:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/09/19 15:40:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 15:40:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 15:40:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/09/19 15:40:06.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2019/09/19 15:43:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2019/09/19 15:52:30.0 XRT_Custom_430_OG [0x1ae]
2019/09/19 15:53:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/09/19 17:15:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 17:15:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 17:15:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/09/19 17:15:36.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2019/09/19 17:18:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2019/09/19 17:39:00.0 XRT_Custom_430_OG [0x1ae]
2019/09/19 17:40:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/09/19 18:52:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 18:52:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 18:52:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/09/19 18:52:36.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2019/09/19 18:55:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2019/09/19 19:15:30.5 XRT_Custom_430_OG [0x1ae]
2019/09/19 19:16:30.5 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/09/19 20:29:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/09/19 20:29:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1

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2019/09/19	20:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/19	20:29:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/19	20:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/19	20:52:30.0	XRT_Custom_430_OG [0x1ae]						
2019/09/19	20:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/19	22:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/19	22:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/19	22:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/19	22:06:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/19	22:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/19	22:29:30.0	XRT_Custom_430_OG [0x1ae]						
2019/09/19	22:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/19	23:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/19	23:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/19	23:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/19	23:44:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/19	23:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/20	00:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/20	00:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/20	00:09:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/09/20	00:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2019/09/20	00:10:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2019/09/20	00:10:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2019/09/20	00:10:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/20	00:12:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2019/09/20	00:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/20	00:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/20	00:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/20	00:19:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/09/20	00:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00		
2019/09/20	00:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/09/20	00:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/09/20	00:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/09/20	00:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/09/20	00:20:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/20	00:22:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2019/09/20	00:22:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/09/20	00:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/20	01:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/20	01:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/20	01:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2019/09/20	01:21:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/09/20	01:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/09/20	01:27:30.5	XRT_Custom_430_OG [0x1ae]						
2019/09/20	01:28:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/09/20	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/20	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/09/20	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		

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2019/09/20	02:00:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2019/09/20	02:00:18.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9
2019/09/20	02:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2019/09/20	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2019/09/20	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2019/09/20	02:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2019/09/20	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/09/20	02:43:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/20	02:43:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/20	02:43:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2019/09/20	02:43:36.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/09/20	02:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/09/20	03:03:30.0	XRT_Custom_430_OG [0x1ae]			
2019/09/20	03:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/09/20	04:14:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/20	04:14:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/20	04:14:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2019/09/20	04:14:36.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/09/20	04:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/09/20	04:40:30.0	XRT_Custom_430_OG [0x1ae]			
2019/09/20	04:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/09/20	05:50:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/20	05:50:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/20	05:50:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2019/09/20	05:50:36.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/09/20	05:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/09/20	06:19:30.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/20	06:19:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/20	06:19:34.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/09/20	06:19:54.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9
2019/09/20	06:19:56.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2019/09/20	06:19:58.0	XRT_ARS_DIS_443_OG [0x1bb] MDP_XRT_ARS_DIS	1	07-F0	d5
2019/09/20	06:22:34.0	XRT_QT_PROG_SET_426_OG [0x1aa] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2019/09/20	06:22:36.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/09/20	06:29:00.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/09/20	06:30:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	04 00 00 00 00
2019/09/20	06:33:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_417_OG [0x1a1] TCIB_XRT_S_HTR_A_ENA	0	04-BC	
2019/09/20	15:00:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 b0 5b 01 f3
2019/09/21	00:00:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2019/09/21	00:10:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	04 00 00 00 00
2019/09/21	10:22:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00