

XRT Timeline to be uploaded on 2019/10/01

Period: 2019/10/01 11:09:00 - 2019/10/05 10:14:00

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Normal mode

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XOB #1BA4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms)

Term	Pointing (x, y)	Comment
10/01 11:22:00 - 10/01 14:59:54	Fixed (578.0, -751.0)	# OP start + 10min : HOP 373 with THEMIS prominence
10/01 15:03:00 - 10/01 17:44:30	Track (131.1, 614.0) @ 10/01 15:00:00	HOP 373 with THEMIS filament
10/02 06:07:00 - 10/02 10:59:54	Fixed (578.0, -751.0)	HOP 373 with THEMIS prominence

PROG= 04 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 92												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1C56: Synoptic 7 Filter w/ Al-mesh(512/2048/5795), Al-poly(512/5795/11571), Thin-Be(3897/16384/32768) - Thick-Be(65536), Al-poly+Ti-poly(4096/2314)

Term	Pointing (x, y)	Comment
10/01 18:11:30 - 10/01 18:18:24	Fixed (0.0, 0.0)	synoptic, shifted 8.5 min

PROG= 14 1-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 5												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 78												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 91												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 52												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2												
Seqn= 46												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17												
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 97												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1C5D: CME watch - 4x4 - AEC 2/3 - Al-poly - AEC2 - Be-thin - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
10/01 18:21:30 - 10/02 01:59:54	Track (228.6, 115.1) @ 10/01 18:18:30	EIS Bright Points
10/02 18:08:30 - 10/03 01:59:54	Track (-4.5, 776.2) @ 10/02 18:05:30	Coronal Hole Edge

PROG= 01 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 30												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2												
Seqn= 6												
1-time(s)												
2.0sec												

Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 83		1-time(s)		2.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C4D: HOP349 - 3-filter Synoptics (Al-mesh[128/1024/5795], Al-poly[256/4096/8192], thin-Be[2048/16384/32768] with 512x512 G-band+Leak(1064,1048)

Term	Pointing (x, y)	Comment
10/02 02:03:00 - 10/02 05:53:54	Fixed (0.0, 0.0)	HOP 349
10/03 02:03:00 - 10/03 05:59:54	Fixed (0.0, 0.0)	HOP 349

PROG= 17 Inf.-time(s)

Subr= 1		1-time(s)		300.0sec								
Seqn= 88		1-time(s)		2.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 50		1-time(s)		2.0sec								
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 56		1-time(s)		2.0sec								
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 81		1-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
Subr= 2		6-time(s)		450.0sec								
Seqn= 8		1-time(s)		2.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6		1-time(s)		2.0sec								
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29		1-time(s)		2.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C0C: Synoptic Q95 2x2 - Al/mesh(512/2048/4096) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(512/4096/8192)

Term	Pointing (x, y)	Comment
10/02 05:57:00 - 10/02 06:03:54	Fixed (0.0, 0.0)	synoptic, shifted -6.0 min
10/02 17:58:30 - 10/02 18:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min
10/03 06:03:00 - 10/03 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 19 1-time(s)

Subr= 1		1-time(s)		2.0sec								
Seqn= 5		1-time(s)		2.0sec								
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 12		1-time(s)		2.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 82		1-time(s)		2.0sec								
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 52		1-time(s)		2.0sec								
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23		1-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BB8: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms)

Term	Pointing (x, y)	Comment
10/02 11:03:00 - 10/02 17:55:24	Track (239.8, 618.2) ^{© 10/02 11:00:00}	HOP 373 with THEMIS filament
10/03 06:13:00 - 10/03 10:17:00	Fixed (578.0, -751.0)	HOP 373 with THEMIS prominence

PROG= 05 Inf.-time(s)

Subr= 1		1-time(s)		2.0sec								
Seqn= 92		1-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 3-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 32 30-time(s) 120.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	34.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	34.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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Term	Pointing (x, y)	Comment
10/01 11:22:00 - 10/01 14:59:54	Fixed (578.0, -751.0)	# OP start + 10min : HOP 373 with THEMIS prominence
10/01 15:03:00 - 10/01 17:44:30	Track (131.1, 614.0) @ 10/01 15:00:00	HOP 373 with THEMIS filament
10/01 18:21:30 - 10/02 01:59:54	Track (228.6, 115.1) @ 10/01 18:18:30	EIS Bright Points
10/02 02:03:00 - 10/02 05:53:54	Fixed (0.0, 0.0)	HOP 349
10/02 06:07:00 - 10/02 10:59:54	Fixed (578.0, -751.0)	HOP 373 with THEMIS prominence
10/02 11:03:00 - 10/02 17:55:24	Track (239.8, 618.2) @ 10/02 11:00:00	HOP 373 with THEMIS filament
10/02 18:08:30 - 10/03 01:59:54	Track (-4.5, 776.2) @ 10/02 18:05:30	Coronal Hole Edge
10/03 02:03:00 - 10/03 05:59:54	Fixed (0.0, 0.0)	HOP 349
10/03 06:13:00 - 10/03 10:17:00	Fixed (578.0, -751.0)	HOP 373 with THEMIS prominence

PROG= 13 30-time(s)

Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

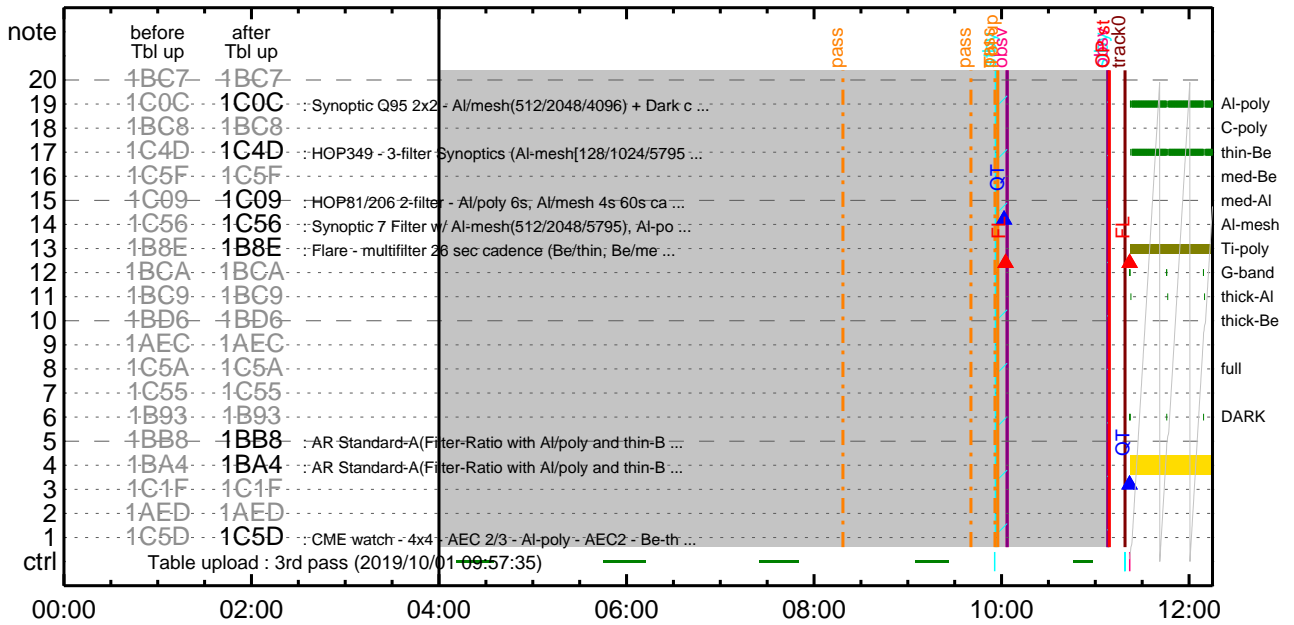
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Flare Detection

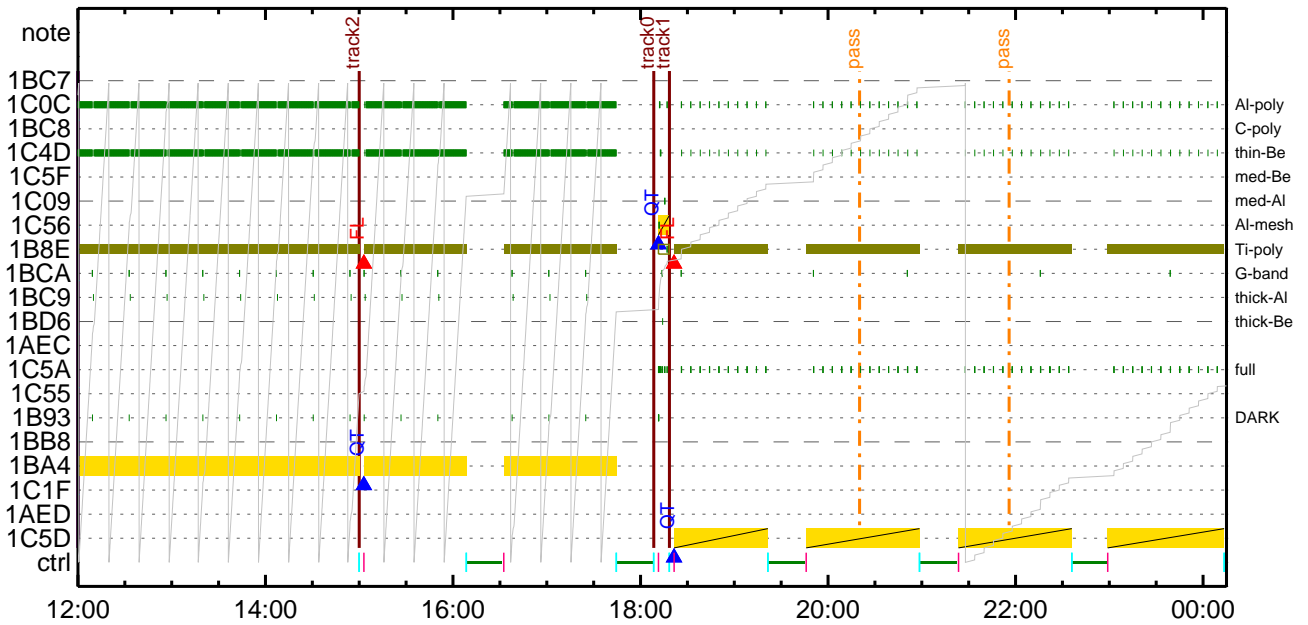
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Term	Pointing (x, y)	Comment
10/01 18:18:48 - 10/02 05:54:18	Track (228.6, 115.1) @ 10/01 18:18:30	EIS Bright Points
10/02 06:04:18 - 10/02 17:55:48	Fixed (578.0, -751.0)	HOP 373 with THEMIS prominence
10/02 18:05:48 - 10/03 06:00:18	Track (-4.5, 776.2) @ 10/02 18:05:30	Coronal Hole Edge
10/03 06:10:18 - 10/05 10:14:00	Fixed (578.0, -751.0)	HOP 373 with THEMIS prominence
Al-poly/Open	Al-poly/Open	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

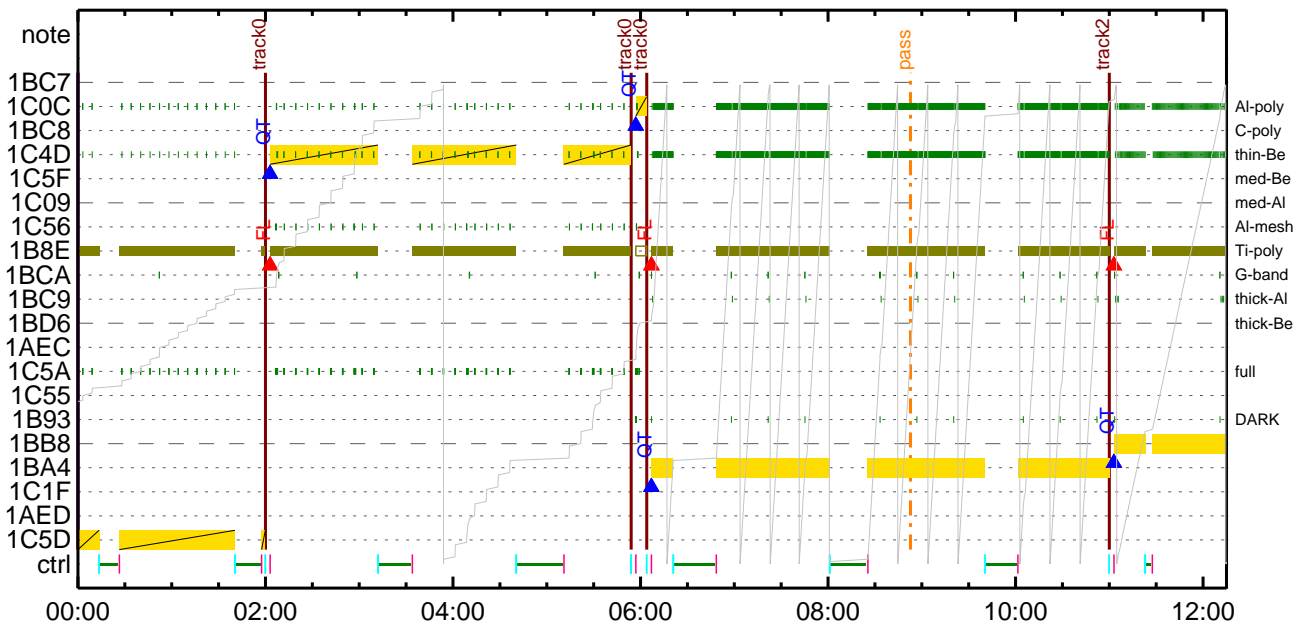
CMDI #0509 2019/10/01



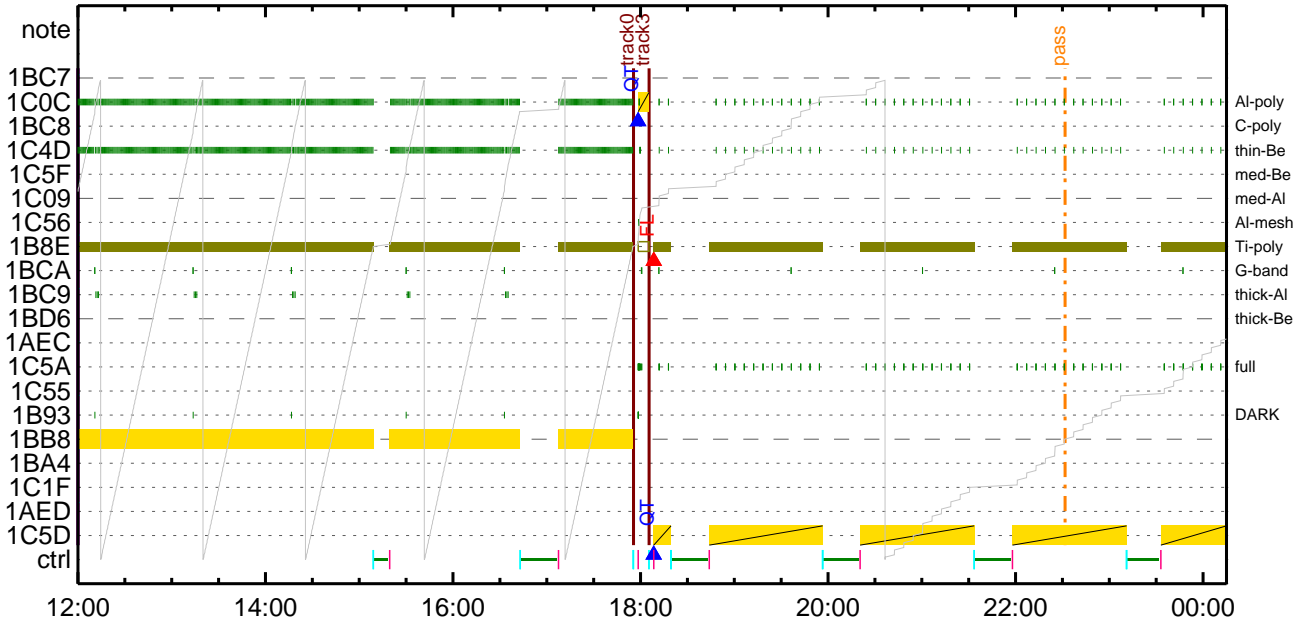
CMDI #0509 2019/10/01



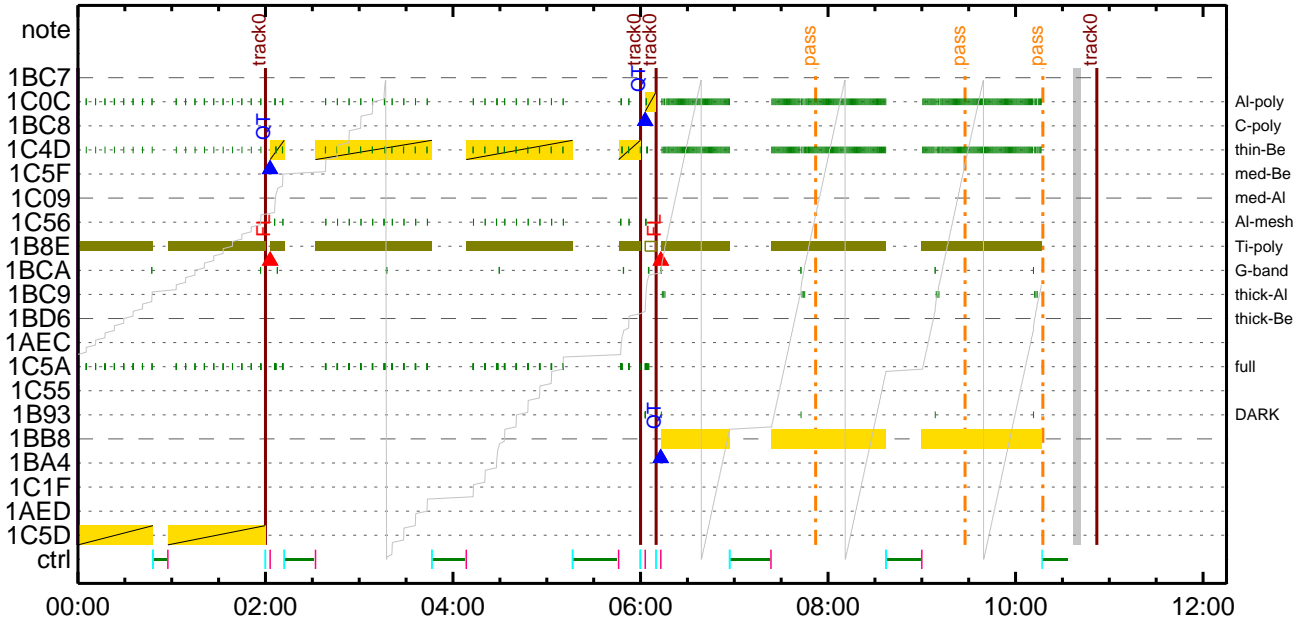
CMDI #0509 2019/10/02



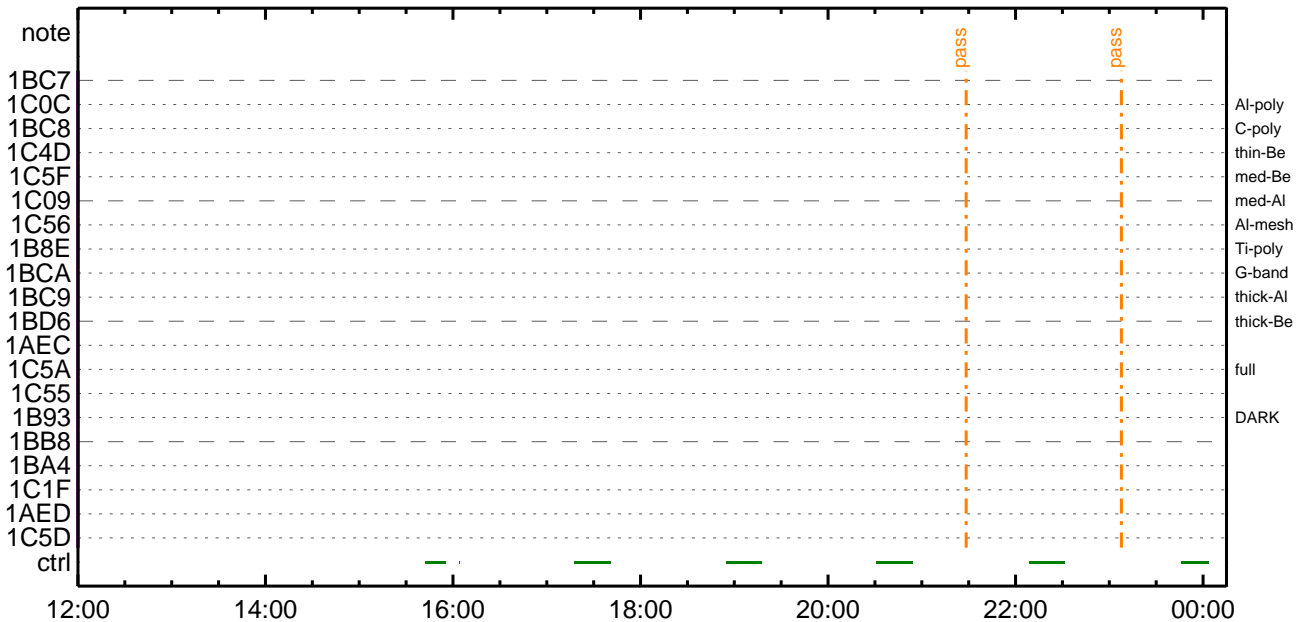
CMDI #0509 2019/10/02



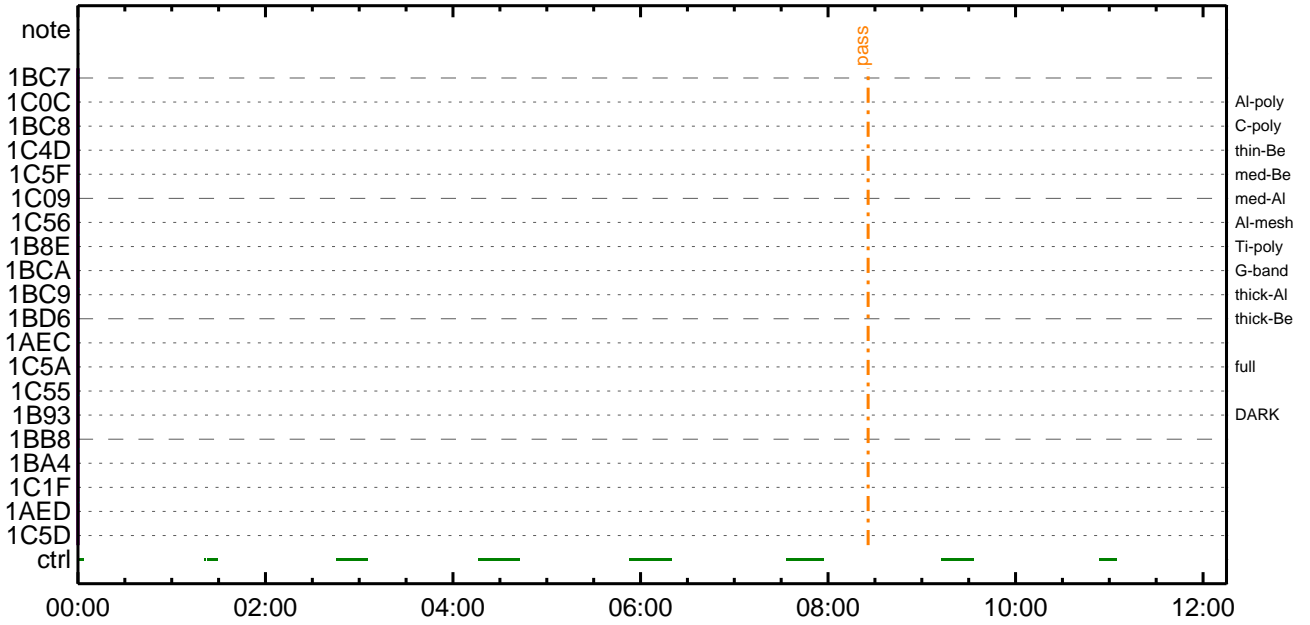
CMDI #0509 2019/10/03



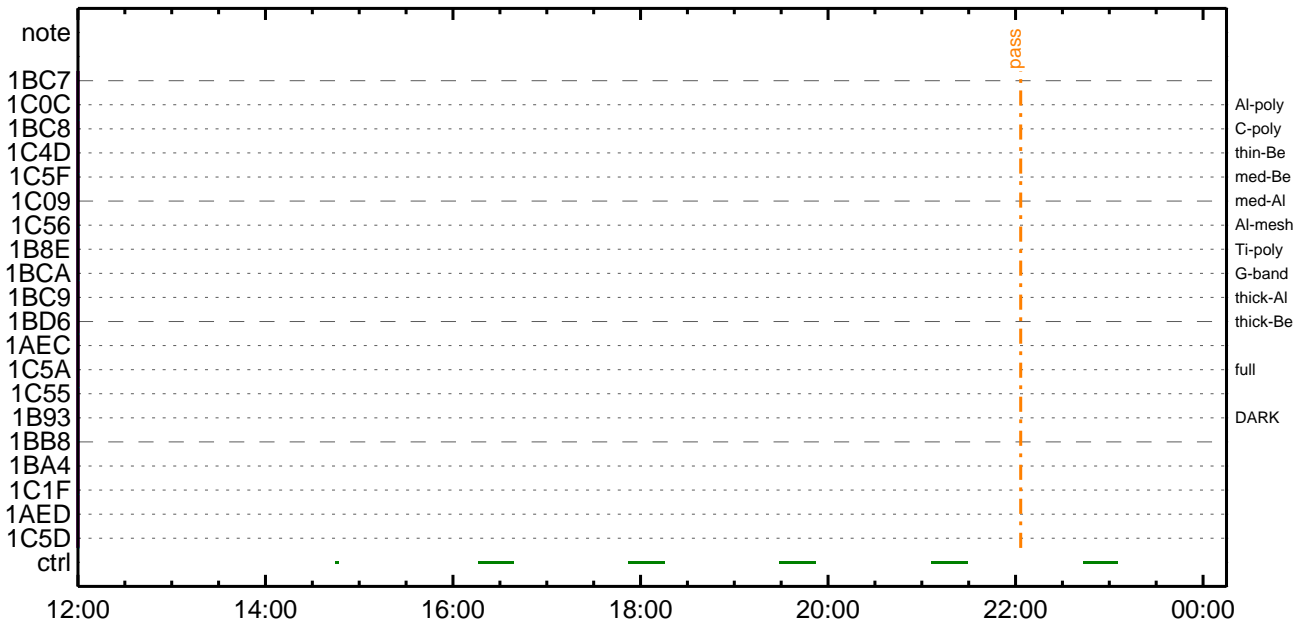
CMDI #0509 2019/10/03



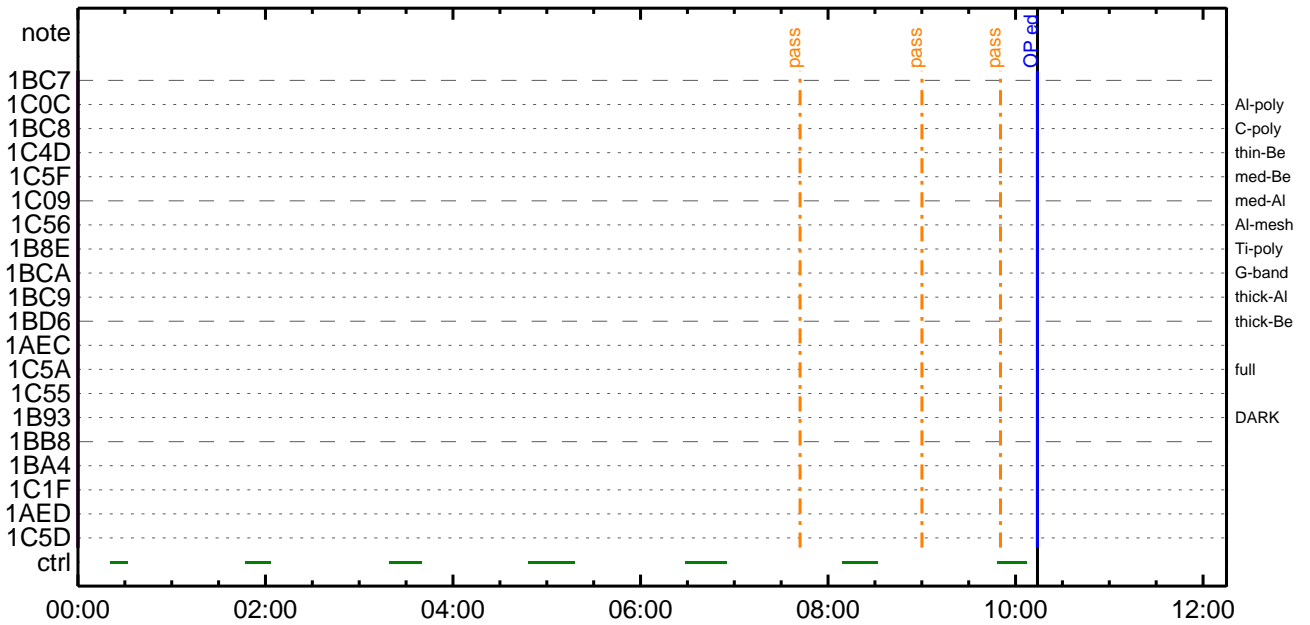
CMDI #0509 2019/10/04



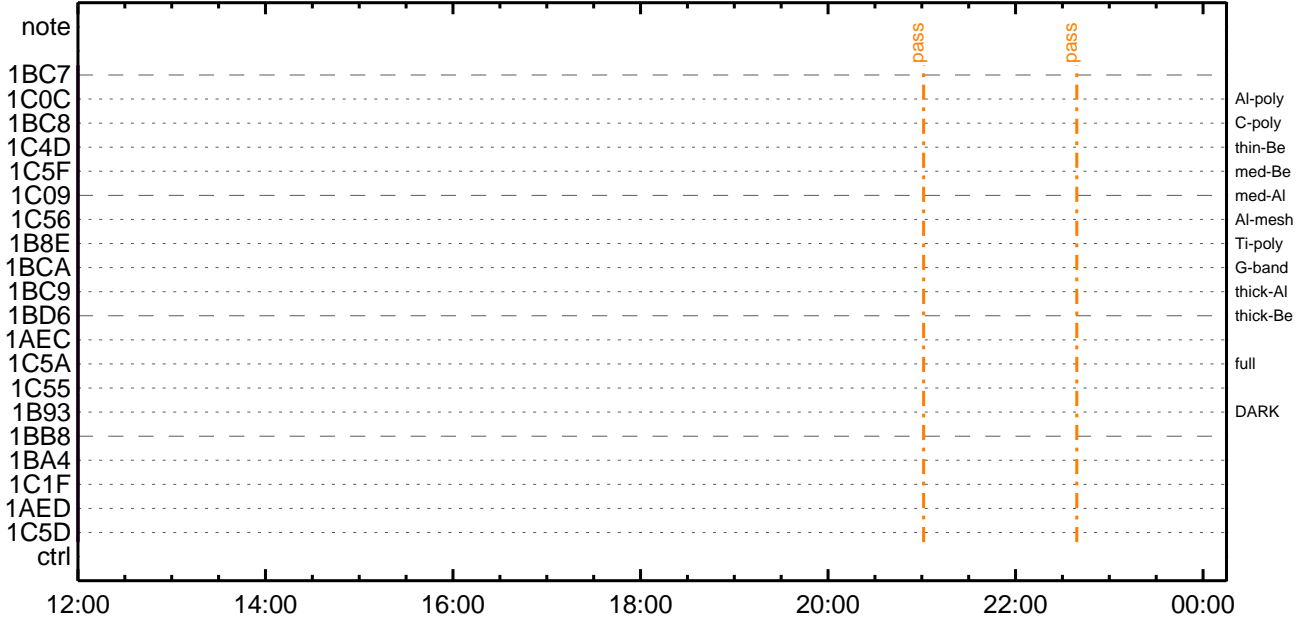
CMDI #0509 2019/10/04



CMDI #0509 2019/10/05



CMDI #0509 2019/10/05




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-368:OP
0104 ( )
0105 S. OG og-368:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²ò³òE;çSET²EEDUMP²î½±°îYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y½Y½E²òðÁDî¿(UT)
0183 +. TI 2019-10-01 11:04:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2019-10-01 11:04:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2019-10-01 11:04:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.
0195 +. TI 2019-10-01 11:08:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °È²¼αīÄè%īíñòîŷÄŷ$ŷÄŷ-¹àîÛ
0200 C.      çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.      çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.      çç[HK1_NEXT_EXEC_PIM]                    EQ      DHU
0203 C.      çç[HK1_NEXT_EXEC_DC]                     EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.      çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.      çç[HK1_DMP_BLOCK_NUM]                     EQ      3
0215 C.      çç[HK1_DMP_REPEAT_NUM]                   EQ      0
0216 C.      çç[HK1_DMA_DMP_PIM]                       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      çç[HK1_PKT_FORM_NO]                       EQ      7
0220 C.      çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.      çç[HK1_S_TLM_BIT_RATE]                    EQ      32k
0222 C.      çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0223 C.      çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îç§
0226 C.      çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαî¼È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èòðîäα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      çç[HK1_PKT_FORM_NO]                       EQ      2
0234 C.      çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.      çç[HK1_S_TLM_BIT_RATE]                    EQ      32K
0236 C.      çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2019-10-01 11:08:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2019-10-01 11:08:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.      [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2019-10-01 11:08:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.      [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÃîôî»ò¼ŷòÈÄð¹òèDCBC•x²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷâŷçŷèòÈ¼α¼Ä»Ûα¹òè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷÈŷ¹.İ Daily±;îñòÈ¹øα¹òèDCBC•x²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;ãLOSŷÄŷ$ŷÄŷ-¼Ä»Û;ã
0276 C.
0277 C. ***** LOS *****
0278 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-369 2019-10-01 13:34:09 94 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. ;ãAOSYÁYſYÄY~¼Ä»Ü;ã
0005 . C.
0006 . C. YÄYſ;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 . C. Áí;È«¿«Ä«•µ°«»Í«Ç«ó«ſ«ç«Y«Ä«Y«×«Y«í;¼«Y«É;È«È«µ«ſí«É;È«È«°«Ç«Ô«««¿«ſ«i¹«ç«Ä«í;ç«Ä«®,«ù«¹««È«««««««Ç«Ä+¿«®««««È««««³««È;ſ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 . C. Upload the Orbit Element and the Target Attitude
0016 . C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 . ( )
0019 . C.
0020 . C.
0021 . C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 . BC (07 00 00 00 18 00)
0024 . C.
0025 . C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 . C.
0027 . C.
0028 . C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 . BC (04 0b f8)
0031 . C.
0032 . C. Wait for AOCSDUMP to end
0033 . C.
0034 . C. Check the dump memory
0035 . C.
0036 . C. Result = OK [ ]
0037 . C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 . BC (02 0a f8)
0040 . C.
0041 . C. <A_***>[TLM STS] FMT = 2 [ ]
0042 . C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCs Commands (Orbital Element Update) *****
0046 . C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 . BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 . C.
0051 . C. <A_ORB>[ORBIT] EPC = 7890839.5 +- 1.0 (s) [ ]
0052 . C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_CHG_ENA
0056 . BC (20)
0057 . C. Verify EIS_MODE_CHG_FLG is ENA
0058 +. DC 07-FC EIS_MODE_MANU
0059 . BC (21 02)
0060 . C. Verify EIS in MANUAL mode
0061 . C. Estimated OBSTBL upload time is 12s
0062 . C. *****
0063 . C. EIS START OBSTBL LOAD
0064 . C. *****
0065 . S. RAM ram-820:EIS_OBSTBL
0066 . ( )
0067 +. DC 07-FC EIS_DUMP_OBSTBL
0068 . BC (07 07 07 00 00 70 00)
0069 . C.
0070 . C. Execute, after the success of OBSTBL upload.
0071 . C. Set EIS TI-commands
0072 +. TI 2019-10-01 11:08:50.0
0073 . DC 07-FC EIS_MODE_CHG_ENA
0074 . BC (20)
0075 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0076 . C. *****
0077 . C. EIS END OBSTBL LOAD
0078 . C. *****
0079 . C.
0080 . C. ***** MDP `ûÄÎ«ſ«ö¼Y««ÈÄ««¹««È«D«C«B«C•«×²«È *****
0081 . C. (¼«°«ſY«°Y«ÄY«ÈY«ſY«ÈY«áY«çY«È«««E¼«¼««¼«Ä«»Ü«¹««È)
0082 . S. DC-BC dcbc-402:DCBC
0083 . (MDP_known_event)
0084 . C.
0085 . C.
0086 . C. ***** YDſ!•İ Daily±¿İÑ«È´Ø«¹««È«D«C«B«C•«×²«È *****
0087 . S. DC-BC dcbc-153:DCBC
0088 . (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089 . C.
0090 . C.
0091 . C. ;ãLOSſÄYſYÄY~¼Ä»Ü;ã
0092 . C.
0093 . C. ***** LOS *****
0094 . C.
```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 80 80 08 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_AEC_RESET
0149 BC (d0)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 0f)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 0d)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2019-10-01 11:08:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 C.
0170 . C. ***** MDP `úÃîîî»ö¼ÿðÊÄð¹ñèDCBC•x²è *****
0171 C. (¼ã°îÿÖÿÄÿËÿÏÿËÿÄÿÇÿÈÿ¼ã¼Ä»Û¹ñè)
0172 . S. DC-BC dcbc-402:DCBC
0173 (MDP_known_event)
0174 C.
0175 C.
0176 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑñË´Ø¹ñèDCBC•x²è *****
0177 . S. DC-BC dcbc-153:DCBC
0178 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0179 C.
0180 C.
0181 . C. ;ãLOSÿÁÿSÿÄÿ-¼Ä»Û;ã
0182 C.
0183 . C. ***** LOS *****
0184 C.

```

*** OP Sequence for XRT ***

```

2019/10/01 11:18:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/10/01 11:18:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/10/01 11:18:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2019/10/01 11:19:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 42 c0 cc 9c
2019/10/01 11:19:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2019/10/01 11:19:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2019/10/01 11:19:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2019/10/01 11:19:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2019/10/01 11:19:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/10/01 11:21:56.0 XRT_QT_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 04
2019/10/01 11:21:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2019/10/01 11:22:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/10/01 14:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/10/01 14:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/10/01 14:59:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2019/10/01 15:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 02 00 00 00 00
2019/10/01 15:00:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2019/10/01 15:00:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2019/10/01 15:00:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2019/10/01 15:00:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2019/10/01 15:00:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/10/01 15:02:56.0 XRT_QT_PROG_SET_420_OG [0x1a4]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 04
2019/10/01 15:02:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0d
2019/10/01 15:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/10/01 16:08:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/10/01 16:08:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/10/01 16:08:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/10/01 16:08:36.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2019/10/01 16:11:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2019/10/01 16:31:30.0 XRT_Custom_430_OG [0x1ae]
2019/10/01 16:32:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/10/01 17:44:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/10/01 17:44:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/10/01 17:44:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2019/10/01 17:44:36.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2019/10/01 17:47:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2019/10/01 18:08:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/10/01 18:08:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2019/10/01 18:08:28.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2019/10/01 18:08:30.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 00 00
2019/10/01 18:08:48.0 XRT_FLD_DIS_409_OG [0x199]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2019/10/01 18:08:50.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2019/10/01 18:08:52.0 XRT_ARS_DIS_443_OG [0x1bb]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2019/10/01 18:11:28.0 XRT_QT_PROG_SET_426_OG [0x1aa]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0e
2019/10/01 18:11:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2019/10/01 18:18:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1

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2019/10/01	18:18:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/01	18:18:28.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/10/01	18:18:30.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	01 00 00 00 00
2019/10/01	18:18:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2019/10/01	18:18:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/10/01	18:18:52.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2019/10/01	18:18:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2019/10/01	18:18:56.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/01	18:21:26.0	XRT_QT_PROG_SET_431_OG [0x1af] MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2019/10/01	18:21:28.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/10/01	18:21:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/01	19:21:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/01	19:21:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/01	19:21:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/01	19:21:36.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/01	19:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/01	19:45:00.0	XRT_Custom_430_OG [0x1ae]			
2019/10/01	19:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/01	20:58:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/01	20:58:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/01	20:58:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/01	20:58:36.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/01	21:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/01	21:22:30.0	XRT_Custom_430_OG [0x1ae]			
2019/10/01	21:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/01	22:36:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/01	22:36:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/01	22:36:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/01	22:36:06.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/01	22:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/01	22:58:00.0	XRT_Custom_430_OG [0x1ae]			
2019/10/01	22:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/02	00:13:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/02	00:13:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/02	00:13:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/02	00:13:36.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/02	00:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/02	00:25:30.0	XRT_Custom_430_OG [0x1ae]			
2019/10/02	00:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/02	01:40:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/02	01:40:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/02	01:40:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/02	01:40:36.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/02	01:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/02	01:56:30.0	XRT_Custom_430_OG [0x1ae]			
2019/10/02	01:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/02	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/02	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/02	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

2019/10/02	02:00:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00	00	00	00	00
2019/10/02	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/10/02	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/10/02	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/10/02	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/10/02	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/02	02:02:56.0	XRT_QT_PROG_SET_404_OG [0x194] MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2019/10/02	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/10/02	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	03:12:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	03:12:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	03:12:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/02	03:12:06.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/10/02	03:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/10/02	03:33:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/02	03:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	04:40:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	04:40:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	04:40:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/02	04:40:36.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/10/02	04:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/10/02	05:10:00.5	XRT_Custom_430_OG [0x1ae]							
2019/10/02	05:11:00.5	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	05:53:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	05:53:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	05:53:58.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2019/10/02	05:54:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00	00	00	00	00
2019/10/02	05:54:18.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9				
2019/10/02	05:54:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2019/10/02	05:54:22.0	XRT_ARS_DIS_443_OG [0x1bb] MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/10/02	05:56:58.0	XRT_QT_PROG_SET_437_OG [0x1b5] MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2019/10/02	05:57:00.5	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	06:03:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	06:03:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	06:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2019/10/02	06:04:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	00	42	c0	cc	9c
2019/10/02	06:04:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/10/02	06:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/10/02	06:04:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/10/02	06:04:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/10/02	06:04:26.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/02	06:06:56.0	XRT_QT_PROG_SET_420_OG [0x1a4] MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2019/10/02	06:06:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/10/02	06:07:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	06:21:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	06:21:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	06:21:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/02	06:21:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							

2019/10/02	06:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/10/02	06:47:30.0	XRT_Custom_430_OG [0x1ae]						
2019/10/02	06:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/10/02	08:01:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	08:01:02.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	08:01:04.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2019/10/02	08:01:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/10/02	08:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/10/02	08:24:30.0	XRT_Custom_430_OG [0x1ae]						
2019/10/02	08:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/10/02	09:40:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	09:40:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	09:40:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2019/10/02	09:40:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/10/02	09:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/10/02	10:00:30.0	XRT_Custom_430_OG [0x1ae]						
2019/10/02	10:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/10/02	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2019/10/02	11:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	02 00 00 00 00		
2019/10/02	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/10/02	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/10/02	11:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/10/02	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/10/02	11:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2019/10/02	11:02:56.0	XRT_QT_PROG_SET_414_OG [0x19e]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2019/10/02	11:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/10/02	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/10/02	11:23:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	11:23:02.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	11:23:04.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2019/10/02	11:23:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/10/02	11:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/10/02	11:26:30.0	XRT_Custom_430_OG [0x1ae]						
2019/10/02	11:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/10/02	15:09:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	15:09:02.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	15:09:04.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2019/10/02	15:09:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/10/02	15:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/10/02	15:18:30.0	XRT_Custom_430_OG [0x1ae]						
2019/10/02	15:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/10/02	16:43:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	16:43:02.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/02	16:43:04.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2019/10/02	16:43:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/10/02	16:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/10/02	17:06:30.0	XRT_Custom_430_OG [0x1ae]						

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2019/10/02	17:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	17:55:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	17:55:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	17:55:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2019/10/02	17:55:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2019/10/02	17:55:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2019/10/02	17:55:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2019/10/02	17:55:52.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/10/02	17:58:28.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2019/10/02	17:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	18:05:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	18:05:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	18:05:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2019/10/02	18:05:30.5	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2019/10/02	18:05:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/10/02	18:05:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/10/02	18:05:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/10/02	18:05:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/10/02	18:05:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/02	18:08:26.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2019/10/02	18:08:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2019/10/02	18:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	18:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	18:19:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	18:19:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/02	18:19:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/10/02	18:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/10/02	18:43:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/02	18:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	19:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	19:56:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	19:56:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/02	19:56:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/10/02	19:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/10/02	20:19:30.0	XRT_Custom_430_OG [0x1ae]							
2019/10/02	20:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	21:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	21:33:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	21:33:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/02	21:33:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/10/02	21:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/10/02	21:57:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/02	21:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/02	23:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	23:11:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/02	23:11:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/02	23:11:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/10/02	23:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2019/10/02	23:32:00.0	XRT_Custom_430_OG [0x1ae]			
2019/10/02	23:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/03	00:48:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	00:48:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	00:48:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/03	00:48:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/03	00:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/03	00:56:30.0	XRT_Custom_430_OG [0x1ae]			
2019/10/03	00:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/03	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/10/03	02:00:00.0	AOCS_Or-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2019/10/03	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2019/10/03	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/10/03	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2019/10/03	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2019/10/03	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/03	02:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2019/10/03	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/10/03	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/03	02:12:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	02:12:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	02:12:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/03	02:12:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/03	02:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/03	02:31:00.0	XRT_Custom_430_OG [0x1ae]			
2019/10/03	02:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/03	03:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	03:46:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	03:46:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/03	03:46:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/03	03:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/03	04:07:30.5	XRT_Custom_430_OG [0x1ae]			
2019/10/03	04:08:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/03	05:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	05:16:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	05:16:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/03	05:16:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/03	05:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/03	05:45:00.0	XRT_Custom_430_OG [0x1ae]			
2019/10/03	05:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/03	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/03	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/10/03	06:00:00.0	AOCS_Or-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2019/10/03	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2019/10/03	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2019/10/03	06:00:22.0	XRT_ARS_DIS_443_OG [0x1bb]			
		MDP_XRT_ARS_DIS	1	07-F0	d5

2019/10/03	06:02:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2019/10/03	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/03	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/03	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/03	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2019/10/03	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	42 c0 cc 9c			
2019/10/03	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/10/03	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/10/03	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/10/03	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/10/03	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/03	06:12:56.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2019/10/03	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/10/03	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/03	06:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/03	06:57:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/03	06:57:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/03	06:57:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/10/03	07:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/10/03	07:22:30.0	XRT_Custom_430_OG [0x1ae]							
2019/10/03	07:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/03	08:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/03	08:37:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/03	08:37:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/03	08:37:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/10/03	08:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/10/03	08:59:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/03	09:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/03	10:17:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/03	10:52:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00 00 00 00			