

# XRT Timeline to be uploaded on 2019/10/26

Period: 2019/10/26 11:06:00 - 2019/10/31 11:17:00

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## Normal mode

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XOB #1C0F: CME watch - 4x4 - AEC 2 - Al-poly - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 120s cad												
Term	Pointing (x, y)		Comment									
10/26 11:19:00 - 10/26 17:41:00	Track ( -40.0, -655.6) @ 10/26 11:16:00		# OP start + 10min BP observations									
10/26 18:18:00 - 10/26 19:59:54	Track ( 7.8, -655.4) @ 10/26 18:15:00		BP observations									
<b>PROG= 20 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 120.0sec												
└─ Seqn= 95 1-time(s) 4.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 30 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1C55: Synoptic Q95 2x2 - Al/mesh(512/2048/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(512/5795/1157)												
Term	Pointing (x, y)		Comment									
10/26 18:08:00 - 10/26 18:14:54	Fixed ( 0.0, 0.0)		synoptic, shifted 5.0 min									
10/27 05:53:00 - 10/27 05:59:54	Fixed ( 0.0, 0.0)		synoptic, shifted -10.0 min									
10/27 17:55:00 - 10/27 18:01:54	Fixed ( 0.0, 0.0)		synoptic, shifted -8.0 min									
10/28 06:03:00 - 10/28 06:09:54	Fixed ( 0.0, 0.0)		synoptic									
10/29 06:29:00 - 10/29 06:35:54	Fixed ( 0.0, 0.0)		synoptic, shifted 26.0 min									
<b>PROG= 06 1-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 78 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 91 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 52 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 16.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 23 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1B55: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (2x2,1ms) - Leak (2x2,1ms) - 180s cad (G-band/Leak first)												
Term	Pointing (x, y)		Comment									
10/26 20:03:00 - 10/27 02:59:54	Fixed ( 479.0, 275.0)		CH observations									
<b>PROG= 02 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 35 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 20-time(s) 180.0sec												
└─ Seqn= 8 1-time(s) 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec												
└─ Seqn= 6 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1C4D: HOP349 - 3-filter Synoptics (Al-mesh[128/1024/5795], Al-poly[256/4096/8192], thin-Be[2048/16384/32768] with 512x512 G-band+Leak(1064,1064))												
Term	Pointing (x, y)		Comment									
10/27 03:29:31 - 10/27 05:49:54	Fixed ( 0.0, 0.0)		HOP349									
10/28 03:03:00 - 10/28 05:59:54	Fixed ( 0.0, 0.0)		HOP 349									
<b>PROG= 17 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 300.0sec												
└─ Seqn= 88 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 50</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 56</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 81</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
<b>Subr= 2</b>		<b>6-time(s)</b>			<b>450.0sec</b>							
<b>Seqn= 8</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 6</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 29</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1C64: HOP387 - Al/poly (Thick-Al) - 1x1 - 8192ms - 512FOV - 60s cad**

Term	Pointing (x, y)	Comment
10/27 06:03:00 - 10/27 12:59:54	Track ( -759.3, 186.6) @ 10/27 06:00:00	HOP 387
10/28 06:13:00 - 10/28 12:59:54	Track ( -612.6, 174.4) @ 10/28 06:10:00	HOP 387
10/29 06:39:00 - 10/29 10:33:00	Track ( -430.5, 165.7) @ 10/29 06:36:00	HOP 387
<b>PROG= 01 Inf.-time(s)</b>		
<b>Subr= 1</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 81</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1064, 1048) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1064, 1048) Q=95 0 0 2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 41</b>		<b>213-time(s)</b> <b>60.0sec</b>
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 8.00s Obs 1x1 512x512 (1064, 1048) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1C12: CME watch - 4x4 - AEC 2 - Al-poly - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 180s cad**

Term	Pointing (x, y)	Comment
10/27 13:03:00 - 10/27 17:51:54	Track ( 135.5, -653.6) @ 10/27 13:00:00	BP observations
10/27 18:05:00 - 10/28 02:59:54	Track ( 169.3, -652.8) @ 10/27 18:02:00	BP observations
10/28 13:03:00 - 10/28 17:53:54	Track ( 293.0, -648.6) @ 10/28 13:00:00	BP observations
10/28 18:10:00 - 10/28 19:30:54	Track ( 324.7, -647.2) @ 10/28 18:04:00	BP observations
<b>PROG= 10 Inf.-time(s)</b>		
<b>Subr= 1</b>		<b>20-time(s)</b> <b>180.0sec</b>
<b>Seqn= 95</b>		<b>1-time(s)</b> <b>4.0sec</b>
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 30</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1C56: Synoptic 7 Filter w/ Al-mesh(512/2048/5795), Al-poly(512/5795/11571), Thin-Be(3897/16384/32768) - Thick-Be(65536), Al-poly+Ti-poly(4096/231)**

Term	Pointing (x, y)	Comment
10/28 18:01:00 - 10/28 18:07:00	Fixed ( 0.0, 0.0)	synoptic, shifted -6.0 min
<b>PROG= 14 1-time(s)</b>		
<b>Subr= 1</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 5</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
<b>Seqn= 78</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/Al-mesh	Open/Al-mesh close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 91</b>		<b>1-time(s)</b> <b>2.0sec</b>
Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Seqn= 52</b>		<b>1-time(s)</b> <b>2.0sec</b>
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 23</b>		<b>1-time(s)</b>		<b>4.0sec</b>								
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
<b>Seqn= 46</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 17</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 97</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1C5A: HOP336 1-filter - Al/poly -384x384, 1024ms and 8192s, 60s-cadence, G-band - 384x384 1ms**

Term	Pointing (x, y)	Comment
10/28 19:38:00 - 10/28 23:42:30	Track ( 0.0, 487.0) @ 10/28 19:31:00	HOP 336 N hemisphere
10/28 23:57:08 - 10/29 06:25:54	Track ( 0.0, -683.0) @ 10/28 23:43:00	HOP 336 S hemisphere
<b>PROG= 08 Inf-time(s)</b>		
<b>Subr= 1</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 16</b>		<b>2-time(s)</b> <b>2.0sec</b>
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 90</b>		<b>1-time(s)</b> <b>30.0sec</b>
Open/G-band	Open/G-band	open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
<b>Subr= 3</b>		<b>15-time(s)</b> <b>2.0sec</b>
<b>Seqn= 72</b>		<b>1-time(s)</b> <b>60.0sec</b>
Al-poly/Open	Al-poly/Ti-poly	close Safe Norm 8.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512)**

Term	Pointing (x, y)	Comment
10/26 11:19:00 - 10/26 17:41:00	Track ( -40.0, -655.6) @ 10/26 11:16:00	# OP start + 10min BP observations
10/26 18:18:00 - 10/26 19:59:54	Track ( 7.8, -655.4) @ 10/26 18:15:00	BP observations
10/26 20:03:00 - 10/27 02:59:54	Fixed ( 479.0, 275.0)	CH observations
10/27 03:29:31 - 10/27 05:49:54	Fixed ( 0.0, 0.0)	HOP349
10/27 06:03:00 - 10/27 12:59:54	Track ( -759.3, 186.6) @ 10/27 06:00:00	HOP 387
10/27 13:03:00 - 10/27 17:51:54	Track ( 135.5, -653.6) @ 10/27 13:00:00	BP observations
10/27 18:05:00 - 10/28 02:59:54	Track ( 169.3, -652.8) @ 10/27 18:02:00	BP observations
10/28 03:03:00 - 10/28 05:59:54	Fixed ( 0.0, 0.0)	HOP 349
10/28 06:13:00 - 10/28 12:59:54	Track ( -612.6, 174.4) @ 10/28 06:10:00	HOP 387
10/28 13:03:00 - 10/28 17:53:54	Track ( 293.0, -648.6) @ 10/28 13:00:00	BP observations
10/28 18:10:06 - 10/28 19:30:54	Track ( 324.7, -647.2) @ 10/28 18:04:00	BP observations
10/28 19:38:00 - 10/28 23:42:30	Track ( 0.0, 487.0) @ 10/28 19:31:00	HOP 336 N hemisphere
10/28 23:57:08 - 10/29 06:25:54	Track ( 0.0, -683.0) @ 10/28 23:43:00	HOP 336 S hemisphere
10/29 06:39:00 - 10/29 10:33:00	Track ( -430.5, 165.7) @ 10/29 06:36:00	HOP 387
<b>PROG= 13 30-time(s)</b>		
<b>Subr= 1</b>		<b>20-time(s)</b> <b>2.0sec</b>
<b>Seqn= 11</b>		<b>1-time(s)</b> <b>2.0sec</b>
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn=100</b>		<b>1-time(s)</b> <b>10.0sec</b>
thin-Be/Open	med-Be/Open	close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 10</b>		<b>1-time(s)</b> <b>2.0sec</b>
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 11</b>		<b>1-time(s)</b> <b>2.0sec</b>
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn= 87</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/G-band	Open/G-band	open Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

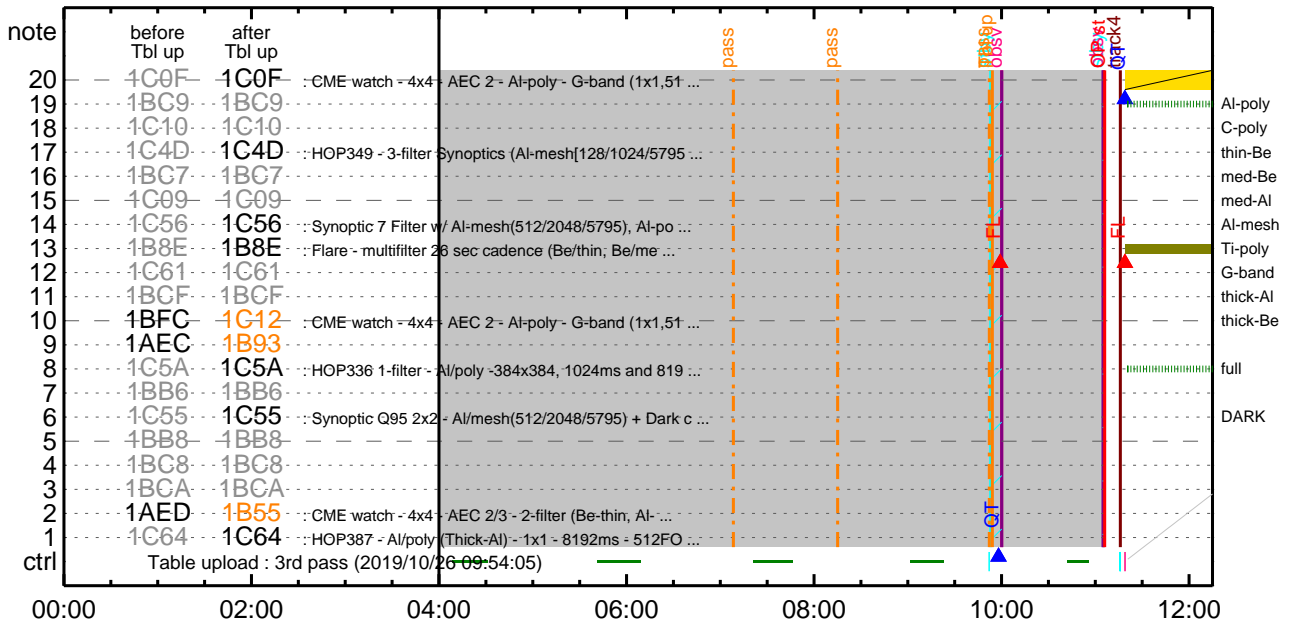
\* \* \* \* \*

**Flare Detection**

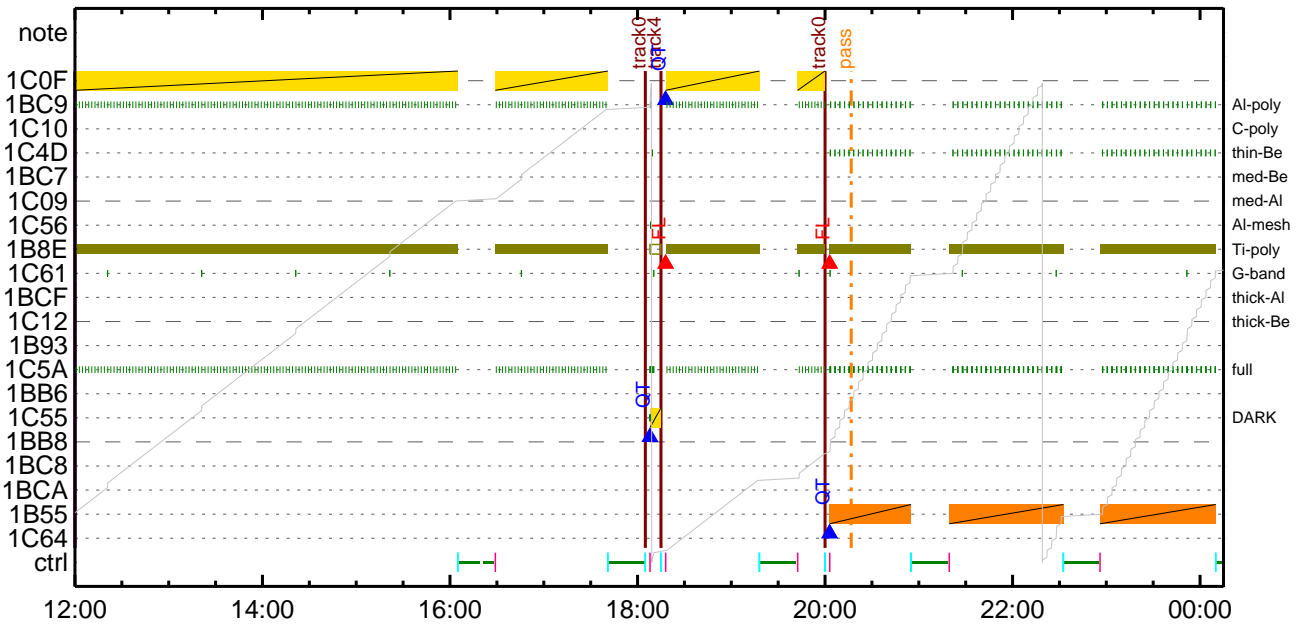
\* \* \* \* \*

FLD Patrol											
Term	Pointing (x, y)						Comment				
10/26 18:15:18 - 10/27 05:50:18	Track (	7.8,	-655.4)	@ 10/26 18:15:00			BP observations				
10/27 06:00:18 - 10/27 17:52:18	Track (	-759.3,	186.6)	@ 10/27 06:00:00			HOP 387				
10/27 18:02:18 - 10/28 06:00:18	Track (	169.3,	-652.8)	@ 10/27 18:02:00			BP observations				
10/28 06:10:18 - 10/28 17:58:18	Track (	-612.6,	174.4)	@ 10/28 06:10:00			HOP 387				
10/28 18:07:24 - 10/29 06:26:18	Track (	324.7,	-647.2)	@ 10/28 18:04:00			BP observations				
10/29 06:36:18 - 10/31 11:17:00	Track (	-430.5,	165.7)	@ 10/29 06:36:00			HOP 387				
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

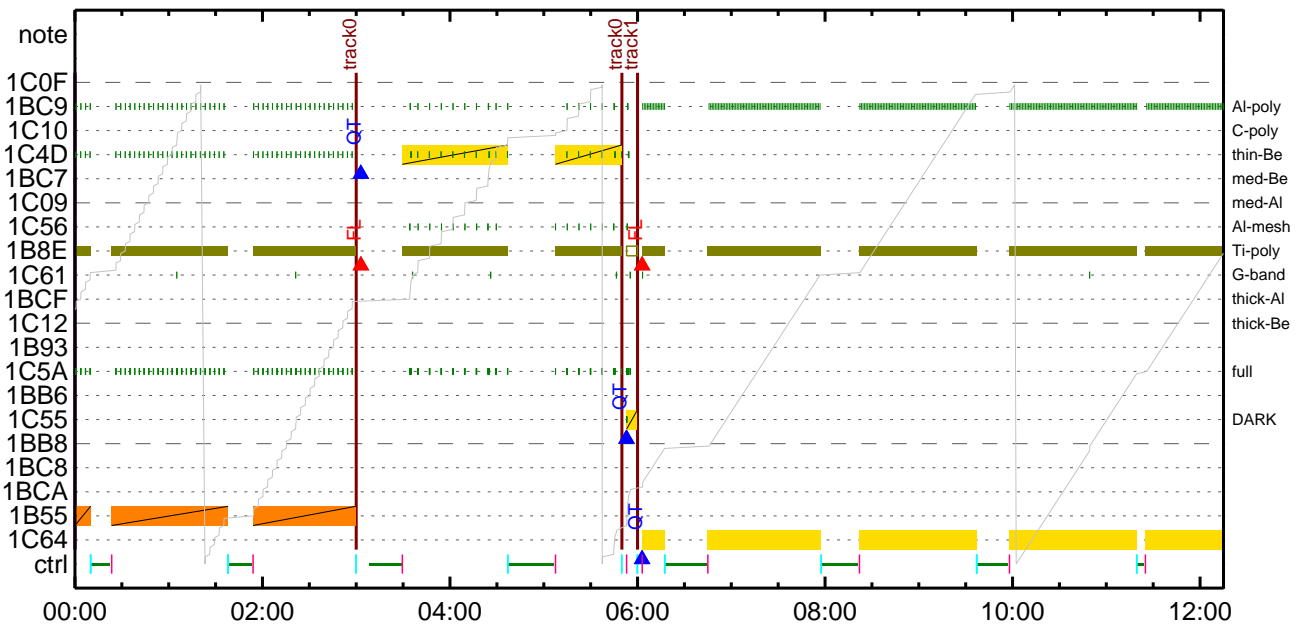
### CMDI #0558 2019/10/26



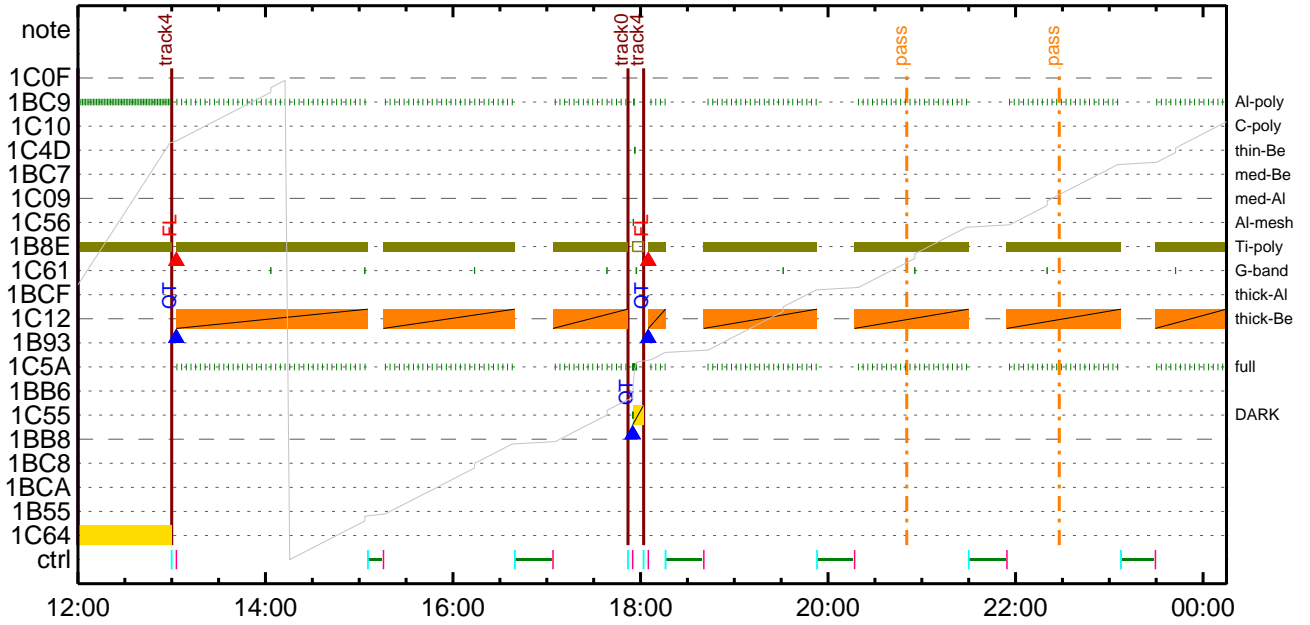
### CMDI #0558 2019/10/26



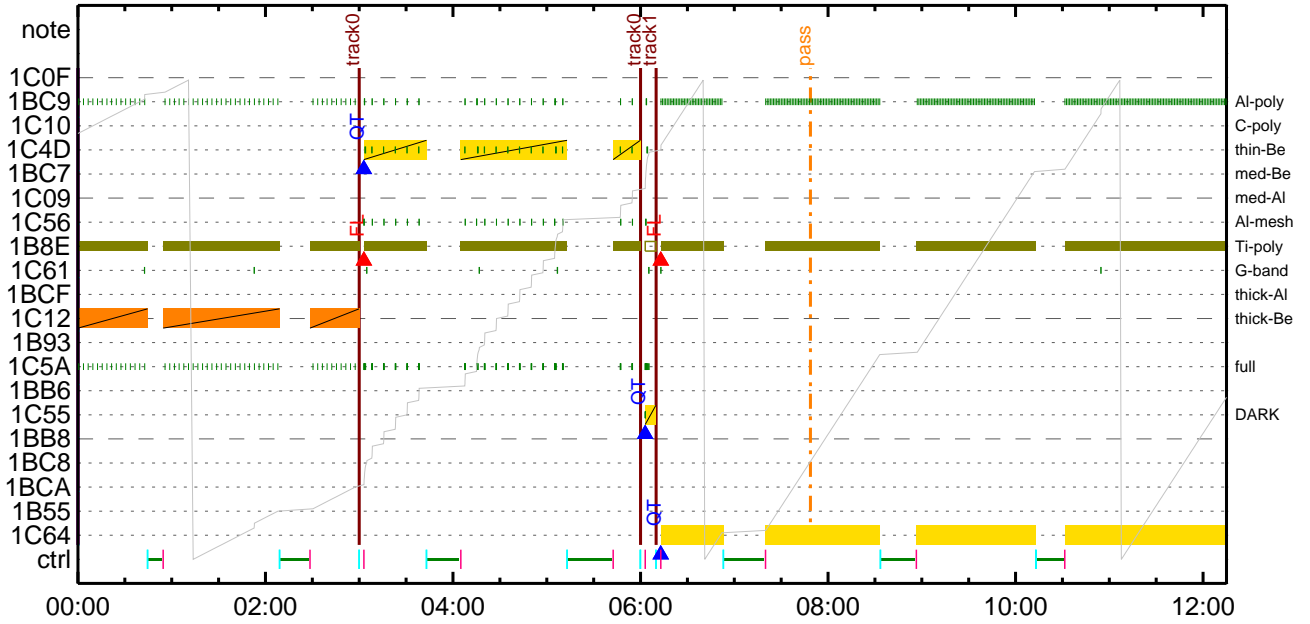
### CMDI #0558 2019/10/27



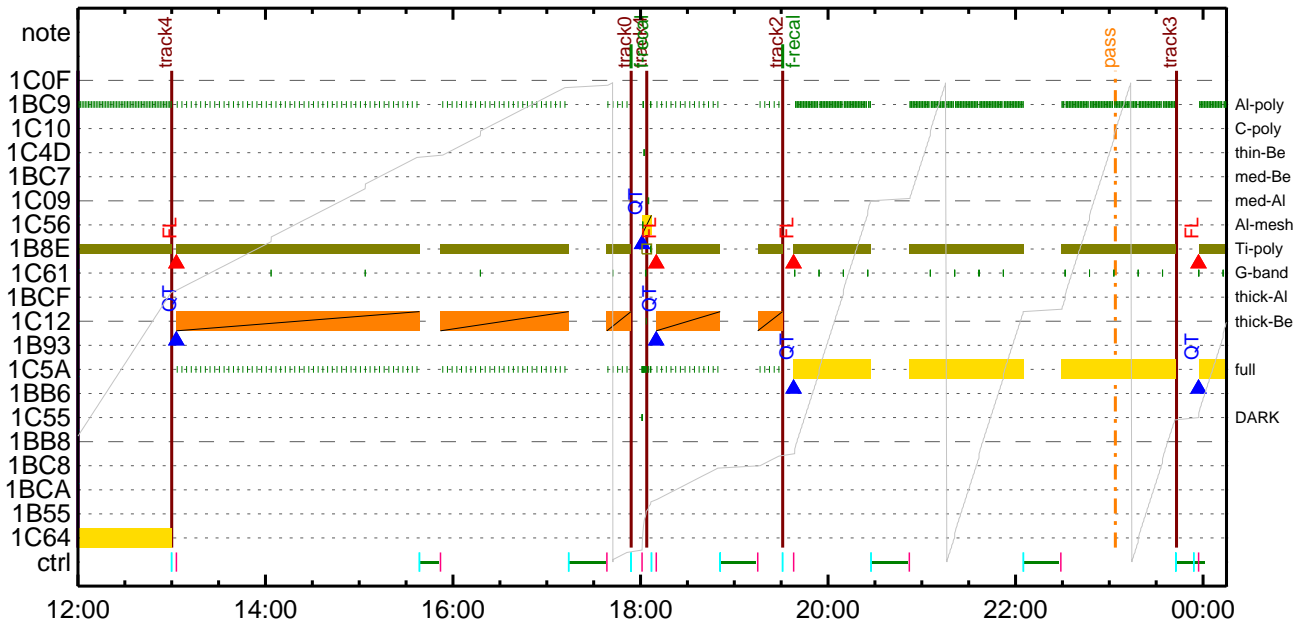
### CMDI #0558 2019/10/27



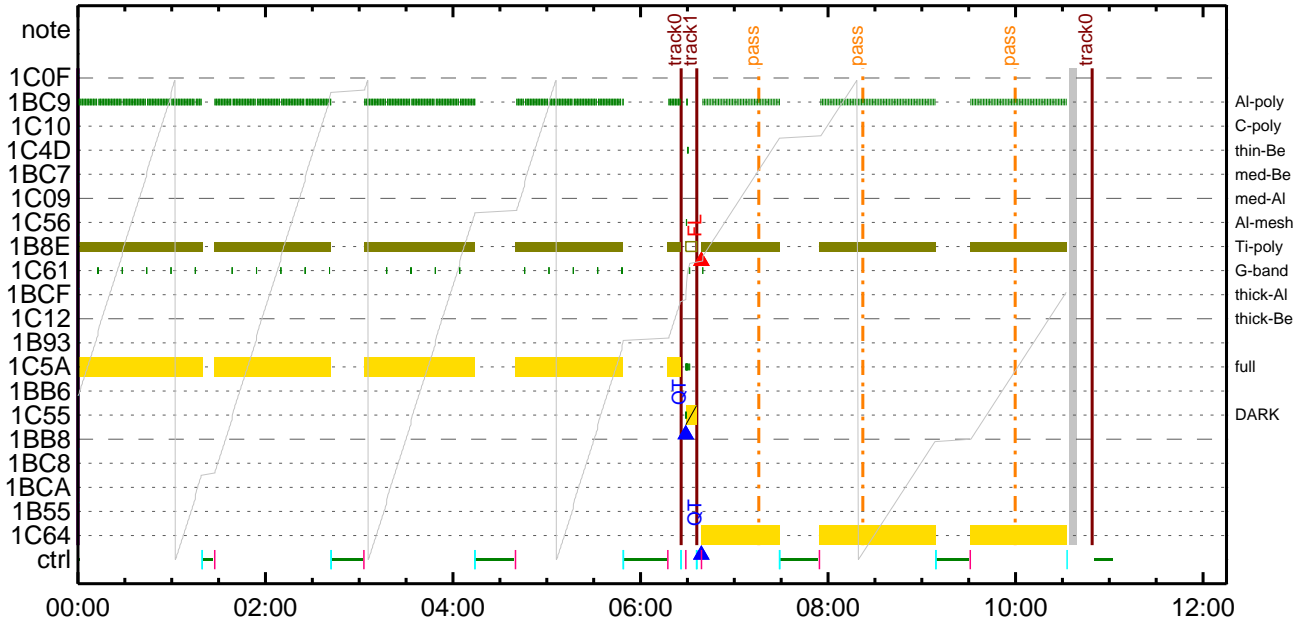
### CMDI #0558 2019/10/28



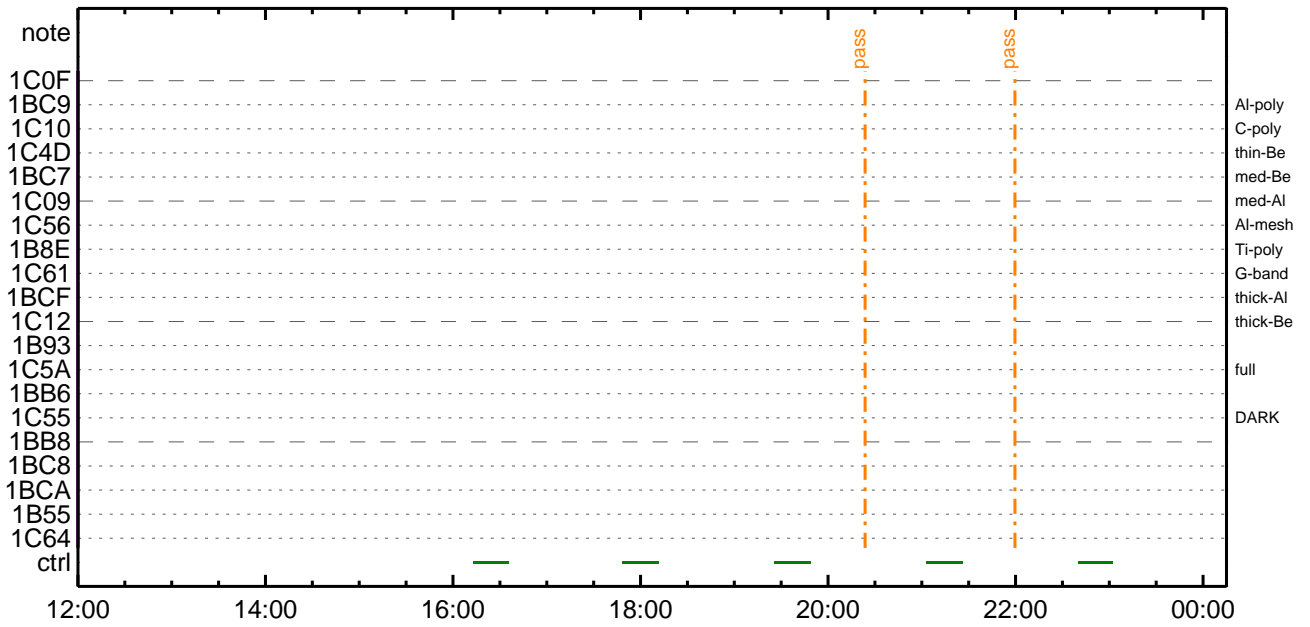
### CMDI #0558 2019/10/28



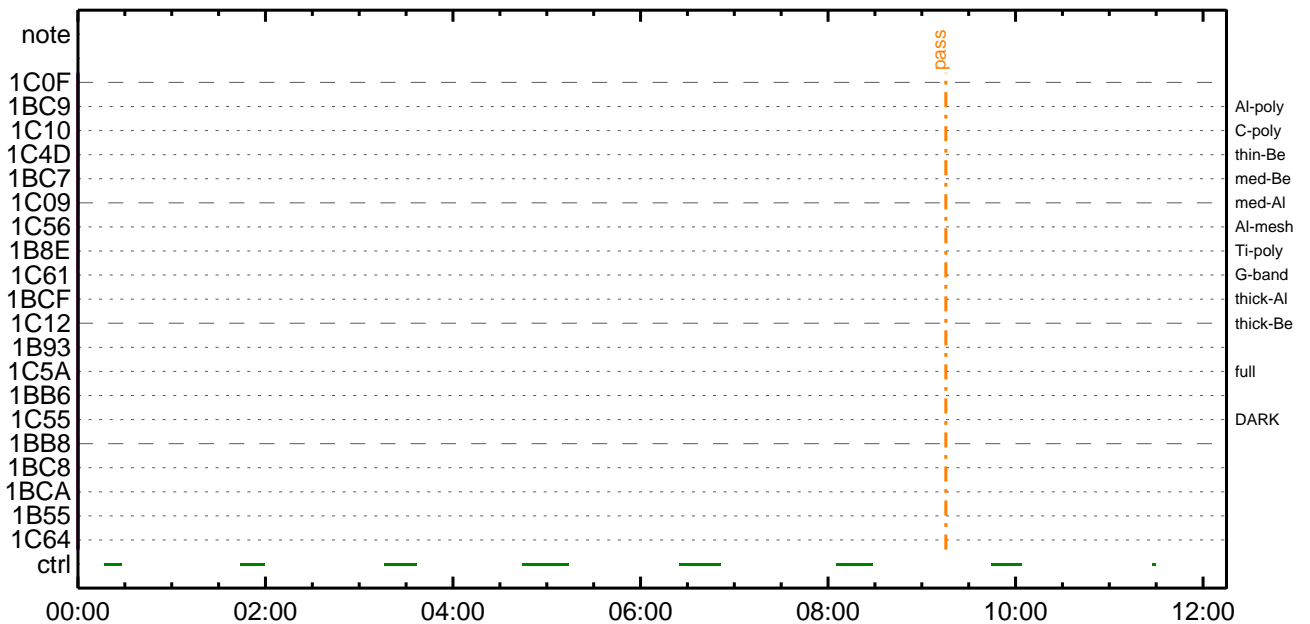
### CMDI #0558 2019/10/29



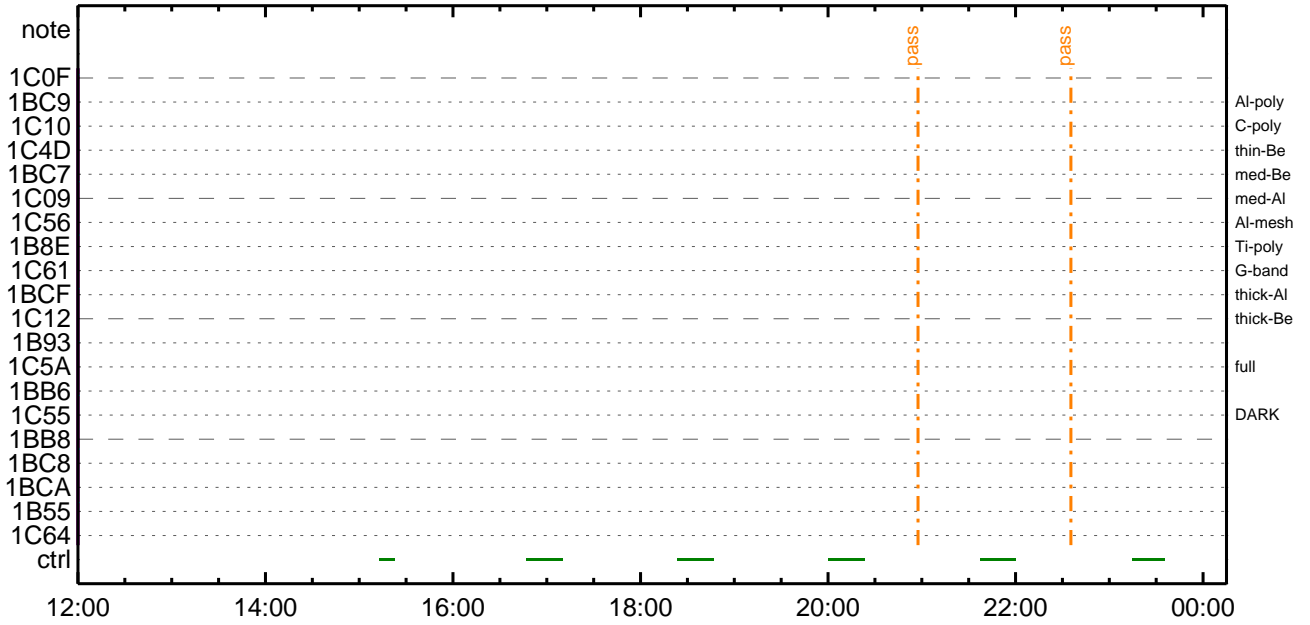
### CMDI #0558 2019/10/29



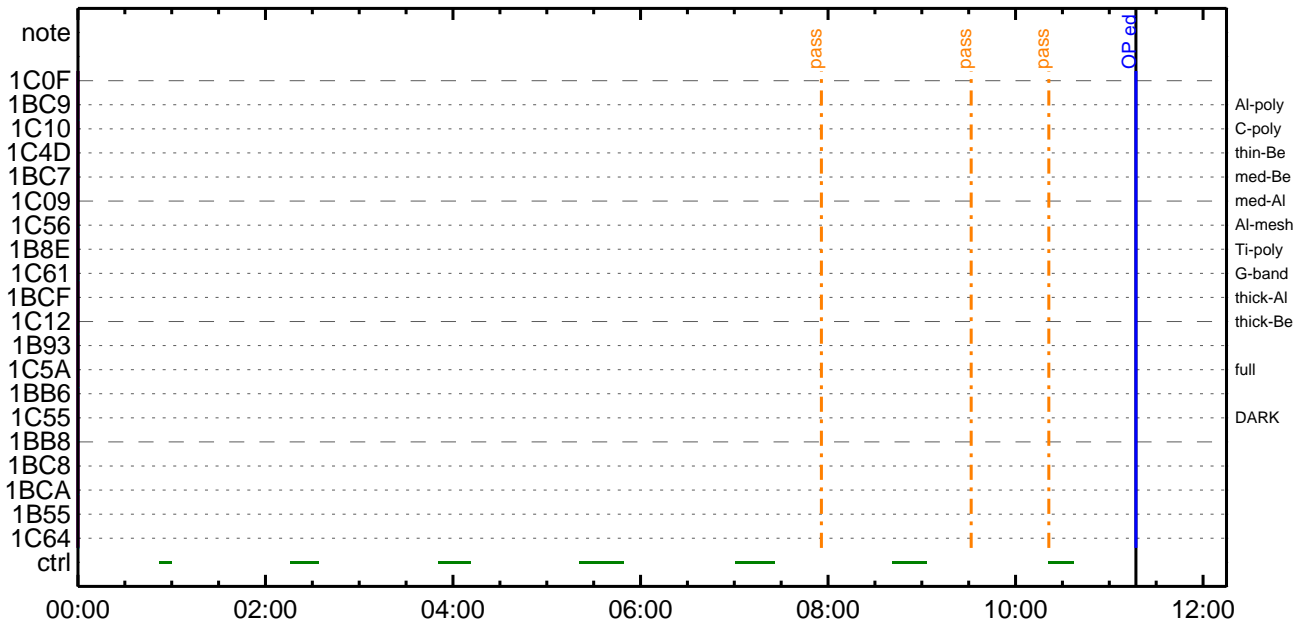
### CMDI #0558 2019/10/30



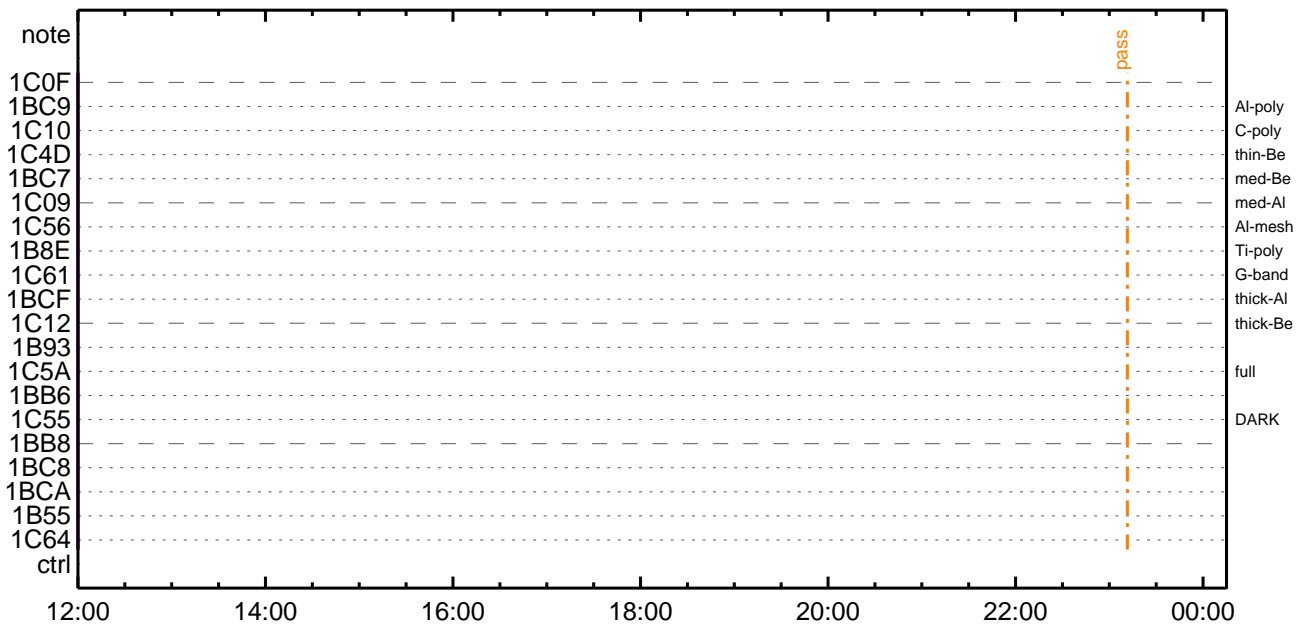
CMDI #0558 2019/10/30



CMDI #0558 2019/10/31



CMDI #0558 2019/10/31





(a) Spacecraft Operation Procedure (real-commands)

```
main-412 2019-10-26 13:59:40 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÁò•µ°È»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÍÉ;ÈòÈ¼°ÇÓò•ò¿¼í¹çòÍ;çÁ®, ùò¹òÈòòòÇÁ+¿®ò•òÈòòòòòÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-412:OP
0020 ( )
0021 . S. OG og-412:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYóY×¼ªª Í»òð³ ÍÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòÍ¼È¹ç•è² ÍOKòð³ ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYóY×¼ªª Í»òð³ ÍÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòÍ¼È¹ç•è² ÍOKòð³ ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYóY×¼ªª Í»òð³ ÍÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPòÍ¼È¹ç•è² ÍOKòð³ ÍÇ§
0081 C.
0082 . C. ***** òÈ²¼òÍ¼Á´ ¶Á°òÈÈ-òòÁ+¿® (¼áµ-YÁYóY×¼½çòðÁÓÃæç¼ªª ò²ò¼¼í¹çòçòâ) *****
0083 C. DHUYâ;¼YÉ;È¼Y¼; Yí;¼YÉ;ÈòòÍáò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADò-Á+¿®NGòÍ¼í¹ç;ç°È²¼òÍTI-CMDÁ+¿®òÍ¼Á¹Ôò•òÈòòòòòÈ;f
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0096 C.                0p0z;çSET0EDUMP0İÆ±°iYÑY¹0Ç¹00|0³0E;f
0097 C.
0098 . C. TIY³YF¥6YÉ00dÅDİç(UT)
0099 +. TI 2019-10-26 11:01:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2019-10-26 11:01:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2019-10-26 11:01:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2019-10-26 11:05:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.                °Ê²¼0İÄê%îÍÑ0İYÁY$YÄY-¹àÛ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]              EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]              EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]               EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİî°èYÄYÓYx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC                (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]              EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]              EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]               EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]              EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC                (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                 EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]               EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]              EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]                 EQ        EXEC
0140 C.
0141 . C. YÄYÓYx½ªİ»0d³İÇ$
0142 C.                çç[HK1_DMP_CHK_FLG]                 EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İÊ¹Ç•ê²İOK0d³İÇ$
0145 C.
0146 . C. DHUYâ;¼YÉ;Ê¼Y½,¥i;¼YÈ;Ë0dİã0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC                (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                 EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]               EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]              EQ        4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2019-10-26 11:05:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC                (21 02)
0163 +. TI 2019-10-26 11:05:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC                (22)
0166 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2019-10-26 11:05:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC                (c3)
0176 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 . C. ***** MDP ´úÄî0İ»ö¼Y0EÄ00¹0E DCBC•x²è *****
0181 C. (¼á°İYÓYÄYÉYF¥YÄYÇYÈ0E¼00¼Ä»Ü0¹0è)
0182 . S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 . C. ***** YD¥¹•İ Daily±çİÑ0E'00¹0E DCBC•x²è *****
0187 . S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 . C. ;ãLOS¥ÁY$YÄY-¼Ä»Ü;ä
0192 C.
0193 . C. ***** LOS *****

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 06 85 83 08 08)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 07 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 08 80 80 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 09 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 0a 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0b 85 83 06 06)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 01)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 0d)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2019-10-26 11:05:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP `úÃîï»ö%ÝðËÄð¹ñèDCBC•x²è *****
0169 C. (%á°îÿÓYÁYÈYÏYÉYáYçYèèE%¼ø¼Á»Û¹ñè)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥D¥¹•İ Daily±¿İÑñÈ´Øñ¹ñèDCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Á»Û;ã
0180 C.
0181 . C. ***** LOS *****
0182 C.

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Oct 26, 19 13:59

XRT\_OGLIST\_0558.chk

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\*\*\* OP Sequence for XRT \*\*\*

2019/10/26	11:15:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/26	11:15:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/26	11:15:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/10/26	11:16:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2019/10/26	11:16:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/10/26	11:16:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/10/26	11:16:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/10/26	11:16:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/10/26	11:16:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/10/26	11:18:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2019/10/26	11:18:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/10/26	11:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/10/26	16:05:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/26	16:05:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/26	16:05:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/10/26	16:05:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/10/26	16:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/10/26	16:28:00.0	XRT_Custom_430_OG [0x1ae]					
2019/10/26	16:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/10/26	17:41:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/26	17:41:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/26	17:41:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/10/26	17:41:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2019/10/26	17:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2019/10/26	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/26	18:04:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/26	18:04:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/10/26	18:05:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2019/10/26	18:05:18.0	XRT_FLD_DIS_409_OG [0x199]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2019/10/26	18:05:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2019/10/26	18:05:22.0	XRT_ARS_DIS_443_OG [0x1bb]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/10/26	18:07:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2019/10/26	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/10/26	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/26	18:14:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2019/10/26	18:14:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2019/10/26	18:15:00.5	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2019/10/26	18:15:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2019/10/26	18:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2019/10/26	18:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2019/10/26	18:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2019/10/26	18:15:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2019/10/26	18:17:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2019/10/26	18:17:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2019/10/26	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2019/10/26	19:18:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		



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2019/10/26	19:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/26	19:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/26	19:18:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/10/26	19:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/10/26	19:41:30.0	XRT_Custom_430_OG [0x1ae]					
2019/10/26	19:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/26	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/26	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/26	19:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/10/26	20:00:00.0	AOCs_Orе-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 e7 8e d5 69	
2019/10/26	20:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/10/26	20:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/10/26	20:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/10/26	20:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/10/26	20:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/26	20:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2019/10/26	20:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2019/10/26	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/26	20:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/26	20:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/26	20:55:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/26	20:55:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/10/26	20:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/10/26	21:18:30.0	XRT_Custom_430_OG [0x1ae]					
2019/10/26	21:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/26	22:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/26	22:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/26	22:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/26	22:32:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/10/26	22:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/10/26	22:55:00.0	XRT_Custom_430_OG [0x1ae]					
2019/10/26	22:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/27	00:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/27	00:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/27	00:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/27	00:10:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/10/27	00:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/10/27	00:22:30.0	XRT_Custom_430_OG [0x1ae]					
2019/10/27	00:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/27	01:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/27	01:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/27	01:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/27	01:38:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/10/27	01:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/10/27	01:53:00.0	XRT_Custom_430_OG [0x1ae]					
2019/10/27	01:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/27	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/27	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/27	02:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	

2019/10/27	03:00:00.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2019/10/27	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/10/27	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/10/27	03:00:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/10/27	03:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/10/27	03:00:26.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/27	03:02:56.0	XRT_QT_PROG_SET_404_OG [0x194] MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2019/10/27	03:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/10/27	03:29:31.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/27	04:37:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/27	04:37:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/27	04:37:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/27	04:37:06.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/10/27	04:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/10/27	05:06:30.5	XRT_Custom_430_OG [0x1ae]							
2019/10/27	05:07:30.5	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/27	05:49:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/27	05:49:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/27	05:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2019/10/27	05:50:00.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2019/10/27	05:50:18.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9				
2019/10/27	05:50:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2019/10/27	05:50:22.0	XRT_ARS_DIS_443_OG [0x1bb] MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/10/27	05:52:58.0	XRT_QT_PROG_SET_432_OG [0x1b0] MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2019/10/27	05:53:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/27	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/27	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/27	05:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2019/10/27	06:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	01	00	00	00	00
2019/10/27	06:00:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/10/27	06:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/10/27	06:00:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/10/27	06:00:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/10/27	06:00:26.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/27	06:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af] MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2019/10/27	06:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2019/10/27	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/27	06:17:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/27	06:17:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/27	06:17:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/27	06:17:36.0	XRT_PREFLR_STRT_435_OG [0x1b3] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/10/27	06:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/10/27	06:44:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/27	06:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/10/27	07:57:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/27	07:57:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/10/27	07:57:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2019/10/27	07:57:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							

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2019/10/27	08:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/27	08:21:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/27	08:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/27	09:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	09:37:02.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	09:37:04.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/27	09:37:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/27	09:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/27	09:57:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/27	09:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/27	11:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	11:19:32.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	11:19:34.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/27	11:19:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/27	11:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/27	11:24:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/27	11:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/27	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	12:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2019/10/27	13:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
			AOCU_NM	5	02-76	04 00 00 00 00			
2019/10/27	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/10/27	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/10/27	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/10/27	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/10/27	13:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/27	13:02:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2019/10/27	13:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2019/10/27	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/27	15:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	15:05:32.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	15:05:34.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/27	15:05:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/27	15:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/27	15:14:30.0	XRT_Custom_430_OG [0x1ae]							
2019/10/27	15:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/27	16:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	16:39:32.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	16:39:34.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/27	16:39:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/27	16:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/27	17:03:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/27	17:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/27	17:51:54.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	17:51:56.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/27	17:51:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2019/10/27	17:52:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
			AOCU_NM	5	02-76	00 00 00 00 00			
2019/10/27	17:52:18.0	XRT_FLD_DIS_409_OG [0x199]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2019/10/27	17:52:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							

2019/10/27	17:52:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2019/10/27	17:54:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/10/27	17:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2019/10/27	18:01:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/27	18:01:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/27	18:01:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/27	18:02:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/10/27	18:02:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00
2019/10/27	18:02:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2019/10/27	18:02:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/10/27	18:02:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2019/10/27	18:02:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/10/27	18:04:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/27	18:04:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2019/10/27	18:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/10/27	18:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/27	18:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/27	18:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/27	18:16:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/27	18:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/27	18:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/27	18:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2019/10/27	19:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/27	19:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/27	19:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/27	19:53:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/27	19:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/27	20:16:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/27	20:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2019/10/27	21:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/27	21:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/27	21:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/27	21:30:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/27	21:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/27	21:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/27	21:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2019/10/27	23:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/27	23:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/27	23:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/27	23:07:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/27	23:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/27	23:28:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/27	23:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2019/10/28	00:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/28	00:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	00:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	00:44:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/28	00:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/28			MDP_XRT_PREFLR_STOP	1	07-F0	e9

2019/10/28	00:53:30.0	XRT_Custom_430_OG [0x1ae]			
2019/10/28	00:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/28	02:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	02:09:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	02:09:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/28	02:09:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/28	02:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/28	02:27:30.0	XRT_Custom_430_OG [0x1ae]			
2019/10/28	02:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/28	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	02:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/10/28	03:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2019/10/28	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2019/10/28	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/10/28	03:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2019/10/28	03:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2019/10/28	03:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/28	03:02:56.0	XRT_QT_PROG_SET_404_OG [0x194]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2019/10/28	03:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/10/28	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/28	03:43:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	03:43:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	03:43:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/28	03:43:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/28	03:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/28	04:04:00.5	XRT_Custom_430_OG [0x1ae]			
2019/10/28	04:05:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/28	05:13:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	05:13:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	05:13:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2019/10/28	05:13:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/10/28	05:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/10/28	05:41:30.0	XRT_Custom_430_OG [0x1ae]			
2019/10/28	05:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/28	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/10/28	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2019/10/28	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2019/10/28	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2019/10/28	06:00:22.0	XRT_ARS_DIS_443_OG [0x1bb]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2019/10/28	06:02:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2019/10/28	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/10/28	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/10/28	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2019/10/28	06:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	01 00 00 00 00
2019/10/28	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			

2019/10/28	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/10/28	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/10/28	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/10/28	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/28	06:12:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2019/10/28	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/10/28	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/28	06:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/28	06:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/28	06:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/28	06:53:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/10/28	06:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/10/28	07:19:00.0	XRT_Custom_430_OG [0x1ae]					
2019/10/28	07:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/28	08:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/28	08:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/28	08:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/28	08:33:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/10/28	08:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/10/28	08:55:30.0	XRT_Custom_430_OG [0x1ae]					
2019/10/28	08:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/28	10:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/28	10:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/28	10:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/28	10:13:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/10/28	10:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/10/28	10:30:30.0	XRT_Custom_430_OG [0x1ae]					
2019/10/28	10:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/28	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/28	12:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/28	12:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2019/10/28	13:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00	00
2019/10/28	13:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/10/28	13:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/10/28	13:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/10/28	13:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/10/28	13:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/28	13:02:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2019/10/28	13:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/10/28	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/28	15:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/28	15:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/10/28	15:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/10/28	15:38:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/10/28	15:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/10/28	15:51:00.0	XRT_Custom_430_OG [0x1ae]					
2019/10/28	15:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/10/28	17:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	



2019/10/28	20:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/28	20:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/28	20:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/28	20:27:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/28	20:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/28	20:51:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/28	20:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/28	22:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/28	22:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/28	22:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/28	22:05:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/28	22:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/28	22:28:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/28	22:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/28	23:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/28	23:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/28	23:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/28	23:42:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/28	23:43:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	03 00 00 00 00			
2019/10/28	23:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/28	23:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/28	23:54:04.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/28	23:54:06.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2019/10/28	23:54:26.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/10/28	23:54:28.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/10/28	23:54:30.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/10/28	23:54:32.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/10/28	23:54:34.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/28	23:57:04.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2019/10/28	23:57:06.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2019/10/28	23:57:08.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/29	01:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	01:19:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	01:19:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/29	01:19:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/29	01:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/29	01:26:30.0	XRT_Custom_430_OG [0x1ae]							
2019/10/29	01:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/29	02:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	02:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	02:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/29	02:42:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/29	02:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/29	03:02:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/29	03:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/29	04:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	04:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	04:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			



2019/10/29	04:14:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/29	04:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/29	04:39:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/29	04:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/29	05:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	05:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	05:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/29	05:49:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/29	05:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/29	06:16:30.0	XRT_Custom_430_OG [0x1ae]							
2019/10/29	06:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/29	06:25:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	06:25:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	06:25:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2019/10/29	06:26:00.5	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2019/10/29	06:26:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2019/10/29	06:26:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2019/10/29	06:26:22.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/10/29	06:28:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06			
2019/10/29	06:29:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/29	06:35:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	06:35:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	06:35:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2019/10/29	06:36:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	01 00 00 00 00			
2019/10/29	06:36:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/10/29	06:36:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/10/29	06:36:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/10/29	06:36:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/10/29	06:36:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/29	06:38:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01			
2019/10/29	06:38:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2019/10/29	06:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/29	07:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	07:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	07:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/29	07:29:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/29	07:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/29	07:53:30.0	XRT_Custom_430_OG [0x1ae]							
2019/10/29	07:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/29	09:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	09:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	09:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2019/10/29	09:09:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/10/29	09:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/10/29	09:30:00.0	XRT_Custom_430_OG [0x1ae]							
2019/10/29	09:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/10/29	10:33:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/10/29	10:49:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			