

XRT Timeline to be uploaded on 2019/11/14

Period: 2019/11/14 10:47:00 - 2019/11/19 10:59:00

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Normal mode

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XOB #1C05: HOP173 Prominence/Filament Al-mesh (4sec) and Al-poly (8sec), 1x1, 512x512 at 1064x1048, fixed exposure, Q98, 300s cad

Term	Pointing (x, y)	Comment
11/14 11:00:00 - 11/14 18:01:24	Fixed (660.0, 630.0)	# OP start + 10min + HOP 269
PROG= 05 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 4 2-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1064, 1048)	DPCM 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1064, 1048)	DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 512x512 (1064, 1048)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 80 30-time(s) 300.0sec		
Open/Al-mesh	Open/Al-mesh close Safe Norm 4.00s Obs 1x1 512x512 (1064, 1048)	Q=98 0 0 2.0sec
Al-poly/Open	Al-poly/Ti-poly close Safe Norm 8.00s Obs 1x1 512x512 (1064, 1048)	Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1C6B: Synoptic Q95 2x2 - Al/mesh(64/512/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(128/1024/4096) +

Term	Pointing (x, y)	Comment
11/14 18:04:30 - 11/14 18:11:24	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min
11/15 18:24:00 - 11/15 18:30:54	Fixed (0.0, 0.0)	synoptic, shifted 21.0 min
11/16 06:03:32 - 11/16 06:09:54	Fixed (0.0, 0.0)	HOP 349 + synoptic
PROG= 18 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024)	DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024)	DPCM 0 0 2.0sec
└─ Seqn= 36 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 85 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 33 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 23 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1C20: AR (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 120s

Term	Pointing (x, y)	Comment
11/14 18:14:30 - 11/15 01:59:54	Track (-683.2, -457.8) @ 11/14 18:11:30	AR 12752 obs.
11/15 05:44:30 - 11/15 17:57:30	Track (-615.0, -460.8) @ 11/15 05:41:30	AR 12752 obs.
PROG= 19 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Seqn= 71 3-time(s) 2.0sec		
Open/thick-Al	Open/thick-Be close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048)	Q=98 3 0 2.0sec
└─ Subr= 2 60-time(s) 120.0sec		
└─ Seqn= 94 1-time(s) 40.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048)	Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048)	Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048)	Q=95 3 0 2.0sec
└─ Seqn= 58 1-time(s) 40.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 1 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 1 2.0sec
└─ Seqn= 48 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 2 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 2 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 71		3-time(s)	2.0sec									
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	3	0	2.0sec
Subr= 2		30-time(s)	120.0sec									
Seqn= 15		1-time(s)	40.0sec									
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Seqn= 58		1-time(s)	40.0sec									
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Seqn= 48		1-time(s)	2.0sec									
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1B8E: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
11/14 11:00:00 - 11/14 18:01:24	Fixed (660.0, 630.0)	# OP start + 10min + HOP 269
11/14 18:14:30 - 11/15 01:59:54	Track (-683.2, -457.8) @ 11/14 18:11:30	AR 12752 obs.
11/15 02:03:00 - 11/15 05:30:00	Fixed (0.0, 0.0)	HOP 349 + synoptic, shifted -28.5 min
11/15 05:44:30 - 11/15 17:57:30	Track (-615.0, -460.8) @ 11/15 05:41:30	AR 12752 obs.
11/15 18:34:00 - 11/16 01:52:30	Track (-530.7, -463.6) @ 11/15 18:31:00	AR 12752 obs.
11/16 02:10:30 - 11/16 06:03:00	Fixed (0.0, 0.0)	HOP 349 + synoptic
11/16 06:13:00 - 11/16 09:54:00	Track (-447.5, -465.5) @ 11/16 06:10:00	AR 12752 obs.

PROG= 13		30-time(s)										
Subr= 1		20-time(s)	2.0sec									
Seqn= 11		1-time(s)	2.0sec									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn=100		1-time(s)	10.0sec									
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 10		1-time(s)	2.0sec									
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)	2.0sec									
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

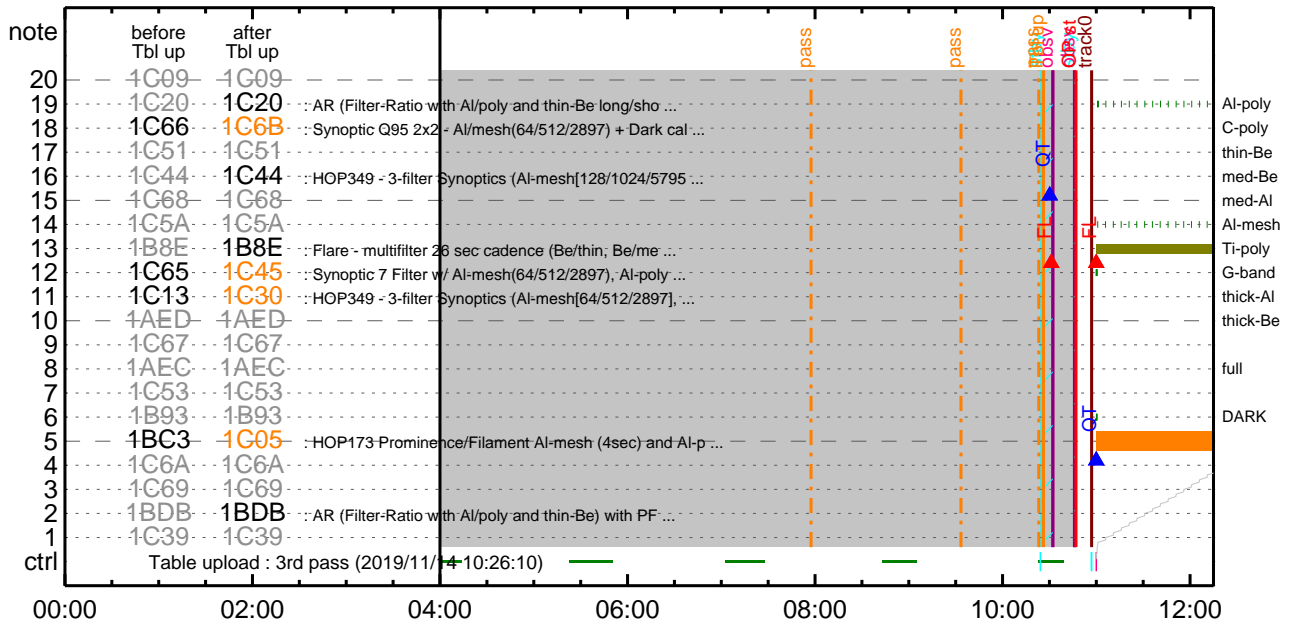
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Flare Detection

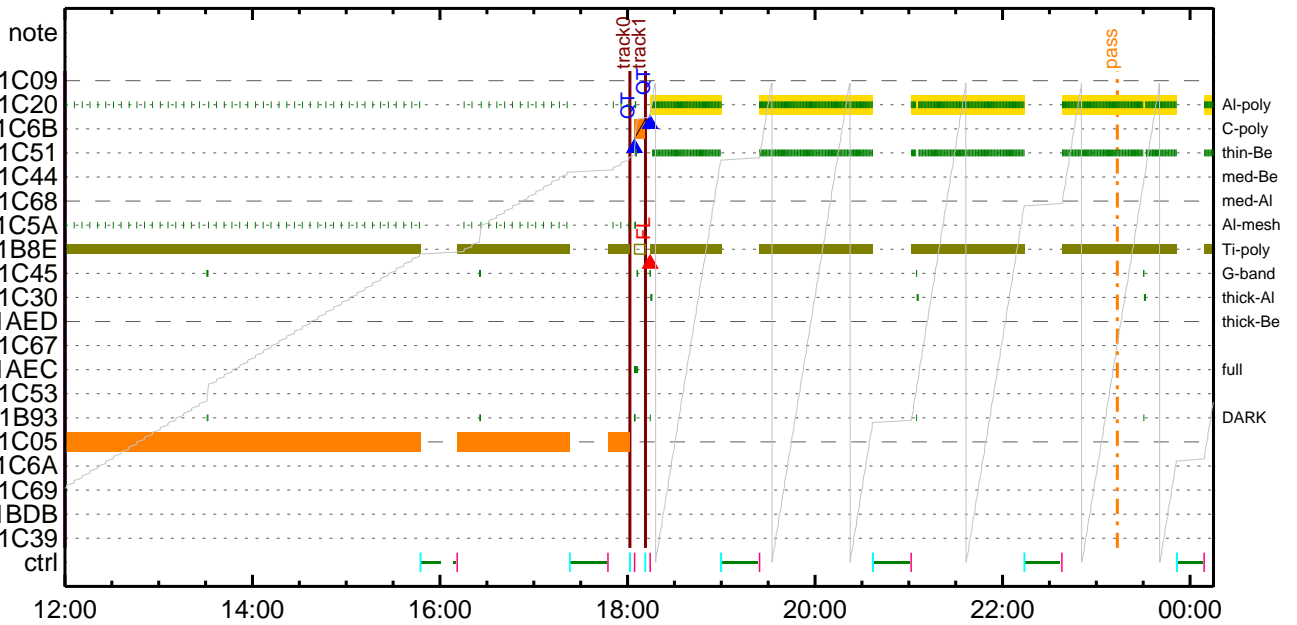
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FLD Patrol												
Term	Pointing (x, y)	Comment										
11/14 18:11:48 - 11/15 05:30:24	Track (-683.2, -457.8) @ 11/14 18:11:30	AR 12752 obs.										
11/15 05:41:48 - 11/15 18:21:18	Track (-615.0, -460.8) @ 11/15 05:41:30	AR 12752 obs.										
11/15 18:31:18 - 11/16 06:03:24	Track (-530.7, -463.6) @ 11/15 18:31:00	AR 12752 obs.										
11/16 06:10:18 - 11/19 10:59:00	Track (-447.5, -465.5) @ 11/16 06:10:00	AR 12752 obs.										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

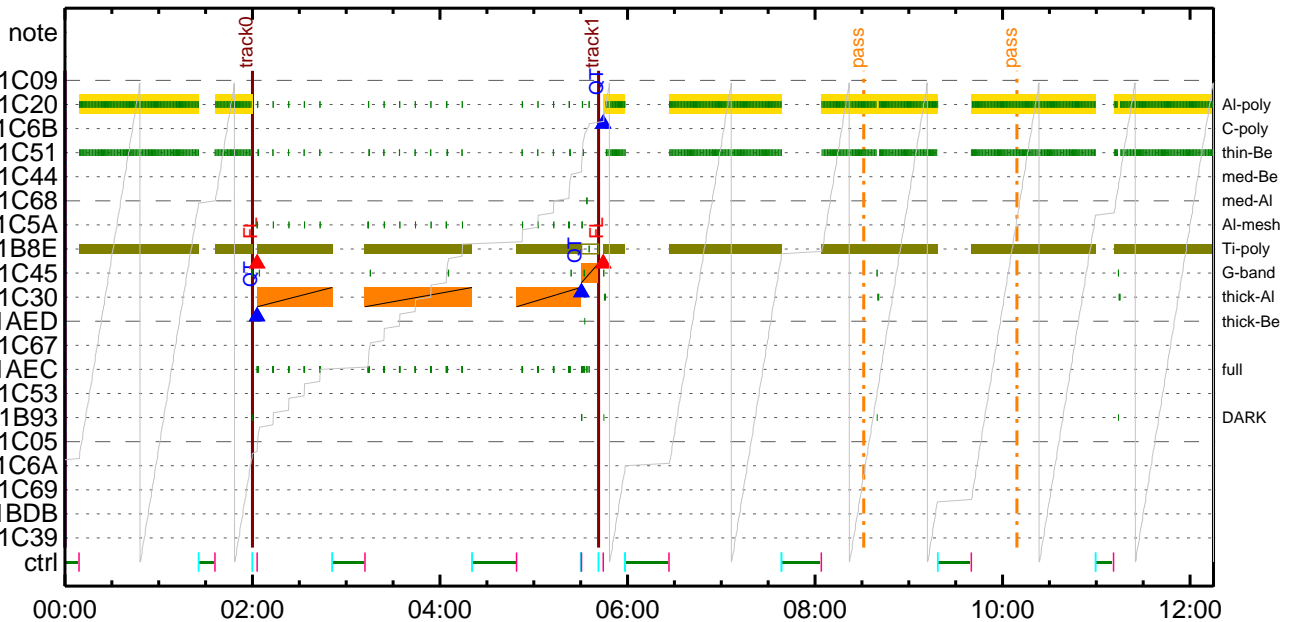
CMDI #0592 2019/11/14



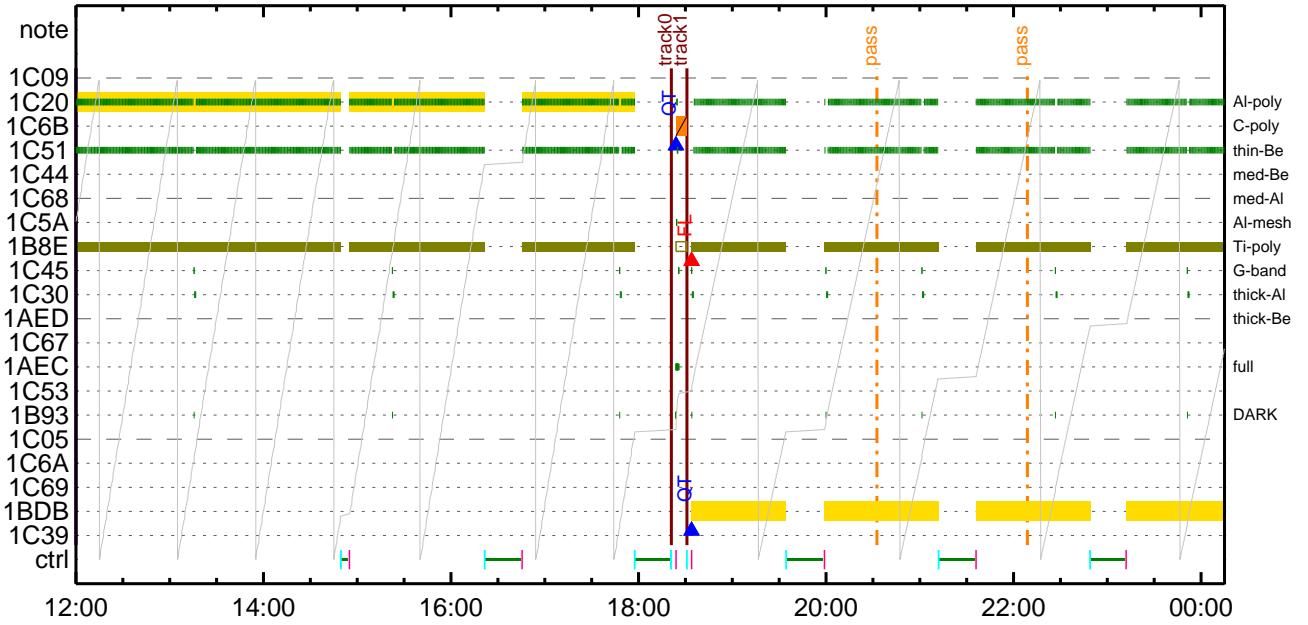
CMDI #0592 2019/11/14



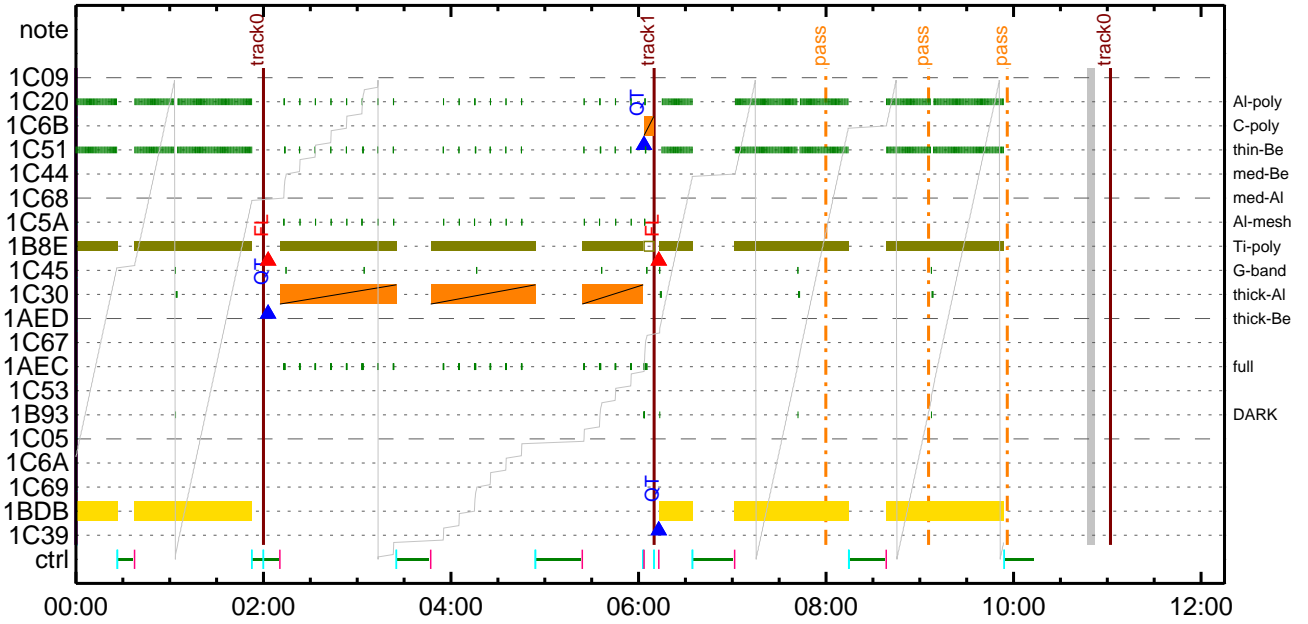
CMDI #0592 2019/11/15



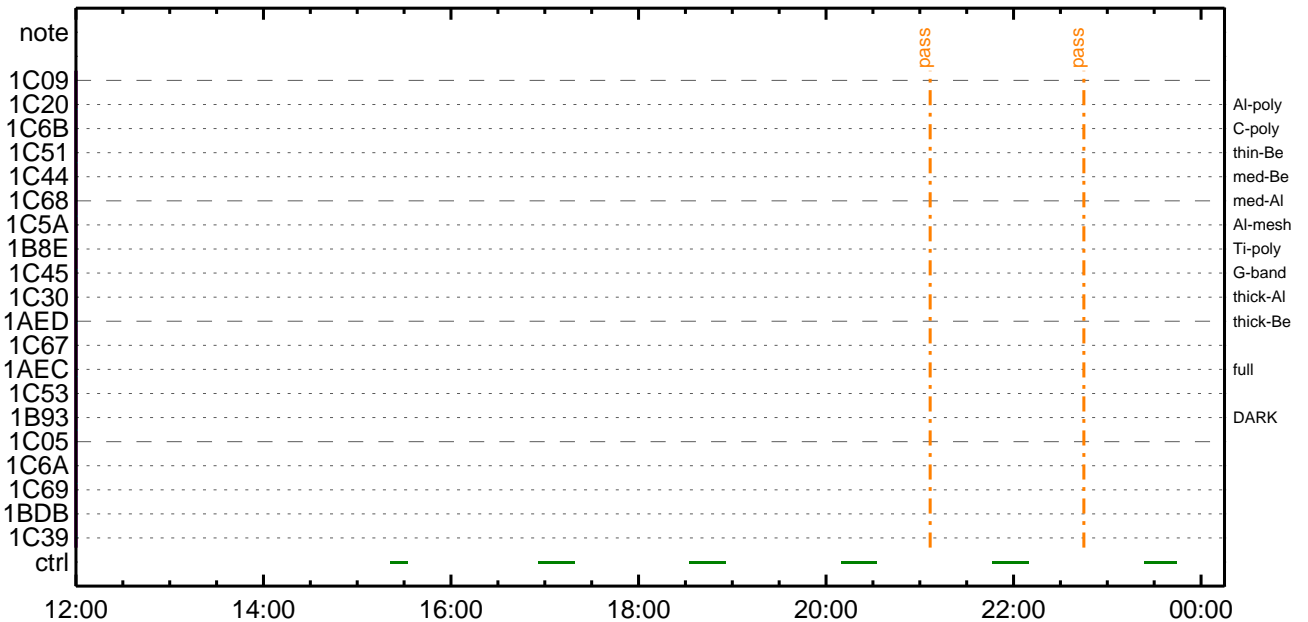
CMDI #0592 2019/11/15



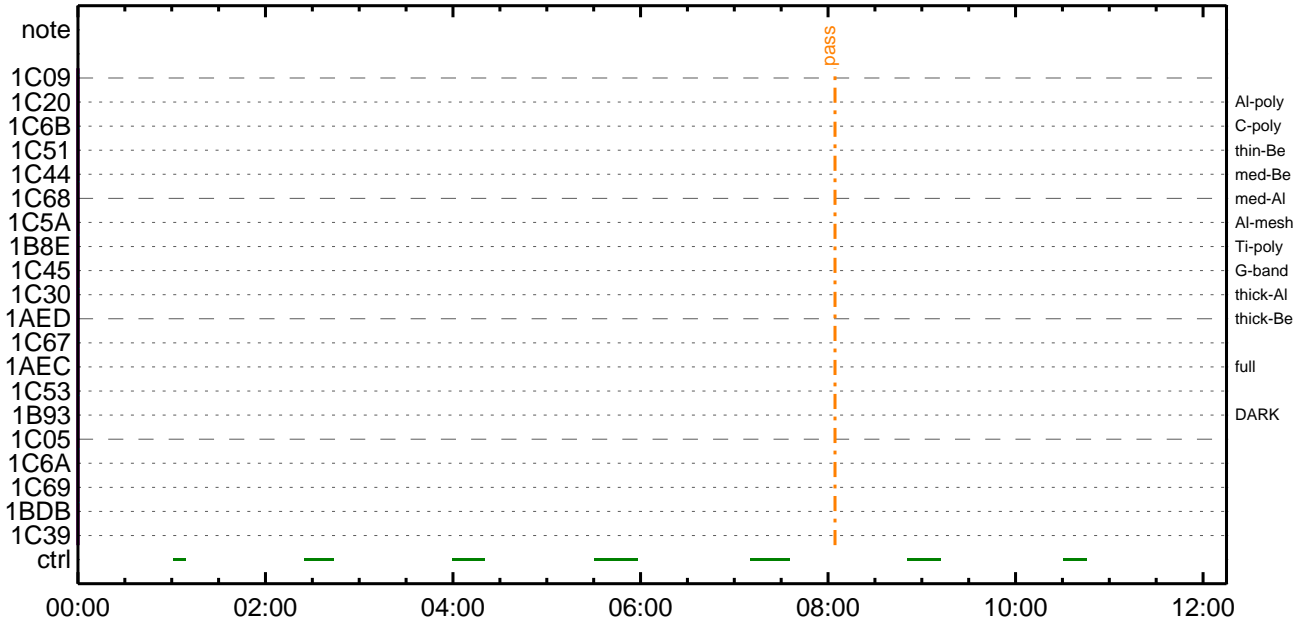
CMDI #0592 2019/11/16



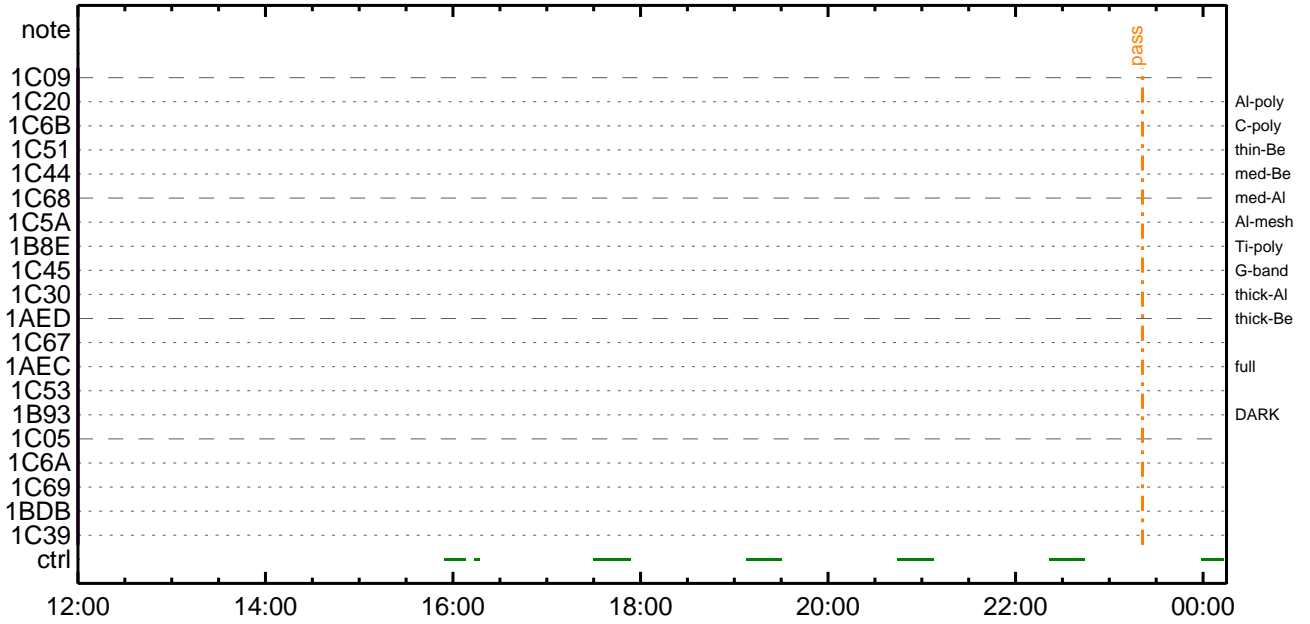
CMDI #0592 2019/11/16



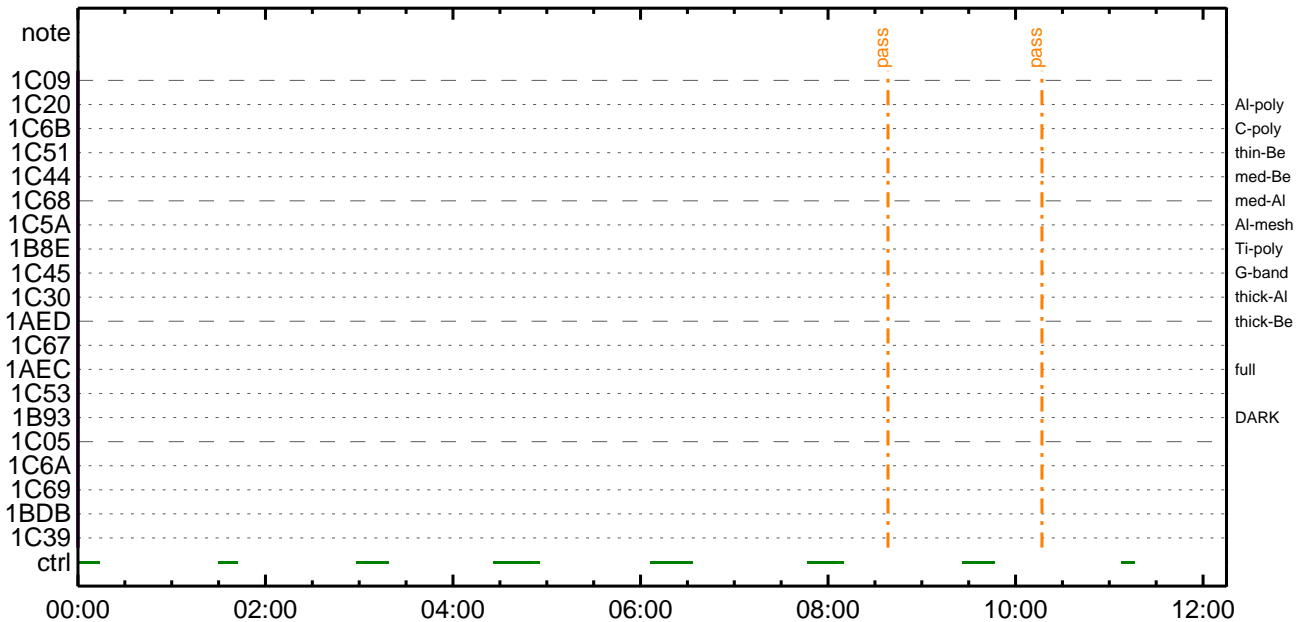
CMDI #0592 2019/11/17



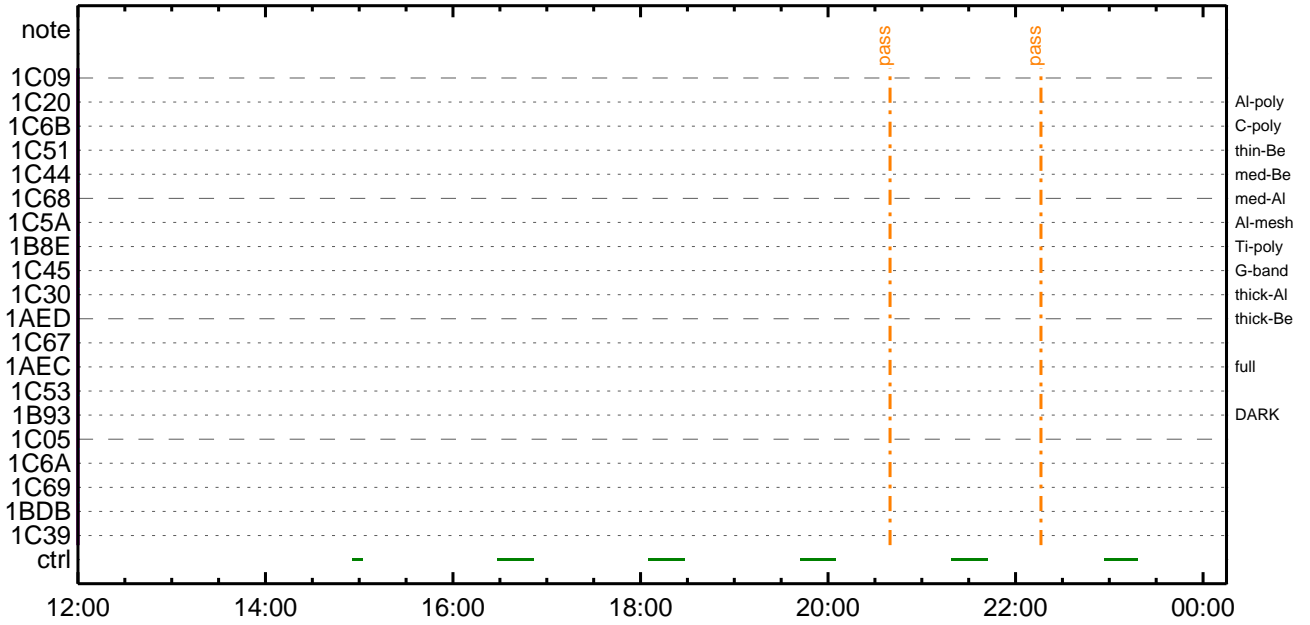
CMDI #0592 2019/11/17



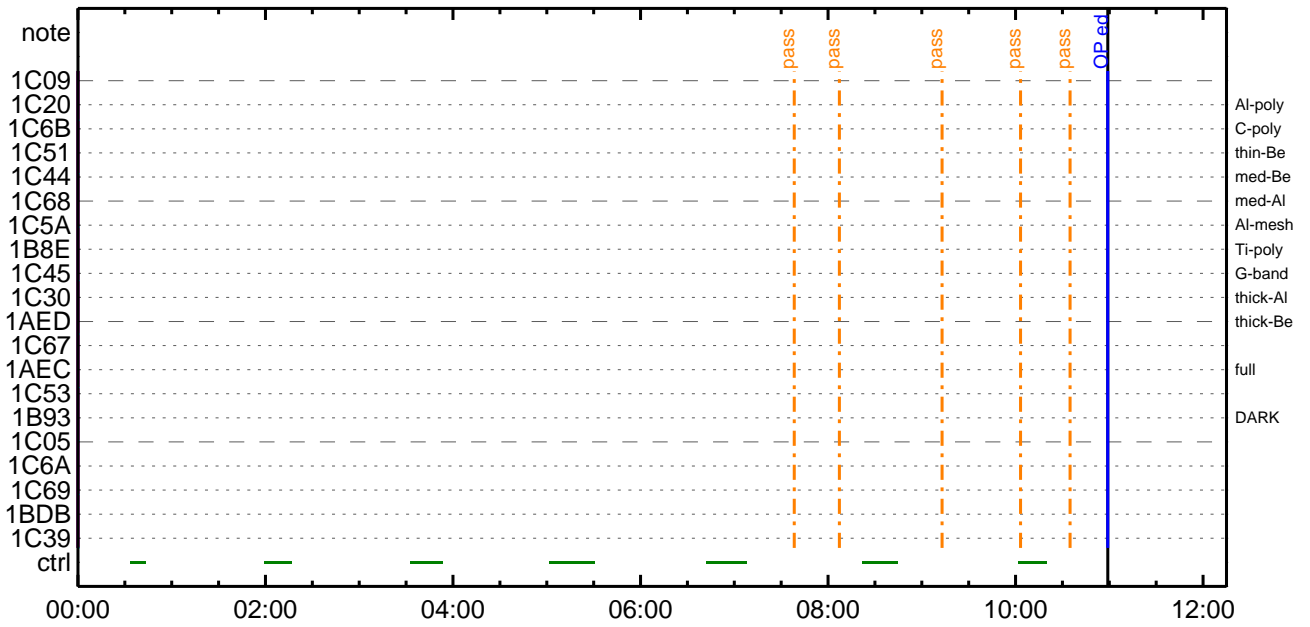
CMDI #0592 2019/11/18



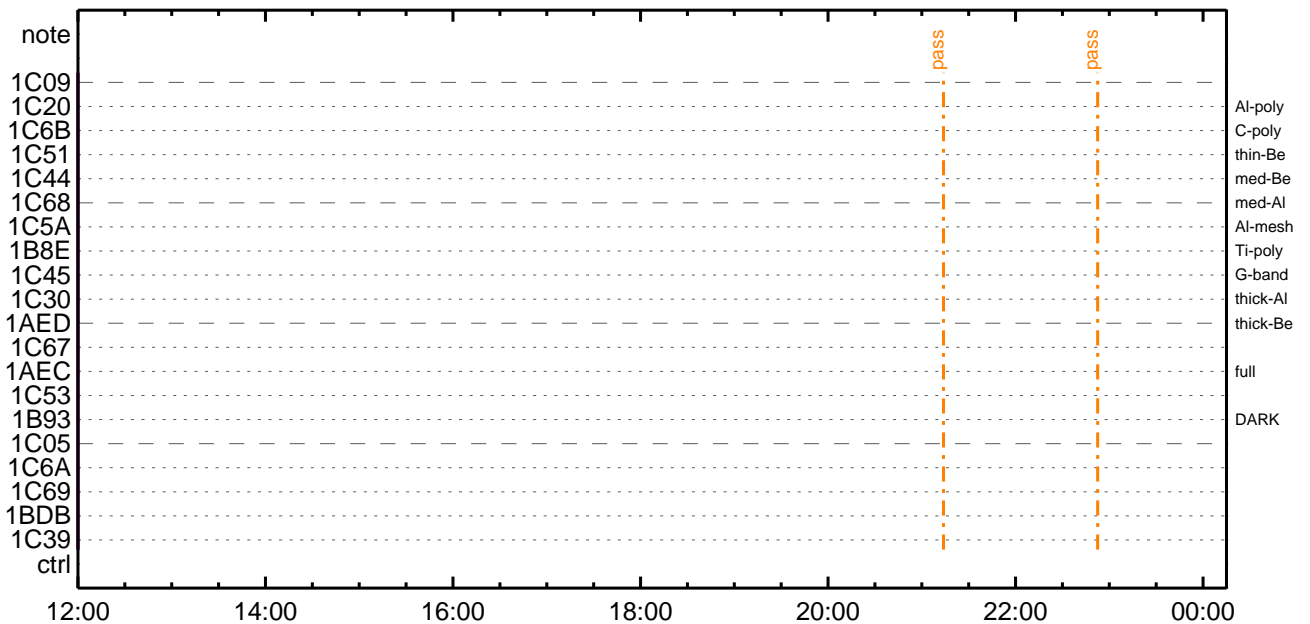
CMDI #0592 2019/11/18



CMDI #0592 2019/11/19



CMDI #0592 2019/11/19



(a) Spacecraft Operation Procedure (real-commands)

```
main-447 2019-11-14 12:41:01 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YÞYÓYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÀð•µ°Æ»Í×ÁÇçÍYçYÄY×YÍ;¼YÉ;ÈÈÈ¼µ•íÉ;ÈÈÈ¼°ÇÖð•ç¼í¹ççí;çÄ®, ùñ¹ðèððçÄ+ç®ð•ðÈððð³ðÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç®µ;ON
0016 C. *****
0017 C. ç" °ÆÄ, Í×ÈYðÀLOSððççí»p´Öðð¹íí, ð•; çÉÖÍ×ðÈXÁÖONðí¹ÖðÈðíðÈððð³ðÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYÍYÄY-¾ÖÄÖð-°ÄÄèð•çç; ç°È²¼ççí°ÆÄ, ¼è¼ççð¼Ä¹Öçç; f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄÍ¼í°ÆÄ,
0033 C. *****
0034 C. ç" RESTART; ÈPT1; Èð•çççç¼¹ççí; ç°È²¼ççí°ÆÄ¹Öçç»°; çDCBC-150ççÈçç; f
0035 C.
0036 . C. ;ãPT1°ÆÄ, ³«»Í; ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÓYÉYÉÄÜÄÖ; ÈÄ•Ä°²óÈè; È, äççí°ÆÄ, °Æ³«; ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, çç¼«Æ°Ää»ßçççç; ä; ç°È²¼ççð¼Ä¹Öçç; f
0055 C. YçYÓYÉYÉÄÜÄÖððÄÄ•Ä°²óÈèð-¼áçç¼¹ççí°ÆÄ¹Öçç»°; çDCBC-150ççÈçç; f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄÍ¼í°ÆÄ,
0059 C. *****
0060 C. ç" RESTART; ÈPT2; Èð•çççç¼¹ççí; ç°È²¼ççí°ÆÄ¹Öçç»°; çDCBC-151ççÈçç; f
0061 C.
0062 . C. ;ãPT2°ÆÄ, ³«»Í; ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÓYÉYÉÄÜÄÖ; ÈÄ•Ä°²óÈè; È, äççí°ÆÄ, °Æ³«; ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ß; çXÁ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÄ, Ää»ß; ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç®µ;OFF; ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```


0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YA6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-447:OP
0104 (
0105 S. OG og-447:OG
0106 (
0107 C.
0108 C. ;ãNMOG&OPîî°eYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YA6Yx½^ãî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãîî°eYAY6Yx;ã
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YA6Yx½^ãî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãîî°eYAY6Yx;ã
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YA6Yx½^ãî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPãîî°eYAY6Yx;ã
0165 C.
0166 C. ***** òê²¼òîî°eYAY6Yx;ã *****
0167 C. DHUYã;4YE;|YA6Yx;ã
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADã-ã÷ç@NGuîî°eYAY6Yx;ã;ç°ê²¼òîî°eYAY6Yx;ã
0180 C. ãpãç;çSETãEDUMPãîî°eYAY6Yx;ã;ç°ê²¼òîî°eYAY6Yx;ã;ç°ê²¼òîî°eYAY6Yx;ã
0181 C.
0182 C. TIY³YpY6Yx;ã;ç°ê²¼òîî°eYAY6Yx;ã (UT)
0183 +. TI 2019-11-14 10:42:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2019-11-14 10:42:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2019-11-14 10:42:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```
0194 C.
0195 +. TI 2019-11-14 10:46:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼õîÄè%îíñõîîŷÄŷ§ŷÄŷ-¹àîŭ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]           EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]        EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]      EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]      EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]       EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]         EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»õ³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLõîî¾È¹ç•è²îOKõ³îç§
0229 C.
0230 C. DHUŷã;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èõðîãõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]         EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2019-11-14 10:46:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC          (21 02)
0247 +. TI 2019-11-14 10:46:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC          (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]        EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2019-11-14 10:46:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC          (41)
0261 C. -----
0262 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2019-11-14 10:46:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]        EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîãî»ö¼ŷõÈÄðõ¹õèDCBC•x²è *****
0276 C. (¼ã°îŷÖŷÄŷÈŷŷŷŷÄŷçŷèÈ¼õõ¼Ä»Ūõ¹õè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•İ Daily±çîñõÈ´õõ¹õèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Ū;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-449 2019-11-14 12:41:01 132 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSYÄY$YÄY-¼Ä»Û;ä
0005  C.
0006  C. YÄYB;¼Y³YFYOYEÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCOS : Reload orbital element (send every contact) *****
0010  C.  Äí;Èò¿òÀò•µ°È»Í×ÁÇòÍYçYÄY×YÍ;¼YÉ;ÈÈÈ%µ•ííÈ;ÈòÈ¼°ÇÓò•ò¿¼í¹çòÍ;çÄ®, ùò¹òÈòÈòçÄ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  C.
0015  C. ***** XRT START *****
0016  C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018  BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020  BC (c3)
0021 . C. ----- Success Verify ? OK / NG _____
0022  C.
0023  C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025  ( )
0026  C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028  BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR _____
0030  C.
0031  C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033  BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035  BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037  BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039  BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041  BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043  BC (cd 06 80 80 20 20)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045  BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047  BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049  BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051  BC (cd 0a 85 83 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053  BC (cd 0b 80 80 08 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055  BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057  BC (cd 10 80 80 08 08)
0058 + DC 07-F0 MDP_XRT_FLD_ENA
0059  BC (d8)
0060 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061  BC (c8)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063  BC (d5)
0064 + DC 07-F0 MDP_XRT_AEC_RESET
0065  BC (d0)
0066 + DC 07-F0 MDP_XRT_FLD_RESET
0067  BC (da)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069  BC (c4 10)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071  BC (c5 0d)
0072 . C. ----- Success Verify ? OK / NG _____
0073  C.
0074  C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076  C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078  BC (c2)
0079 +. TI 2019-11-14 10:46:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081  BC (c2)
0082 . C. ----- Success Verify ? OK / NG _____
0083  C.
0084  C. ***** XRT END *****
0085  C. *****
0086  C. SOT table upload
0087  C. *****
0088  C. < Stop SP table >
0089 +. DC 07-F0 MDP_SP_CTRL_MANU
0090  BC (61)
0091  C. -----
0092  C. MDP_SP_CTRL_MODE = MANU [ ]
0093  C. -----
0094  C.
0095 . C. <Upload SP Observation Table>
```

```

0096 . S. RAM ram-281:MDP_OBS_S
0097 ( )
0098 C.
0099 . C. < Dump RAMID=MDP_OBS_S >
0100 +. DC 07-F0 MDP_DUMP_SPTBL
0101 BC (83 07 00 00 00 38 b8)
0102 C. -----
0103 C. MDP_OBS_S verify = OK/NG [ ]
0104 C. -----
0105 C.
0106 C. *****
0107 C. SOT TI command set
0108 C. *****
0109 C. Execute, after the success of TBL upload.
0110 +. TI 2019-11-14 10:46:18.0
0111 DC 07-F0 MDP_SOT_MODE_OBSV
0112 BC (40)
0113 C. -----
0114 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0115 C. -----
0116 C.
0117 C.
0118 . C. ***** MDP 'ûÃîñî»ö%ÿñÊÂðñ¹ñèDCBC•x²è *****
0119 C. (%ã°îÿÓÿÃÿÊÿPÿËÿäÿçÿèñ%ç%ã%Ûñ¹ñè)
0120 . S. DC-BC dcbc-402:DCBC
0121 (MDP_known_event)
0122 C.
0123 C.
0124 . C. ***** ÿDÿ¹•î Daily±;îññÊ'øñ¹ñèDCBC•x²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127 C.
0128 C.
0129 . C. ;ãLOSÿÃÿSÿÿÿ-¼Ã»Û;ã
0130 C.
0131 . C. ***** LOS *****
0132 C.

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Nov 14, 19 12:41

XRT_OGLIST_0592.chk

Page 1/7

*** OP Sequence for XRT ***

2019/11/14	10:56:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/11/14	10:56:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/11/14	10:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2019/11/14	10:57:00.0	AOCS_OrE-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 c8 01 c5 59			
2019/11/14	10:57:18.0	XRT_FLD_ENA_437_OG [0x1b5]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/11/14	10:59:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/11/14	10:59:50.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/11/14	10:59:52.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/11/14	10:59:54.0	XRT_FLD_RESET_442_OG [0x1ba]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/11/14	10:59:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2019/11/14	10:59:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2019/11/14	11:00:00.0	XRT_CTRL_AUTO_447_OG [0x1bf]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/11/14	15:47:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/11/14	15:47:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/11/14	15:47:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/11/14	15:47:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/11/14	15:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/11/14	16:10:00.0	XRT_Custom_430_OG [0x1ae]						
2019/11/14	16:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/11/14	17:23:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/11/14	17:23:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/11/14	17:23:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/11/14	17:23:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2019/11/14	17:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2019/11/14	17:46:30.0	XRT_Custom_430_OG [0x1ae]						
2019/11/14	17:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/11/14	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/11/14	18:01:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/11/14	18:01:28.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2019/11/14	18:01:30.0	AOCS_OrE-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2019/11/14	18:01:48.0	XRT_FLD_DIS_409_OG [0x199]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2019/11/14	18:01:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2019/11/14	18:01:52.0	XRT_ARS_DIS_443_OG [0x1bb]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/11/14	18:04:28.0	XRT_QT_PROG_SET_416_OG [0x1a0]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2019/11/14	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2019/11/14	18:11:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/11/14	18:11:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2019/11/14	18:11:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2019/11/14	18:11:30.0	AOCS_OrE-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	01 03 74 01 f3			
2019/11/14	18:11:48.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2019/11/14	18:11:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2019/11/14	18:11:52.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2019/11/14	18:11:54.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2019/11/14	18:11:56.0	XRT_FLD_RESET_434_OG [0x1b2]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2019/11/14	18:14:26.0	XRT_QT_PROG_SET_401_OG [0x191]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2019/11/14	18:14:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2019/11/14	18:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]						

Nov 14, 19 12:41

XRT_OGLIST_0592.chk

Page 2/7

2019/11/14	19:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/14	19:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/14	19:00:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/11/14	19:00:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/11/14	19:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/11/14	19:23:30.0	XRT_Custom_430_OG [0x1ae]				
2019/11/14	19:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/11/14	20:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/14	20:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/14	20:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/11/14	20:37:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/11/14	20:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/11/14	21:00:30.0	XRT_Custom_430_OG [0x1ae]				
2019/11/14	21:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/11/14	22:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/14	22:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/14	22:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/11/14	22:14:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/11/14	22:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/11/14	22:37:00.0	XRT_Custom_430_OG [0x1ae]				
2019/11/14	22:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/11/14	23:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/14	23:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/14	23:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/11/14	23:51:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/11/14	23:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/11/15	00:08:00.0	XRT_Custom_430_OG [0x1ae]				
2019/11/15	00:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/11/15	01:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/15	01:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/15	01:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/11/15	01:25:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2019/11/15	01:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2019/11/15	01:35:00.0	XRT_Custom_430_OG [0x1ae]				
2019/11/15	01:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/11/15	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/15	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/15	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2019/11/15	02:00:00.0	AOCS_Or-e-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2019/11/15	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2019/11/15	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2019/11/15	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2019/11/15	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2019/11/15	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2019/11/15	02:02:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2019/11/15	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2019/11/15	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2019/11/15	02:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2019/11/15	02:51:02.0	XRT_CTRL_MANU_402_OG [0x192]				

Nov 14, 19 12:41

XRT_OGLIST_0592.chk

2019/11/15	02:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	02:51:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/15	02:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/15	03:11:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/15	03:12:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/15	04:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	04:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	04:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	04:20:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/15	04:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/15	04:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/15	04:48:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/15	04:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/15	05:30:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/15	05:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	05:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	05:30:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	05:30:24.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/11/15	05:30:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/11/15	05:30:28.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2019/11/15	05:30:30.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2019/11/15	05:30:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/11/15	05:41:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2019/11/15	05:41:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/15	05:41:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	05:41:30.0	AOCS_OrE-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	05:41:48.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2019/11/15	05:41:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	5	02-76	01 03 74 01 f3	
2019/11/15	05:41:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/11/15	05:41:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/11/15	05:41:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/11/15	05:44:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/11/15	05:44:28.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/15	05:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2019/11/15	05:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2019/11/15	05:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/15	05:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	05:58:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	06:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/15	06:25:31.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/15	06:26:31.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/15	07:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/15	07:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	07:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	07:38:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/15	07:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/15	08:03:01.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/15	08:04:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/15	09:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	09:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

Nov 14, 19 12:41

XRT_OGLIST_0592.chk

2019/11/15	09:18:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/11/15	09:18:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/11/15	09:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/11/15	09:39:01.0	XRT_Custom_430_OG [0x1ae]							
2019/11/15	09:40:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/11/15	10:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	10:59:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	10:59:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/11/15	10:59:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/11/15	11:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/11/15	11:10:01.0	XRT_Custom_430_OG [0x1ae]							
2019/11/15	11:11:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/11/15	14:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	14:49:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	14:49:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/11/15	14:49:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/11/15	14:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/11/15	14:54:00.0	XRT_Custom_430_OG [0x1ae]							
2019/11/15	14:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/11/15	16:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	16:21:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	16:21:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/11/15	16:21:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/11/15	16:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/11/15	16:44:30.0	XRT_Custom_430_OG [0x1ae]							
2019/11/15	16:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/11/15	17:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	17:57:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	17:57:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/11/15	17:57:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/11/15	18:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/11/15	18:20:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	18:20:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	18:20:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2019/11/15	18:21:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2019/11/15	18:21:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2019/11/15	18:21:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2019/11/15	18:21:22.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/11/15	18:23:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2019/11/15	18:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2019/11/15	18:30:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	18:30:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/15	18:30:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2019/11/15	18:31:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 03 74 01 f3				
2019/11/15	18:31:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2019/11/15	18:31:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2019/11/15	18:31:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2019/11/15	18:31:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2019/11/15	18:31:26.0	XRT_FLD_RESET_434_OG [0x1b2]							

Nov 14, 19 12:41

XRT_OGLIST_0592.chk

2019/11/15	18:33:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/15	18:33:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2019/11/15	18:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/11/15	19:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/15	19:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	19:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	19:34:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/15	19:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/15	19:58:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/15	19:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG				
2019/11/15	21:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/15	21:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	21:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	21:12:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/15	21:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/15	21:35:01.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/15	21:36:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG				
2019/11/15	22:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/15	22:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	22:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/15	22:49:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/15	22:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/15	23:11:01.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/15	23:12:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG				
2019/11/16	00:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/16	00:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	00:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	00:26:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/16	00:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/16	00:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/16	00:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG				
2019/11/16	01:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/16	01:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	01:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	01:52:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/16	01:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/16	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/16	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	02:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2019/11/16	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	00 00 00 00
2019/11/16	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/11/16	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/11/16	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/11/16	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/11/16	02:02:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/16	02:02:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2019/11/16	02:09:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2019/11/16	02:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG				

2019/11/16	03:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/16	03:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	03:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	03:25:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/16	03:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/16	03:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/16	03:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2019/11/16	04:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/11/16	04:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/16	04:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	04:54:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	04:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/16	05:23:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/16	05:24:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/16	06:03:00.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/11/16	06:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/16	06:03:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	06:03:24.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2019/11/16	06:03:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	XRT_FLD_DIS_409_OG [0x199]				
2019/11/16	06:03:28.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	d9	
2019/11/16	06:03:30.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_ARS_DIS	1	07-F0	c9	
2019/11/16	06:03:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	d5	
2019/11/16	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_408_OG [0x198]				
2019/11/16	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c4 12	
2019/11/16	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c0	
2019/11/16	06:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]	MDP_XRT_FOCUS_POSITION	1	07-F0	c1	
2019/11/16	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCS_OrE-point_Start_3_OG [0x099]				
2019/11/16	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	5	02-76	01 03 74 01 f3	
2019/11/16	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2019/11/16	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2019/11/16	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2019/11/16	06:12:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2019/11/16	06:12:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/16	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2019/11/16	06:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2019/11/16	06:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_408_OG [0x198]				
2019/11/16	06:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/16	06:34:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	06:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	07:00:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da	
2019/11/16	07:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2019/11/16	08:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2019/11/16	08:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]				
2019/11/16	08:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2019/11/16	08:14:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2019/11/16	08:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	08:37:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2019/11/16	08:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
			XRT_Custom_430_OG [0x1ae]				
			XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	

Nov 14, 19 12:41

XRT_OGLIST_0592.chk

Page 7/7

2019/11/16	09:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/16	09:54:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2019/11/16	09:54:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2019/11/16	09:54:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2019/11/16	09:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2019/11/16	11:02:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				