

# XRT Timeline to be uploaded on 2020/03/28

Period: 2020/03/28 10:50:00 - 2020/04/07 11:15:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

XOB #1C6E: HOP336 1-filter - Al/poly -384x384, 1024ms and 8192s, 300s-cadence, G-band - 384x384 1ms													
Term	Pointing (x, y)					Comment							
03/28 11:17:00 - 03/28 17:59:30	Track ( 59.7, 68.0) @ 03/28 11:00:00					# OP start + 10min, BP tracking							
03/28 18:36:00 - 03/29 01:54:30	Track ( 129.9, 66.9) @ 03/28 18:33:00					BP tracking							
<b>PROG= 05 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 16 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 90 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3 3-time(s) 2.0sec													
└─ Seqn= 72 1-time(s) 300.0sec													
	Al-poly/Open	Al-poly/Ti-poly	close	Safe	Norm	8.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

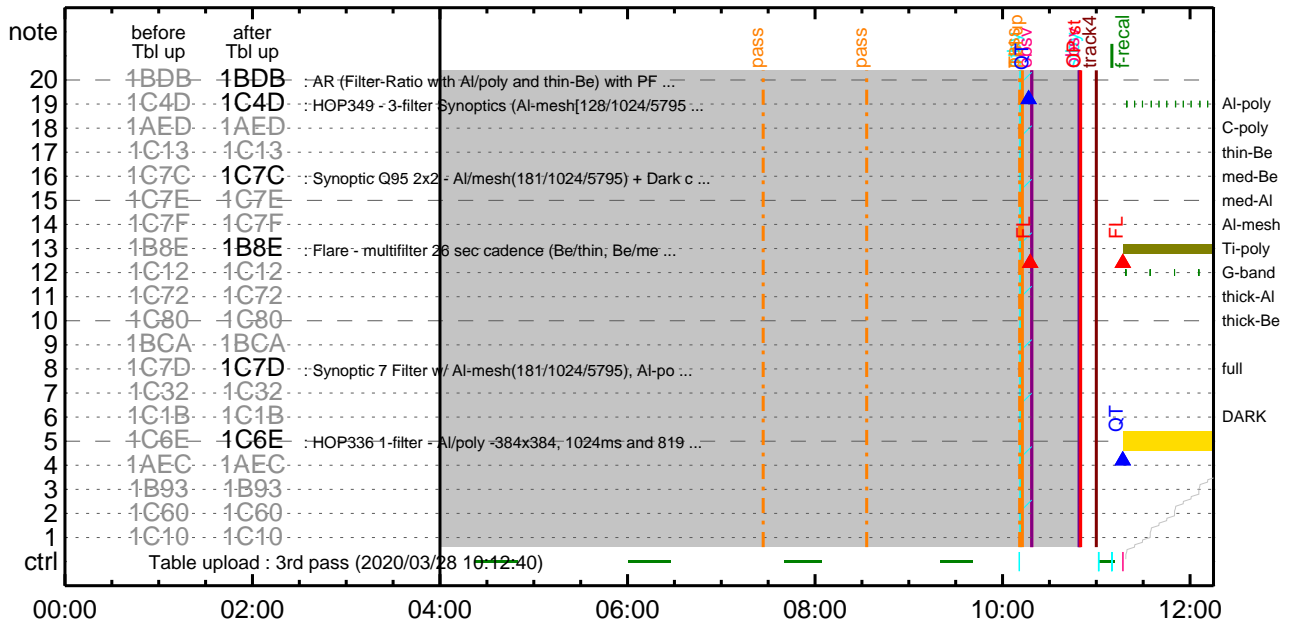
XOB #1C7C: Synoptic Q95 2x2 - Al/mesh(181/1024/5795) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(256/2897/8192)													
Term	Pointing (x, y)					Comment							
03/28 18:26:00 - 03/28 18:32:54	Fixed ( 0.0, 0.0)					synoptic, shifted 23.0 min							
<b>PROG= 16 1-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 44 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 33 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 23 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C4D: HOP349 - 3-filter Synoptics (Al-mesh[128/1024/5795], Al-poly[256/4096/8192], thin-Be[2048/16384/32768] with 512x512 G-band+Leak(1064,1048)													
Term	Pointing (x, y)					Comment							
03/29 02:12:00 - 03/29 05:52:54	Fixed ( 0.0, 0.0)					synoptic, shifted -7.0 min, HOP349							
<b>PROG= 19 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 300.0sec													
└─ Seqn= 88 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 50 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 56 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 81 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
└─ Subr= 2 6-time(s) 450.0sec													
└─ Seqn= 8 1-time(s) 2.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

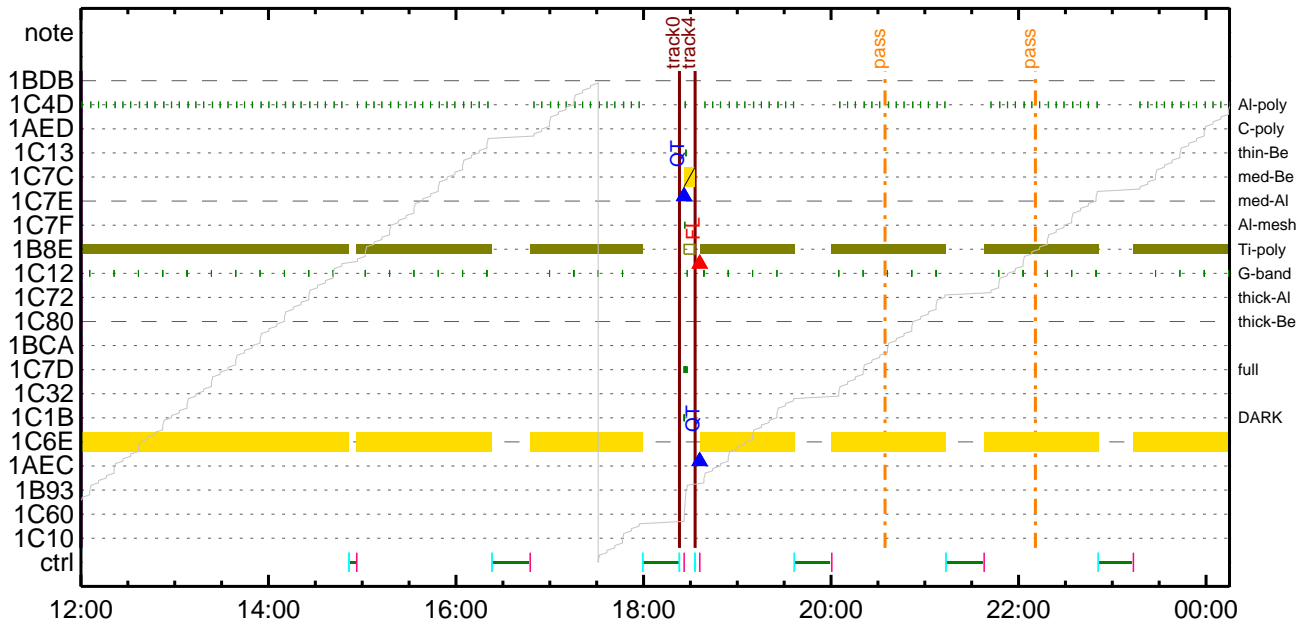


Term	Pointing (x, y)								Comment	
03/28 18:33:18 - 03/29 05:53:18	Track (	129.9, 66.9)	<sup>03/28 18:33:00</sup>	BP tracking						
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval	

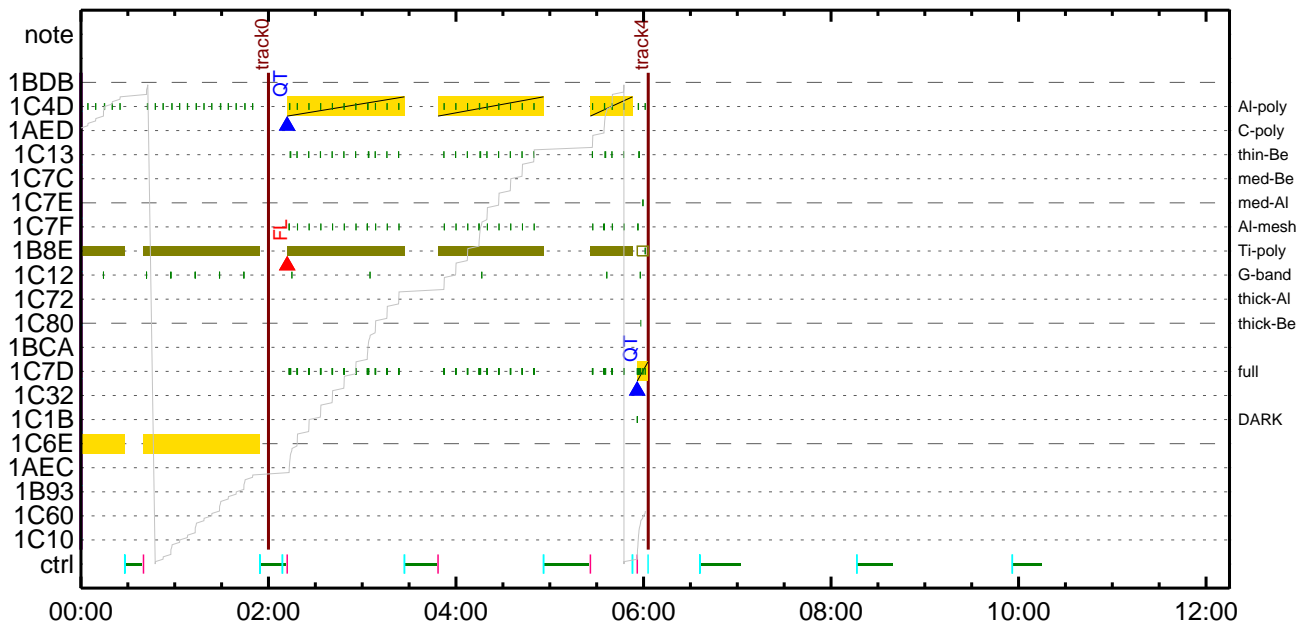
### CMDI #0856 2020/03/28



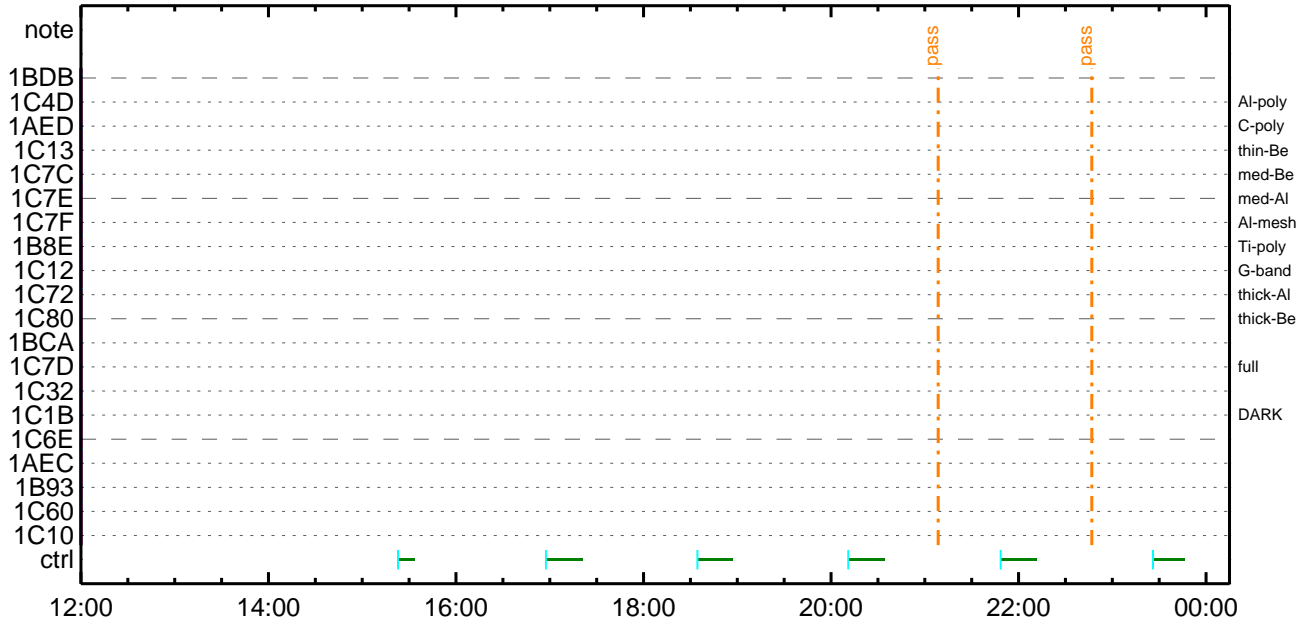
### CMDI #0856 2020/03/28



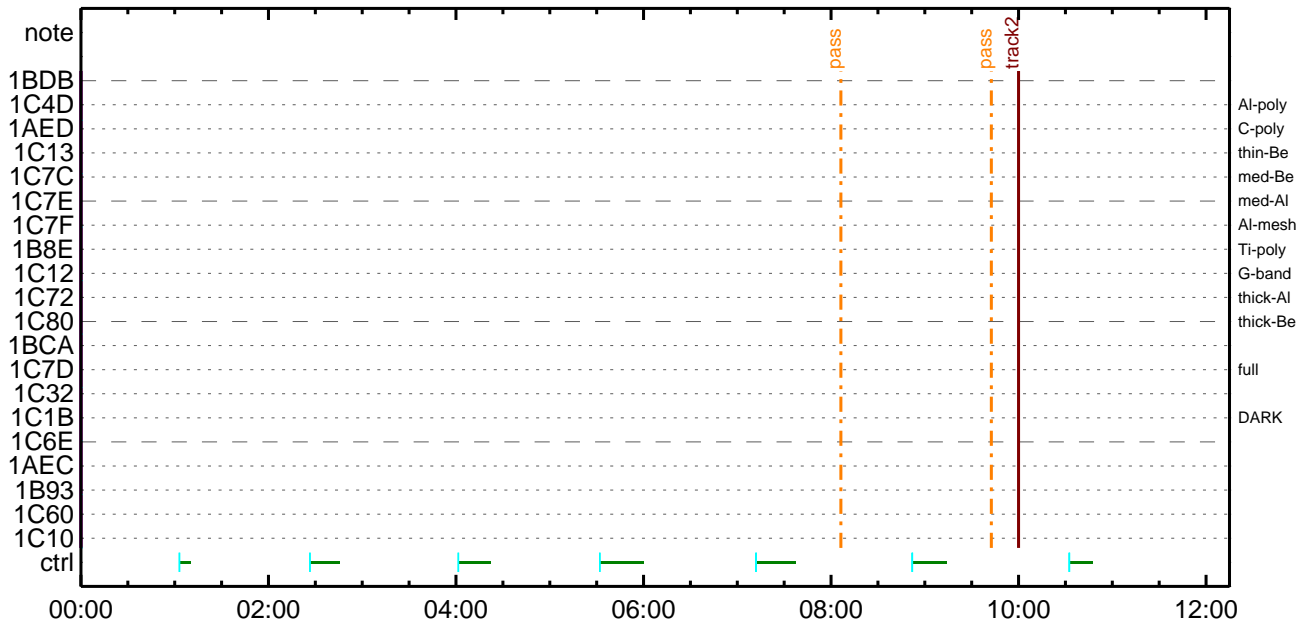
### CMDI #0856 2020/03/29



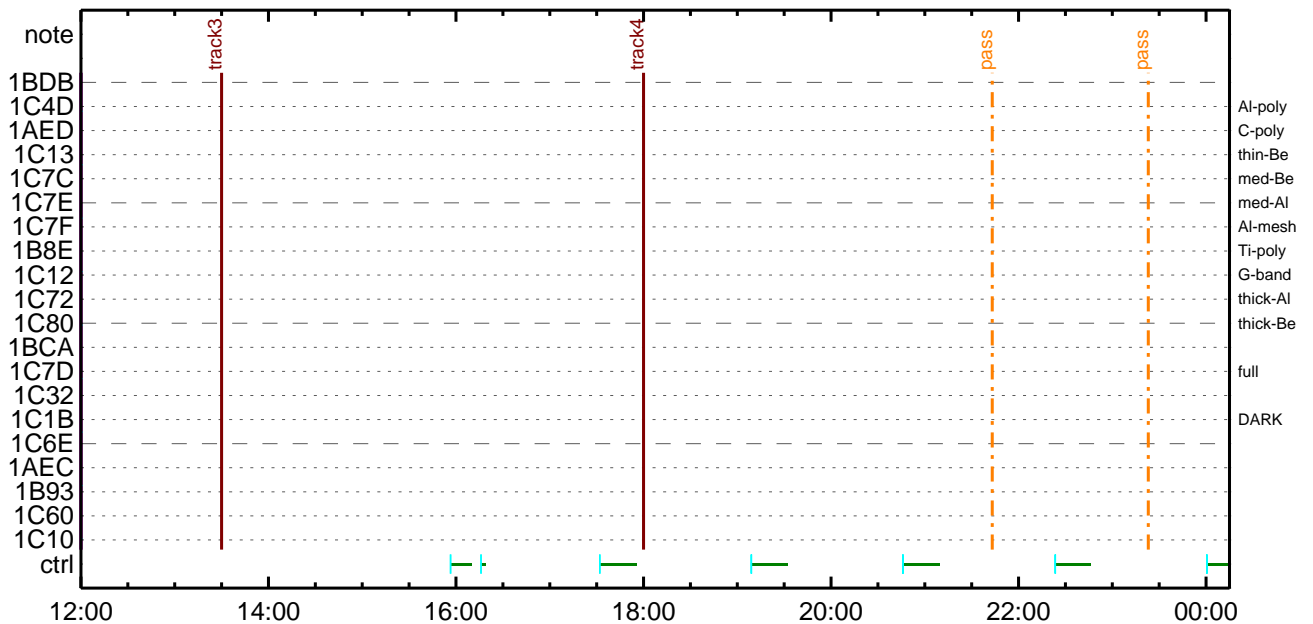
CMDI #0856 2020/03/29



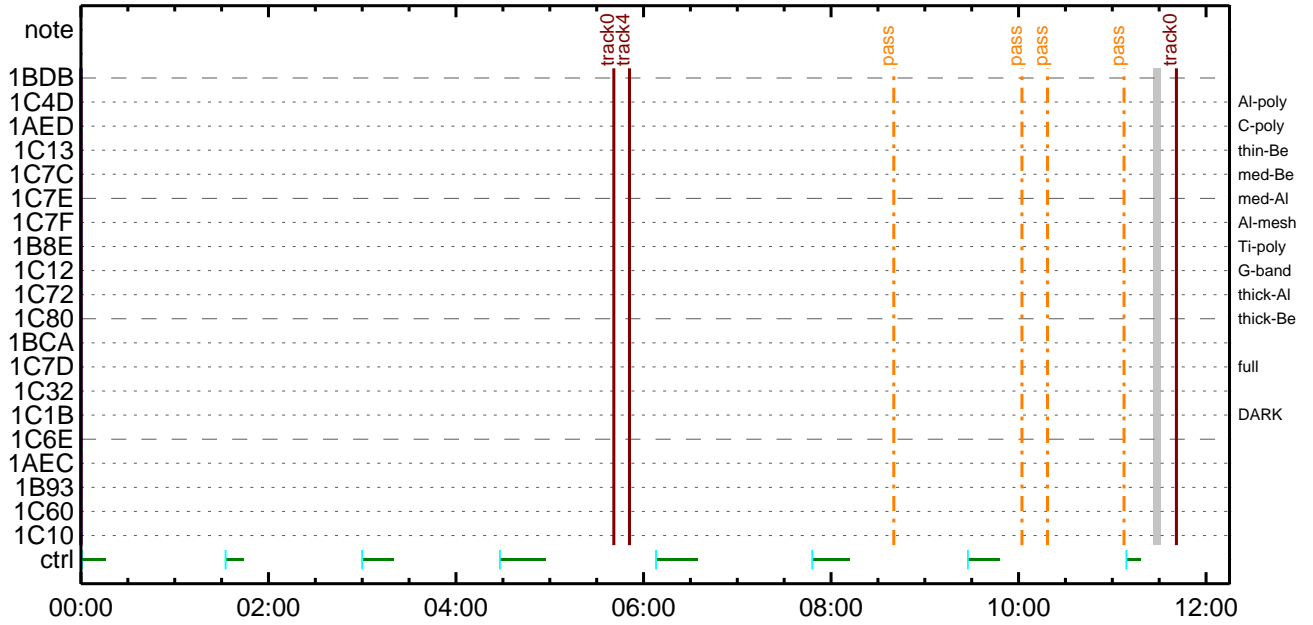
CMDI #0856 2020/03/30



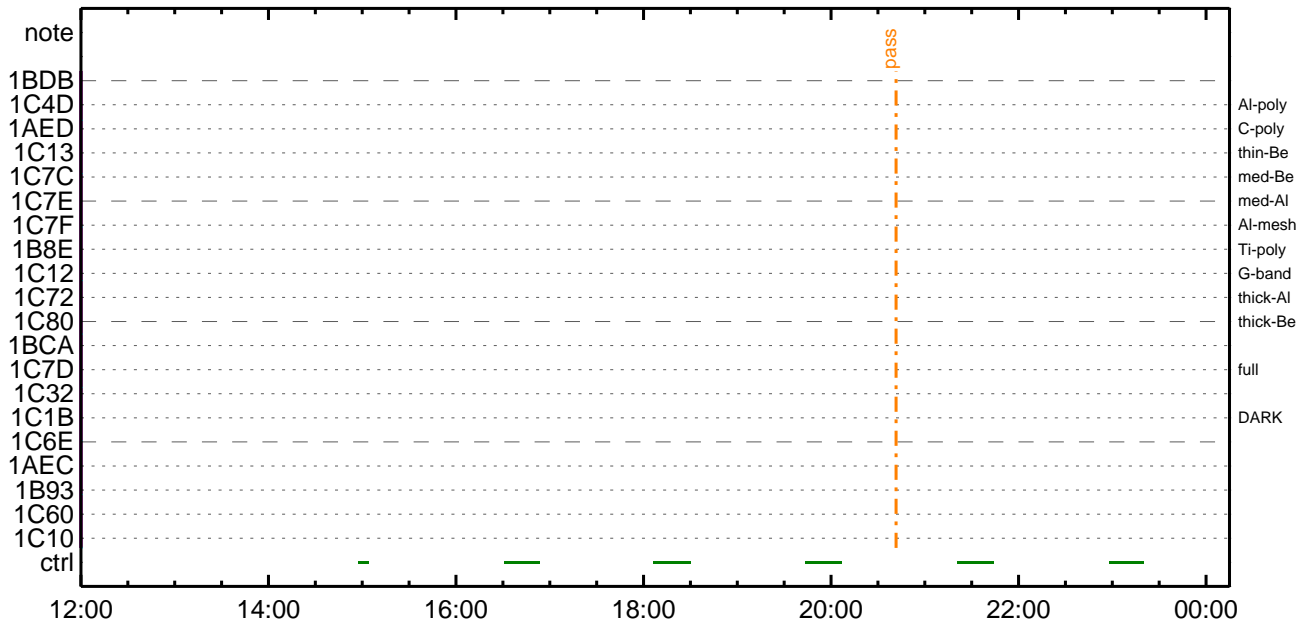
CMDI #0856 2020/03/30



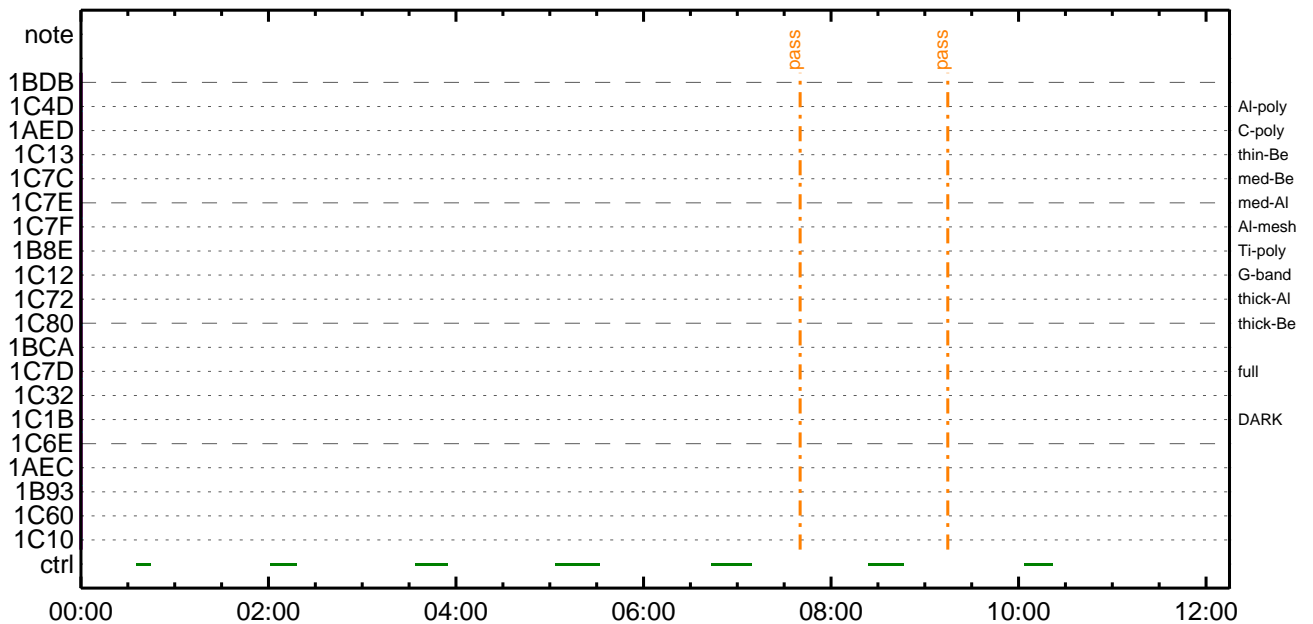
CMDI #0856 2020/03/31



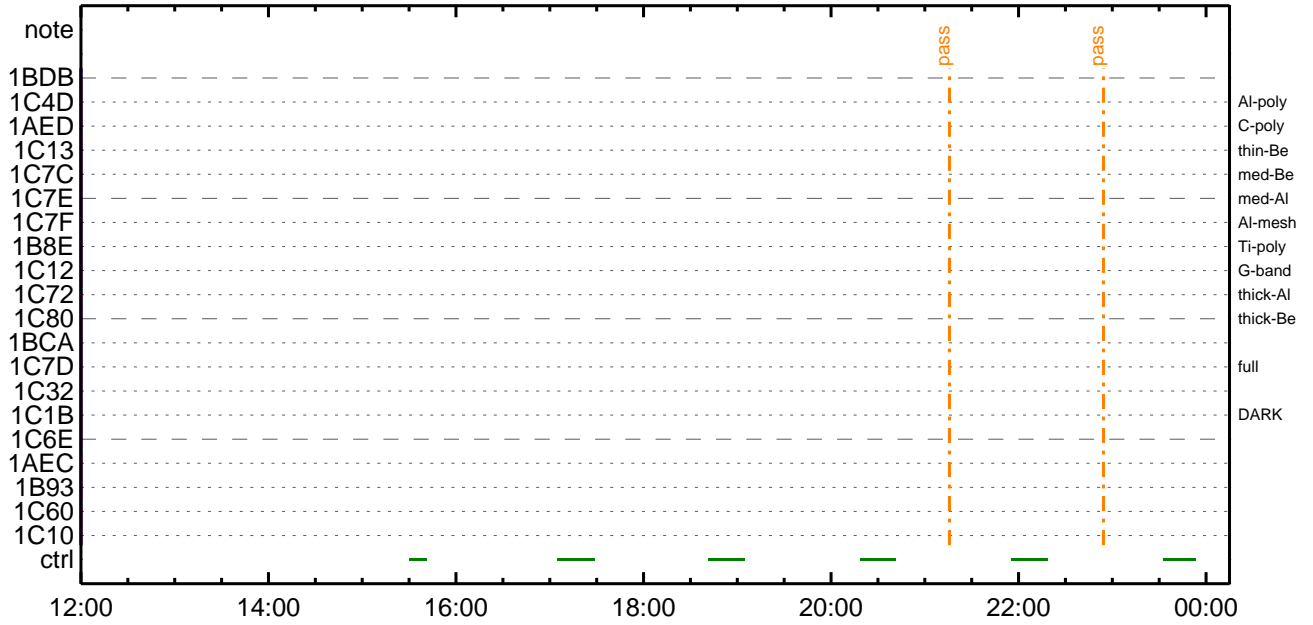
CMDI #0856 2020/03/31



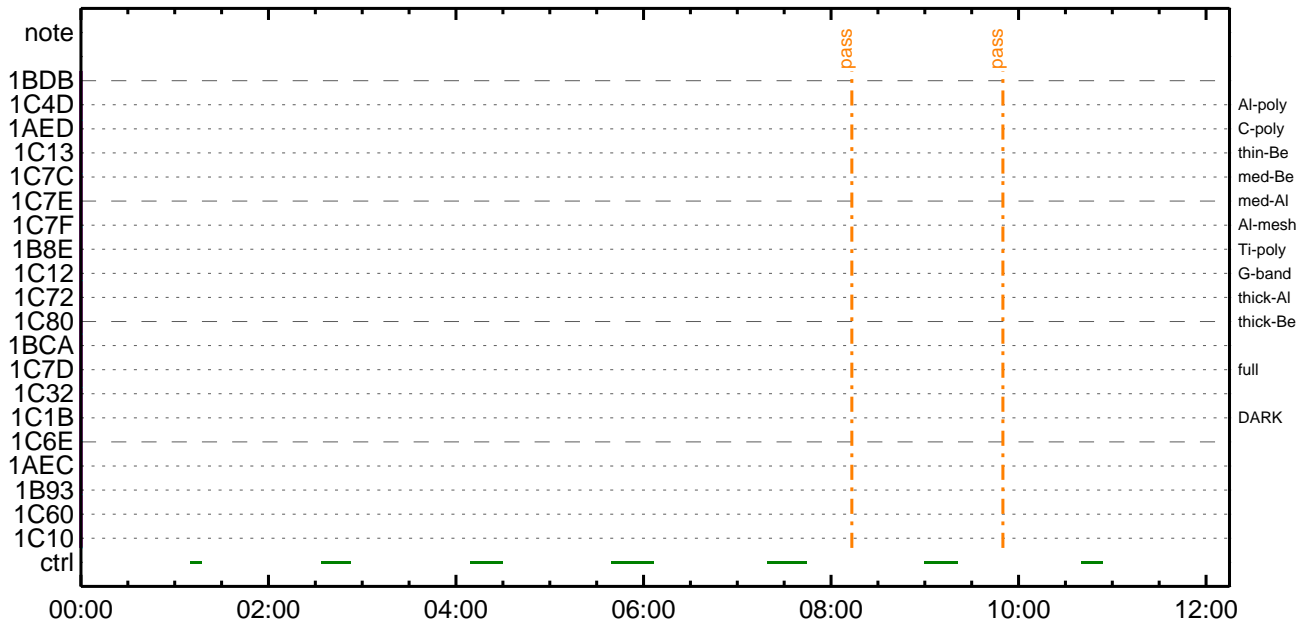
CMDI #0856 2020/04/01



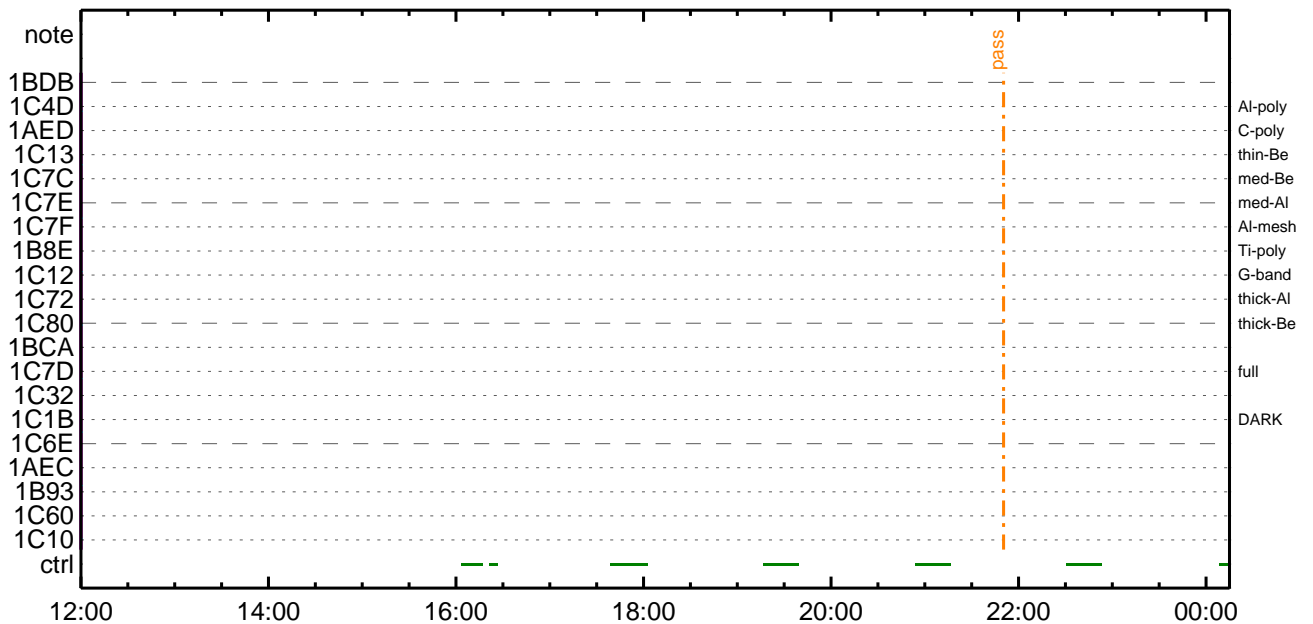
CMDI #0856 2020/04/01



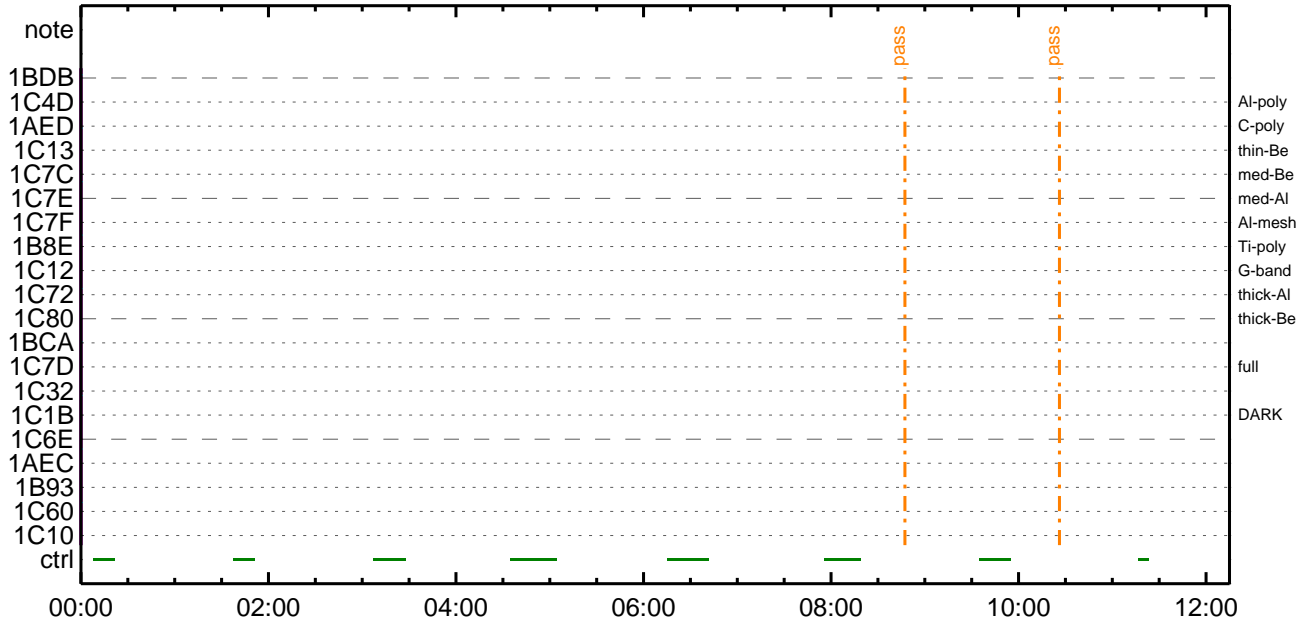
CMDI #0856 2020/04/02



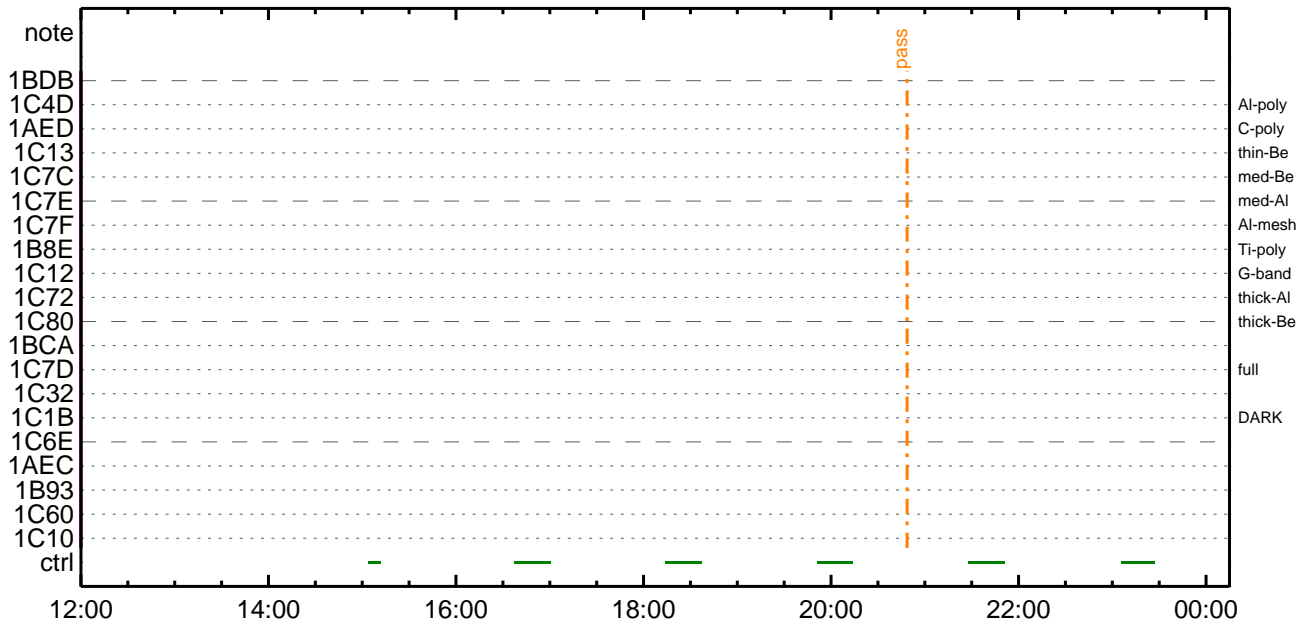
CMDI #0856 2020/04/02



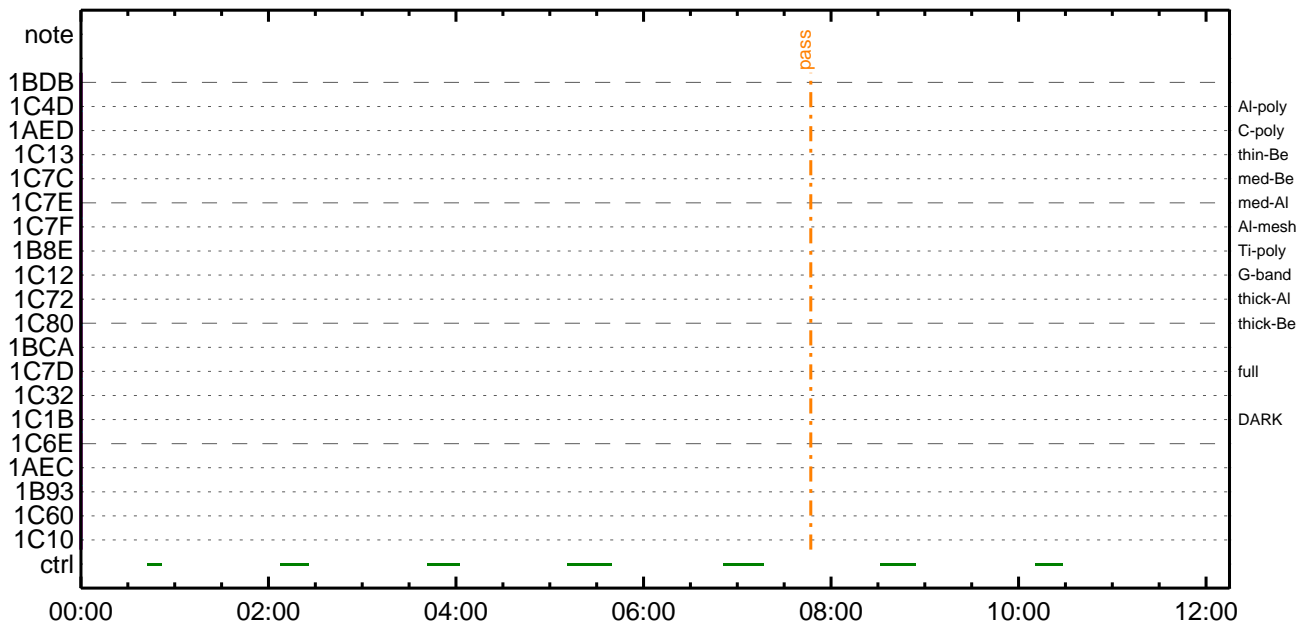
CMDI #0856 2020/04/03



CMDI #0856 2020/04/03

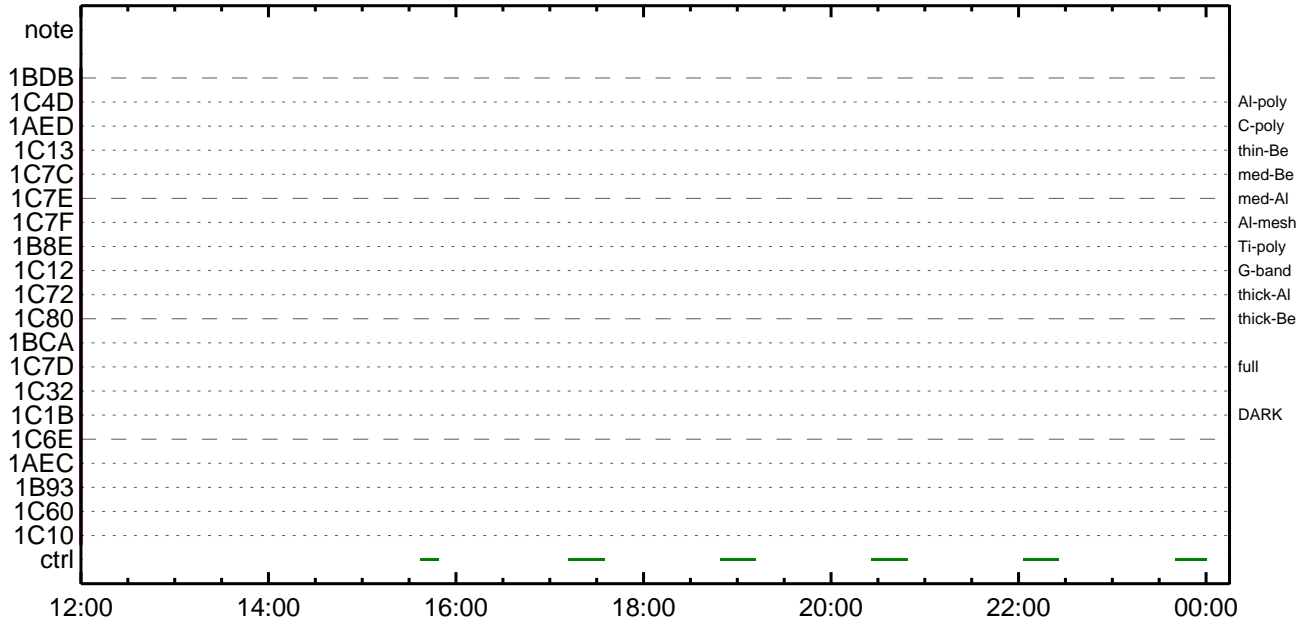


CMDI #0856 2020/04/04

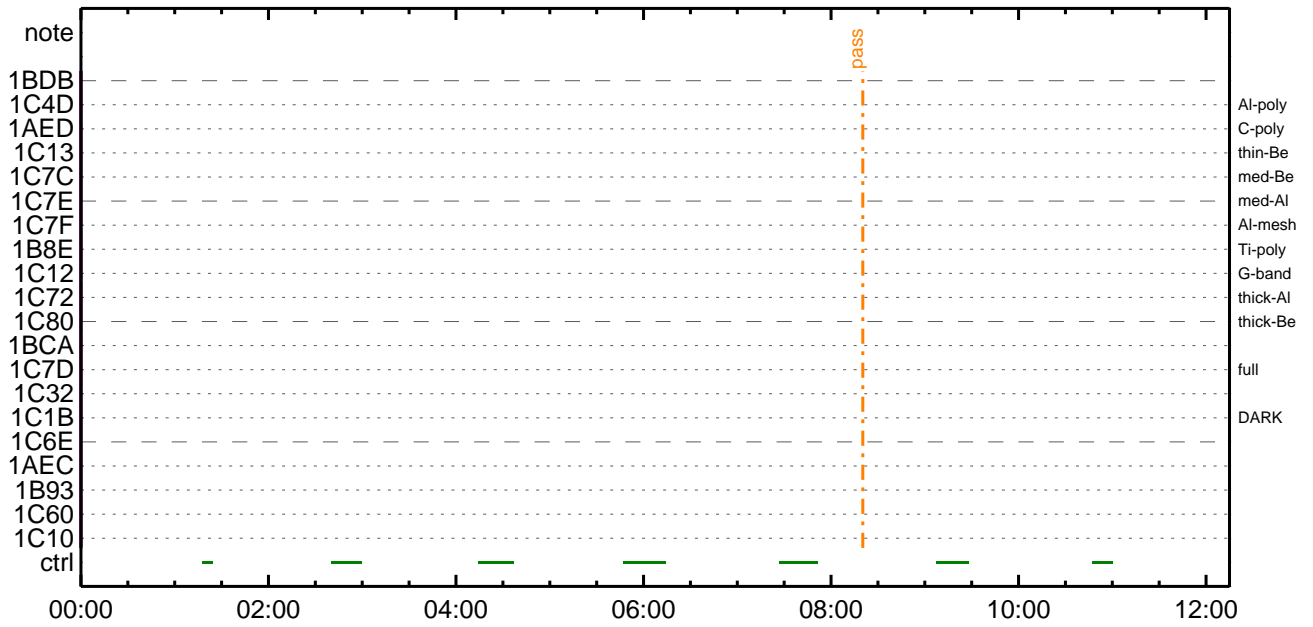




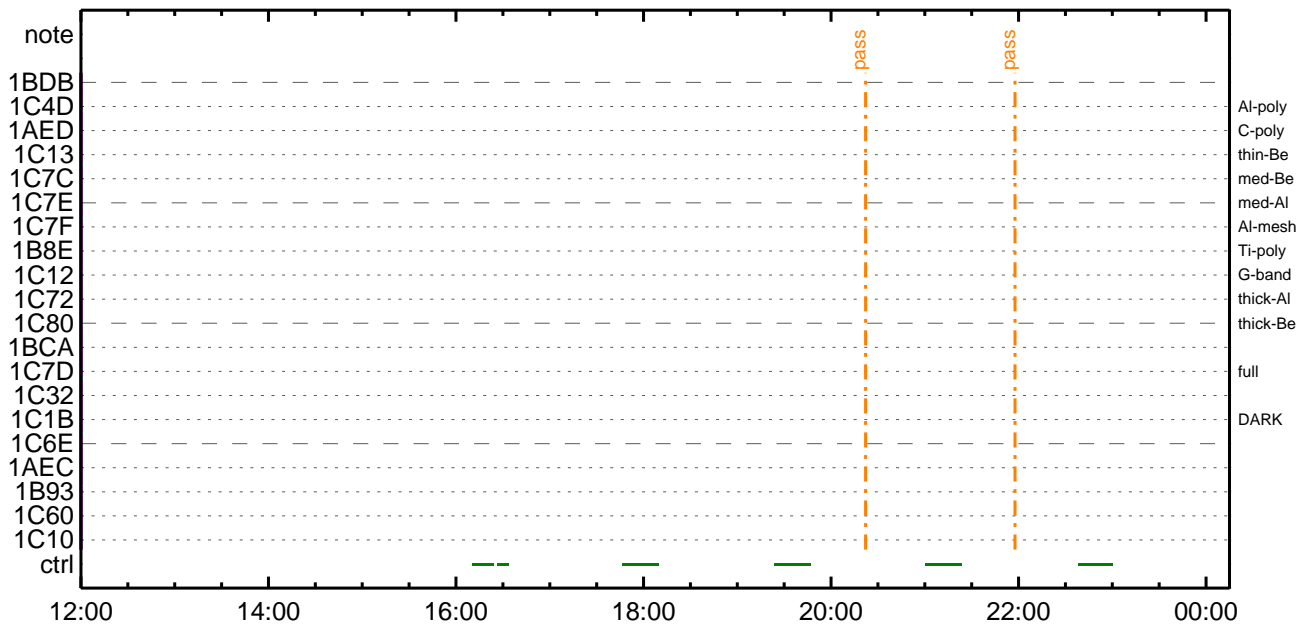
CMDI #0856 2020/04/04



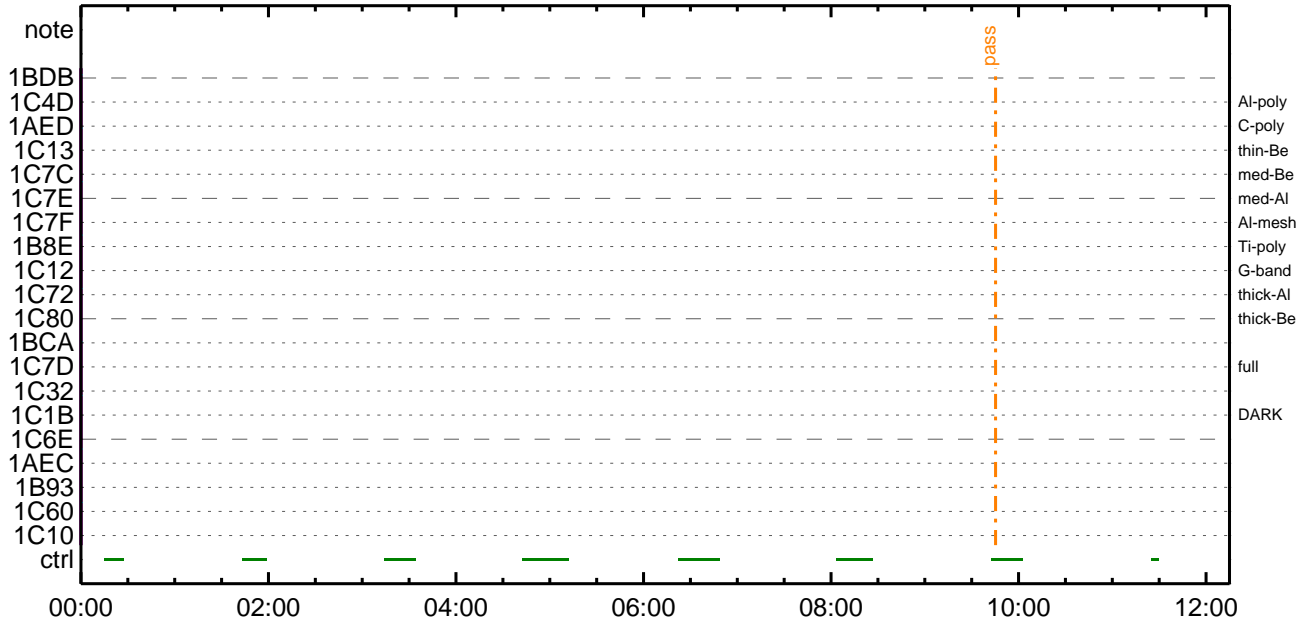
CMDI #0856 2020/04/05



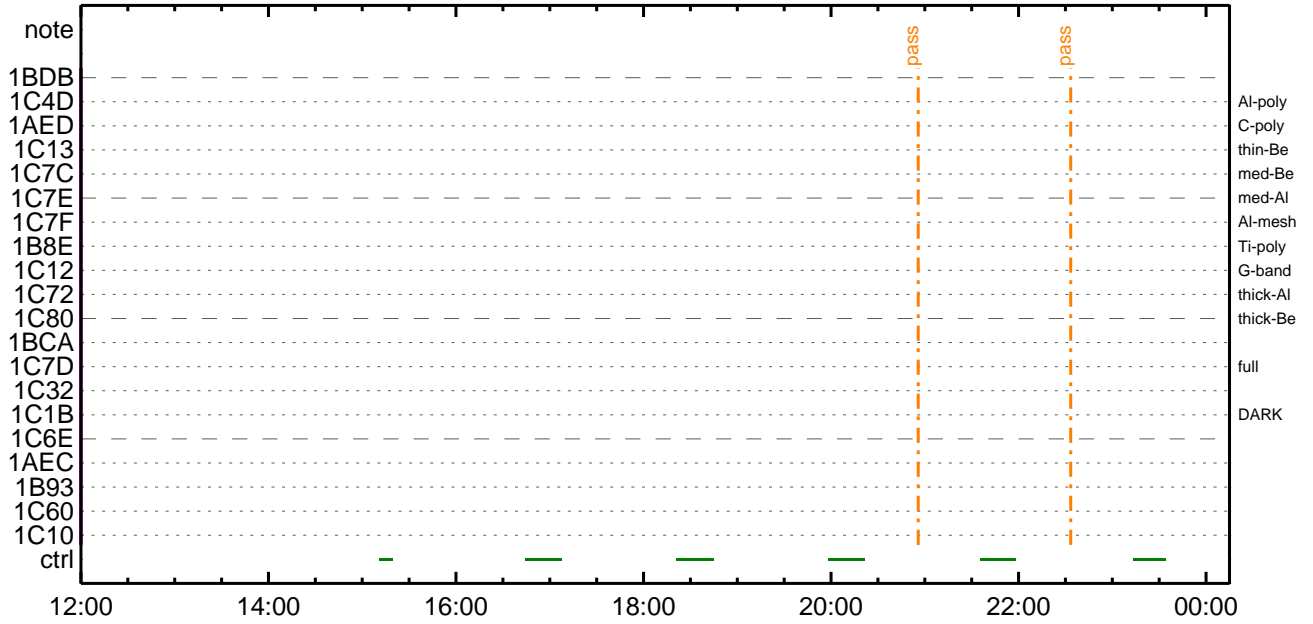
CMDI #0856 2020/04/05



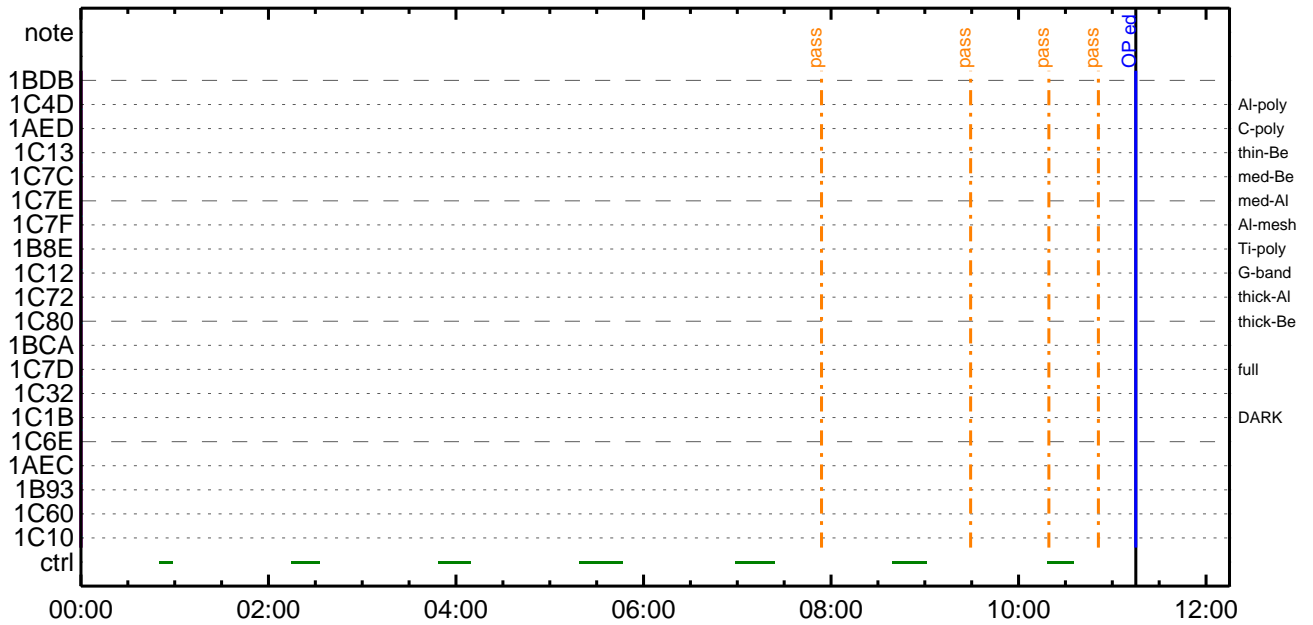
CMDI #0856 2020/04/06



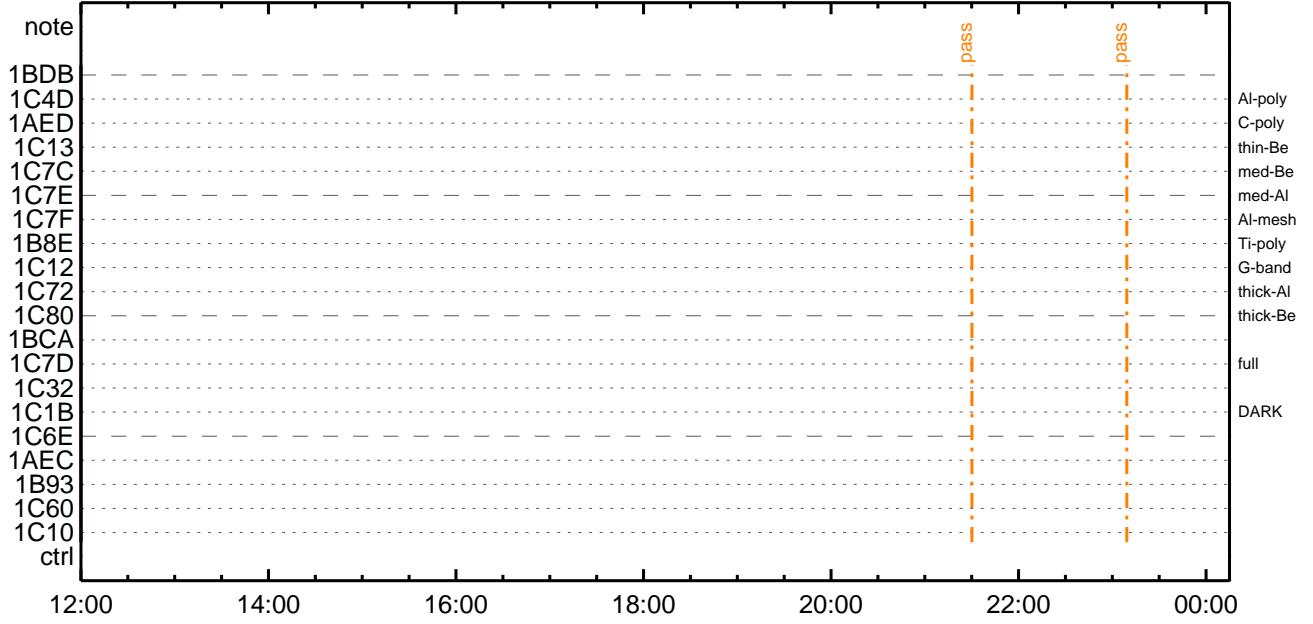
CMDI #0856 2020/04/06



CMDI #0856 2020/04/07



CMDI #0856 2020/04/07





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0096 C.          SET EDUMP I±°iYÑY¹aÇ¹Ôa|a³aE;f
0097 C.
0098 C. TIY³YF¥ÖYÉaðdÄDİ¿(UT)
0099 +. TI 2020-03-28 10:45:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2020-03-28 10:45:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2020-03-28 10:45:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2020-03-28 10:49:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °E²¼aİÄè%îÍÑaİYÁY§YÁY-¹àİÜ
0116 C.          ÇÇ[HK1_TI_CMD_ENA/DIS]       EQ          ENA
0117 C.          ÇÇ[HK1_TI_CMD_NUM]         EQ          4
0118 C.          ÇÇ[HK1_NEXT_EXEC_PIM]      EQ          DHU
0119 C.          ÇÇ[HK1_NEXT_EXEC_DC]      EQ          0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÀYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          ÇÇ[HK1_DMP_TOP_ADRS_1]     EQ          07
0129 C.          ÇÇ[HK1_DMP_TOP_ADRS_0]     EQ          2B
0130 C.          ÇÇ[HK1_DMP_BLOCK_NUM]      EQ          3
0131 C.          ÇÇ[HK1_DMP_REPEAT_NUM]     EQ          0
0132 C.          ÇÇ[HK1_DMA_DMP_PIM]       EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          ÇÇ[HK1_PKT_FORM_NO]        EQ          7
0136 C.          ÇÇ[HK1_PKT_GEN_TIME]       EQ          0.25 s
0137 C.          ÇÇ[HK1_S_TLM_BIT_RATE]     EQ          32k
0138 C.          ÇÇ[HK1_X_TLM_BIT_RATE]    EQ          4M
0139 C.          ÇÇ[HK1_DMP_CHK_FLG]       EQ          EXEC
0140 C.
0141 C. YÀYÖY×½ªİ»að³İÇ§
0142 C.          ÇÇ[HK1_DMP_CHK_FLG]       EQ          NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKað³İÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y¼,¥i;¼YÈ;Èaðİã¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          ÇÇ[HK1_PKT_FORM_NO]        EQ          2
0150 C.          ÇÇ[HK1_PKT_GEN_TIME]       EQ          0.5S
0151 C.          ÇÇ[HK1_S_TLM_BIT_RATE]     EQ          32K
0152 C.          ÇÇ[HK1_X_TLM_BIT_RATE]    EQ          4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2020-03-28 10:49:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC          (21 02)
0163 +. TI 2020-03-28 10:49:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC          (22)
0166 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2020-03-28 10:49:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC          (c3)
0176 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´úÃİaİ»ö¼YªEÄa¹aèDCBC•x²è *****
0181 C. (%ã°İYÖYÁYÉY¥YÉYÁYÇYèaE¼aª¼Å»Ü¹aè)
0182 C. S. DC-BC dcbc-402:DCBC
0183 C. (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** YD¥¹•İ Daily±¿İÑaÈ¹Øa¹aèDCBC•x²è *****
0187 C. S. DC-BC dcbc-153:DCBC
0188 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;ãLOS¥ÁY§YÁY-¼Å»Ü;ã
0192 C.
0193 C. ***** LOS *****

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 14)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 0d)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2020-03-28 10:49:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP `uãîï»ö%ýðÉâð¹ðēDCBC•x²è *****
0169 C. (%ã°îÿÓYÁYÉYÉYÉYáYçYèè%¼¼¼¼»Û¹ðè)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥D¥¹•İ Daily±¿İÑðÉ'Ø¹ðēDCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Å»Û;ã
0180 C.
0181 . C. ***** LOS *****
0182 C.

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\*\*\* OP Sequence for XRT \*\*\*

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2020/03/28 11:00:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 04 00 00 00 00
2020/03/28 11:01:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 11:01:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 11:01:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2020/03/28 11:01:36.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT       1 07-F0 e8
2020/03/28 11:04:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP       1 07-F0 e9
2020/03/28 11:09:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 11:09:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 11:09:58.0 XRT_FOCUS_RECALIBRATE_427_OG [0x1ab]
                        XRT_FOCUS_RECAL           2 07-F8 78 00
2020/03/28 11:13:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION         4 07-F8 22 fe 97 00
2020/03/28 11:14:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA           1 07-F0 d8
2020/03/28 11:14:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA       1 07-F0 c8
2020/03/28 11:14:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET         1 07-F0 d0
2020/03/28 11:14:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2020/03/28 11:14:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2020/03/28 11:16:56.0 XRT_QT_PROG_SET_421_OG [0x1a5]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 05
2020/03/28 11:16:58.0 XRT_FL_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_FL_PROG_SET       2 07-F0 c5 0d
2020/03/28 11:17:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2020/03/28 14:51:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 14:51:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 14:51:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2020/03/28 14:51:36.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT       1 07-F0 e8
2020/03/28 14:54:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP       1 07-F0 e9
2020/03/28 14:55:30.0 XRT_Custom_430_OG [0x1ae]
2020/03/28 14:56:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2020/03/28 16:23:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 16:23:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 16:23:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2020/03/28 16:23:06.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT       1 07-F0 e8
2020/03/28 16:26:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP       1 07-F0 e9
2020/03/28 16:46:30.0 XRT_Custom_430_OG [0x1ae]
2020/03/28 16:47:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2020/03/28 17:59:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 17:59:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 17:59:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET         1 07-F0 da
2020/03/28 17:59:36.0 XRT_PREFLR_STRT_435_OG [0x1b3]
                        MDP_XRT_PREFLR_STRT       1 07-F0 e8
2020/03/28 18:02:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP       1 07-F0 e9
2020/03/28 18:22:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 18:22:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2020/03/28 18:22:58.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2020/03/28 18:23:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2020/03/28 18:23:18.0 XRT_FLD_DIS_409_OG [0x199]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2020/03/28 18:23:20.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS       1 07-F0 c9
2020/03/28 18:23:22.0 XRT_ARS_DIS_443_OG [0x1bb]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2020/03/28 18:25:58.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 10
2020/03/28 18:26:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2020/03/28 18:32:54.0 XRT_CTRL_MANU_402_OG [0x192]

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2020/03/28	18:32:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/28	18:32:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/28	18:33:00.0	AOCs_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2020/03/28	18:33:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00	
2020/03/28	18:33:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2020/03/28	18:33:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2020/03/28	18:33:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2020/03/28	18:33:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2020/03/28	18:35:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/03/28	18:35:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2020/03/28	18:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2020/03/28	19:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/03/28	19:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/28	19:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/28	19:36:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/03/28	19:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/03/28	19:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/03/28	20:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2020/03/28	21:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/03/28	21:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/28	21:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/28	21:13:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/03/28	21:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/03/28	21:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/03/28	21:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2020/03/28	22:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/03/28	22:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/28	22:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/28	22:51:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/03/28	22:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/03/28	23:12:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/03/28	23:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2020/03/29	00:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/03/29	00:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/29	00:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/29	00:28:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/03/29	00:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/03/29	00:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/03/29	00:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2020/03/29	01:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/03/29	01:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/29	01:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/29	01:54:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/03/29	01:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/03/29	02:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/03/29	02:08:54.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	00 00 00 00 00	
2020/03/29	02:08:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/03/29	02:08:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
		XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	

2020/03/29	02:09:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2020/03/29	02:09:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2020/03/29	02:09:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2020/03/29	02:09:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2020/03/29	02:09:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/03/29	02:11:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2020/03/29	02:11:58.0	XRT_FL_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2020/03/29	02:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/03/29	03:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	03:27:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	03:27:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/03/29	03:27:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/03/29	03:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/03/29	03:47:30.0	XRT_Custom_430_OG [0x1ae]							
2020/03/29	03:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/03/29	04:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	04:56:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	04:56:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/03/29	04:56:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/03/29	04:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/03/29	05:25:00.0	XRT_Custom_430_OG [0x1ae]							
2020/03/29	05:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/03/29	05:52:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	05:52:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	05:52:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	00
2020/03/29	05:53:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2020/03/29	05:53:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2020/03/29	05:53:22.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2020/03/29	05:55:58.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2020/03/29	05:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/03/29	06:02:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	06:03:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2020/03/29	06:04:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_404_OG [0x194]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2020/03/29	06:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	06:36:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	06:36:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/03/29	06:36:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/03/29	06:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/03/29	08:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	08:16:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	08:16:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/03/29	08:16:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/03/29	08:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/03/29	09:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	09:56:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/03/29	09:56:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/03/29	09:56:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/03/29	09:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2020/03/29	15:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	15:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	15:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/03/29	15:23:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/03/29	15:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/03/29	16:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	16:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	16:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/03/29	16:57:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/03/29	17:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/03/29	18:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	18:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	18:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/03/29	18:34:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/03/29	18:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/03/29	20:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	20:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	20:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/03/29	20:11:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/03/29	20:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/03/29	21:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	21:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	21:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/03/29	21:48:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/03/29	21:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/03/29	23:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	23:26:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/29	23:26:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/03/29	23:26:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/03/29	23:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/03/30	01:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/30	01:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/30	01:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/03/30	01:03:06.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/03/30	01:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/03/30	02:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/30	02:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/30	02:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/03/30	02:26:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/03/30	02:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/03/30	04:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/30	04:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/03/30	04:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/03/30	04:01:36.0	XRT_PREFLR_STRT_435_OG [0x1b3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/03/30	04:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/03/30	05:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1







