

# XRT Timeline to be uploaded on 2020/10/29

Period: 2020/10/29 10:49:00 - 2020/11/03 11:33:00

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Normal mode

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**XOB #1B89: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

Term	Pointing (x, y)	Comment
10/29 11:11:30 - 10/29 17:57:00	Track ( 647.4, -431.4) @ 10/29 10:59:00	# OP start + 10min, AR observation (AR12778)
10/29 18:33:30 - 10/30 01:53:00	Track ( 688.9, -427.2) @ 10/29 18:30:30	AR observation (AR12778)
10/30 06:03:00 - 10/30 17:59:54	Track ( 745.4, -420.5) @ 10/30 06:00:00	AR observation (AR12778)

**PROG= 16 Inf.-time(s)**

Subr= 1	1-time(s)	2.0sec										
Seqn= 92	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 47	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96	4-time(s)	60.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1C7D: Synoptic 7 Filter w/ Al-mesh(181/1024/5795), Al-poly(256/2897/8192), Thin-Be(1024/11571/23142) - Thick-Be(65536), Al-poly+Ti-poly(512/8192),**

Term	Pointing (x, y)	Comment
10/29 18:23:00 - 10/29 18:30:24	Fixed ( 0.0, 0.0)	synoptic, shifted 20.5 min

**PROG= 19 1-time(s)**

Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 88	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 44	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23	1-time(s)	4.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 46	1-time(s)	2.0sec										
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17	1-time(s)	2.0sec										
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 86	1-time(s)	2.0sec										
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1C2C: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[1024/11571/23142] with 512x512 G-band+Leak - 45min cad) +**

Term	Pointing (x, y)	Comment
10/30 02:09:30 - 10/30 05:49:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic
10/31 02:03:00 - 10/31 06:08:24	Fixed ( 0.0, 0.0)	HOP349 and synoptic

**PROG= 10 Inf.-time(s)**

Subr= 1	1-time(s)	600.0sec										
Seqn= 1	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 99 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 33 1-time(s) 2.0sec</b>												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 30 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 4-time(s) 600.0sec</b>												
<b>Seqn= 8 1-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 29 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1C8F: Synoptic Q95 2x2 - Al/mesh(64/512/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(128/1024/4096) +**

Term	Pointing (x, y)	Comment
10/30 05:53:00 - 10/30 05:59:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic
10/30 18:03:00 - 10/30 18:09:54	Fixed ( 0.0, 0.0)	synoptic
10/31 06:11:30 - 10/31 06:19:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic

**PROG= 12 1-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 5 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 36 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 85 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 54 1-time(s) 2.0sec</b>												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 23 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1BB9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

Term	Pointing (x, y)	Comment
10/30 18:13:00 - 10/30 22:59:54	Track ( 795.7, -413.1) @ 10/30 18:10:00	AR observation (AR12778)
10/31 06:23:00 - 10/31 10:29:00	Track ( 835.4, -405.4) @ 10/31 06:20:00	AR Observation (AR12778)

**PROG= 14 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 92 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Subr= 2 5-time(s) 2.0sec</b>												
<b>Seqn= 47 1-time(s) 2.0sec</b>												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 96 4-time(s) 90.0sec</b>												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1C5A: HOP336 1-filter - Al/poly -384x384, 1024ms and 8192s, 60s-cadence, G-band - 384x384 1ms**

Term	Pointing (x, y)	Comment
10/30 23:03:00 - 10/31 01:59:54	Track ( -36.1, -114.1) @ 10/30 23:00:00	HOP393
<b>PROG= 01 Inf.-time(s)</b>		
Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 16 2-time(s) 2.0sec		
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec		
Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 90 1-time(s) 30.0sec		
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec		
Subr= 3 15-time(s) 2.0sec		
└─ Seqn= 72 1-time(s) 60.0sec		
└─ Al-poly/Open Al-poly/Ti-poly close Safe Norm 8.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec		
└─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

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**Flare mode**

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**XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G**

Term	Pointing (x, y)	Comment
10/29 11:11:30 - 10/29 17:57:00	Track ( 647.4, -431.4) @ 10/29 10:59:00	# OP start + 10min, AR observation (AR12778)
10/29 18:33:30 - 10/30 01:53:00	Track ( 688.9, -427.2) @ 10/29 18:30:30	AR observation (AR12778)
10/30 02:09:30 - 10/30 05:49:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic
10/30 06:03:00 - 10/30 17:59:54	Track ( 745.4, -420.5) @ 10/30 06:00:00	AR observation (AR12778)
10/30 18:13:00 - 10/30 22:59:54	Track ( 795.7, -413.1) @ 10/30 18:10:00	AR observation (AR12778)
10/30 23:03:00 - 10/31 01:59:54	Track ( -36.1, -114.1) @ 10/30 23:00:00	HOP393
10/31 02:03:00 - 10/31 06:08:24	Fixed ( 0.0, 0.0)	HOP349 and synoptic
10/31 06:23:00 - 10/31 10:29:00	Track ( 835.4, -405.4) @ 10/31 06:20:00	AR Observation (AR12778)
<b>PROG= 04 30-time(s)</b>		
Subr= 1 20-time(s) 2.0sec		
└─ Seqn= 11 1-time(s) 2.0sec		
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec		
└─ Seqn= 73 1-time(s) 10.0sec		
└─ thin-Be/Open med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
└─ med-Be/Open Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 10 1-time(s) 2.0sec		
└─ med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec		
└─ Seqn= 11 1-time(s) 2.0sec		
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec		
└─ Seqn= 87 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec		
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec		
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec		
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

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**Active Region Search**

\* \* \* \* \*

NOT USED

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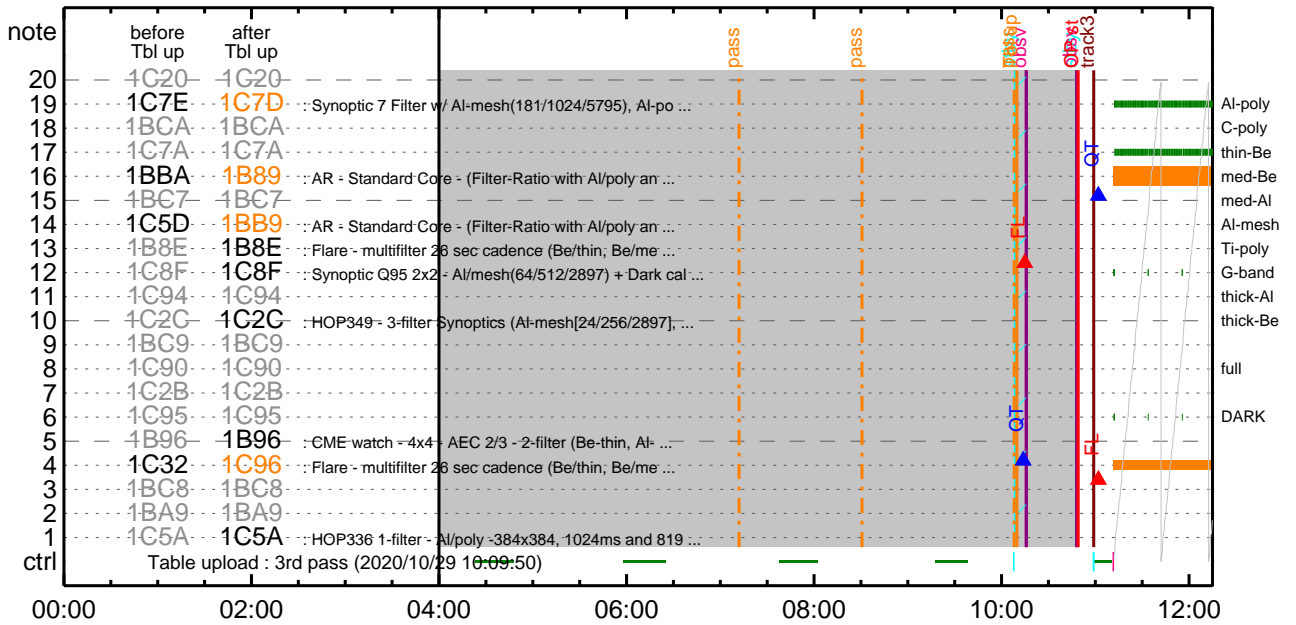
**Flare Detection**

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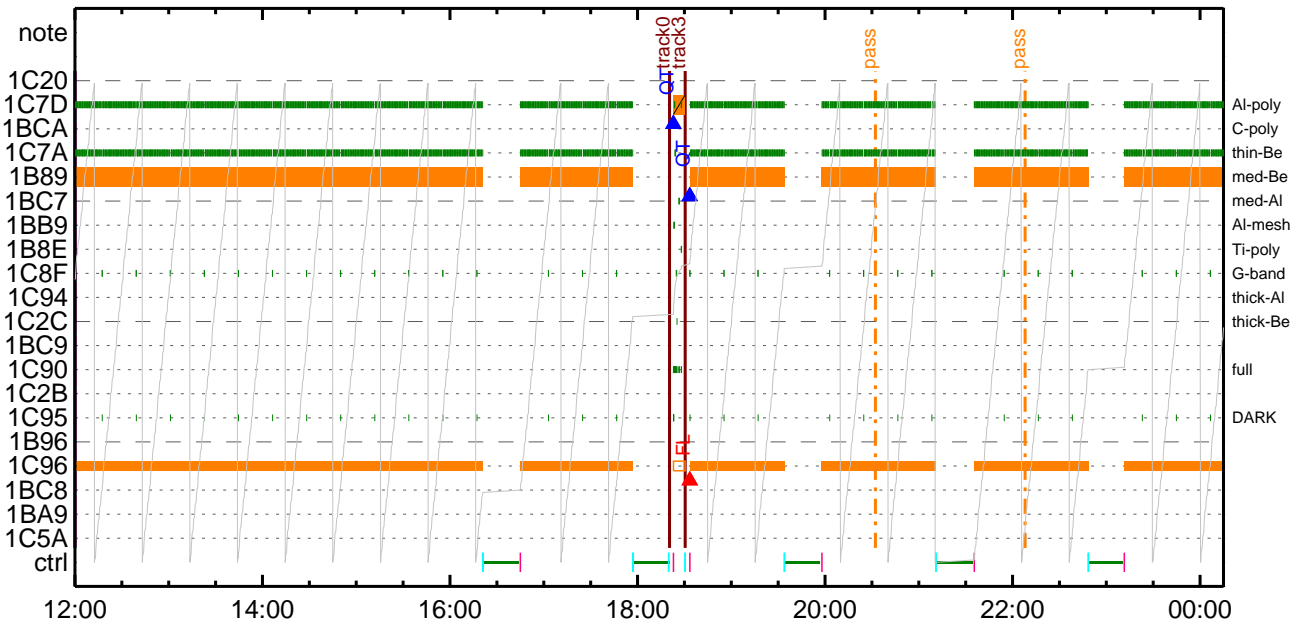
**FLD Patrol**

Term	Pointing (x, y)	Comment
10/29 18:30:48 - 10/30 05:50:18	Track ( 688.9, -427.2) @ 10/29 18:30:30	AR observation (AR12778)
10/30 06:00:18 - 10/30 18:00:18	Track ( 745.4, -420.5) @ 10/30 06:00:00	AR observation (AR12778)
10/30 18:10:18 - 10/31 06:08:48	Track ( 795.7, -413.1) @ 10/30 18:10:00	AR observation (AR12778)
10/31 06:20:18 - 11/03 11:33:00	Track ( 835.4, -405.4) @ 10/31 06:20:00	AR Observation (AR12778)
Al-poly/Open Al-poly/Open close Safe Norm 8ms Obs 8x8 Q=50 30sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

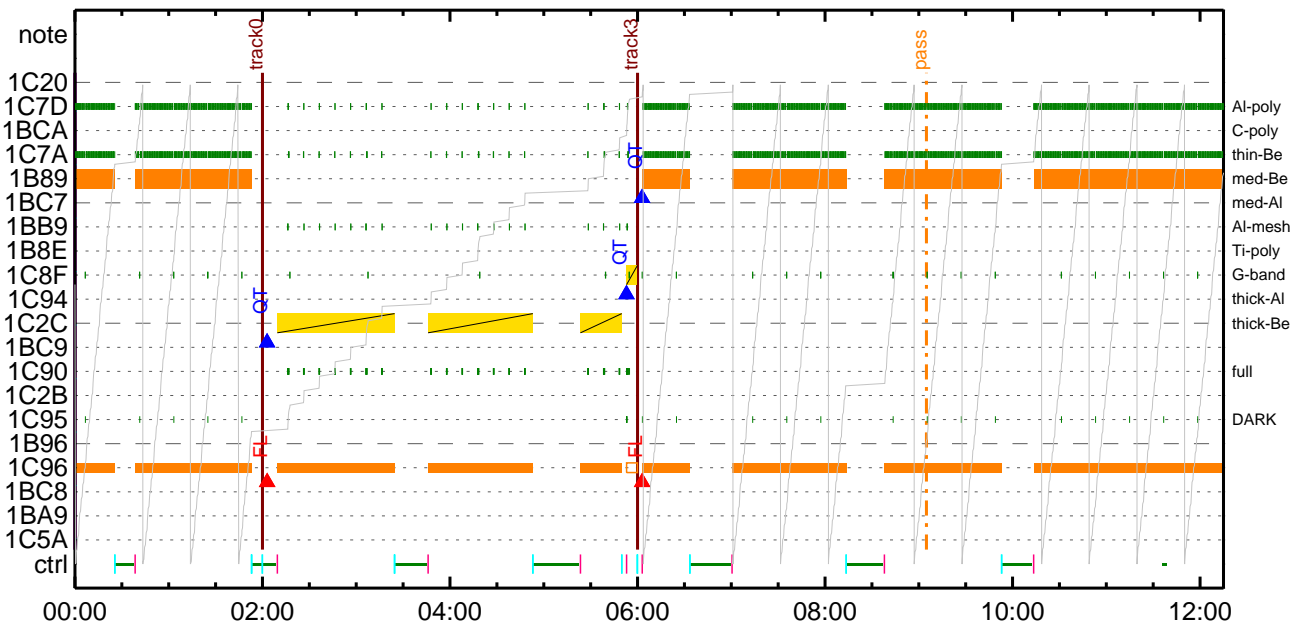
### CMDI #0262 2020/10/29



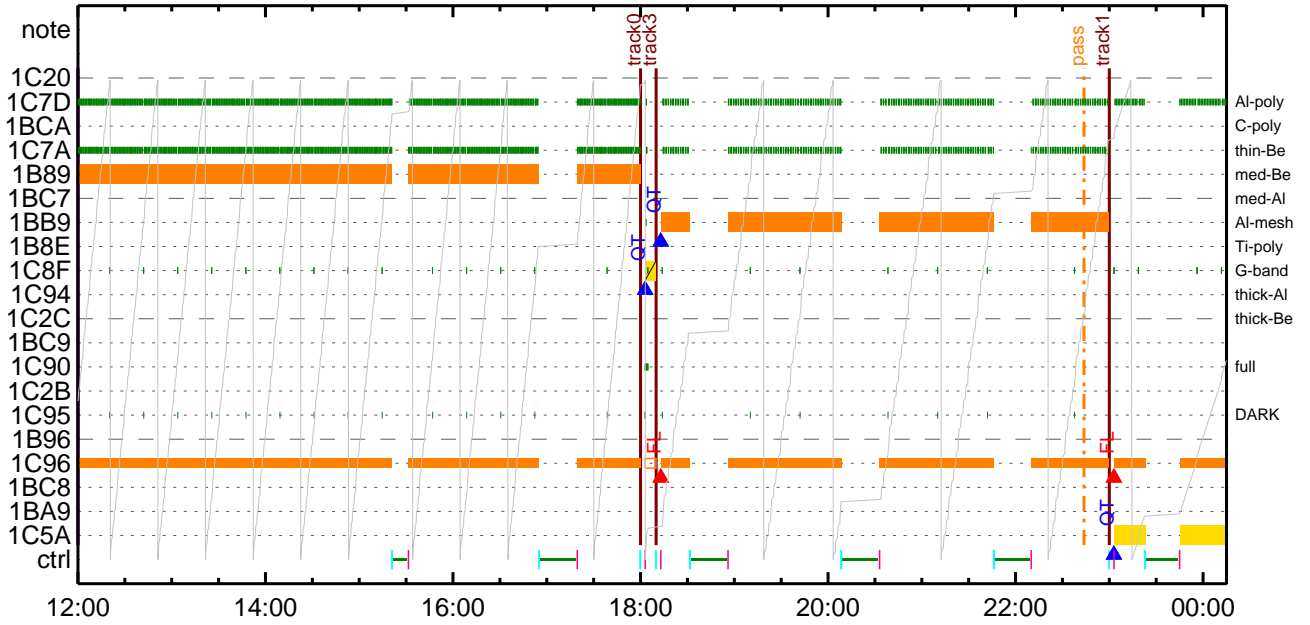
### CMDI #0262 2020/10/29



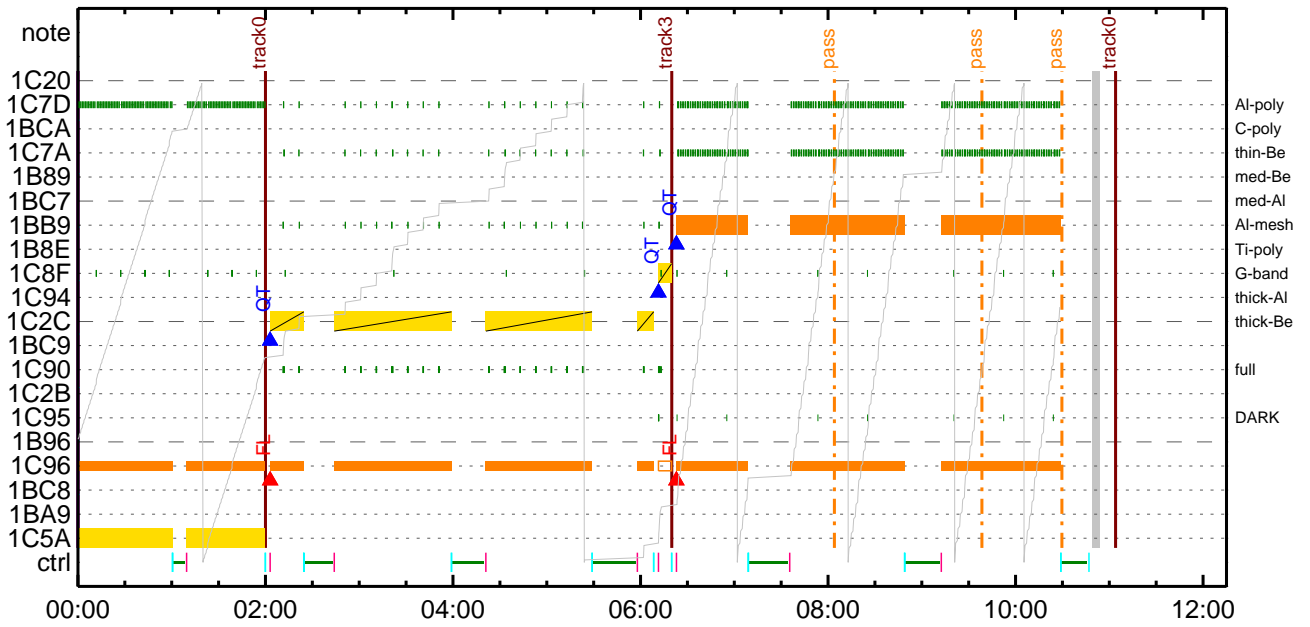
### CMDI #0262 2020/10/30



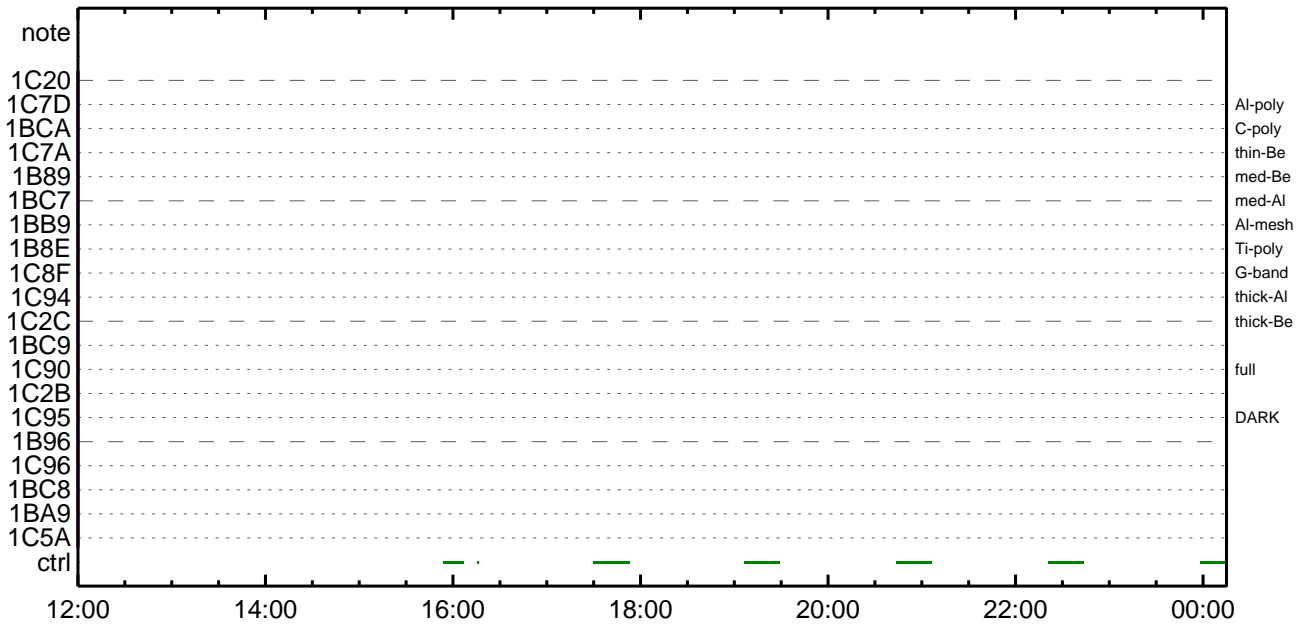
CMDI #0262 2020/10/30



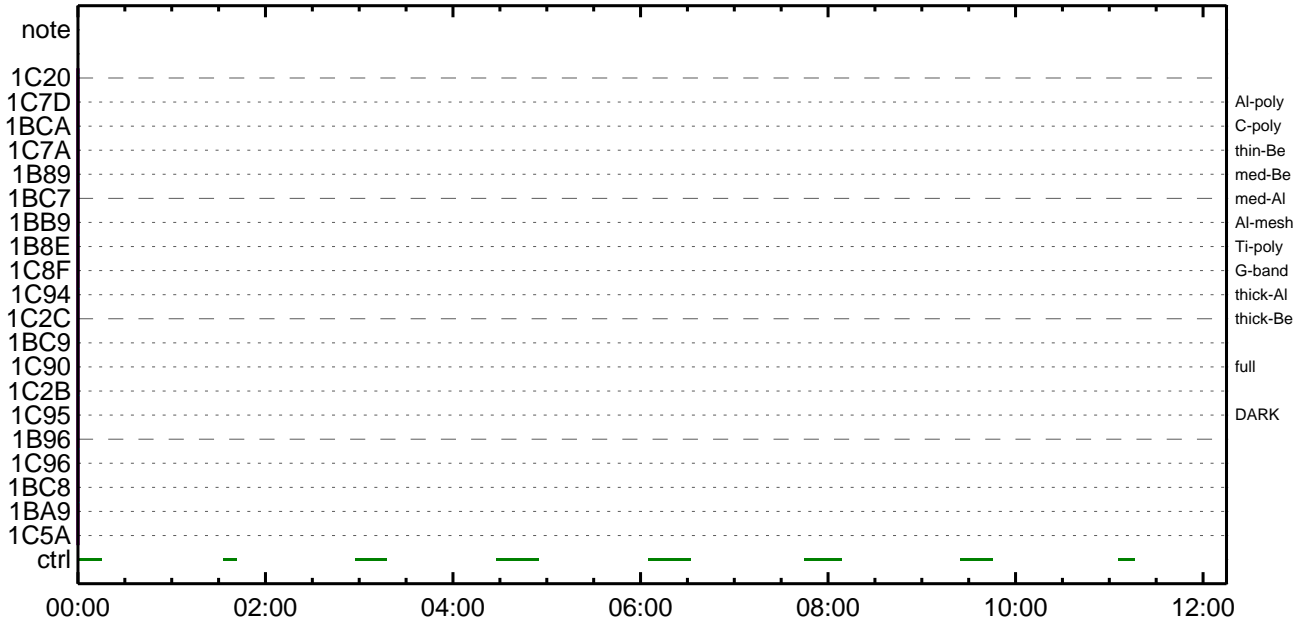
CMDI #0262 2020/10/31



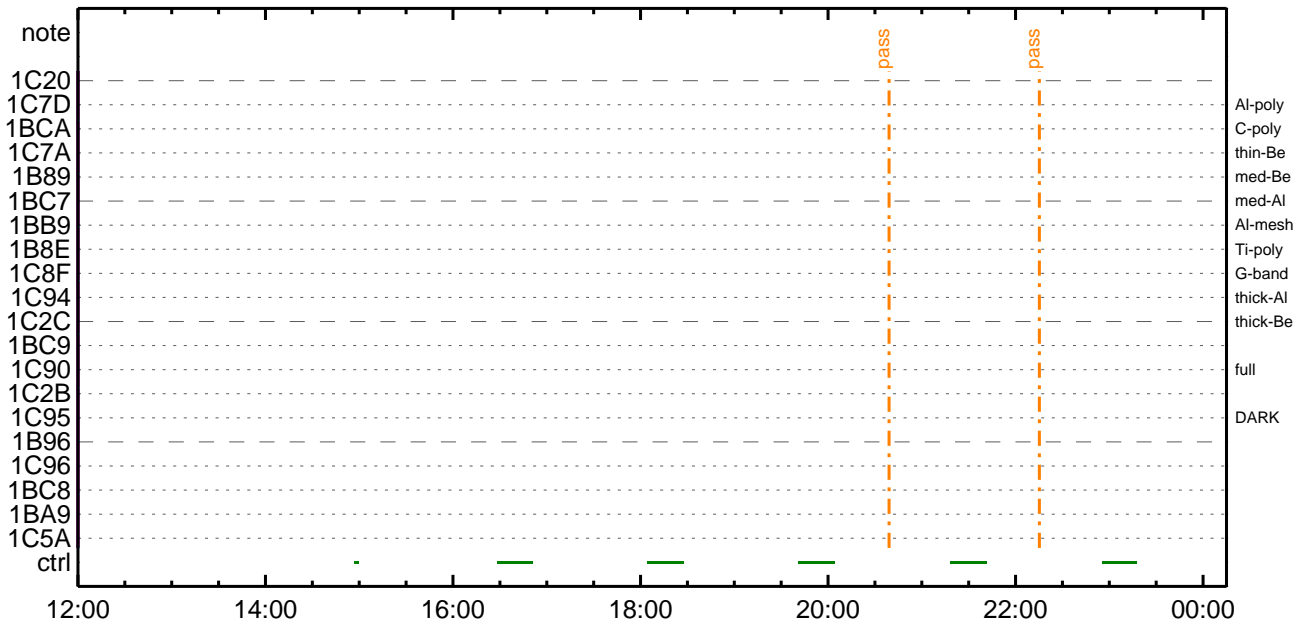
CMDI #0262 2020/10/31



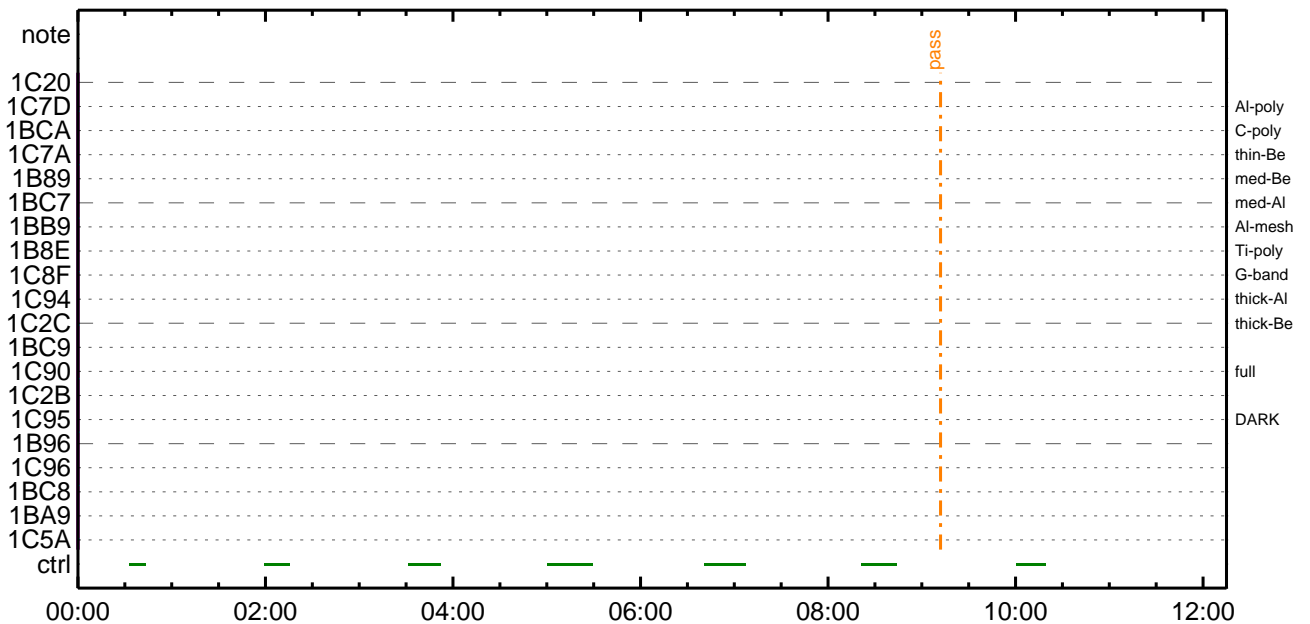
CMDI #0262 2020/11/01



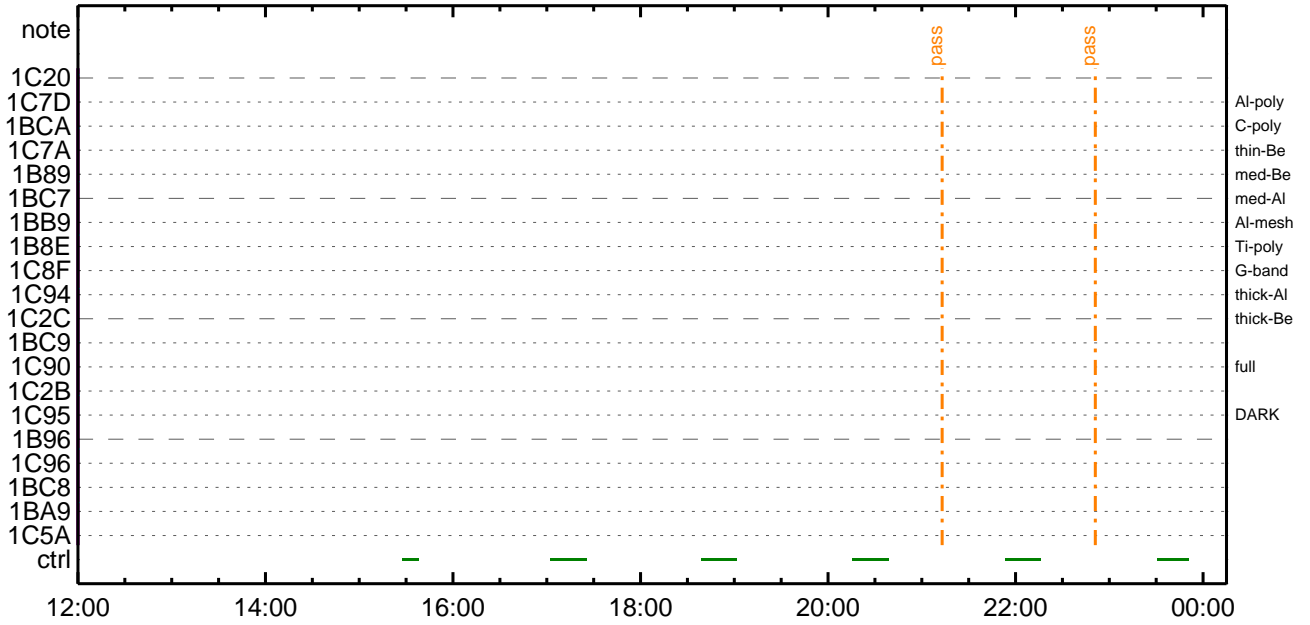
CMDI #0262 2020/11/01



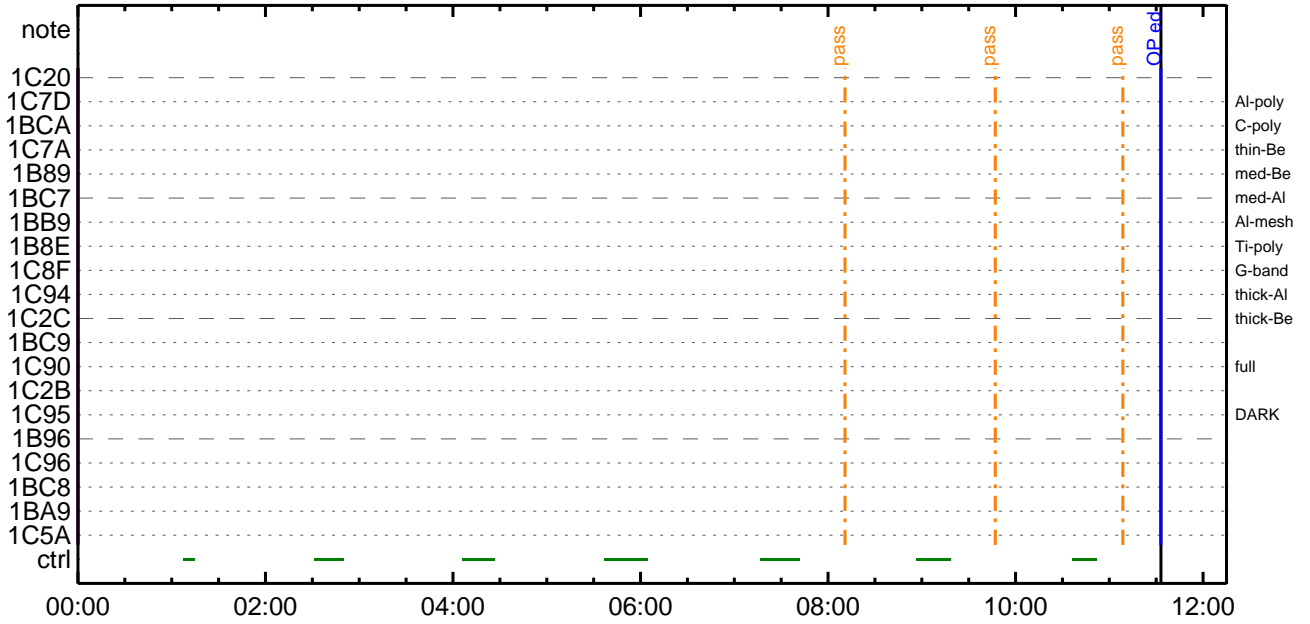
CMDI #0262 2020/11/02



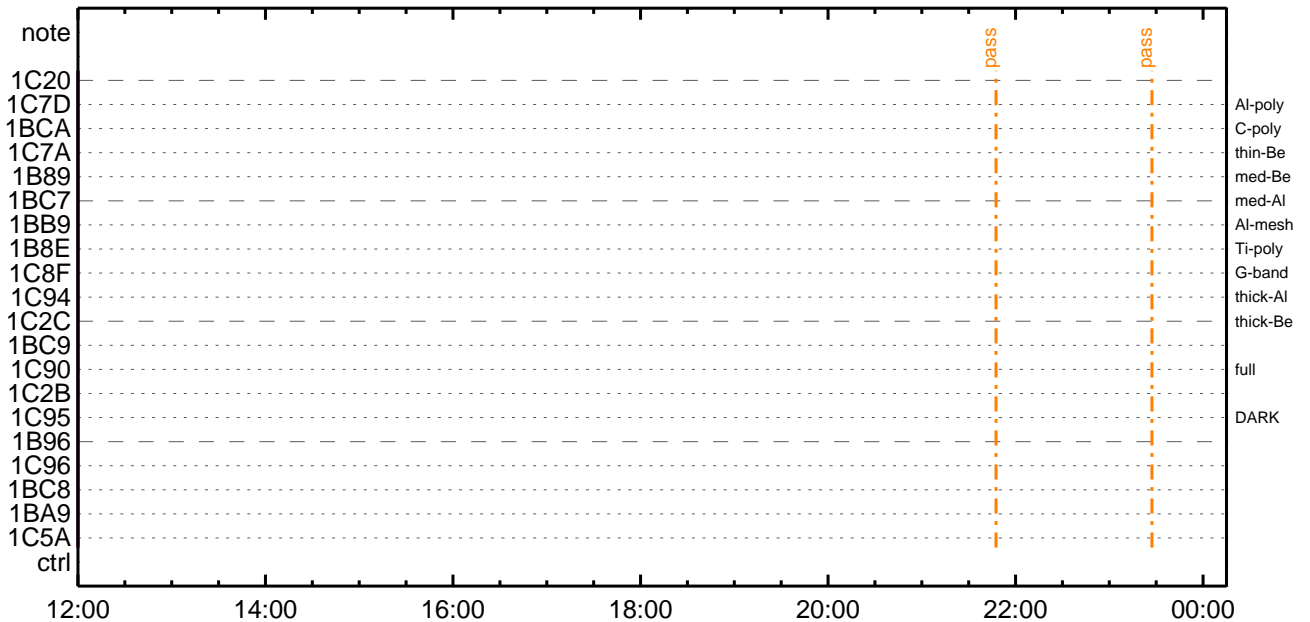
CMDI #0262 2020/11/02



CMDI #0262 2020/11/03



CMDI #0262 2020/11/03



(a) Spacecraft Operation Procedure (real-commands)

```
main-115 2020-10-29 11:48:27 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YBYóYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËçòðÁâ•µ°Ë»Í×ÁÇçúÝçYÁY×Yí;¼YÉ;ËË%µ•íÉ;ËòÉ¼°ÇÓâ•ç¼í¹çú;çÀ®, ùñ¹âëñèðçÀ+ç®â•çÉâçç³âÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ; | YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-115:OP
0020 ()
0021 . S. OG og-115:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYóY×¼²¹Í»ðð³ÍÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGâÍ¼É¹ç•ë²ÏOKðð³ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYóY×¼²¹Í»ðð³ÍÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGâÍ¼É¹ç•ë²ÏOKðð³ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYóY×¼²¹Í»ðð³ÍÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPâÍ¼É¹ç•ë²ÏOKðð³ÍÇ§
0081 C.
0082 . C. ***** ²¼ðÍ¼Ã´ ¶Á°âËË¬°Á+ç® (¼âµ-YÀYóY×¼²¹Í»ðð³ÍÇ§ðð³ÍÇ§¼²¹Í»ðð³ÍÇ§ççç) *****
0083 C. DHUYâ;¼YÉ;Ë¼Y¼; Yí;¼YÉ;ËððÍá¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADâ¬Á+ç®NGâÍ¼¹ç;ç°É²¼ðÍTI-CMDÁ+ç®ðÍ¼Á¹Ôâ•çÉâçç³âÈ;f
```



```

0096 C.          SET EDUMP I±°iYÑY¹aÇ¹Öa|a³aE;f
0097 C.
0098 C. TIY³YF¥ÖYÉaðdÄDİ¿(UT)
0099 +. TI 2020-10-29 10:44:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2020-10-29 10:44:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2020-10-29 10:44:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2020-10-29 10:48:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °E²¼aİÄè%îÍÑaİYÁY§YÁY-¹àÛ
0116 C.          çç[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0118 C.          çç[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          çç[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 C. *****
0122 C. TIİî°èYÀYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          çç[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          çç[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          çç[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          çç[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          çç[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          çç[HK1_PKT_FORM_NO]          EQ          7
0136 C.          çç[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          çç[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          çç[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          çç[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 C. YÀYÖY×½ªİ»að³İÇ§
0142 C.          çç[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKað³İÇ§
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y½,¥i;¼YÈ;Èaðİáa¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          çç[HK1_PKT_FORM_NO]          EQ          2
0150 C.          çç[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          çç[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          çç[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2020-10-29 10:48:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC          (21 02)
0163 +. TI 2020-10-29 10:48:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC          (22)
0166 C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2020-10-29 10:48:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC          (c3)
0176 C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´úÄîaİ»ö¼YªEÄa¹aèDCBC•x²è *****
0181 C. (%á°îYÖYÁYÉY¥YÉYÁYÇYèaE¼aª¼Á»Üa¹aè)
0182 C. S. DC-BC dcbc-402:DCBC
0183 C. (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** YD¥¹•İ Daily±¿İÑaÈ¹Øa¹aèDCBC•x²è *****
0187 C. S. DC-BC dcbc-153:DCBC
0188 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;ãLOS¥ÁY§YÁY-¼Á»Ü;ã
0192 C.
0193 C. ***** LOS *****

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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 12s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2020-10-29 10:48:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP (uAÎqÎ»ö¼ÝðÈÀð¹ªèDCBC•×²è *****
0156 C. (¼â°îÝÔYÄEÿPÿÈYäYçYèªE¾¼ª¼Å»Ûª¹ªè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ÝDÝ¹•Ï Daily±¿ÍÑªÈ´Øª¹ªèDCBC•×²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOSÝÁY§YÄY¹¼Å»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 05)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 0d)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2020-10-29 10:48:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP `úÃîï»ö%ÝðËÄð¹ñèDCBC•x²è *****
0169 C. (%ã°îÿÓYÁYÈYÏYÉYáYçYèèE%¼ø¼Á»Û¹ñè)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥D¥¹•İ Daily±¿İÑñÈ´Øñ¹ñèDCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Á»Û;ã
0180 C.
0181 . C. ***** LOS *****
0182 C.

```

\*\*\* OP Sequence for XRT \*\*\*

```

2020/10/29 10:58:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2020/10/29 10:58:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2020/10/29 10:58:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2020/10/29 10:59:00.0 AOCS_Ore-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 03 00 00 00 00
2020/10/29 10:59:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2020/10/29 10:59:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2020/10/29 10:59:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2020/10/29 10:59:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2020/10/29 10:59:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2020/10/29 11:01:56.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 10
2020/10/29 11:01:58.0 XRT_FL_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 04
2020/10/29 11:10:30.0 XRT_Custom_430_OG [0x1ae]
2020/10/29 11:11:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2020/10/29 16:21:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2020/10/29 16:21:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2020/10/29 16:21:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2020/10/29 16:21:06.0 XRT_PREFLR_STRT_407_OG [0x197]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2020/10/29 16:24:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2020/10/29 16:44:00.0 XRT_Custom_430_OG [0x1ae]
2020/10/29 16:45:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2020/10/29 17:57:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2020/10/29 17:57:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2020/10/29 17:57:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2020/10/29 17:57:06.0 XRT_PREFLR_STRT_407_OG [0x197]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2020/10/29 18:00:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2020/10/29 18:19:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2020/10/29 18:19:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2020/10/29 18:19:58.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2020/10/29 18:20:18.0 XRT_FLD_DIS_409_OG [0x199]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2020/10/29 18:20:20.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2020/10/29 18:20:22.0 XRT_ARS_DIS_443_OG [0x1bb]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2020/10/29 18:20:30.0 AOCS_Ore-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2020/10/29 18:22:58.0 XRT_QT_PROG_SET_416_OG [0x1a0]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 13
2020/10/29 18:23:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2020/10/29 18:30:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2020/10/29 18:30:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2020/10/29 18:30:28.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2020/10/29 18:30:30.0 AOCS_Ore-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 03 00 00 00 00
2020/10/29 18:30:48.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2020/10/29 18:30:50.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2020/10/29 18:30:52.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2020/10/29 18:30:54.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2020/10/29 18:30:56.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2020/10/29 18:33:26.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 10
2020/10/29 18:33:28.0 XRT_FL_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 04
2020/10/29 18:33:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2020/10/29 19:34:00.0 XRT_CTRL_MANU_400_OG [0x190]

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2020/10/29	19:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/29	19:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/29	19:34:06.0	XRT_PREFLR_STRT_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/29	19:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/29	19:57:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/29	19:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/29	21:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/29	21:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/29	21:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/29	21:11:06.0	XRT_PREFLR_STRT_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/29	21:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/29	21:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/29	21:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/29	22:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/29	22:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/29	22:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/29	22:48:36.0	XRT_PREFLR_STRT_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/29	22:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/29	23:10:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/29	23:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	00:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	00:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	00:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	00:25:36.0	XRT_PREFLR_STRT_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	00:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/30	00:37:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/30	00:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	01:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	01:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	01:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	01:53:06.0	XRT_PREFLR_STRT_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	01:53:06.0	XRT_PREFLR_STRT_407_OG [0x197]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/30	01:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/30	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	02:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2020/10/30	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2020/10/30	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2020/10/30	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2020/10/30	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2020/10/30	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2020/10/30	02:02:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	02:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2020/10/30	02:08:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2020/10/30	02:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	03:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	03:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	03:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	03:24:36.0	XRT_PREFLR_STRT_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	



2020/10/30	03:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/30	03:45:00.0	XRT_Custom_430_OG [0x1ae]					
2020/10/30	03:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	04:53:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	04:53:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	04:53:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	04:53:06.0	XRT_PREFLR_STRT_407_OG [0x197]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/30	04:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/30	05:22:30.0	XRT_Custom_430_OG [0x1ae]					
2020/10/30	05:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	05:49:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	05:49:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	05:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2020/10/30	05:50:18.0	XRT_FLD_DIS_409_OG [0x199]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2020/10/30	05:50:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2020/10/30	05:50:22.0	XRT_ARS_DIS_443_OG [0x1bb]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2020/10/30	05:52:58.0	XRT_QT_PROG_SET_431_OG [0x1af]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2020/10/30	05:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	05:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2020/10/30	06:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2020/10/30	06:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2020/10/30	06:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2020/10/30	06:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2020/10/30	06:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2020/10/30	06:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	06:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2020/10/30	06:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2020/10/30	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	06:33:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	06:33:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	06:33:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	06:33:36.0	XRT_PREFLR_STRT_407_OG [0x197]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/30	06:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/30	06:59:30.5	XRT_Custom_430_OG [0x1ae]					
2020/10/30	07:00:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	08:13:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	08:13:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	08:13:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	08:13:36.0	XRT_PREFLR_STRT_407_OG [0x197]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/30	08:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/30	08:37:00.0	XRT_Custom_430_OG [0x1ae]					
2020/10/30	08:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	09:53:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	09:53:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	09:53:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	09:53:06.0	XRT_PREFLR_STRT_407_OG [0x197]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2020/10/30	09:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/10/30	10:12:30.0	XRT_Custom_430_OG [0x1ae]							
2020/10/30	10:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/30	15:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	15:21:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	15:21:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/10/30	15:21:06.0	XRT_PREFLR_STRT_407_OG [0x197]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/10/30	15:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/10/30	15:30:30.0	XRT_Custom_430_OG [0x1ae]							
2020/10/30	15:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/30	16:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	16:55:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	16:55:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/10/30	16:55:06.0	XRT_PREFLR_STRT_407_OG [0x197]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/10/30	16:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/10/30	17:18:30.0	XRT_Custom_430_OG [0x1ae]							
2020/10/30	17:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/30	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2020/10/30	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2020/10/30	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2020/10/30	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2020/10/30	18:00:22.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2020/10/30	18:02:58.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2020/10/30	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/30	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2020/10/30	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2020/10/30	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2020/10/30	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2020/10/30	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2020/10/30	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2020/10/30	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/10/30	18:12:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2020/10/30	18:12:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2020/10/30	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/30	18:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	18:31:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	18:31:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/10/30	18:31:36.0	XRT_PREFLR_STRT_407_OG [0x197]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/10/30	18:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/10/30	18:55:00.0	XRT_Custom_430_OG [0x1ae]							
2020/10/30	18:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/30	20:08:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	20:08:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/30	20:08:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/10/30	20:08:36.0	XRT_PREFLR_STRT_407_OG [0x197]							

2020/10/30	20:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/30	20:32:00.0	XRT_Custom_430_OG [0x1ae]					
2020/10/30	20:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	21:46:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	21:46:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	21:46:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	21:46:06.0	XRT_PREFLR_STRT_407_OG [0x197]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/30	21:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/30	22:09:00.0	XRT_Custom_430_OG [0x1ae]					
2020/10/30	22:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2020/10/30	23:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2020/10/30	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2020/10/30	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2020/10/30	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2020/10/30	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2020/10/30	23:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	23:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2020/10/30	23:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2020/10/30	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/30	23:23:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	23:23:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/30	23:23:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/30	23:23:06.0	XRT_PREFLR_STRT_407_OG [0x197]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/30	23:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/30	23:44:00.0	XRT_Custom_430_OG [0x1ae]					
2020/10/30	23:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/31	01:00:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/31	01:00:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/31	01:00:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/31	01:00:36.0	XRT_PREFLR_STRT_407_OG [0x197]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2020/10/31	01:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2020/10/31	01:08:30.0	XRT_Custom_430_OG [0x1ae]					
2020/10/31	01:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2020/10/31	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/31	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2020/10/31	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2020/10/31	02:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2020/10/31	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2020/10/31	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2020/10/31	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2020/10/31	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2020/10/31	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2020/10/31	02:02:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2020/10/31	02:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2020/10/31	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	

2020/10/31	02:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	02:24:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	02:24:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/10/31	02:24:36.0	XRT_PREFLR_STRT_407_OG [0x197]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/10/31	02:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/10/31	02:43:00.0	XRT_Custom_430_OG [0x1ae]							
2020/10/31	02:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/31	03:59:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	03:59:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	03:59:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/10/31	03:59:06.0	XRT_PREFLR_STRT_407_OG [0x197]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/10/31	04:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/10/31	04:20:00.0	XRT_Custom_430_OG [0x1ae]							
2020/10/31	04:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/31	05:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	05:29:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	05:29:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/10/31	05:29:06.0	XRT_PREFLR_STRT_407_OG [0x197]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/10/31	05:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/10/31	05:57:00.0	XRT_Custom_430_OG [0x1ae]							
2020/10/31	05:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/31	06:08:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	06:08:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	06:08:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2020/10/31	06:08:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2020/10/31	06:08:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2020/10/31	06:08:52.0	XRT_ARS_DIS_443_OG [0x1bb]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2020/10/31	06:11:28.0	XRT_QT_PROG_SET_431_OG [0x1af]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2020/10/31	06:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/31	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	06:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	06:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2020/10/31	06:20:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2020/10/31	06:20:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2020/10/31	06:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2020/10/31	06:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2020/10/31	06:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2020/10/31	06:20:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/10/31	06:22:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2020/10/31	06:22:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2020/10/31	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/31	07:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	07:09:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2020/10/31	07:09:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2020/10/31	07:09:06.0	XRT_PREFLR_STRT_407_OG [0x197]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2020/10/31	07:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2020/10/31	07:34:30.0	XRT_Custom_430_OG [0x1ae]							
2020/10/31	07:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2020/10/31	08:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							

2020/10/31	08:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/10/31	08:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/10/31	08:49:06.0	XRT_PREFLR_STRT_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/10/31	08:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/10/31	09:11:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/10/31	09:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_AUTO	1	07-F0	c0
2020/10/31	10:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2020/10/31	10:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/10/31	10:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2020/10/31	10:29:06.0	XRT_PREFLR_STRT_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2020/10/31	10:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2020/10/31	10:47:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2020/10/31	11:04:00.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00