

XRT Timeline to be uploaded on 2021/05/06

Period: 2021/05/06 10:34:00 - 2021/05/11 11:21:00

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Normal mode

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XOB #1CA8: HOP415 - Thin-Be and Al/poly+Ti/poly - 512x512 - 1x1 - 240 sec cad - Gband												
Term	Pointing (x, y)	Comment										
05/06 11:24:30 - 05/06 12:36:30	Fixed (870.0, -300.0)	# OP start + 10min HOP415 2/2										
05/07 07:07:00 - 05/07 07:59:00	Track (532.0, 41.8) @ 05/07 07:04:00	HOP415 1/4										
05/07 08:46:00 - 05/07 09:39:00	Track (423.2, 41.3) @ 05/07 08:43:00	HOP415 2/4										
05/07 10:25:00 - 05/07 11:20:30	Track (314.3, 40.8) @ 05/07 10:22:00	HOP415 3/4										
05/07 12:04:00 - 05/07 13:59:54	Track (205.2, 40.3) @ 05/07 12:01:00	HOP415 4/4										
PROG= 18 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 92 2-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Subr= 2 15-time(s) 240.0sec												
└─ Seqn= 12 1-time(s) 2.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 11.3s Obs 1x1 512x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 32.0s Obs 1x1 512x512 (1024, 1024) DPCM 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1CA8: HOP416 Thin-Be and Al/poly+Ti/poly - 256x256 - 1x1 - 60sec cad - Gband AEC0												
Term	Pointing (x, y)	Comment										
05/06 13:03:01 - 05/06 13:59:54	Track (-22.1, -289.0) @ 05/06 12:58:00	HOP416										
PROG= 10 Inf.-time(s)												
└─ Subr= 1 2-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Subr= 2 60-time(s) 60.0sec												
└─ Seqn= 28 1-time(s) 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 32.0s Obs 1x1 256x256 (1064, 1048) Q=95 0 0 2.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 11.3s Obs 1x1 256x256 (1064, 1048) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1CA1: HOP410 - CME watch - 4x4 - AEC 2/3 - Al-poly - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 120s cad (G-band/Leak first)												
Term	Pointing (x, y)	Comment										
05/06 14:03:00 - 05/06 22:27:00	Fixed (0.0, 0.0)	HOP410										
05/07 14:03:00 - 05/07 22:59:54	Fixed (0.0, 0.0)	HOP410										
PROG= 19 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 30 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 30-time(s) 120.0sec												
└─ Seqn= 6 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1C2B: AR (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 90s												
Term	Pointing (x, y)	Comment										
05/06 23:03:00 - 05/07 01:43:00	Track (-513.6, -301.8) @ 05/06 23:00:00	AR obs										
PROG= 05 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 71 3-time(s) 2.0sec												
└─ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 3 0 2.0sec												
└─ Subr= 2 60-time(s) 90.0sec												
└─ Seqn= 94 1-time(s) 40.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec												
└─ Seqn= 58 1-time(s) 40.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─ Seqn= 48 1-time(s) 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C98: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[512/8192/23142] with 512x512 G-band+Leak - 45min cad) + C

Term	Pointing (x, y)	Comment
05/07 02:10:00 - 05/07 05:51:54	Fixed (0.0, 0.0)	HOP349 and synoptic, shifted -8.0 min

PROG= 08 Inf.-time(s)

Subr= 1 1-time(s) 600.0sec												
Seqn= 1 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 4-time(s) 600.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C9E: Synoptic Q95 2x2 - Al/mesh(24/256/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(45/512/4096) + T

Term	Pointing (x, y)	Comment
05/07 05:55:00 - 05/07 06:01:54	Fixed (0.0, 0.0)	HOP349 and synoptic, shifted -8.0 min
05/08 03:28:00 - 05/08 03:33:00	Fixed (0.0, 0.0)	synoptic, shifted 3.5 min

PROG= 15 1-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 1 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 84 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BD4: AR (Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 30s cad-2

Term	Pointing (x, y)	Comment
05/07 06:05:00 - 05/07 06:19:00	Track (-461.9, -300.3) ^{© 05/07 06:02:00}	AR obs
05/07 23:32:31 - 05/08 03:24:54	Track (-329.5, -297.7) ^{© 05/07 23:00:00}	AR obs

PROG= 01 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 71 3-time(s) 2.0sec												
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	3	0	2.0sec
Subr= 2 100-time(s) 30.0sec												

Seqn= 15		1-time(s)		15.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
Seqn= 58		1-time(s)		12.0sec								
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GB

Term	Pointing (x, y)	Comment
05/06 11:24:30 - 05/06 12:36:30	Fixed (870.0, -300.0)	# OP start + 10min HOP415 2/2
05/06 13:03:01 - 05/06 13:59:54	Track (-22.1, -289.0) @ 05/06 12:58:00	HOP416
05/06 14:03:00 - 05/06 22:27:00	Fixed (0.0, 0.0)	HOP410
05/06 23:03:00 - 05/07 01:43:00	Track (-513.6, -301.8) @ 05/06 23:00:00	AR obs
05/07 02:10:00 - 05/07 05:51:54	Fixed (0.0, 0.0)	HOP349 and synoptic, shifted -8.0 min
05/07 06:05:00 - 05/07 06:19:00	Track (-461.9, -300.3) @ 05/07 06:02:00	AR obs
05/07 07:07:00 - 05/07 07:59:00	Track (532.0, 41.8) @ 05/07 07:04:00	HOP415 1/4
05/07 08:46:00 - 05/07 09:39:00	Track (423.2, 41.3) @ 05/07 08:43:00	HOP415 2/4
05/07 10:25:00 - 05/07 11:20:30	Track (314.3, 40.8) @ 05/07 10:22:00	HOP415 3/4
05/07 12:04:00 - 05/07 13:59:54	Track (205.2, 40.3) @ 05/07 12:01:00	HOP415 4/4
05/07 14:03:00 - 05/07 22:59:54	Fixed (0.0, 0.0)	HOP410
05/07 23:32:31 - 05/08 03:24:54	Track (-329.5, -297.7) @ 05/07 23:00:00	AR obs

PROG= 04 30-time(s)

Subr= 1		20-time(s)		2.0sec								
Seqn= 11		1-time(s)		2.0sec								
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73		1-time(s)		10.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec								
Seqn= 10		1-time(s)		2.0sec								
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)		2.0sec								
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87		1-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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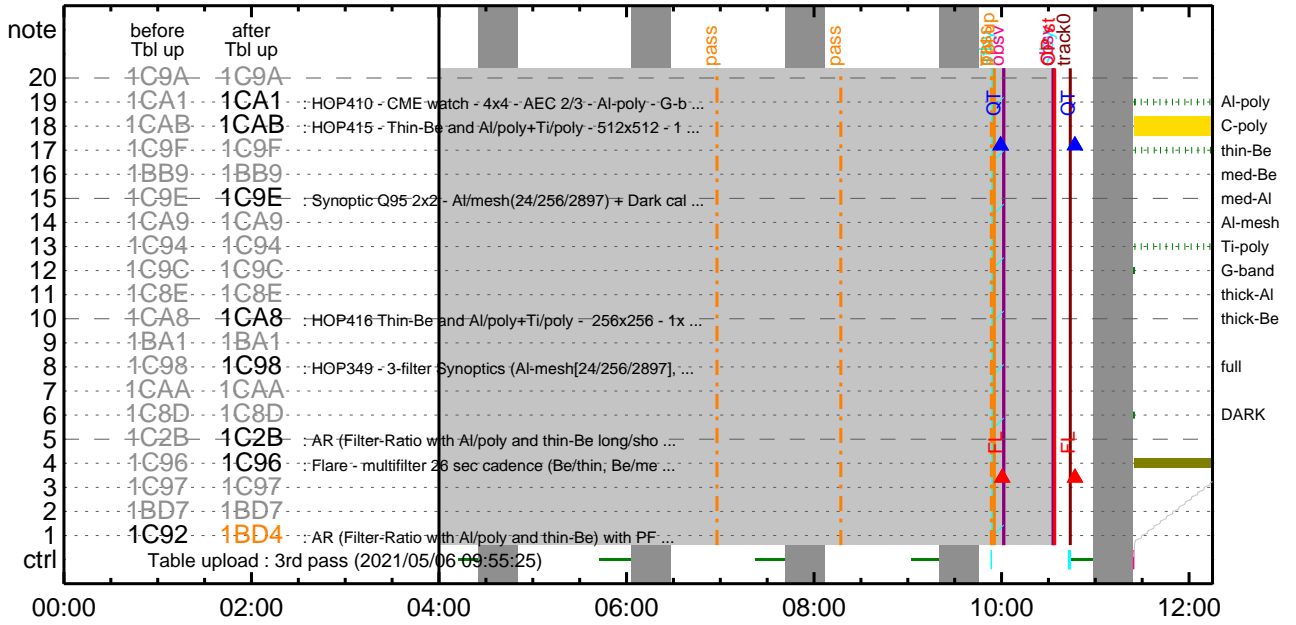
Flare Detection

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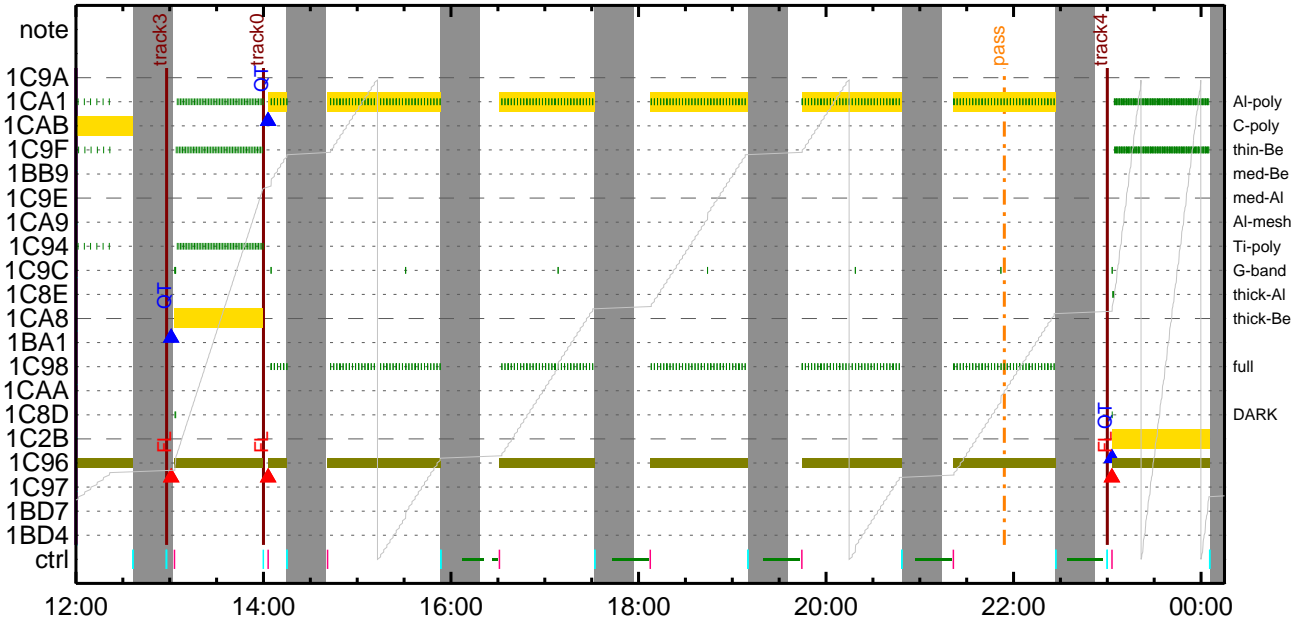
FLD Patrol

Term	Pointing (x, y)	Comment
05/07 06:02:18 - 05/08 03:25:18	Track (-461.9, -300.3) @ 05/07 06:02:00	AR obs
Al-poly/Open	Al-poly/Open	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

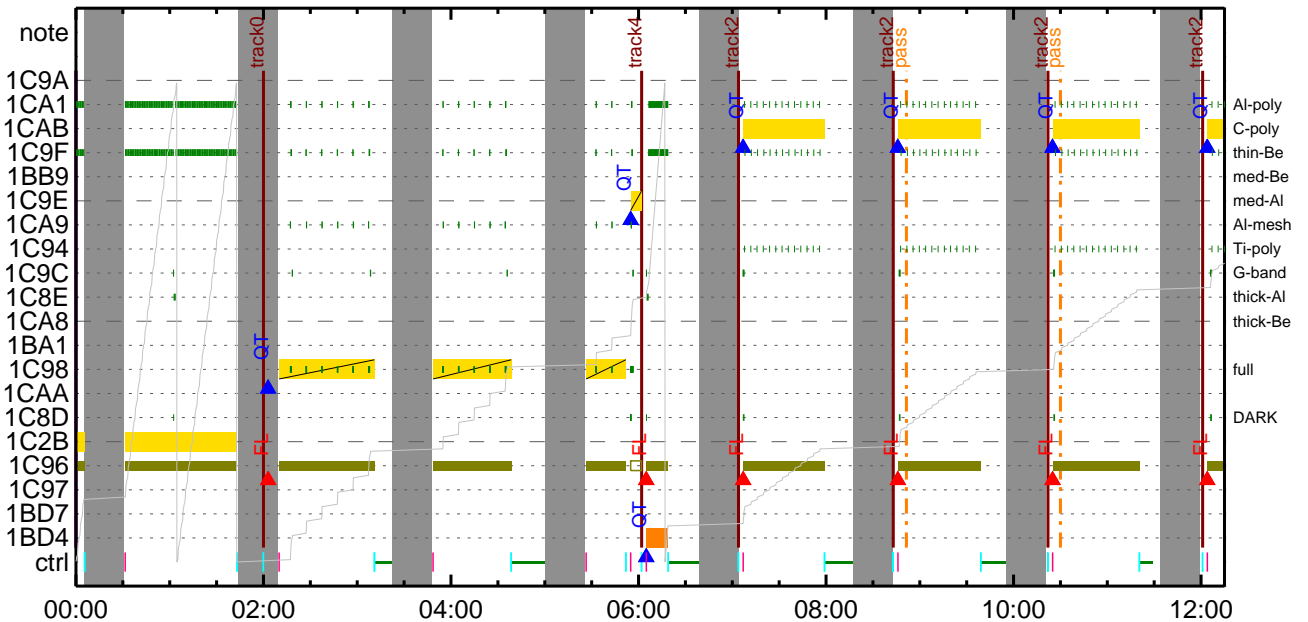
CMDI #0629 2021/05/06



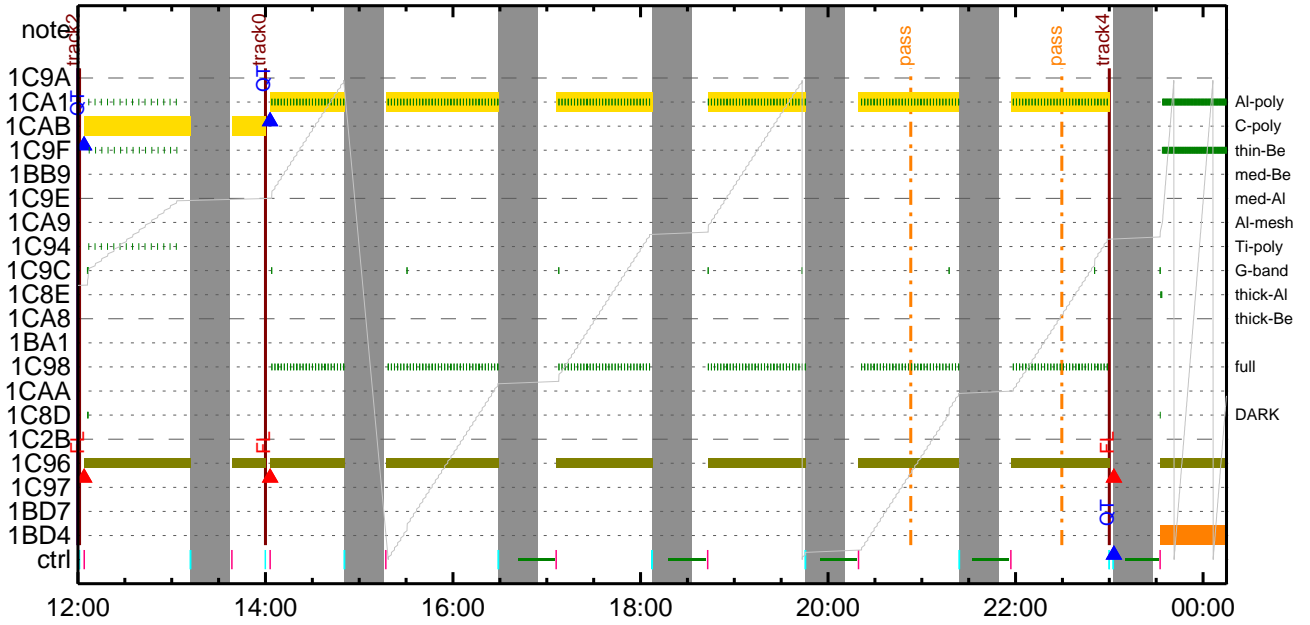
CMDI #0629 2021/05/06



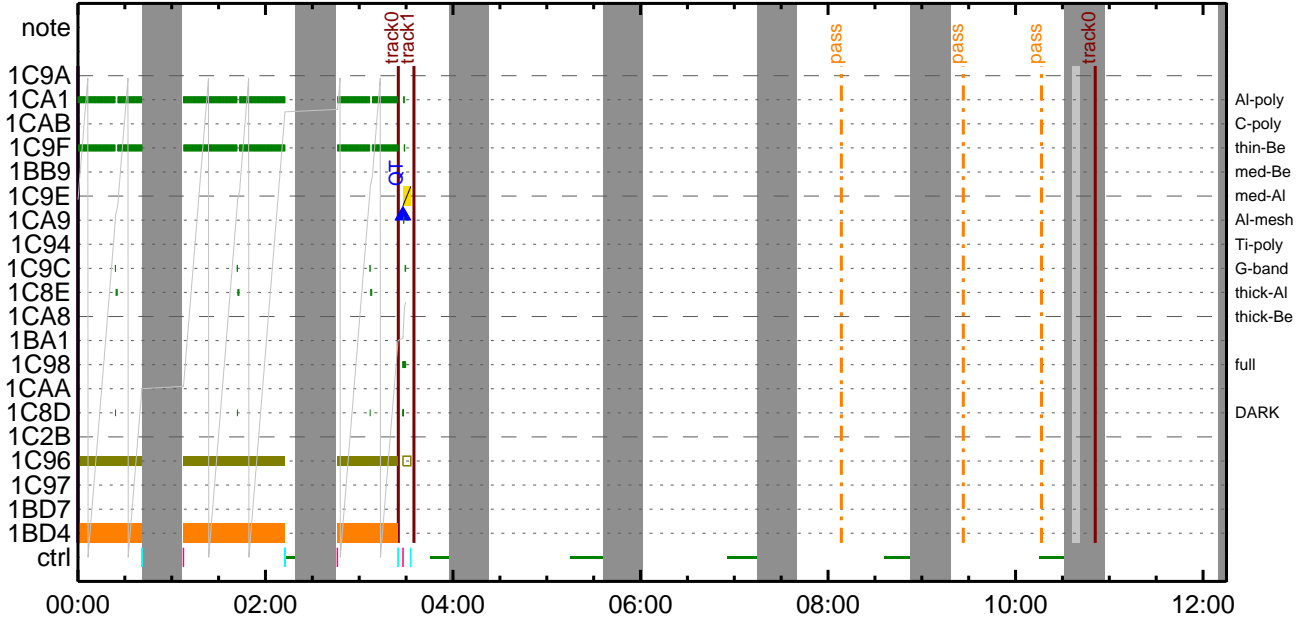
CMDI #0629 2021/05/07



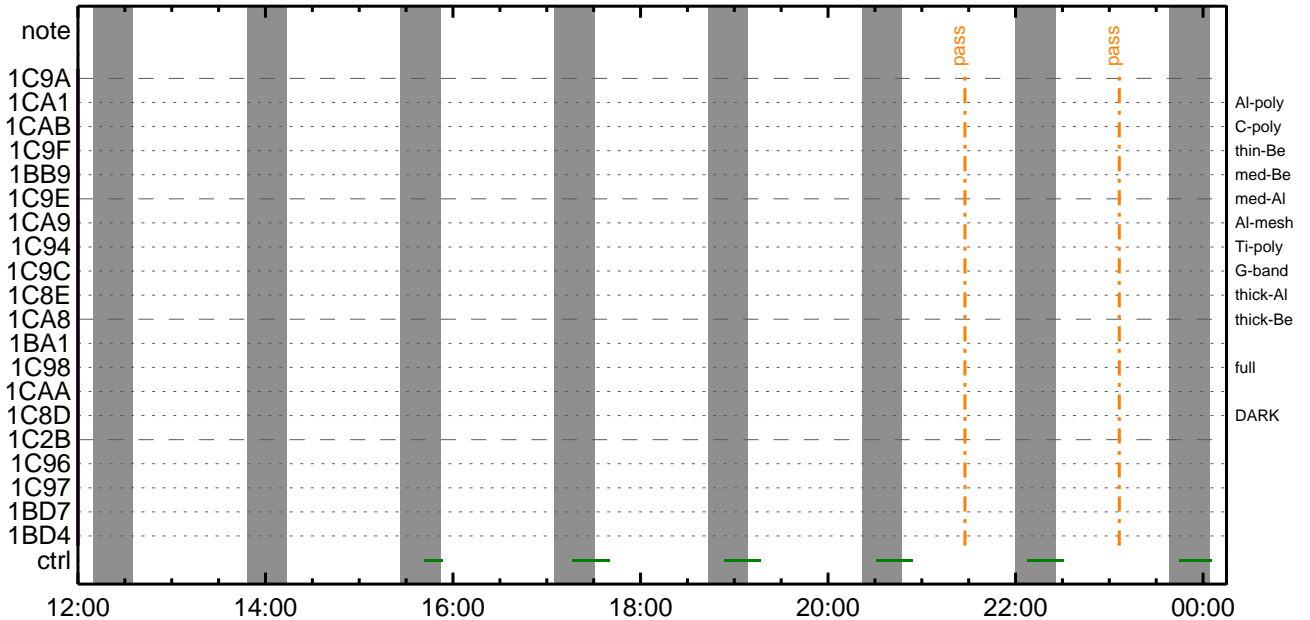
CMDI #0629 2021/05/07



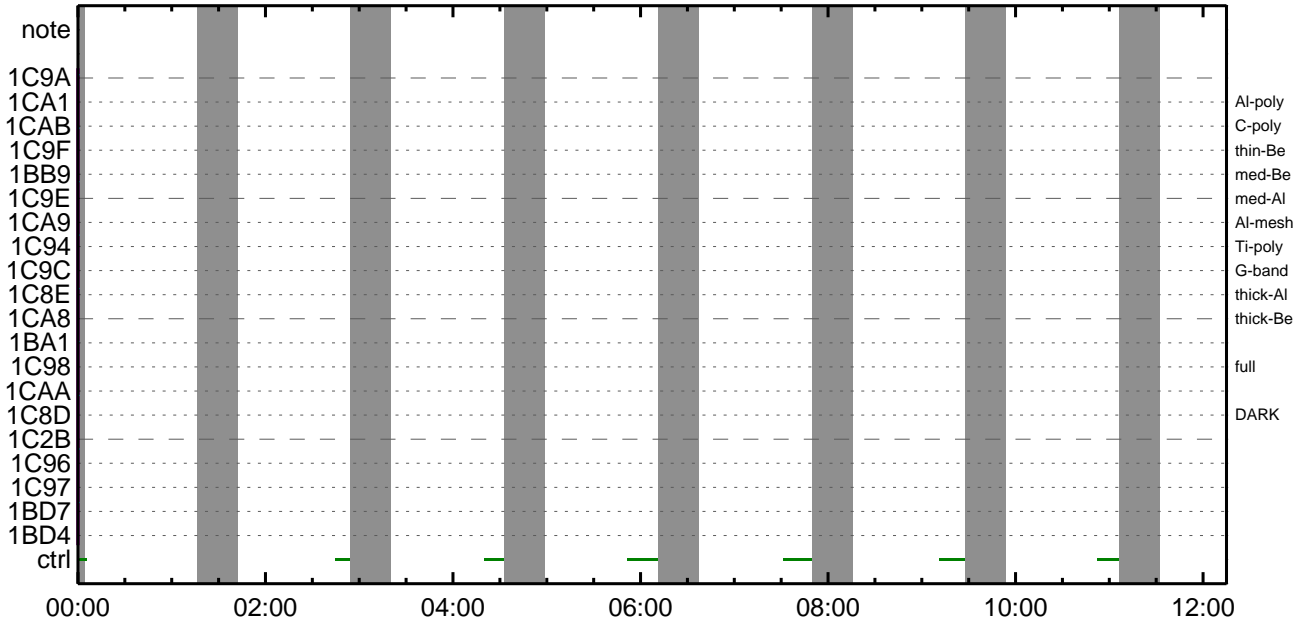
CMDI #0629 2021/05/08



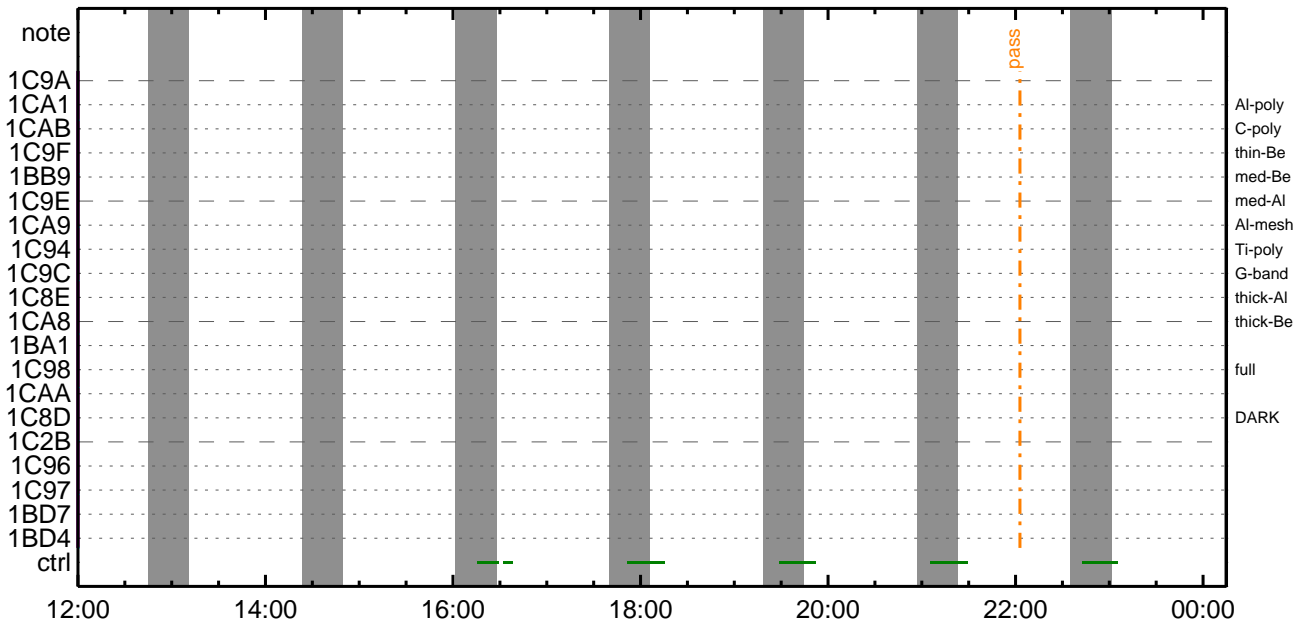
CMDI #0629 2021/05/08



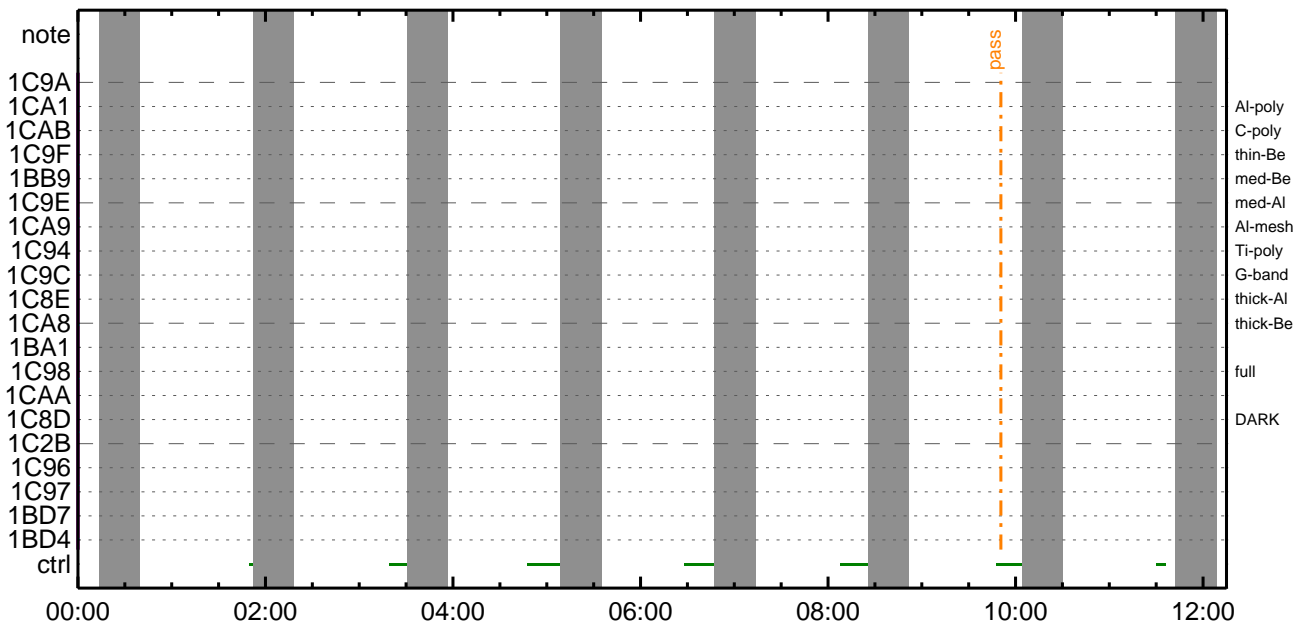
CMDI #0629 2021/05/09



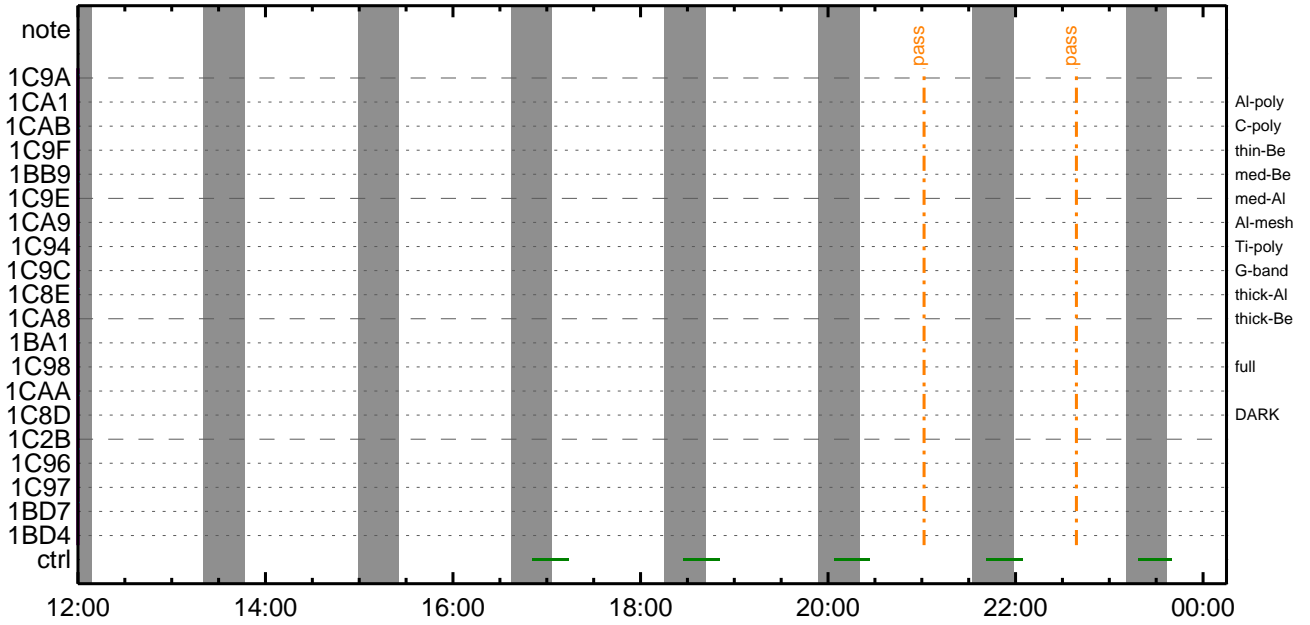
CMDI #0629 2021/05/09



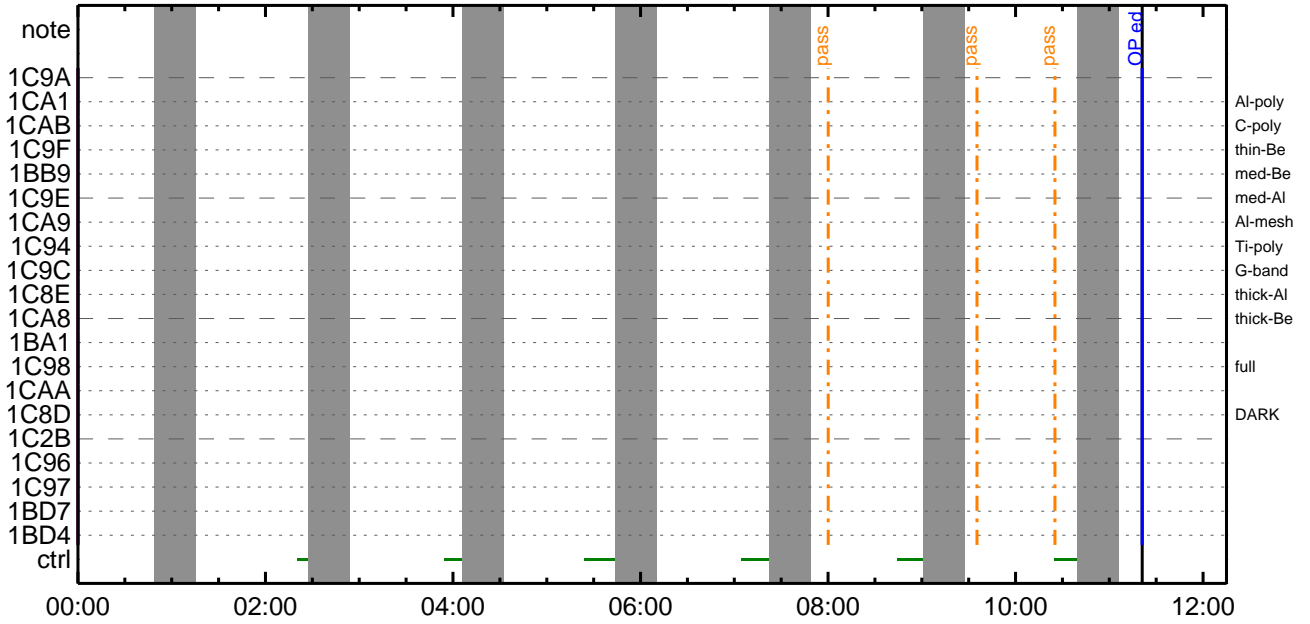
CMDI #0629 2021/05/10



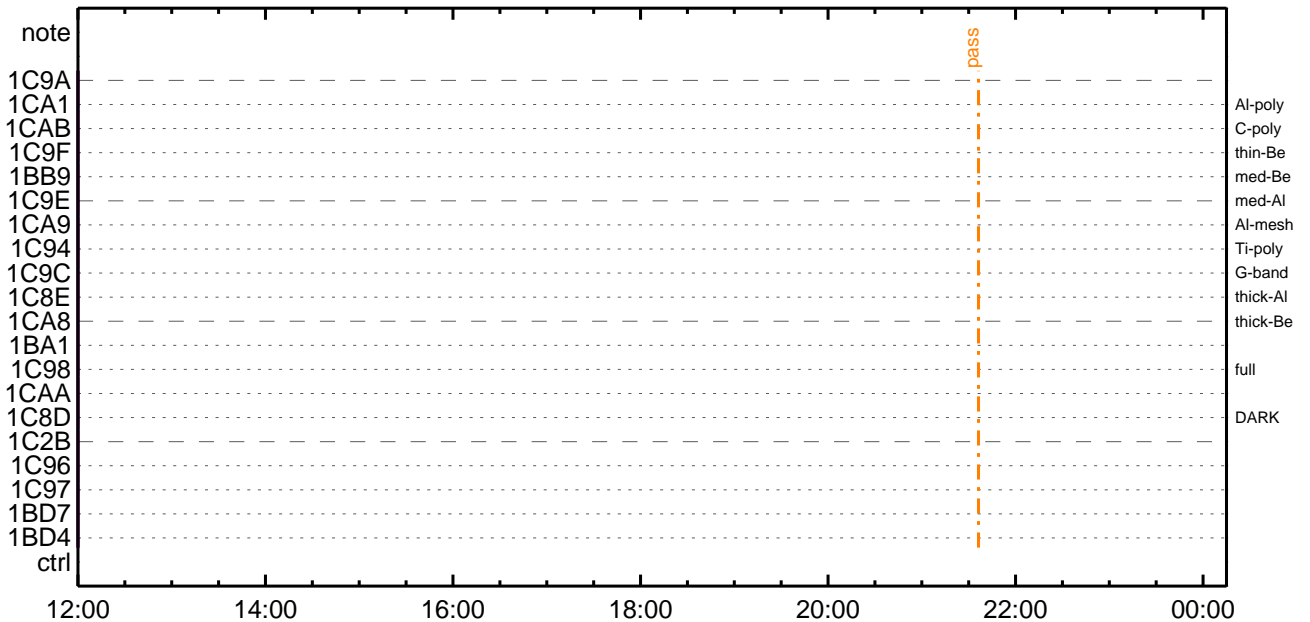
CMDI #0629 2021/05/10



CMDI #0629 2021/05/11



CMDI #0629 2021/05/11




```

0096 C.          0300;SET0EDUMPAI±0iYNY¹0Ç¹Ô0|0³0E;E
0097 C.
0098 . C. TIY³YBYÖYÉ00dADi¿(UT)
0099 +. TI 2021-05-06 10:29:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2021-05-06 10:29:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2021-05-06 10:29:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2021-05-06 10:33:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °E²¼0IÄè%ííN0îYÁY§YÁY-¹àîÜ
0116 C.          ÇÇ[HK1_TI_CMD_ENA/DIS]          EQ          ENA
0117 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          4
0118 C.          ÇÇ[HK1_NEXT_EXEC_PIM]          EQ          DHU
0119 C.          ÇÇ[HK1_NEXT_EXEC_DC]          EQ          0xB3
0120 C.
0121 . C. *****
0122 C. TIîî°èYÁYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          ÇÇ[HK1_DMP_TOP_ADRS_1]          EQ          07
0129 C.          ÇÇ[HK1_DMP_TOP_ADRS_0]          EQ          2B
0130 C.          ÇÇ[HK1_DMP_BLOCK_NUM]          EQ          3
0131 C.          ÇÇ[HK1_DMP_REPEAT_NUM]          EQ          0
0132 C.          ÇÇ[HK1_DMA_DMP_PIM]          EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          ÇÇ[HK1_PKT_FORM_NO]          EQ          7
0136 C.          ÇÇ[HK1_PKT_GEN_TIME]          EQ          0.25 s
0137 C.          ÇÇ[HK1_S_TLM_BIT_RATE]          EQ          32k
0138 C.          ÇÇ[HK1_X_TLM_BIT_RATE]          EQ          4M
0139 C.          ÇÇ[HK1_DMP_CHK_FLG]          EQ          EXEC
0140 C.
0141 . C. YÁYÖY×¼³¹î»0ð³îÇ§
0142 C.          ÇÇ[HK1_DMP_CHK_FLG]          EQ          NON
0143 C.
0144 . C. RAM ID=TI_TBL0I%È¹Ç•è²IOK0ð³îÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼,Yî;¼YÈ;Ë0ðîá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          ÇÇ[HK1_PKT_FORM_NO]          EQ          2
0150 C.          ÇÇ[HK1_PKT_GEN_TIME]          EQ          0.5S
0151 C.          ÇÇ[HK1_S_TLM_BIT_RATE]          EQ          32K
0152 C.          ÇÇ[HK1_X_TLM_BIT_RATE]          EQ          4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2021-05-06 10:33:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 . C.
0162 C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C.
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2021-05-06 10:33:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2021-05-06 10:33:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 . C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2021-05-06 10:33:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 . C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ´úÃî0î»ö¼Y0ÈÄ00¹0èDCBC•×²è *****
0192 C. (¼0îYÖYÁYÉYBÿYÁYÇYè0E¼00¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

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```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øα¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. *****
0130 C. SOT table upload
0131 C. *****
0132 . C. < Stop SP table >
0133 +. DC 07-F0 MDP_SP_CTRL_MANU
0134 BC (61)
0135 C. -----
0136 C. MDP_SP_CTRL_MODE = MANU [ ]
0137 C. -----
0138 C.
0139 . C. <Upload SP Observation Table>
0140 . S. RAM ram-282:MDP_OBS_S
0141 ( )
0142 C.
0143 . C. < Dump RAMID=MDP_OBS_S >
0144 +. DC 07-F0 MDP_DUMP_SPTBL
0145 BC (83 07 00 00 00 38 b8)
0146 C. -----
0147 C. MDP_OBS_S verify = OK/NG [ ]
0148 C. -----
0149 C.
0150 C. *****
0151 C. SOT TI command set
0152 C. *****
0153 C. Execute, after the success of TBL upload.
0154 +. TI 2021-05-06 10:33:18.0
0155 DC 07-F0 MDP_SOT_MODE_OBSV
0156 BC (40)
0157 . C. -----
0158 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0159 C. -----
0160 C.
0161 C.
0162 . C. ***** MDP `úÃîï»ö%ÝðËÄð¹ñèDCBC•x²è *****
0163 C. (%ã°îÿÓYÁYÈYFÿEYáYçYèèE%¼σσ%Á»Û¹ñè)
0164 . S. DC-BC dcbc-402:DCBC
0165 (MDP_known_event)
0166 C.
0167 C.
0168 . C. ***** ¥D¥¹•İ Daily±¿İññE´Ø¹ñèDCBC•x²è *****
0169 . S. DC-BC dcbc-153:DCBC
0170 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0171 C.
0172 C.
0173 . C. ¡ãLOS¥Á¥$¥Ã¥-¼Á»Û;ä
0174 C.
0175 . C. ***** LOS *****
0176 C.

```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 04 04)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 85 83 02 02)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 20 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 85 83 08 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c 80 80 20 08)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 80 80 08 20)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0f 80 80 06 06)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 10 80 80 08 08)
0146 + DC 07-F0 MDP_XRT_FLD_ENA
0147 BC (d8)
0148 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0149 BC (c8)
0150 + DC 07-F0 MDP_XRT_ARS_DIS
0151 BC (d5)
0152 + DC 07-F0 MDP_XRT_AEC_RESET
0153 BC (d0)
0154 + DC 07-F0 MDP_XRT_FLD_RESET
0155 BC (da)
0156 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0157 BC (c4 12)
0158 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0159 BC (c5 04)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C.
0163 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0164 C.
0165 +. DC 07-F0 MDP_XRT_MODE_OBSV
0166 BC (c2)
0167 +. TI 2021-05-06 10:33:02.0
0168 DC 07-F0 MDP_XRT_MODE_OBSV
0169 BC (c2)
0170 . C. ----- Success Verify ? OK / NG ____
0171 C.
0172 C. ***** XRT END *****
0173 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0174 +. DC 07-FC EIS_MODE_CHG_ENA
0175 BC (20)
0176 . C. Verify EIS_MODE_CHG_FLG is ENA
0177 +. DC 07-FC EIS_MODE_MANU
0178 BC (21 02)
0179 . C. Verify EIS in MANUAL mode
0180 . C. Estimated OBSTBL upload time is 16s
0181 C. *****
0182 C. EIS START OBSTBL LOAD
0183 C. *****
0184 . S. RAM ram-820:EIS_OBSTBL
0185 ( )
0186 +. DC 07-FC EIS_DUMP_OBSTBL
0187 BC (07 07 07 00 00 70 00)
0188 C.
0189 C. Execute, after the success of OBSTBL upload.
0190 C. Set EIS TI-commands
0191 +. TI 2021-05-06 10:33:50.0
0192 DC 07-FC EIS_MODE_CHG_ENA
0193 BC (20)

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```
0194 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0195 C. *****
0196 C. EIS END OBSTBL LOAD
0197 C. *****
0198 C.
0199 . C. ***** MDP `úÃîñî»ô¼ýñëâðñ¹ñëDCBC•x²è *****
0200 C. (¼ã°îÿÓÿÃÿÊÿPÿËÿáÿçÿèñ¼ññ¼Ã»Ûñ¹ñë)
0201 . S. DC-BC dcbc-402:DCBC
0202 (MDP_known_event)
0203 C.
0204 C.
0205 . C. ***** ÿÐÿ¹•ï Daily±¿îññë´Øñ¹ñëDCBC•x²è *****
0206 . S. DC-BC dcbc-153:DCBC
0207 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0208 C.
0209 C.
0210 . C. ;ãLOSÿÁÿ§ÿÿÃÿ¹¼Ã»Û;ã
0211 C.
0212 . C. ***** LOS *****
0213 C.
```

*** OP Sequence for XRT ***

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2021/05/06 10:43:00.5 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/05/06 10:43:02.5 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/05/06 10:43:04.5 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/05/06 10:43:06.5 XRT_PREFLR_STRT_440_OG [0x1b8]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2021/05/06 10:43:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/05/06 10:43:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/05/06 10:43:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2021/05/06 10:44:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 1a a7 b2 a8
2021/05/06 10:44:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2021/05/06 10:44:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2021/05/06 10:44:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2021/05/06 10:44:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2021/05/06 10:44:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/05/06 10:46:14.5 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2021/05/06 10:46:56.0 XRT_QT_PROG_SET_435_OG [0x1b3]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 12
2021/05/06 10:46:58.0 XRT_FL_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 04
2021/05/06 11:23:30.0 XRT_Custom_430_OG [0x1ae]
2021/05/06 11:24:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2021/05/06 12:36:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/05/06 12:36:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/05/06 12:36:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/05/06 12:36:36.0 XRT_PREFLR_STRT_440_OG [0x1b8]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2021/05/06 12:39:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2021/05/06 12:57:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/05/06 12:57:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/05/06 12:57:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2021/05/06 12:58:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 03 00 00 00 00
2021/05/06 12:58:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2021/05/06 12:58:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2021/05/06 12:58:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2021/05/06 12:58:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2021/05/06 12:58:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/05/06 13:00:56.0 XRT_QT_PROG_SET_438_OG [0x1b6]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0a
2021/05/06 13:00:58.0 XRT_FL_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 04
2021/05/06 13:02:01.0 XRT_Custom_430_OG [0x1ae]
2021/05/06 13:03:01.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2021/05/06 13:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/05/06 13:59:56.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2021/05/06 14:00:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 00 00
2021/05/06 14:00:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2021/05/06 14:00:18.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2021/05/06 14:00:20.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2021/05/06 14:00:22.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2021/05/06 14:00:24.0 XRT_FLD_RESET_431_OG [0x1af]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/05/06 14:02:56.0 XRT_QT_PROG_SET_449_OG [0x1c1]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 13
2021/05/06 14:02:58.0 XRT_FL_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 04
2021/05/06 14:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]

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2021/05/06	14:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	14:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	14:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/06	14:15:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/06	14:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/06	14:40:00.0	XRT_Custom_430_OG [0x1ae]				
2021/05/06	14:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/06	15:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	15:53:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	15:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/06	15:53:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/06	15:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/06	16:30:00.0	XRT_Custom_430_OG [0x1ae]				
2021/05/06	16:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/06	17:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	17:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	17:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/06	17:32:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/06	17:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/06	18:06:30.0	XRT_Custom_430_OG [0x1ae]				
2021/05/06	18:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/06	19:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	19:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	19:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/06	19:10:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/06	19:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/06	19:43:30.0	XRT_Custom_430_OG [0x1ae]				
2021/05/06	19:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/06	20:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	20:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	20:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/06	20:48:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/06	20:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/06	21:20:30.0	XRT_Custom_430_OG [0x1ae]				
2021/05/06	21:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/06	22:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	22:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	22:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/06	22:27:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/06	22:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/06	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/06	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2021/05/06	23:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 03 74 01 f3
2021/05/06	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2021/05/06	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2021/05/06	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2021/05/06	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2021/05/06	23:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]				

2021/05/06	23:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da			
2021/05/06	23:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2021/05/06	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04		
2021/05/07	00:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/05/07	00:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	00:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	00:05:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2021/05/07	00:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2021/05/07	00:30:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2021/05/07	00:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2021/05/07	01:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/05/07	01:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	01:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	01:43:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2021/05/07	01:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2021/05/07	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2021/05/07	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	02:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2021/05/07	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	00	00	00
2021/05/07	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2021/05/07	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2021/05/07	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2021/05/07	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2021/05/07	02:02:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2021/05/07	02:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2021/05/07	02:09:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04		
2021/05/07	02:10:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2021/05/07	03:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/05/07	03:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	03:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	03:11:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2021/05/07	03:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2021/05/07	03:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2021/05/07	03:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2021/05/07	04:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/05/07	04:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	04:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	04:38:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da			
2021/05/07	04:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2021/05/07	05:25:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2021/05/07	05:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]						
2021/05/07	05:51:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/05/07	05:51:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	05:51:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/05/07	05:52:18.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2021/05/07	05:52:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2021/05/07	05:52:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2021/05/07			MDP_XRT_ARS_DIS	1	07-F0	d5			

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2021/05/07	05:54:58.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2021/05/07	05:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/07	06:01:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/07	06:01:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/07	06:01:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2021/05/07	06:02:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	04	03	74	01	f3
2021/05/07	06:02:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/05/07	06:02:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/05/07	06:02:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/05/07	06:02:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/07	06:02:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/07	06:04:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2021/05/07	06:04:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/05/07	06:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/07	06:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/07	06:19:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/07	06:19:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/07	06:19:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/07	06:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/07	07:03:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/07	07:03:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/07	07:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2021/05/07	07:04:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	02	00	00	00	ef
2021/05/07	07:04:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/05/07	07:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/05/07	07:04:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/05/07	07:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/07	07:04:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/07	07:06:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2021/05/07	07:06:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/05/07	07:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/07	07:59:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/07	07:59:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/07	07:59:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/07	07:59:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/07	08:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/07	08:42:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/07	08:42:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/07	08:42:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2021/05/07	08:43:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	02	00	00	00	fa
2021/05/07	08:43:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/05/07	08:43:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/05/07	08:43:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/05/07	08:43:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/07	08:43:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/07	08:45:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2021/05/07	08:45:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							

2021/05/07	08:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/07	09:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	09:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	09:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/07	09:39:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/07	09:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/07	10:21:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	10:21:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	10:21:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2021/05/07	10:22:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	02 00 00 05 78	
2021/05/07	10:22:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2021/05/07	10:22:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2021/05/07	10:22:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2021/05/07	10:22:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/05/07	10:22:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/07	10:24:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2021/05/07	10:24:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2021/05/07	10:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/07	11:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	11:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	11:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/07	11:20:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/07	11:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/07	12:00:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	12:00:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	12:00:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2021/05/07	12:01:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	02 00 00 10 62	
2021/05/07	12:01:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2021/05/07	12:01:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2021/05/07	12:01:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2021/05/07	12:01:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/05/07	12:01:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/07	12:03:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2021/05/07	12:03:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2021/05/07	12:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/07	13:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	13:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	13:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/07	13:12:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/07	13:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/07	13:37:30.0	XRT_Custom_430_OG [0x1ae]					
2021/05/07	13:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/07	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/07	13:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2021/05/07	14:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2021/05/07	14:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2021/05/07	14:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					

2021/05/07	14:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0
2021/05/07	14:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2021/05/07	14:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/07	14:02:56.0	XRT_QT_PROG_SET_449_OG [0x1c1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2021/05/07	14:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2021/05/07	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/07	14:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	14:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	14:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/07	14:50:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/07	14:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/07	15:16:00.0	XRT_Custom_430_OG [0x1ae]				
2021/05/07	15:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/07	16:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	16:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	16:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/07	16:29:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/07	16:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/07	17:05:00.0	XRT_Custom_430_OG [0x1ae]				
2021/05/07	17:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/07	18:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	18:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	18:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/07	18:07:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/07	18:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/07	18:42:00.0	XRT_Custom_430_OG [0x1ae]				
2021/05/07	18:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/07	19:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	19:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	19:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/07	19:45:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/07	19:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/07	20:18:30.0	XRT_Custom_430_OG [0x1ae]				
2021/05/07	20:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/07	21:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	21:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	21:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/07	21:24:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/07	21:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/07	21:56:00.0	XRT_Custom_430_OG [0x1ae]				
2021/05/07	21:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/07	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2021/05/07	23:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 03 74 01 f3
2021/05/07	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2021/05/07	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2021/05/07	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2021/05/07	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]				

2021/05/07	23:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/07	23:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	23:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/07	23:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/07	23:02:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/07	23:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2021/05/07	23:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2021/05/07	23:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/07	23:31:31.5	XRT_Custom_430_OG [0x1ae]				
2021/05/07	23:32:31.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/08	00:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/08	00:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/08	00:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/08	00:41:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/08	00:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/08	01:06:30.0	XRT_Custom_430_OG [0x1ae]				
2021/05/08	01:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/08	02:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/08	02:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/08	02:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/08	02:12:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/08	02:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/08	02:45:00.0	XRT_Custom_430_OG [0x1ae]				
2021/05/08	02:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/08	03:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/08	03:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/08	03:24:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2021/05/08	03:25:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2021/05/08	03:25:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2021/05/08	03:25:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2021/05/08	03:25:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2021/05/08	03:27:58.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2021/05/08	03:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/08	03:33:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/08	03:35:00.0	AOCS_OrE-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	01 00 00 00 00
2021/05/08	03:35:00.5	XRT_TCIB_XRT_S_HTR_A_ENA_429_OG [0x1ad]	TCIB_XRT_S_HTR_A_ENA	0	04-BC	
2021/05/08	10:51:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00