

XRT Timeline to be uploaded on 2021/05/08

Period: 2021/05/08 10:41:00 - 2021/05/13 10:55:00

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Normal mode

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XOB #1C9F: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(362/3897/16384) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-Al												
Term	Pointing (x, y)		Comment									
05/11 09:33:00 - 05/11 09:39:54	Fixed (0.0, 0.0)		synoptic disk center									
PROG= 17 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0 0 2.0sec
└─ Seqn= 1 1-time(s) 2.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└─ Seqn= 99 1-time(s) 2.0sec												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└─ Seqn= 84 1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└─ Seqn= 23 1-time(s) 4.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 46 1-time(s) 2.0sec												
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0 0 2.0sec
└─ Seqn= 2 1-time(s) 2.0sec												
	med-Al/Open	med-Al/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0 0 2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0 0 2.0sec
└─ Seqn= 13 1-time(s) 2.0sec												
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0 0 2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1BC7: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(2048ms), Al/Poly(4096ms) - w leak image-1ms												
Term	Pointing (x, y)		Comment									
05/11 09:43:00 - 05/11 09:49:54	Fixed (-528.4, -528.4)		XRT Post Bakeout #1									
PROG= 02 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 51 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)		Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)		Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)		Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)		Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 3 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 34 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1BC8: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms												
Term	Pointing (x, y)		Comment									
05/11 09:53:00 - 05/11 09:59:54	Fixed (528.4, -528.4)		XRT Post Bakeout #2									
PROG= 14 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 38 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)		Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)		Q=90	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)		Q=98	0 0 2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)		Q=98	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 3 2-time(s) 2.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec												

└─ Seqn= 34		1-time(s)	2.0sec																	
	└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec						
	└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval							

XOB #1BC9: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
05/11 10:03:00 - 05/11 10:09:54	Fixed (528.4, 528.4)	XRT Post Bakeout #3

└─ Subr= 07		1-time(s)	2.0sec																	
	└─ Subr= 1	1-time(s)	2.0sec																	
	└─ Seqn= 21	1-time(s)	2.0sec																	
	└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0	0	2.0sec						
	└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0	0	2.0sec						
	└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0	0	2.0sec						
	└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0	0	2.0sec						
	└─ Subr= 2	1-time(s)	2.0sec																	
	└─ Seqn= 3	2-time(s)	2.0sec																	
	└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	└─ Subr= 3	2-time(s)	2.0sec																	
	└─ Seqn= 34	1-time(s)	2.0sec																	
	└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec						
	└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval							

XOB #1BCA: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
05/11 10:13:00 - 05/11 10:24:30	Fixed (-528.4, 528.4)	XRT Post Bakeout #4

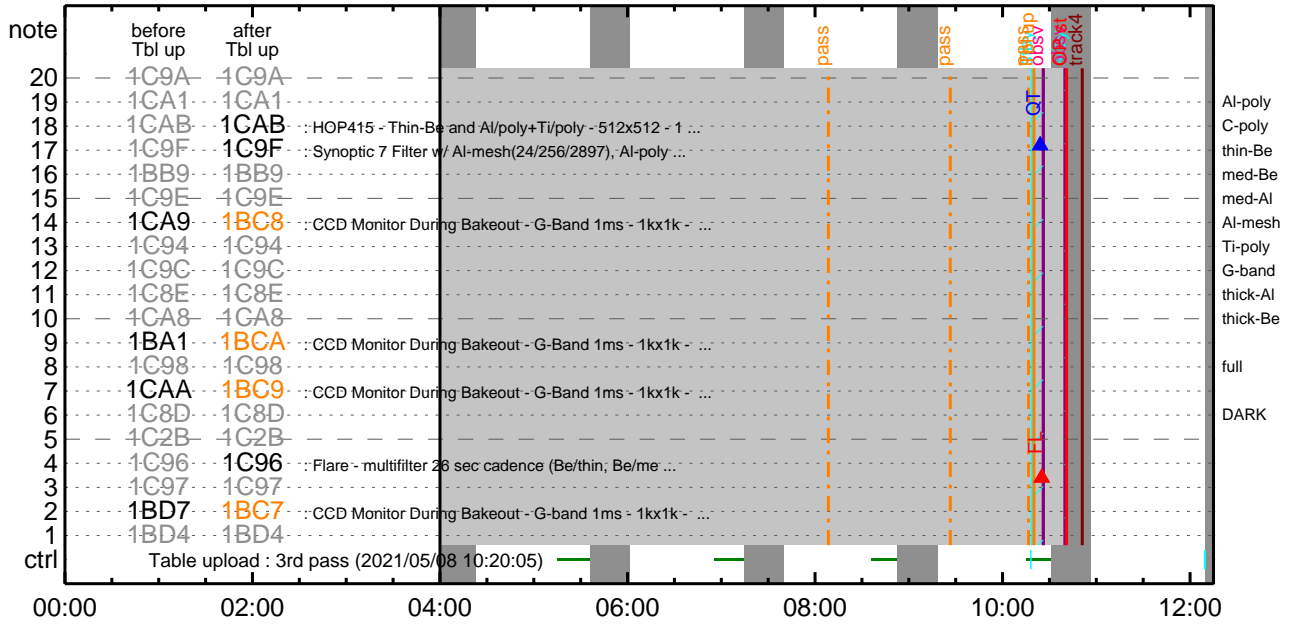
└─ Subr= 09		1-time(s)	2.0sec																	
	└─ Subr= 1	1-time(s)	2.0sec																	
	└─ Seqn= 14	1-time(s)	2.0sec																	
	└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0	0	2.0sec						
	└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0	0	2.0sec						
	└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0	0	2.0sec						
	└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024	(1536, 512)	Q=98	0	0	2.0sec						
	└─ Subr= 2	1-time(s)	2.0sec																	
	└─ Seqn= 3	2-time(s)	2.0sec																	
	└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
	└─ Subr= 3	2-time(s)	2.0sec																	
	└─ Seqn= 34	1-time(s)	2.0sec																	
	└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec						
	└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec						
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval							

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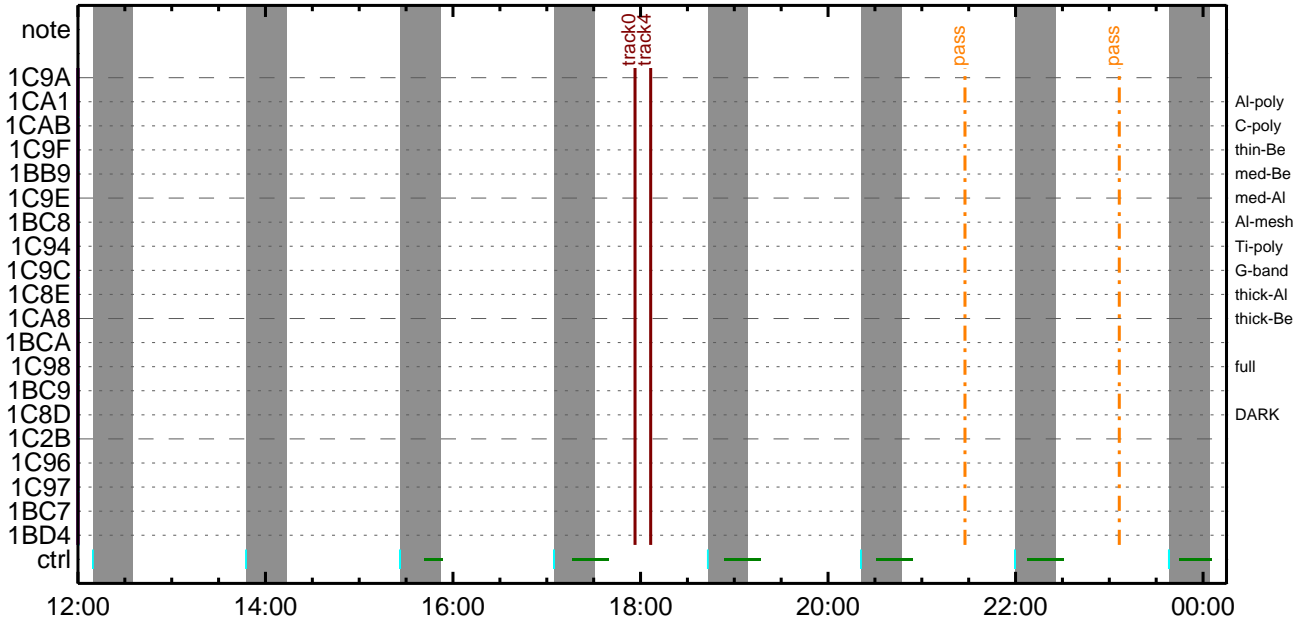
* * * * *
                                Flare mode                                * * * * *
                                NOT USED
* * * * *
                                Active Region Search                        * * * * *
                                NOT USED
* * * * *
                                Flare Detection                            * * * * *
                                NOT USED

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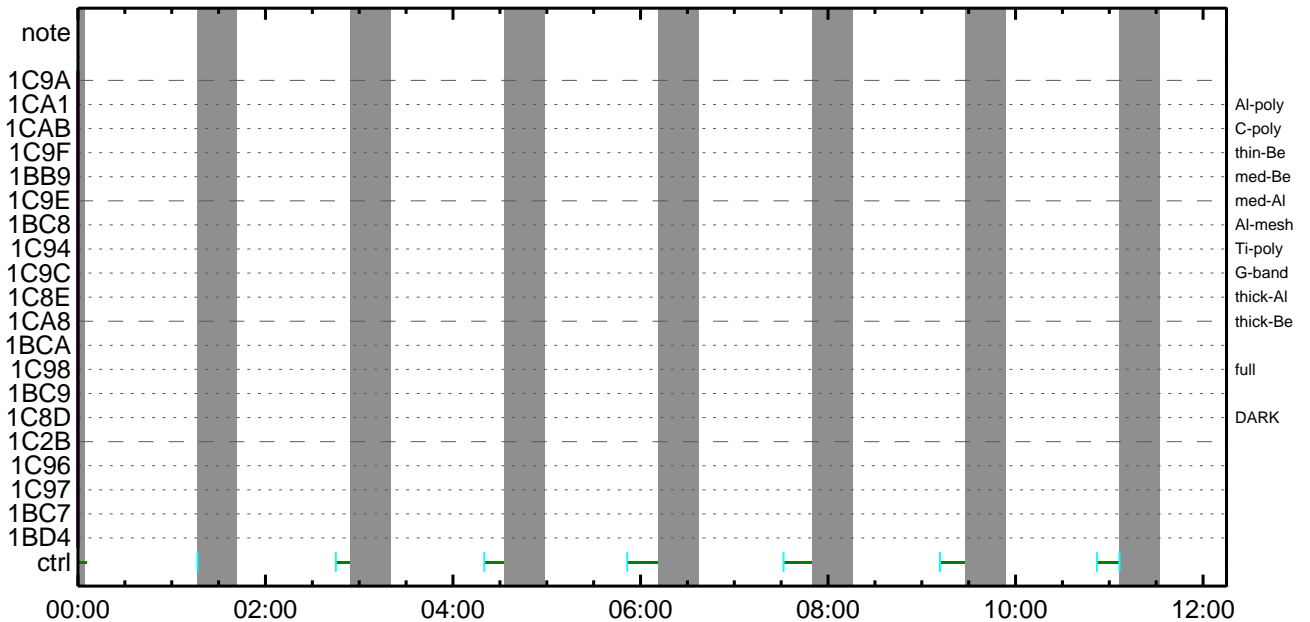
CMDI #0636 2021/05/08



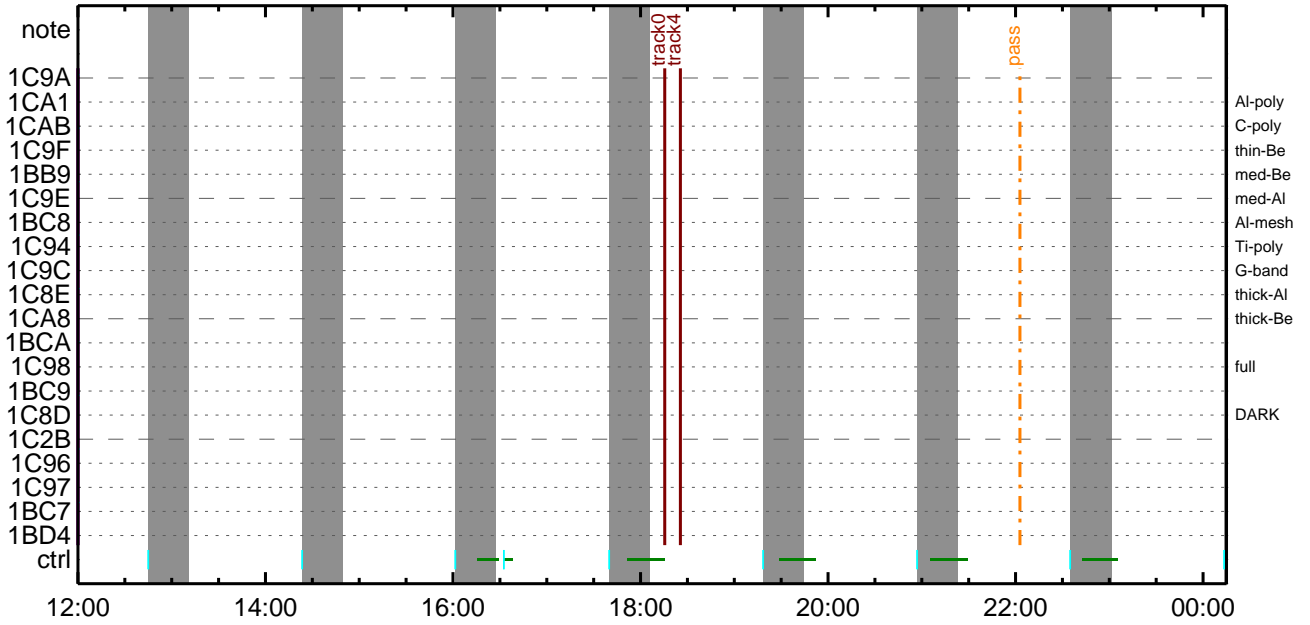
CMDI #0636 2021/05/08



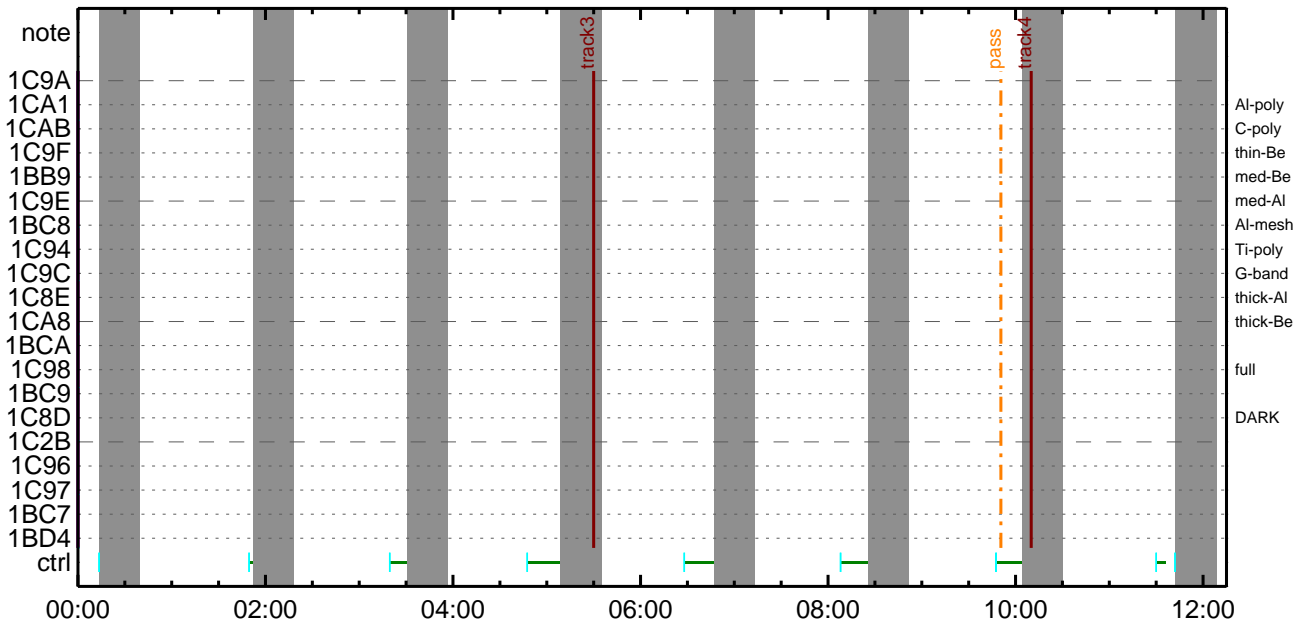
CMDI #0636 2021/05/09



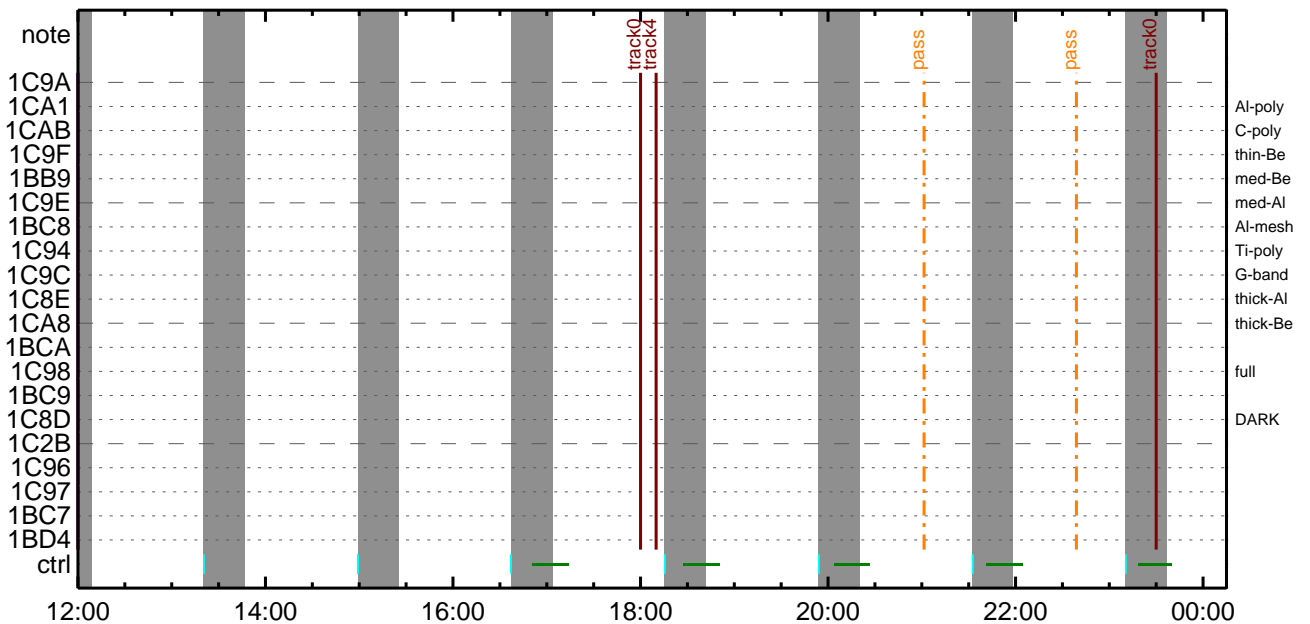
CMDI #0636 2021/05/09



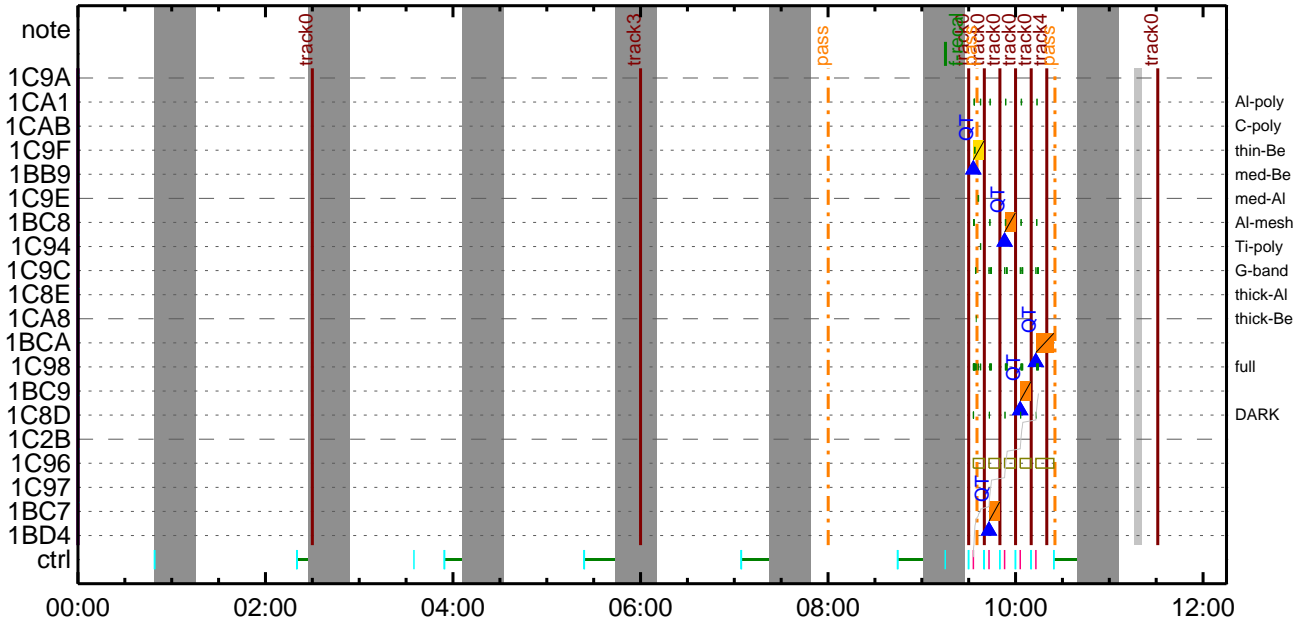
CMDI #0636 2021/05/10



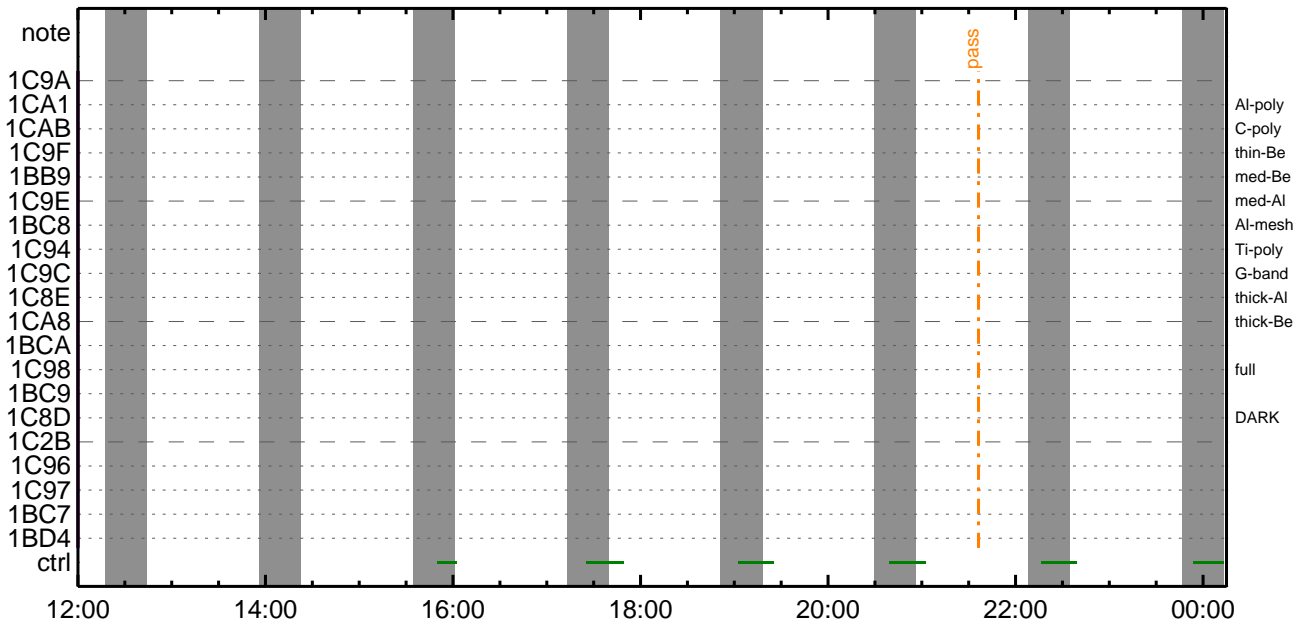
CMDI #0636 2021/05/10



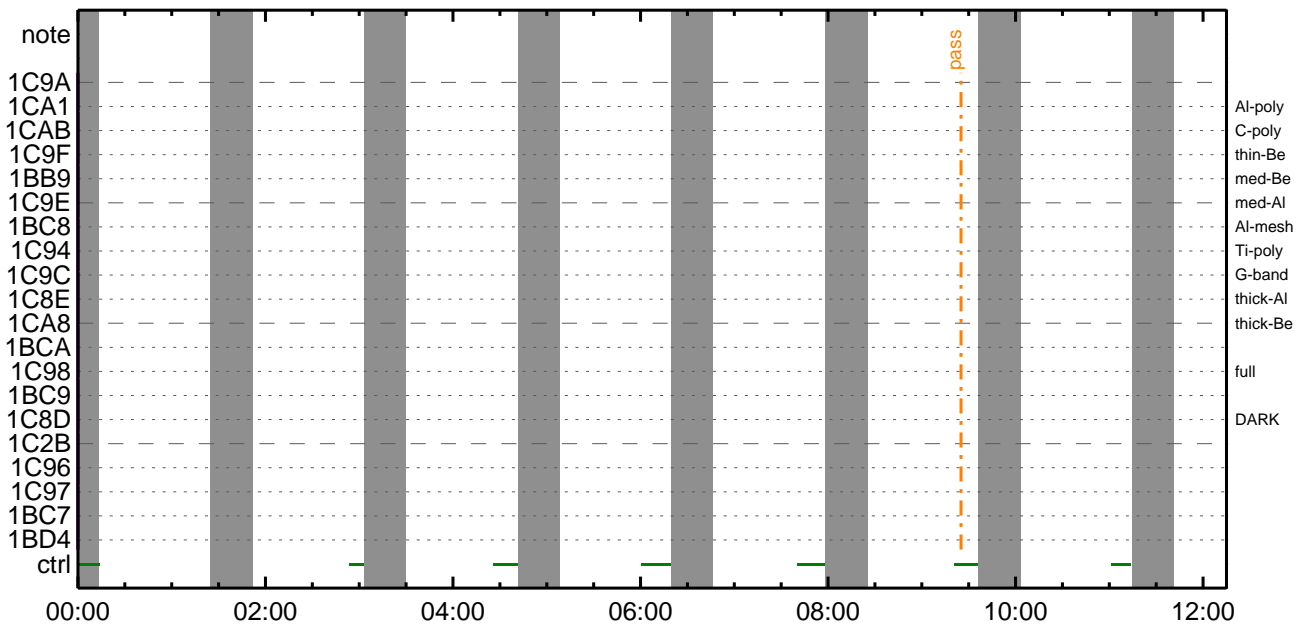
CMDI #0636 2021/05/11



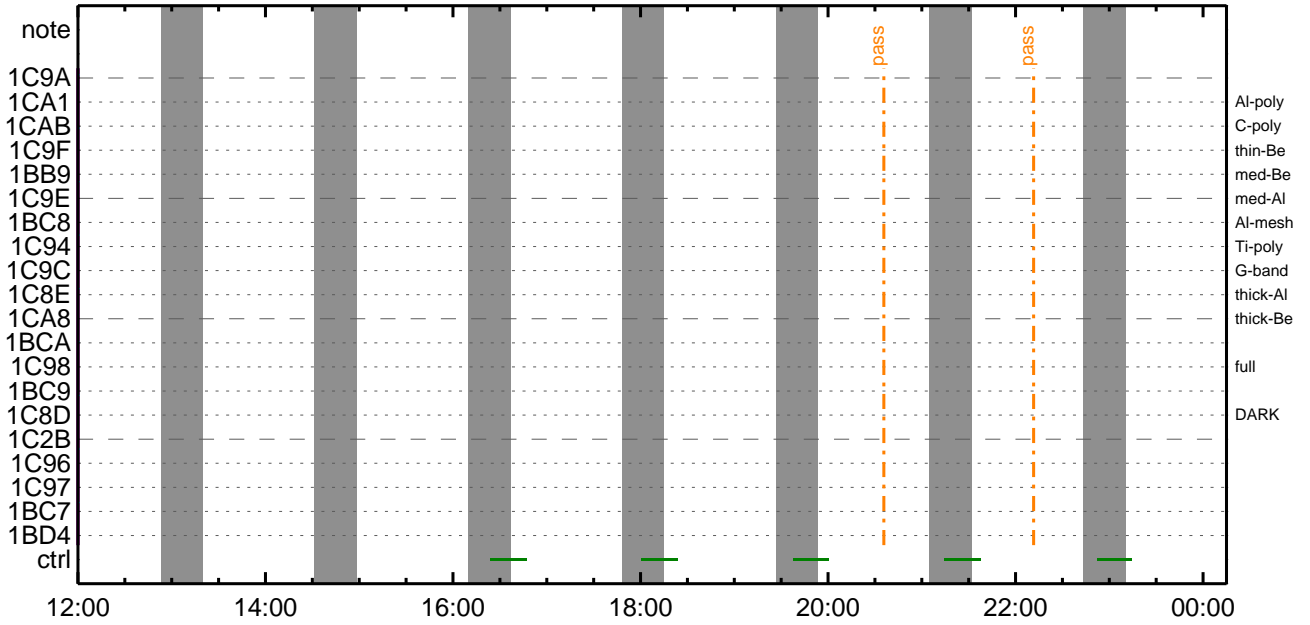
CMDI #0636 2021/05/11



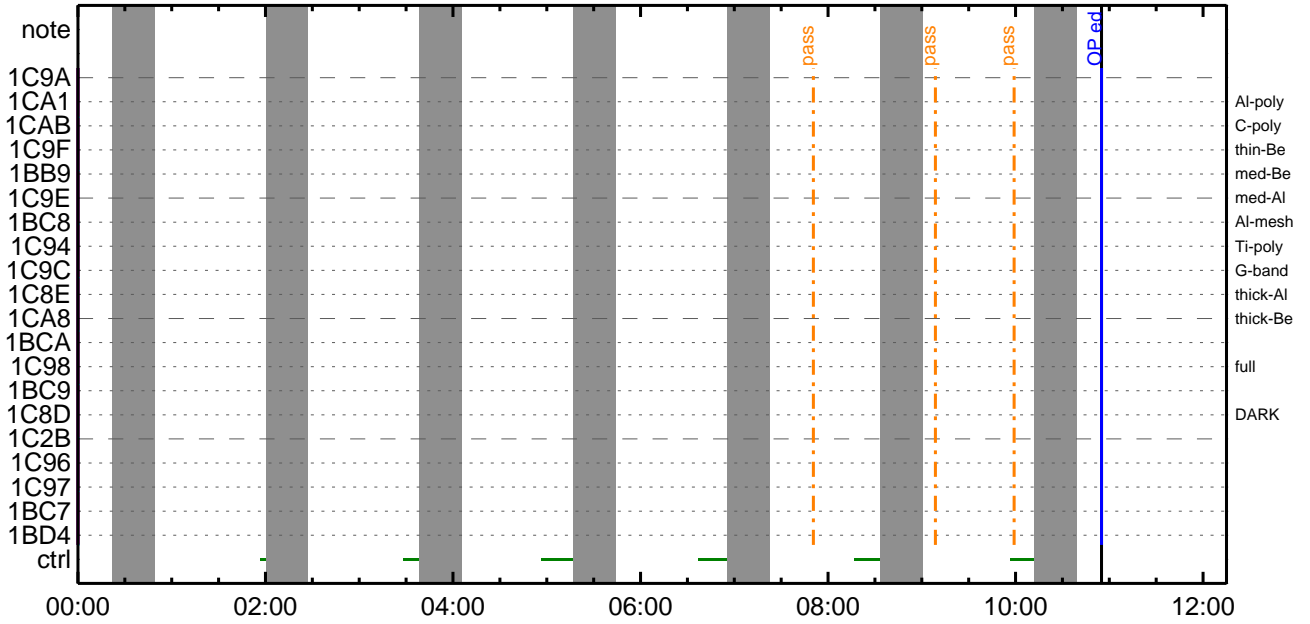
CMDI #0636 2021/05/12



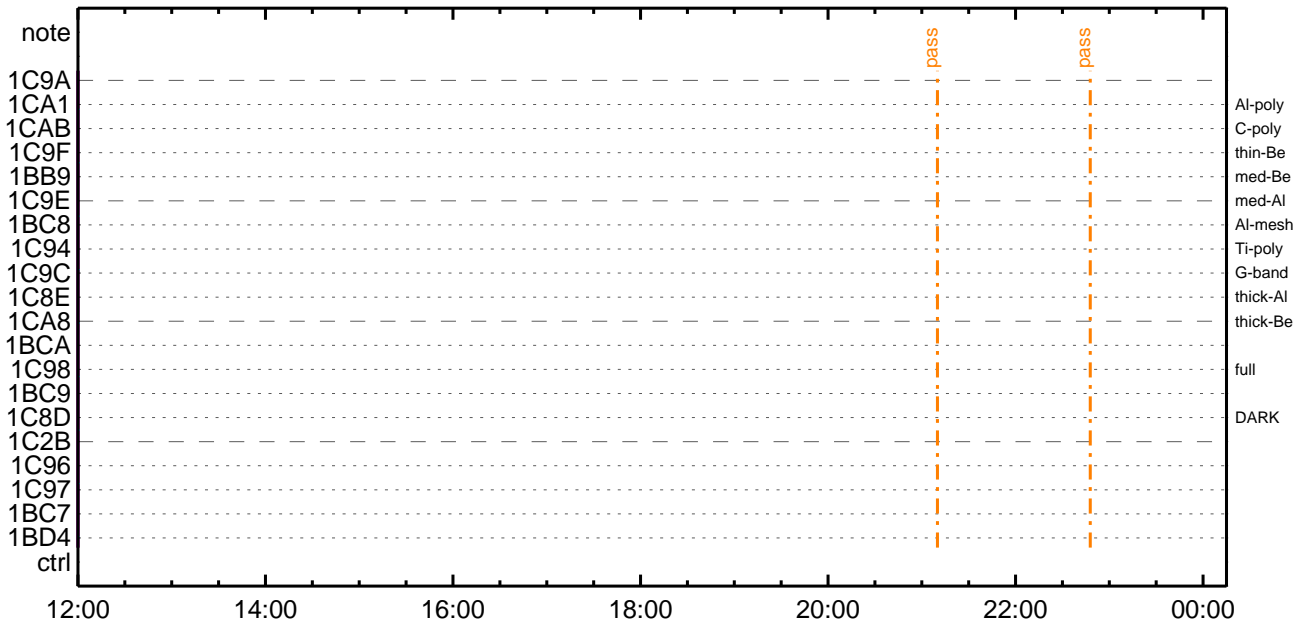
CMDI #0636 2021/05/12



CMDI #0636 2021/05/13



CMDI #0636 2021/05/13




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0096 C.                0B0Z;çSET0EDUMP0IÆ±°iYNY¹0Ç¹Ô0|0³0E;£
0097 C.
0098 . C. TIY³YF¥ÖYÉ00ðÁDİ¿(UT)
0099 +. TI 2021-05-08 10:36:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2021-05-08 10:36:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2021-05-08 10:36:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2021-05-08 10:40:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.                °E²¼0İÄê%îÍÑ0İYÁ¥§YÁY-¹àİÛ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÁYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC                (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC                (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 . C. YÁYÖY×½ªİ»0ð³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İÈ¹Ç•è²İOK0ð³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y½,¥İ;¼YÈ;È0ðİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC                (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2021-05-08 10:40:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC                (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2021-05-08 10:40:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC                (21 02)
0174 +. TI 2021-05-08 10:40:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC                (22)
0177 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2021-05-08 10:40:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC                (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ´úÃİ0İ»ö¼Y0ÈÄ00¹0èDCBC•×²è *****
0192 C. (¼0İYÖYÁYÉYÏYÉYÁYçYè0È¼00¼Á»Û0¹0é)
0193 . S. DC-BC dcbc-402:DCBC
```



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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```
0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 6s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-821:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2021-05-08 10:40:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP (uAfiI»ö¼YpEAĐa¹nèDCBC•x²è *****
0156 C. (¼a°îYÓYAYÈYpYÈYáYcYèaÈ¾¼aa¼A»Ùa¹aè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** YDY¹•İ Daily±¿İÑnÈ´Øa¹aèDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥AY¹¼A»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-479 2021-05-08 13:11:36 136 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èó¿òÁò•µ°È»Í×ÁÇóÍY¿Y×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼ì¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-283:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2021-05-08 10:40:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 06 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 07 80 80 08 08)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 08 80 80 20 20)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 09 80 80 20 08)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 0a 80 80 08 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0b c0 c0 10 10)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0c 40 c0 10 10)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0d 40 40 10 10)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0e c0 40 10 10)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0f 80 80 06 06)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 10 80 80 08 08)
0094 + DC 07-F0 MDP_XRT_FLD_ENA
0095 BC (d8)
```

```
0096 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0097 BC (c8)
0098 + DC 07-F0 MDP_XRT_ARS_DIS
0099 BC (d5)
0100 + DC 07-F0 MDP_XRT_AEC_RESET
0101 BC (d0)
0102 + DC 07-F0 MDP_XRT_FLD_RESET
0103 BC (da)
0104 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0105 BC (c4 12)
0106 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0107 BC (c5 04)
0108 . C. ----- Success Verify ? OK / NG ____
0109 C.
0110 C.
0111 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0112 C.
0113 +. DC 07-F0 MDP_XRT_MODE_OBSV
0114 BC (c2)
0115 +. TI 2021-05-08 10:40:02.0
0116 DC 07-F0 MDP_XRT_MODE_OBSV
0117 BC (c2)
0118 . C. ----- Success Verify ? OK / NG ____
0119 C.
0120 C. ***** XRT END *****
0121 C.
0122 . C. ***** MDP 'úÃîñî»ö¼ÝñÊÃðñ¹ñèDCBC•x²è *****
0123 C. (%ã°îÿÓÿÄÿÈÿÞÿËÿÀÿÇÿÈñ¼ññ¼Ã»Ûñ¹ñè)
0124 . S. DC-BC dcbc-402:DCBC
0125 (MDP_known_event)
0126 C.
0127 C.
0128 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎññË´Øñ¹ñèDCBC•x²è *****
0129 . S. DC-BC dcbc-153:DCBC
0130 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0131 C.
0132 C.
0133 . C. ;ãLOSÿÄÿ§ÿÄÿÿ-¼Ã»Û;ã
0134 C.
0135 . C. ***** LOS *****
0136 C.
```

*** OP Sequence for XRT ***

```

2021/05/08 10:51:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 04 00 00 00 00
2021/05/08 12:09:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 12:09:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 12:09:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/05/08 12:09:36.0 XRT_PREFLR_STRT_440_OG [0x1b8]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/05/08 12:12:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/05/08 13:47:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 13:47:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 13:47:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/05/08 13:47:36.0 XRT_PREFLR_STRT_440_OG [0x1b8]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/05/08 13:50:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/05/08 15:26:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 15:26:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 15:26:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/05/08 15:26:06.0 XRT_PREFLR_STRT_440_OG [0x1b8]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/05/08 15:29:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/05/08 17:04:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 17:04:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 17:04:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/05/08 17:04:36.0 XRT_PREFLR_STRT_440_OG [0x1b8]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/05/08 17:07:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/05/08 17:56:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2021/05/08 18:06:30.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 04 00 00 00 00
2021/05/08 18:43:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 18:43:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 18:43:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/05/08 18:43:06.0 XRT_PREFLR_STRT_440_OG [0x1b8]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/05/08 18:46:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/05/08 20:21:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 20:21:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 20:21:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/05/08 20:21:06.0 XRT_PREFLR_STRT_440_OG [0x1b8]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/05/08 20:24:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/05/08 21:59:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 21:59:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 21:59:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/05/08 21:59:36.0 XRT_PREFLR_STRT_440_OG [0x1b8]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/05/08 22:02:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/05/08 23:38:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 23:38:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/08 23:38:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/05/08 23:38:06.0 XRT_PREFLR_STRT_440_OG [0x1b8]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/05/08 23:41:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/05/09 01:16:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/09 01:16:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/05/09 01:16:34.0 XRT_FLD_RESET_415_OG [0x19f]

```

2021/05/09	01:16:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/09	01:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/09	02:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/09	02:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	02:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	02:45:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/09	02:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/09	04:20:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/09	04:20:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	04:20:05.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	04:20:07.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/09	04:23:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/09	05:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/09	05:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	05:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	05:51:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/09	05:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/09	07:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/09	07:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	07:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	07:31:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/09	07:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/09	09:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/09	09:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	09:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	09:11:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/09	09:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/09	10:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/09	10:52:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	10:52:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	10:52:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/09	10:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/09	11:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/09	11:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	11:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	11:06:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/09	11:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/09	12:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/09	12:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	12:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	12:45:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/09	12:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/09	14:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/09	14:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	14:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/09	14:23:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/09			MDP_XRT_PREFLR_STRT	1	07-F0	e8

2021/05/09	14:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/09	16:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	16:01:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	16:01:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/09	16:01:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/09	16:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/09	16:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	16:32:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	16:32:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/09	16:32:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/09	16:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/09	17:40:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	17:40:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	17:40:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/09	17:40:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/09	17:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/09	18:15:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2021/05/09	18:25:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2021/05/09	19:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	19:18:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	19:18:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/09	19:18:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/09	19:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/09	20:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	20:57:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	20:57:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/09	20:57:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/09	21:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/09	22:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	22:35:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/09	22:35:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/09	22:35:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/09	22:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/10	00:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	00:13:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	00:13:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/10	00:13:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/10	00:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/10	01:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	01:49:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	01:49:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/10	01:49:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/10	01:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/10	03:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	03:19:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	03:19:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/10	03:19:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							

2021/05/10	03:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/10	04:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	04:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	04:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/10	04:47:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/10	04:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/10	05:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 00 00 00 00	
2021/05/10	06:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	06:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	06:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/10	06:28:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/10	06:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/10	08:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	08:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	08:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/10	08:08:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/10	08:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/10	09:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	09:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	09:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/10	09:47:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/10	09:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/10	10:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00	
2021/05/10	11:30:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	11:30:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	11:30:05.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/10	11:30:07.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/10	11:33:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/10	11:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	11:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	11:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/10	11:42:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/10	11:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/10	13:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	13:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	13:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/10	13:20:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/10	13:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/10	14:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	14:59:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	14:59:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/10	14:59:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/10	15:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/10	16:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	16:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/10	16:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	

2021/05/10	16:37:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/10	16:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/10	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2021/05/10	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2021/05/10	18:15:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	18:15:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	18:15:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/10	18:15:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/10	18:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/10	19:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	19:54:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	19:54:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/10	19:54:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/10	19:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/10	21:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	21:32:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	21:32:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/10	21:32:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/10	21:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/10	23:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	23:10:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/10	23:10:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/10	23:10:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/10	23:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/10	23:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 ce da 01 f3				
2021/05/11	00:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	00:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	00:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/11	00:49:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/11	00:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/11	02:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	02:20:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	02:20:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/11	02:20:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/11	02:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/11	02:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 36 0c 01 f3				
2021/05/11	03:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	03:35:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_404_OG [0x194]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2021/05/11	03:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	03:54:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	03:54:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/11	03:54:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/11	03:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/11	05:24:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	05:24:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	05:24:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/11	05:24:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							

2021/05/11	05:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/11	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]		5	02-76	03 00 00 00 00	
			AOCU_NM				
2021/05/11	07:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	07:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	07:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/11	07:04:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/11	07:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/11	08:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	08:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	08:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/11	08:44:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/11	08:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/11	09:15:00.0	XRT_CTRL_MANU_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	09:15:10.0	XRT_FOCUS_RECALIBRATE_427_OG [0x1ab]	XRT_FOCUS_RECAL	2	07-F8	78 00	
			XRT_FOCUS_RECAL	2	07-F8	78 00	
2021/05/11	09:19:10.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/05/11	09:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	09:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	09:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/05/11	09:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]		5	02-76	00 00 00 00 00	
			AOCU_NM				
2021/05/11	09:30:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2021/05/11	09:30:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2021/05/11	09:30:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/05/11	09:32:58.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2021/05/11	09:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/11	09:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	09:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	09:39:58.0	XRT_FOCUS_POSITION_416_OG [0x1a0]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/05/11	09:40:00.0	AOCS_ORe-point_Start_6_OG [0x09c]		5	02-76	00 2e f9 2e f9	
			AOCU_NM				
2021/05/11	09:42:52.0	XRT_ARS_DIS_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5	
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/05/11	09:42:54.0	XRT_FLRCTRL_DIS_436_OG [0x1b4]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2021/05/11	09:42:56.0	XRT_FLD_DIS_407_OG [0x197]	MDP_XRT_FLD_DIS	1	07-F0	d9	
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2021/05/11	09:42:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2021/05/11	09:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/11	09:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	09:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	09:49:58.0	XRT_FOCUS_POSITION_416_OG [0x1a0]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/05/11	09:50:00.0	AOCS_ORe-point_Start_7_OG [0x09d]		5	02-76	00 2e f9 d1 07	
			AOCU_NM				
2021/05/11	09:52:52.0	XRT_ARS_DIS_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5	
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/05/11	09:52:54.0	XRT_FLRCTRL_DIS_436_OG [0x1b4]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2021/05/11	09:52:56.0	XRT_FLD_DIS_407_OG [0x197]	MDP_XRT_FLD_DIS	1	07-F0	d9	
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2021/05/11	09:52:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2021/05/11	09:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/11	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/11	09:59:58.0	XRT_FOCUS_POSITION_416_OG [0x1a0]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/05/11	10:00:00.0	AOCS_ORe-point_Start_8_OG [0x09e]		5	02-76	00 d1 07 d1 07	
			AOCU_NM				

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2021/05/11	10:02:52.0	XRT_ARS_DIS_444_OG [0x1bc]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/11	10:02:54.0	XRT_FLRCTRL_DIS_436_OG [0x1b4]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2021/05/11	10:02:56.0	XRT_FLD_DIS_407_OG [0x197]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2021/05/11	10:02:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2021/05/11	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/11	10:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	10:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	10:09:58.0	XRT_FOCUS_POSITION_416_OG [0x1a0]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2021/05/11	10:10:00.0	AOCS_OrE-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 d1 07 2e f9				
2021/05/11	10:12:52.0	XRT_ARS_DIS_444_OG [0x1bc]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/11	10:12:54.0	XRT_FLRCTRL_DIS_436_OG [0x1b4]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2021/05/11	10:12:56.0	XRT_FLD_DIS_407_OG [0x197]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2021/05/11	10:12:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2021/05/11	10:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/11	10:20:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2021/05/11	10:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	10:24:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	10:24:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/11	10:24:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/11	10:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/11	11:31:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				