

# XRT Timeline to be uploaded on 2021/05/11

Period: 2021/05/11 11:21:00 - 2021/05/15 11:01:00

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Normal mode

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## XOB #1BB9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/11 11:34:00 - 05/11 16:59:54	Track ( -407.8, 286.8) <sup>05/11 11:31:00</sup>	# OP start + 10min, AR12822
05/11 17:50:00 - 05/12 06:49:54	Track ( -359.7, 287.6) <sup>05/11 17:30:00</sup>	AR12822
<b>PROG= 16 Inf.-time(s)</b>		
Subr= 1 1-time(s) 2.0sec		
Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
Subr= 2 5-time(s) 2.0sec		
Seqn= 47 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
Seqn= 96 4-time(s) 90.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 2.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 2.0sec
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1CA9: Synoptic Q95 2x2 - Al/mesh(16/181/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/4096) + T

Term	Pointing (x, y)	Comment
05/11 17:03:00 - 05/11 17:10:00	Fixed ( 0.0, 0.0)	synoptic, shifted for XRT stray light
05/12 06:53:00 - 05/12 06:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted
05/12 17:33:00 - 05/12 17:45:30	Fixed ( 0.0, 0.0)	synoptic, shifted for XRT stray light
05/13 01:48:00 - 05/13 01:56:30	Fixed ( 0.0, 0.0)	HOP349 disk center
05/13 06:28:00 - 05/13 06:37:00	Fixed ( 0.0, 0.0)	synoptic, shifted
<b>PROG= 03 1-time(s)</b>		
Subr= 1 1-time(s) 2.0sec		
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024)	DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024)	DPCM 0 0 2.0sec
Seqn= 75 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 9 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 53 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 23 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1CAD: Stray light study 2021-01; Al-mesh and Al-poly, 2x2 full FOV(1min-cad) and 1x1 384 on AR(10sec-cad)

Term	Pointing (x, y)	Comment
05/11 17:12:30 - 05/11 17:29:54	Fixed ( 0.0, 0.0)	synoptic, shifted for XRT stray light
05/12 21:04:30 - 05/12 21:30:00	Track ( -152.1, 289.1) <sup>05/12 18:00:00</sup>	AR12822
<b>PROG= 01 1-time(s)</b>		
Subr= 2 16-time(s) 2.0sec		
Seqn= 78 1-time(s) 10.0sec		
Open/Al-mesh	Open/thick-Al close Safe Norm 2.83s Obs 1x1 384x384 (1024, 1024)	Q=95 0 0 2.0sec
Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1024, 1024)	Q=95 0 0 2.0sec
Subr= 3 8-time(s) 2.0sec		
Seqn= 66 1-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	DPCM 0 0 2.0sec
Al-poly/Open	Al-poly/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024)	DPCM 0 0 2.0sec
Seqn= 78 3-time(s) 10.0sec		
Open/Al-mesh	Open/thick-Al close Safe Norm 2.83s Obs 1x1 384x384 (1024, 1024)	Q=95 0 0 2.0sec

Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1B96: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)**

Term	Pointing (x, y)	Comment
05/12 07:03:00 - 05/12 09:20:30	Track ( 316.5, 54.1) @ 05/12 07:00:00	HOP417
05/13 07:23:30 - 05/13 09:56:30	Track ( 515.1, 47.5) @ 05/13 07:00:00	HOP417
<b>PROG= 20 Inf.-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 30 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
<b>Subr= 2 10-time(s) 360.0sec</b>		
<b>Seqn= 8 1-time(s) 2.0sec</b>		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

Term	Pointing (x, y)	Comment
05/12 10:04:30 - 05/12 17:29:54	Track ( -221.4, 288.9) @ 05/12 10:00:00	AR12822
05/12 18:25:00 - 05/12 21:02:00	Track ( -152.1, 289.1) @ 05/12 18:00:00	AR12822
05/12 21:39:00 - 05/13 01:44:54	Track ( -152.1, 289.1) @ 05/12 18:00:00	AR12822
<b>PROG= 12 Inf.-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 92 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
<b>Subr= 2 5-time(s) 2.0sec</b>		
<b>Seqn= 47 1-time(s) 2.0sec</b>		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
<b>Seqn= 96 4-time(s) 120.0sec</b>		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1CAE: Stray light study 2021-2 ; med-Al, 2x2 full FOV(1min-cad) and 1x1 384x384 (18sec-cad) at Center**

Term	Pointing (x, y)	Comment
05/12 17:48:00 - 05/12 17:59:54	Fixed ( 0.0, 0.0)	synoptic, shifted for XRT stray light
<b>PROG= 05 1-time(s)</b>		
<b>Subr= 2 14-time(s) 2.0sec</b>		
<b>Seqn= 78 1-time(s) 10.0sec</b>		
Open/Al-mesh	Open/thick-Al close	Safe Norm 2.83s Obs 1x1 384x384 (1024, 1024) Q=95 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=95 0 0 2.0sec
<b>Subr= 3 8-time(s) 2.0sec</b>		
<b>Seqn= 82 1-time(s) 2.0sec</b>		
med-Al/Open	med-Al/Open close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
<b>Seqn= 78 3-time(s) 10.0sec</b>		
Open/Al-mesh	Open/thick-Al close	Safe Norm 2.83s Obs 1x1 384x384 (1024, 1024) Q=95 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1C8D: Alignment with North Pole Al/poly 1443ms Q95 2x2 (G-band and VLS=CLS) - 5min cad**

Term	Pointing (x, y)	Comment
05/13 02:40:00 - 05/13 04:24:54	Fixed ( 0.0, 930.0)	Co-alignment N-limb
<b>PROG= 06 1-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 23 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
<b>Subr= 2 24-time(s) 300.0sec</b>		
<b>Seqn= 69 1-time(s) 2.0sec</b>		
Al-poly/Open	med-Be/Open close	Safe Norm 1.41s Obs 2x2 2048x1536 (1024, 768) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1C8E: Alignment with East limb Al/poly 1443ms Q95 2x2 (G-band and VLS=CLS) - 8 min cad												
Term		Pointing (x, y)				Comment						
05/13 04:40:00 - 05/13 06:24:54		Fixed (-970.0, 0.0)				Co-alignment E-limb						
PROG= 11 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─┬─ Seqn= 23 1-time(s) 2.0sec												
└─┬─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─┬─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─┬─ Subr= 2 15-time(s) 480.0sec												
└─┬─ Seqn= 70 1-time(s) 2.0sec												
└─┬─ Al-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 1536x2048 (1280, 1024) Q=95 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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### Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G												
Term		Pointing (x, y)				Comment						
05/11 11:34:00 - 05/11 16:59:54		Track (-407.8, 286.8) <sup>Ⓜ</sup> 05/11 11:31:00				# OP start + 10min, AR12822						
05/11 17:50:00 - 05/12 06:49:54		Track (-359.7, 287.6) <sup>Ⓜ</sup> 05/11 17:30:00				AR12822						
05/12 07:03:00 - 05/12 09:20:30		Track ( 316.5, 54.1) <sup>Ⓜ</sup> 05/12 07:00:00				HOP417						
05/12 10:04:30 - 05/12 17:29:54		Track (-221.4, 288.9) <sup>Ⓜ</sup> 05/12 10:00:00				AR12822						
05/12 18:25:00 - 05/12 21:02:00		Track (-152.1, 289.1) <sup>Ⓜ</sup> 05/12 18:00:00				AR12822						
05/12 21:39:00 - 05/13 01:44:54		Track (-152.1, 289.1) <sup>Ⓜ</sup> 05/12 18:00:00				AR12822						
05/13 07:23:30 - 05/13 09:56:30		Track ( 515.1, 47.5) <sup>Ⓜ</sup> 05/13 07:00:00				HOP417						
PROG= 04 30-time(s)												
└─ Subr= 1 20-time(s) 2.0sec												
└─┬─ Seqn= 11 1-time(s) 2.0sec												
└─┬─ Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─┬─ Seqn= 73 1-time(s) 10.0sec												
└─┬─ thin-Be/Open med-Be/Open close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─ med-Be/Open Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─ Subr= 2 1-time(s) 2.0sec												
└─┬─ Seqn= 10 1-time(s) 2.0sec												
└─┬─ med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└─┬─ Seqn= 11 1-time(s) 2.0sec												
└─┬─ Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└─┬─ Seqn= 87 1-time(s) 2.0sec												
└─┬─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└─┬─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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### Active Region Search

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NOT USED

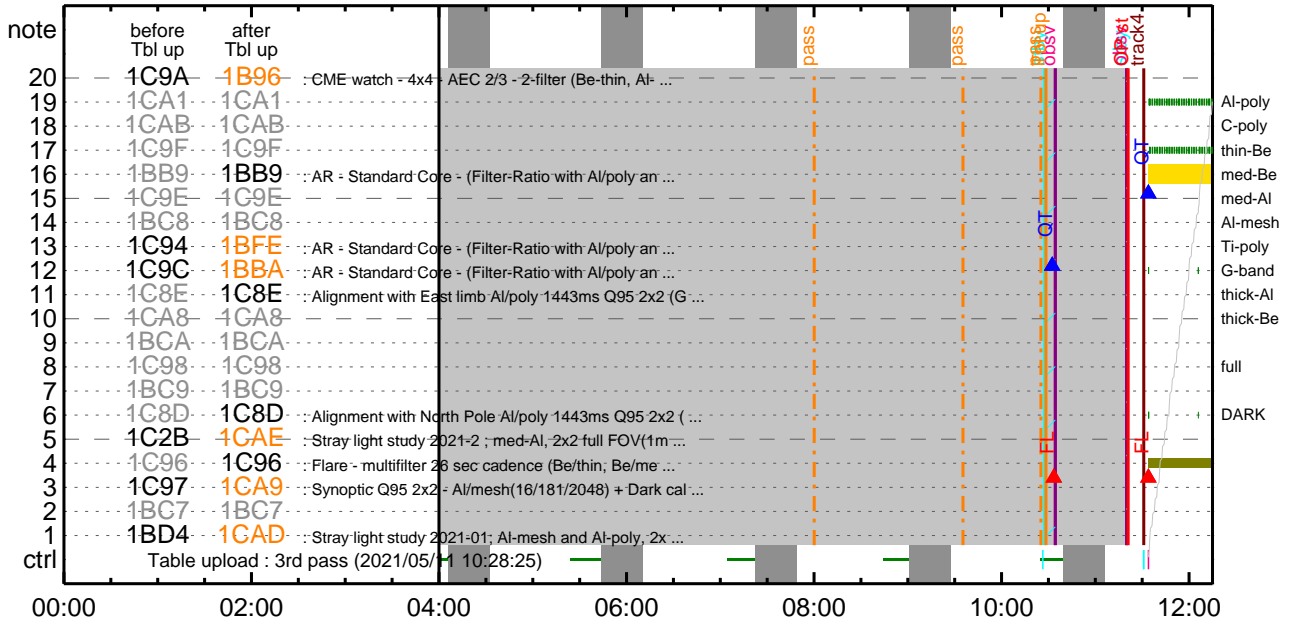
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### Flare Detection

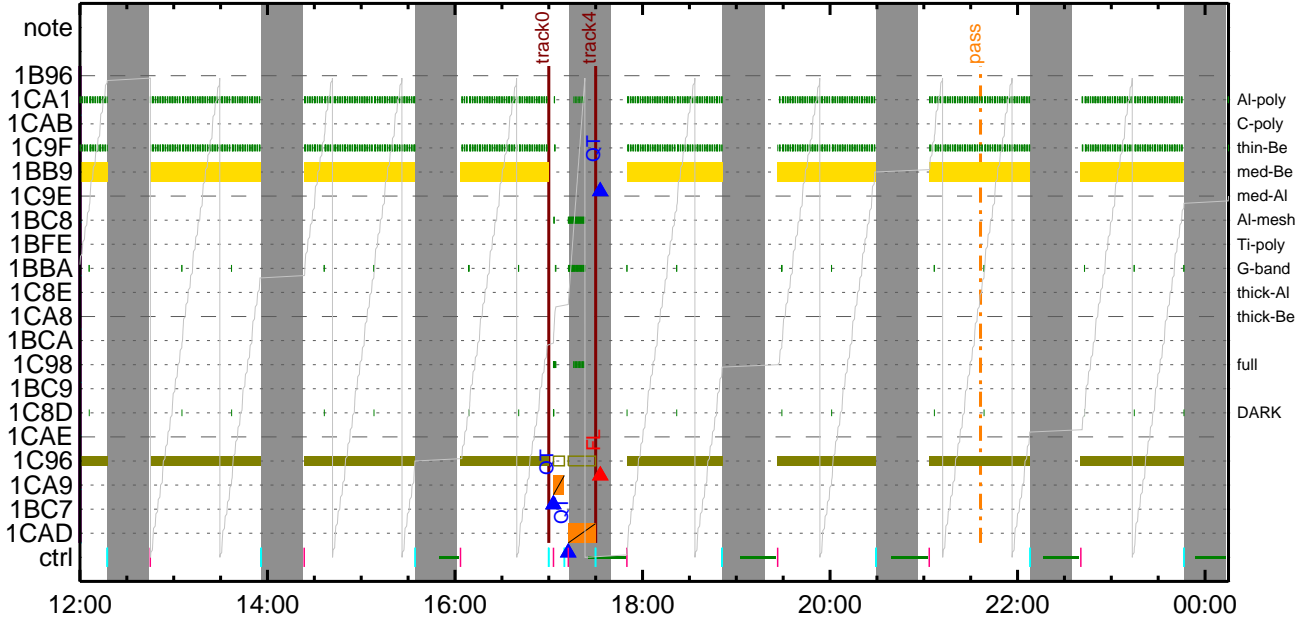
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FLD Patrol												
Term		Pointing (x, y)				Comment						
05/11 17:30:18 - 05/12 06:50:18		Track (-359.7, 287.6) <sup>Ⓜ</sup> 05/11 17:30:00				AR12822						
05/12 07:00:18 - 05/12 17:30:18		Track ( 316.5, 54.1) <sup>Ⓜ</sup> 05/12 07:00:00				HOP417						
05/12 18:00:18 - 05/12 21:02:24		Track (-152.1, 289.1) <sup>Ⓜ</sup> 05/12 18:00:00				AR12822						
05/12 21:30:24 - 05/13 01:45:18		Track (-152.1, 289.1) <sup>Ⓜ</sup> 05/12 18:00:00				AR12822						
05/13 07:00:18 - 05/15 11:01:00		Track ( 515.1, 47.5) <sup>Ⓜ</sup> 05/13 07:00:00				HOP417						
Al-poly/Open		Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

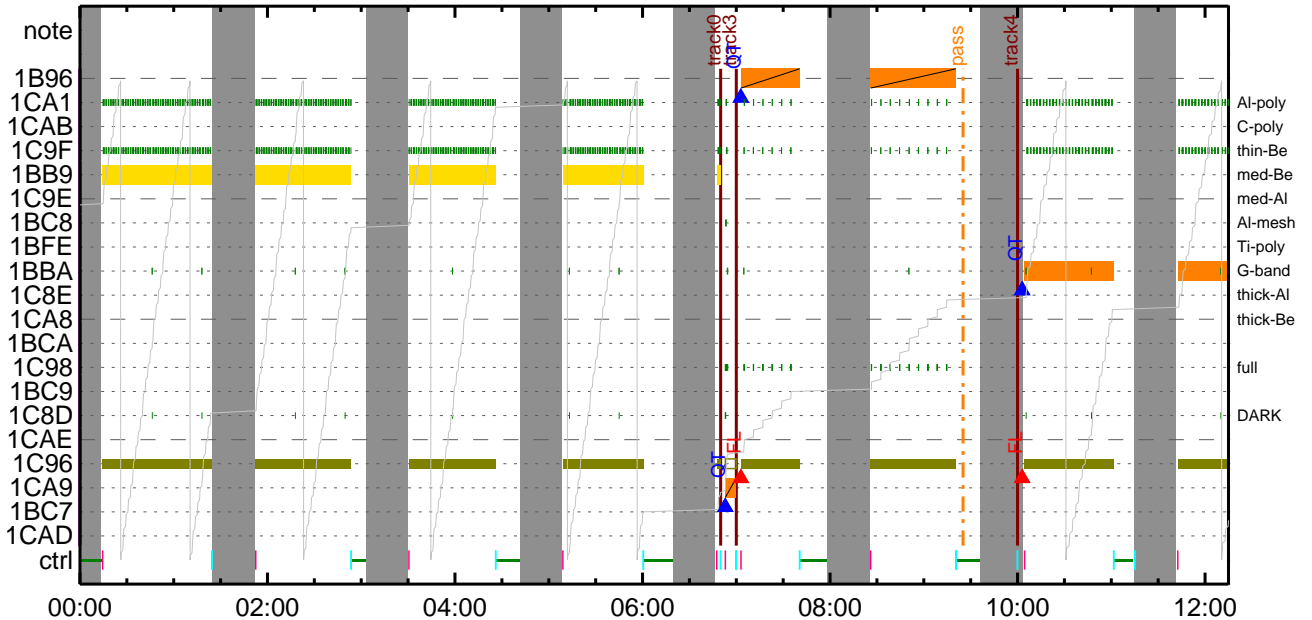
CMDI #0642 2021/05/11



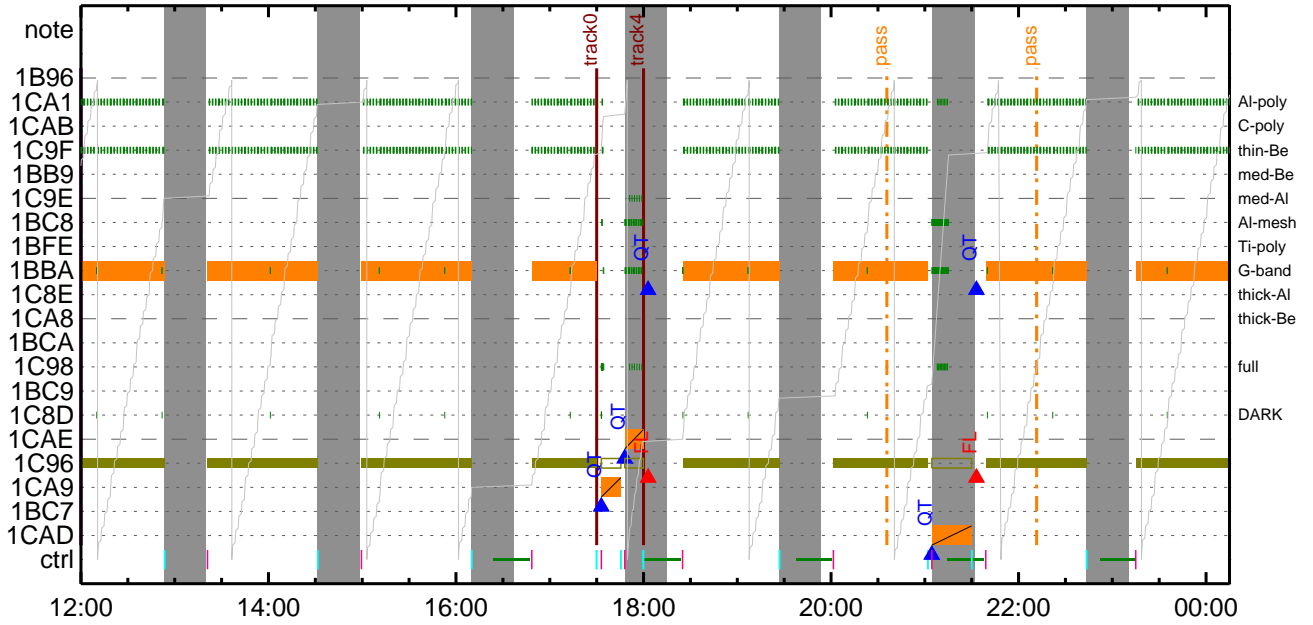
CMDI #0642 2021/05/11



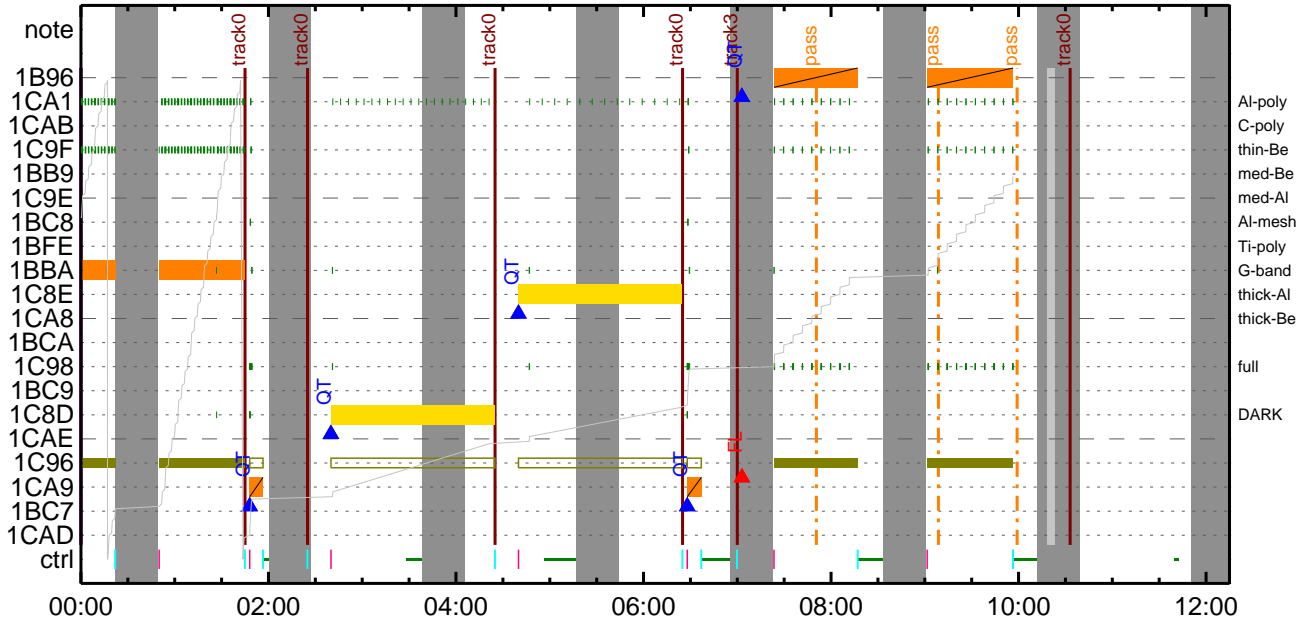
CMDI #0642 2021/05/12



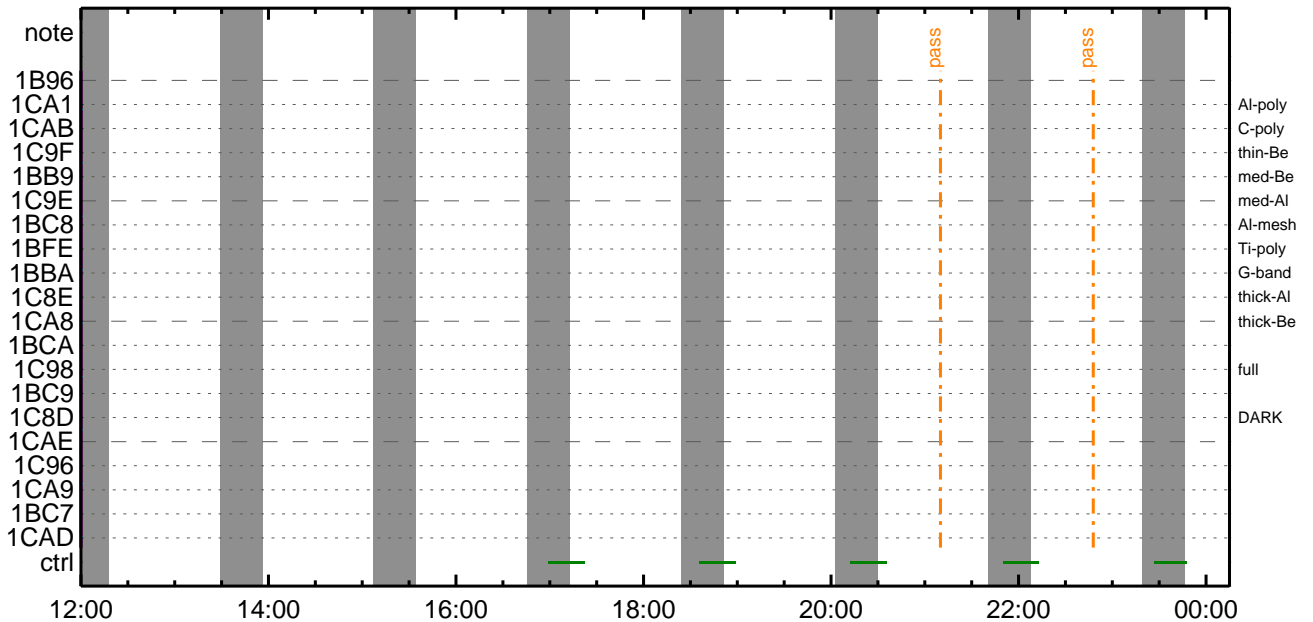
CMDI #0642 2021/05/12



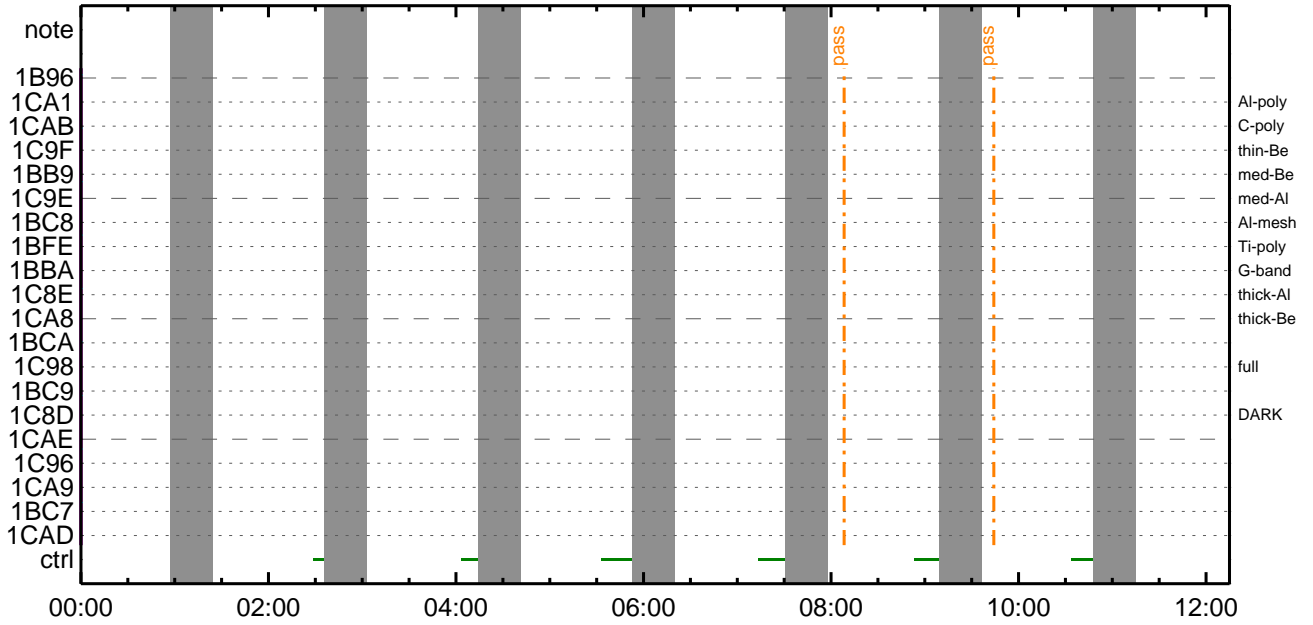
CMDI #0642 2021/05/13



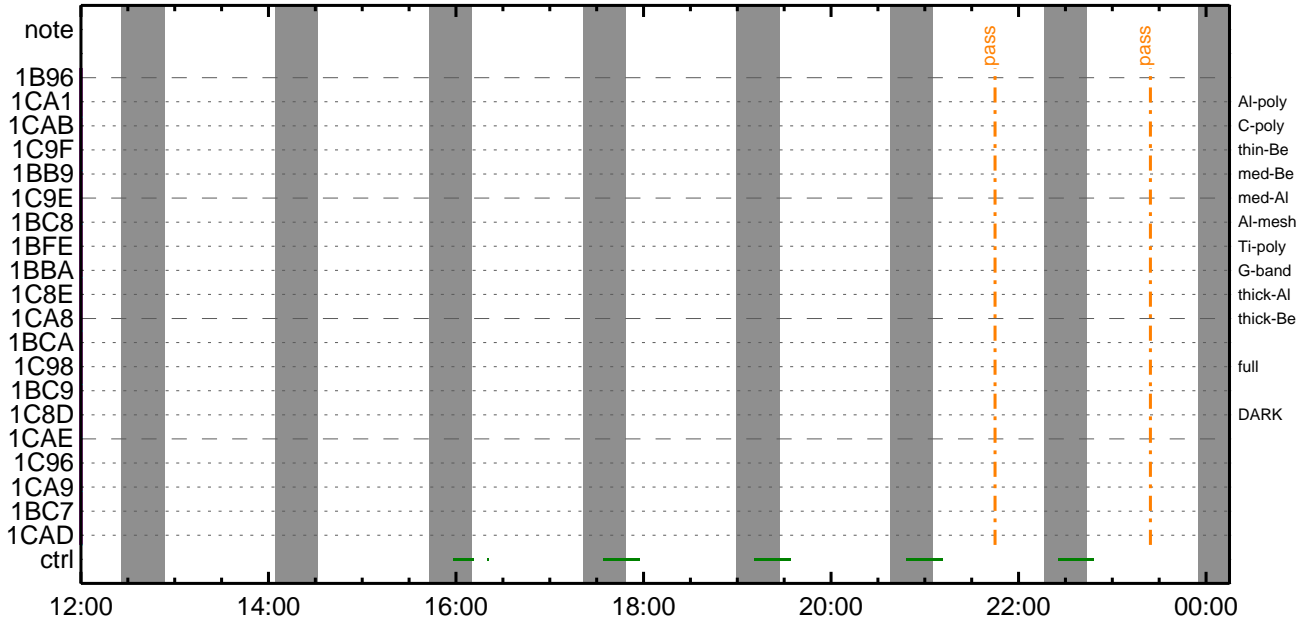
CMDI #0642 2021/05/13



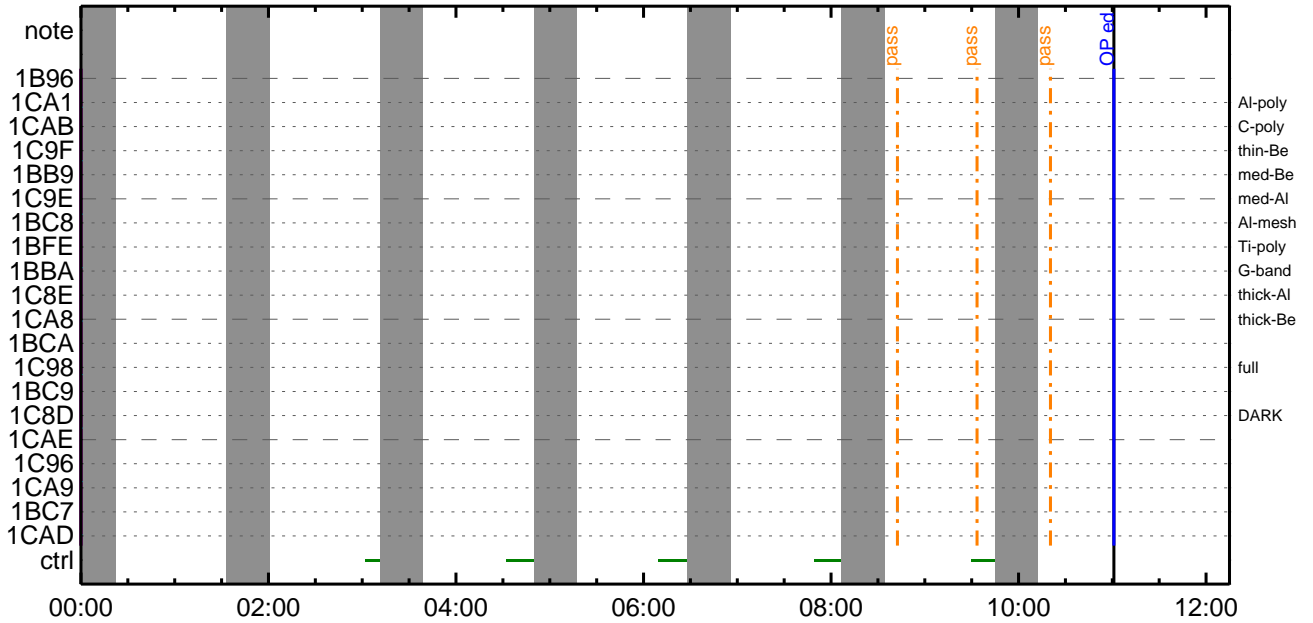
CMDI #0642 2021/05/14

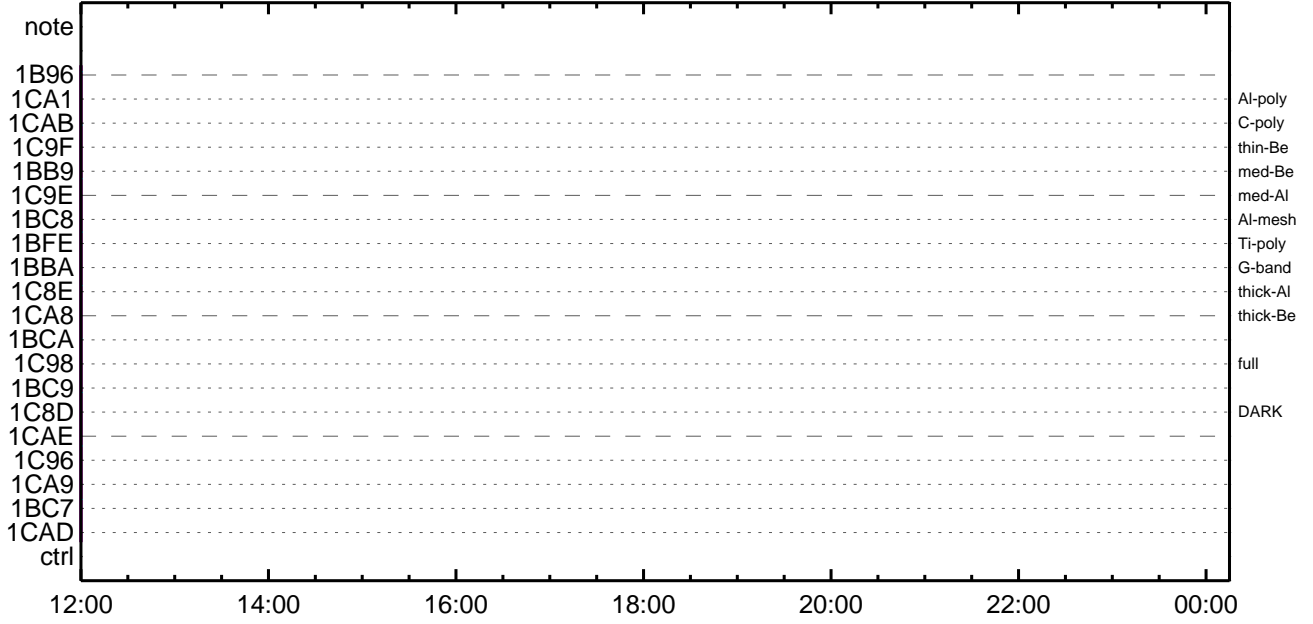


CMDI #0642 2021/05/14



CMDI #0642 2021/05/15









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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-481:OP
0104 ( )
0105 S. OG og-481:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î¼E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î¼E¹ç•è²¼²îTI-CMDÁ÷ç²î¼E¹Ô²•²E²²²³²E;f
0180 C. ²²²ç;çSET²E²DUMP²î¼E¹ç•è²¼²î¼E¹Ô²³²E;f
0181 C.
0182 C. TIY³Y²Y²Y²E²ò²ÁDîç(UT)
0183 +. TI 2021-05-11 11:16:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2021-05-11 11:16:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2021-05-11 11:16:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```

0194 C.
0195 +. TI 2021-05-11 11:20:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄê%îíñαîîŷÄŷ§ŷÄŷ-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾Ê¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÊ;Ê¼ŷ¼. ŷî;¼ŷÊ;Êαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2021-05-11 11:20:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2021-05-11 11:20:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2021-05-11 11:20:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2021-05-11 11:20:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ŷαÊÄα¹αèDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÊŷŷŷÄŷçŷèè¼αα¼Ä»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```





(a) Spacecraft Operation Procedure (real-commands)

```
main-483 2021-05-11 12:06:56 136 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ä
0005 C.
0006 C. YÁY$;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÄ•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈÈ¼°ÇÓã•¿¿¼í¹ç¿Í;çÀ®,ù¿¹¿è¿¿ãÇÁ+¿®ã•¿È¿¿¿¿¿¿¿¿¿¿¿¿;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-284:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2021-05-11 11:20:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 80 80 20 20)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 08)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 08 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 06 06)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 80 80 08 08)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0c 80 60 20 18)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0d a0 80 18 20)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0f 80 80 06 06)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 10 80 80 08 08)
0094 + DC 07-F0 MDP_XRT_FLD_ENA
0095 BC (d8)
```

```
0096 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0097 BC (c8)
0098 + DC 07-F0 MDP_XRT_ARS_DIS
0099 BC (d5)
0100 + DC 07-F0 MDP_XRT_AEC_RESET
0101 BC (d0)
0102 + DC 07-F0 MDP_XRT_FLD_RESET
0103 BC (da)
0104 + DC 07-F0 MDP_XRT_QT_PROG_SET
0105 BC (c4 0d)
0106 + DC 07-F0 MDP_XRT_FL_PROG_SET
0107 BC (c5 04)
0108 . C. ----- Success Verify ? OK / NG ____
0109 C.
0110 C.
0111 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0112 C.
0113 + DC 07-F0 MDP_XRT_MODE_OBSV
0114 BC (c2)
0115 + TI 2021-05-11 11:20:02.0
0116 DC 07-F0 MDP_XRT_MODE_OBSV
0117 BC (c2)
0118 . C. ----- Success Verify ? OK / NG ____
0119 C.
0120 C. ***** XRT END *****
0121 C.
0122 . C. ***** MDP 'úÃîñî»ö¼ÝðËÂð¹ñèDCBC•x²è *****
0123 C. (%ã°îÿÓÿÄÿËÿPÿËÿâÿçÿèñ¼ññ¼Ä»Ûñ¹ñè)
0124 . S. DC-BC dcbc-402:DCBC
0125 (MDP_known_event)
0126 C.
0127 C.
0128 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎññË´Øñ¹ñèDCBC•x²è *****
0129 . S. DC-BC dcbc-153:DCBC
0130 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0131 C.
0132 C.
0133 . C. ;ãLOSÿÄÿSÿËÿÄÿ-¼Ä»Û;ã
0134 C.
0135 . C. ***** LOS *****
0136 C.
```

\*\*\* OP Sequence for XRT \*\*\*

2021/05/11	11:30:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	11:30:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	11:30:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2021/05/11	11:31:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	04 00 00 00 00		
2021/05/11	11:31:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2021/05/11	11:31:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2021/05/11	11:31:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2021/05/11	11:31:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/05/11	11:31:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/11	11:33:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2021/05/11	11:33:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2021/05/11	11:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/11	12:17:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	12:17:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	12:17:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/11	12:17:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/05/11	12:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/11	12:44:00.0	XRT_Custom_430_OG [0x1ae]					
2021/05/11	12:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/11	13:56:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	13:56:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	13:56:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/11	13:56:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/05/11	13:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/11	14:22:30.0	XRT_Custom_430_OG [0x1ae]					
2021/05/11	14:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/11	15:34:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	15:34:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	15:34:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/11	15:34:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/05/11	15:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/11	16:02:30.0	XRT_Custom_430_OG [0x1ae]					
2021/05/11	16:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/11	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	16:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2021/05/11	17:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2021/05/11	17:00:18.0	XRT_FLD_DIS_409_OG [0x199]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2021/05/11	17:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2021/05/11	17:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/05/11	17:02:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2021/05/11	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/11	17:10:00.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	17:10:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/11	17:10:04.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2021/05/11	17:10:24.0	XRT_FLD_DIS_437_OG [0x1b5]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2021/05/11	17:12:24.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		

2021/05/11	17:12:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/11	17:12:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2021/05/11	17:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/11	17:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	17:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	17:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2021/05/11	17:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2021/05/11	17:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/05/11	17:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/05/11	17:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/05/11	17:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/11	17:30:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/11	17:32:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2021/05/11	17:32:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2021/05/11	17:49:00.0	XRT_Custom_430_OG [0x1ae]							
2021/05/11	17:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/11	18:51:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	18:51:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	18:51:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/11	18:51:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/11	18:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/11	19:25:30.0	XRT_Custom_430_OG [0x1ae]							
2021/05/11	19:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/11	20:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	20:29:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	20:29:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/11	20:29:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/11	20:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/11	21:02:30.0	XRT_Custom_430_OG [0x1ae]							
2021/05/11	21:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/11	22:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	22:08:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	22:08:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/11	22:08:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/11	22:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/11	22:39:30.0	XRT_Custom_430_OG [0x1ae]							
2021/05/11	22:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/11	23:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	23:46:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/11	23:46:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/11	23:46:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/11	23:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/12	00:13:30.0	XRT_Custom_430_OG [0x1ae]							
2021/05/12	00:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/12	01:24:30.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	01:24:32.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	01:24:34.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/12	01:24:36.5	XRT_PREFLR_STRT_440_OG [0x1b8]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/12	01:27:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				



2021/05/12	01:51:30.0	XRT_Custom_430_OG [0x1ae]			
2021/05/12	01:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/12	02:53:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	02:53:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	02:53:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/12	02:53:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/12	02:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/12	03:29:30.0	XRT_Custom_430_OG [0x1ae]			
2021/05/12	03:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/12	04:26:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	04:26:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	04:26:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/12	04:26:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/12	04:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/12	05:08:00.0	XRT_Custom_430_OG [0x1ae]			
2021/05/12	05:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/12	06:00:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	06:00:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	06:00:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/12	06:00:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/12	06:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/12	06:46:30.0	XRT_Custom_430_OG [0x1ae]			
2021/05/12	06:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/12	06:49:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	06:49:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	06:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2021/05/12	06:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2021/05/12	06:50:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2021/05/12	06:50:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2021/05/12	06:50:22.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2021/05/12	06:52:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2021/05/12	06:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/12	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	06:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	06:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2021/05/12	07:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	03 00 00 00 00
2021/05/12	07:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2021/05/12	07:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2021/05/12	07:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2021/05/12	07:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2021/05/12	07:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/12	07:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2021/05/12	07:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2021/05/12	07:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/05/12	07:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	07:40:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/05/12	07:40:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/12	07:40:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/12	07:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			

2021/05/12	08:25:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/12	08:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2021/05/12	09:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/12	09:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	09:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	09:20:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/12	09:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/05/12	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/12	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	09:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	10:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2021/05/12	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 00 00 00 00		
2021/05/12	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2021/05/12	10:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2021/05/12	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2021/05/12	10:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/05/12	10:02:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/12	10:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2021/05/12	10:03:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2021/05/12	10:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2021/05/12	11:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/12	11:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	11:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	11:01:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/12	11:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/05/12	11:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/12	11:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	11:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	11:15:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/12	11:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/05/12	11:41:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/12	11:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2021/05/12	12:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/12	12:53:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	12:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	12:53:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/12	12:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/05/12	13:20:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/12	13:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2021/05/12	14:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/12	14:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	14:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	14:31:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/12	14:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/05/12	14:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/12	14:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2021/05/12	16:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/12	16:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/12	16:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

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2021/05/12	16:10:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/12	16:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/12	16:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/12	16:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/12	17:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	17:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	17:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2021/05/12	17:30:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2021/05/12	17:30:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2021/05/12	17:30:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2021/05/12	17:30:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/12	17:32:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2021/05/12	17:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/12	17:45:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	17:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	17:45:34.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2021/05/12	17:45:54.0	XRT_FLD_DIS_437_OG [0x1b5]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2021/05/12	17:47:54.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2021/05/12	17:47:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/12	17:47:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2021/05/12	17:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/12	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	17:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2021/05/12	18:00:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00	00	00	00
2021/05/12	18:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/05/12	18:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/05/12	18:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/05/12	18:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/12	18:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/12	18:02:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2021/05/12	18:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/05/12	18:24:00.0	XRT_Custom_430_OG [0x1ae]								
2021/05/12	18:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/12	19:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	19:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	19:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2021/05/12	19:27:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/05/12	19:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/05/12	20:00:30.0	XRT_Custom_430_OG [0x1ae]								
2021/05/12	20:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/05/12	21:02:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	21:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/05/12	21:02:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2021/05/12	21:02:24.0	XRT_FLD_DIS_437_OG [0x1b5]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2021/05/12	21:04:24.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2021/05/12	21:04:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/05/12	21:04:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]								

2021/05/12	21:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/12	21:30:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/12	21:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/12	21:30:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2021/05/12	21:30:24.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2021/05/12	21:30:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2021/05/12	21:30:28.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2021/05/12	21:30:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/05/12	21:30:32.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/12	21:33:02.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2021/05/12	21:33:04.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2021/05/12	21:38:00.0	XRT_Custom_430_OG [0x1ae]					
2021/05/12	21:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/12	22:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/12	22:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/12	22:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/12	22:43:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/12	22:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/12	23:14:00.0	XRT_Custom_430_OG [0x1ae]					
2021/05/12	23:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/13	00:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/13	00:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/13	00:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/13	00:22:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/13	00:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/13	00:49:00.5	XRT_Custom_430_OG [0x1ae]					
2021/05/13	00:50:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/13	01:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/13	01:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/13	01:44:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/05/13	01:45:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2021/05/13	01:45:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2021/05/13	01:45:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2021/05/13	01:45:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/05/13	01:47:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2021/05/13	01:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/05/13	01:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/13	01:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/13	01:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/05/13	01:56:36.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/05/13	01:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/05/13	02:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/13	02:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/05/13	02:24:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2021/05/13	02:25:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 ad 59 00 00	
2021/05/13	02:25:18.0	XRT_FLD_DIS_425_OG [0x1a9]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2021/05/13	02:39:54.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2021/05/13	02:39:56.0	XRT_ARS_DIS_444_OG [0x1bc]					

2021/05/13	02:39:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/05/13	02:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06	
2021/05/13	04:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/13	04:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	04:24:58.0	XRT_FOCUS_POSITION_433_OG [0x1b1]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	04:25:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2021/05/13	04:25:18.0	XRT_FLD_DIS_425_OG [0x1a9]	AOCU_NM	5	02-76	00	00 00 56 35	
2021/05/13	04:39:54.0	XRT_ARS_DIS_444_OG [0x1bc]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2021/05/13	04:39:56.0	XRT_FLRCTRL_DIS_436_OG [0x1b4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/05/13	04:39:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2021/05/13	04:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]	XRT_QT_PROG_SET	2	07-F0	c4	0b	
2021/05/13	06:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/13	06:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	06:24:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	06:25:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2021/05/13	06:25:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00	00 00 00 00	
2021/05/13	06:25:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2021/05/13	06:25:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2021/05/13	06:27:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/05/13	06:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	XRT_QT_PROG_SET	2	07-F0	c4	03	
2021/05/13	06:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/13	06:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	06:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	06:37:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/13	06:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/05/13	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/13	06:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	06:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	07:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2021/05/13	07:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	00 00 00 00	
2021/05/13	07:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2021/05/13	07:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2021/05/13	07:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2021/05/13	07:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/05/13	07:02:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/13	07:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	XRT_QT_PROG_SET	2	07-F0	c4	14	
2021/05/13	07:22:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04	
2021/05/13	07:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/13	08:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	08:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	08:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13	08:17:06.0	XRT_PREFLR_STRT_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/05/13	08:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/05/13	09:00:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/05/13	09:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0		
2021/05/13	09:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/05/13	09:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/05/13			MDP_XRT_CTRL_MANU	1	07-F0	c1		

2021/05/13	09:56:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2021/05/13	09:56:36.0	XRT_PREFLR_STRT_440_OG [0x1b8] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/05/13	09:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/05/13	10:33:00.0	AOCS_Orpoint_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00