

XRT Timeline to be uploaded on 2021/08/17

Period: 2021/08/17 11:12:00 - 2021/08/21 11:25:00

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Normal mode

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XOB #1BC7: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(2048ms), Al/Poly(4096ms) - w leak image-1ms

Term	Pointing (x, y)	Comment
08/18 12:38:00 - 08/18 12:44:54	Fixed (-528.4, -528.4)	XRT post bakeout #1
PROG= 02 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 51 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 3 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 34 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1BC8: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
08/18 12:48:00 - 08/18 12:54:54	Fixed (528.4, -528.4)	XRT post bakeout #2
PROG= 13 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 38 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 3 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 34 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1BC9: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
08/18 12:58:00 - 08/18 13:04:54	Fixed (528.4, 528.4)	XRT post bakeout #3
PROG= 14 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 21 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 3 2-time(s) 2.0sec		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Subr= 3 2-time(s) 2.0sec		
└─ Seqn= 34 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1BCA: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
08/18 13:08:00 - 08/18 13:14:54	Fixed (-528.4, 528.4)	XRT post bakeout #4
PROG= 09 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 14 1-time(s) 2.0sec		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec	
└─ Subr= 2 1-time(s) 2.0sec		

Seqn= 92		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec			
Subr= 2		5-time(s)		2.0sec											
Seqn= 47		1-time(s)		2.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
Seqn= 96		4-time(s)		90.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G												
Term		Pointing (x, y)				Comment						
08/18 13:28:00 - 08/18 17:59:54		Track (638.9, 168.8) ^{Ⓢ 08/18 13:25:00}				AR obs.						
08/18 18:13:00 - 08/18 21:09:54		Track (61.8, 719.7) ^{Ⓢ 08/18 18:10:00}				QS obs.						
08/18 21:13:00 - 08/19 01:59:54		Track (682.9, 174.6) ^{Ⓢ 08/18 21:10:00}				AR obs.						
08/19 02:03:00 - 08/19 05:42:54		Fixed (0.0, 0.0)				HOP349 and synoptic						
08/19 06:43:00 - 08/19 10:40:00		Track (731.4, 182.3) ^{Ⓢ 08/19 06:40:00}				HOP419						

PROG= 04 30-time(s)

Subr= 1		20-time(s)		2.0sec											
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Seqn= 73		1-time(s)		10.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Seqn= 87		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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Active Region Search

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NOT USED

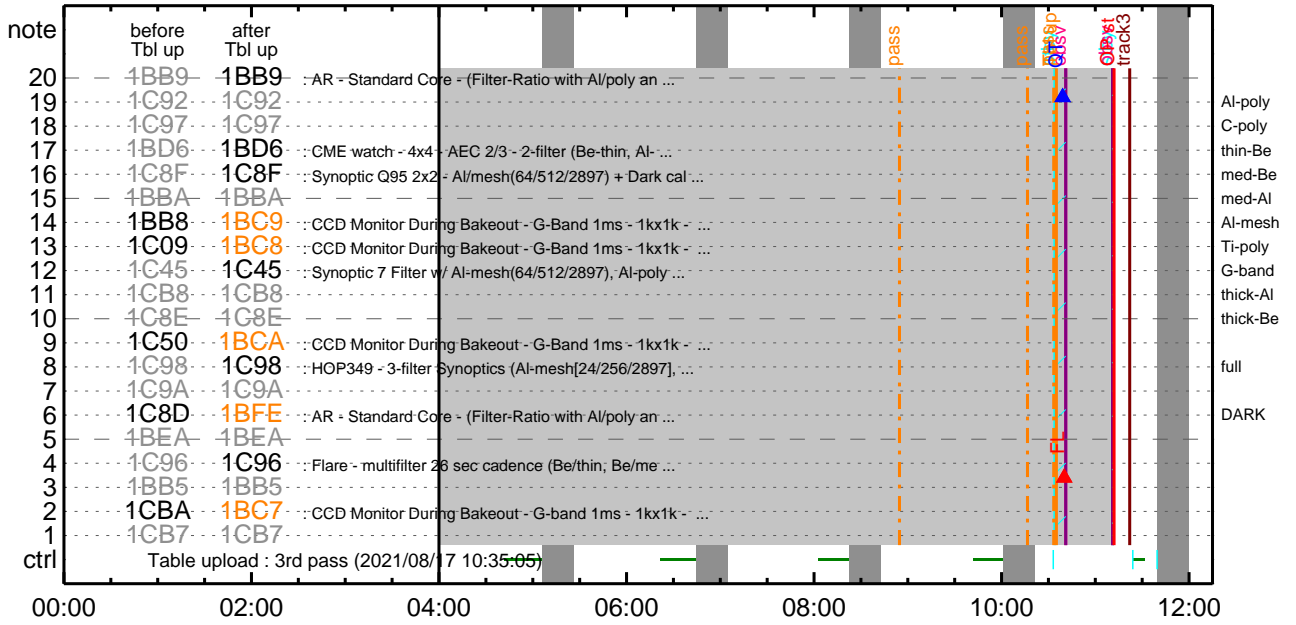
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Flare Detection

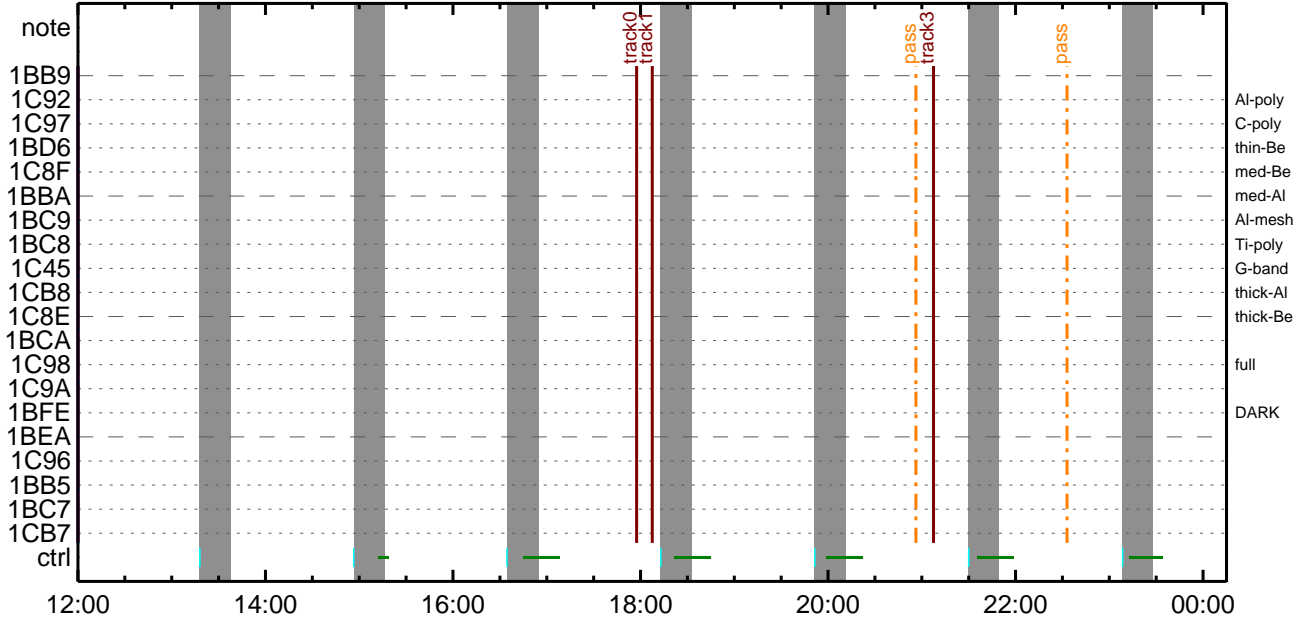
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FLD Patrol													
Term		Pointing (x, y)				Comment							
08/18 13:25:18 - 08/18 18:00:18		Track (638.9, 168.8) ^{Ⓢ 08/18 13:25:00}				AR obs.							
08/18 18:10:18 - 08/19 05:43:18		Track (61.8, 719.7) ^{Ⓢ 08/18 18:10:00}				QS obs.							
08/19 06:40:18 - 08/21 11:25:00		Track (731.4, 182.3) ^{Ⓢ 08/19 06:40:00}				HOP419							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

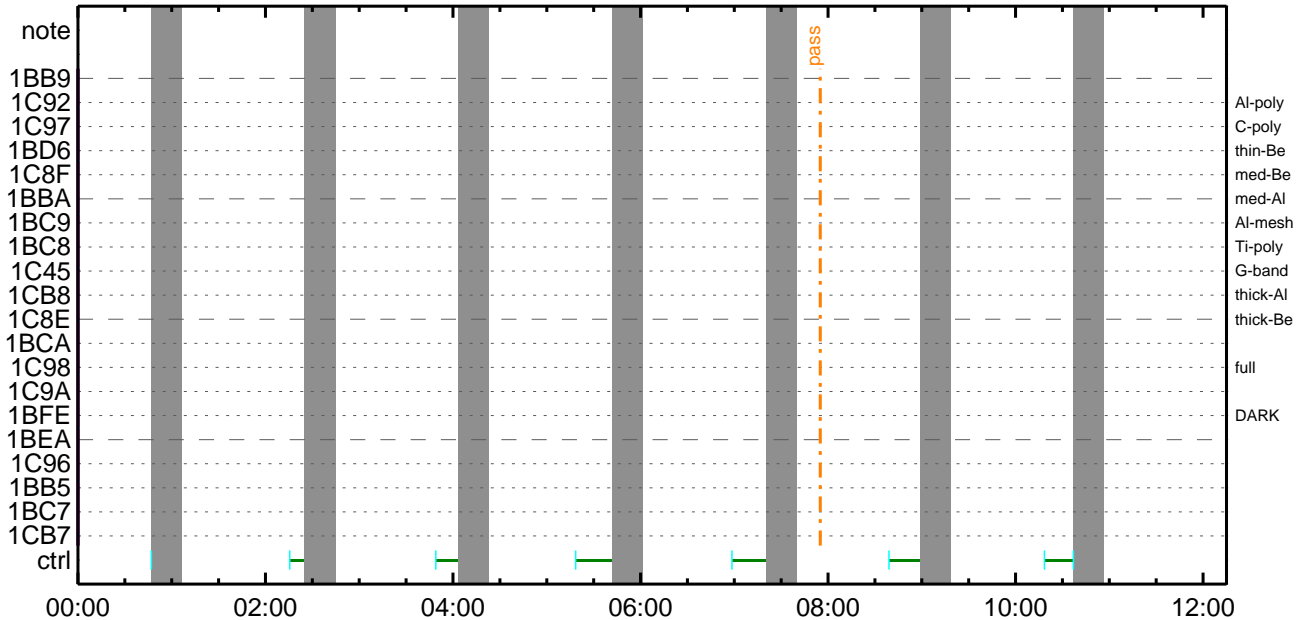
CMDI #0816 2021/08/17



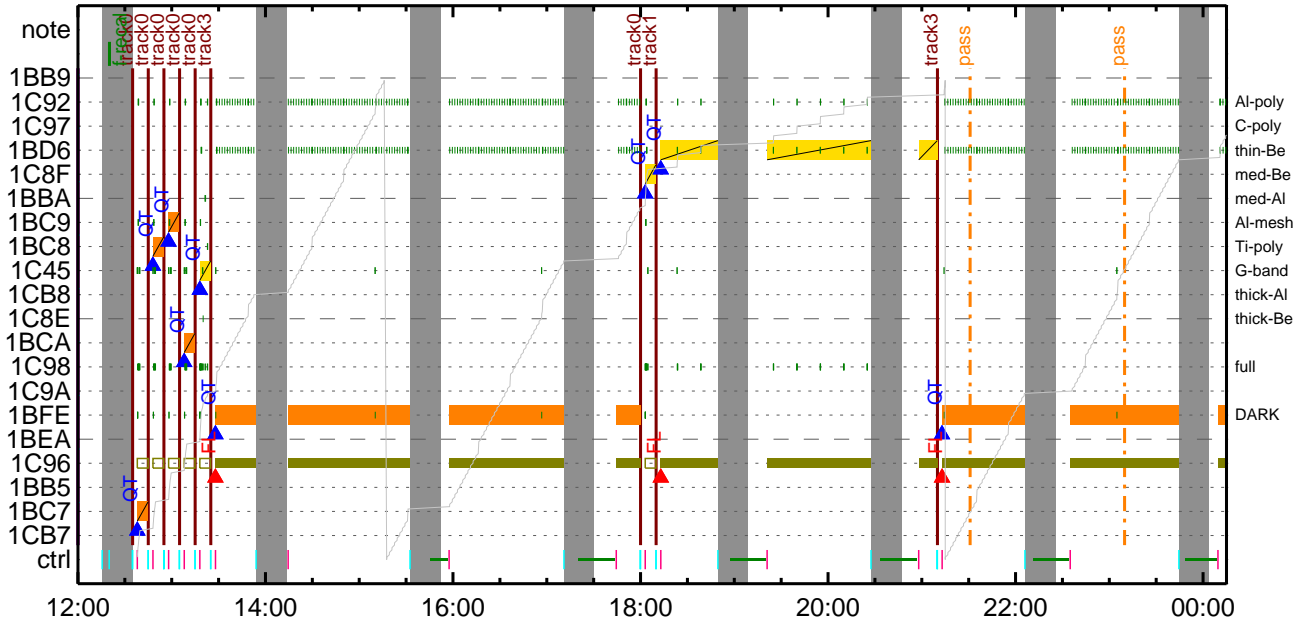
CMDI #0816 2021/08/17



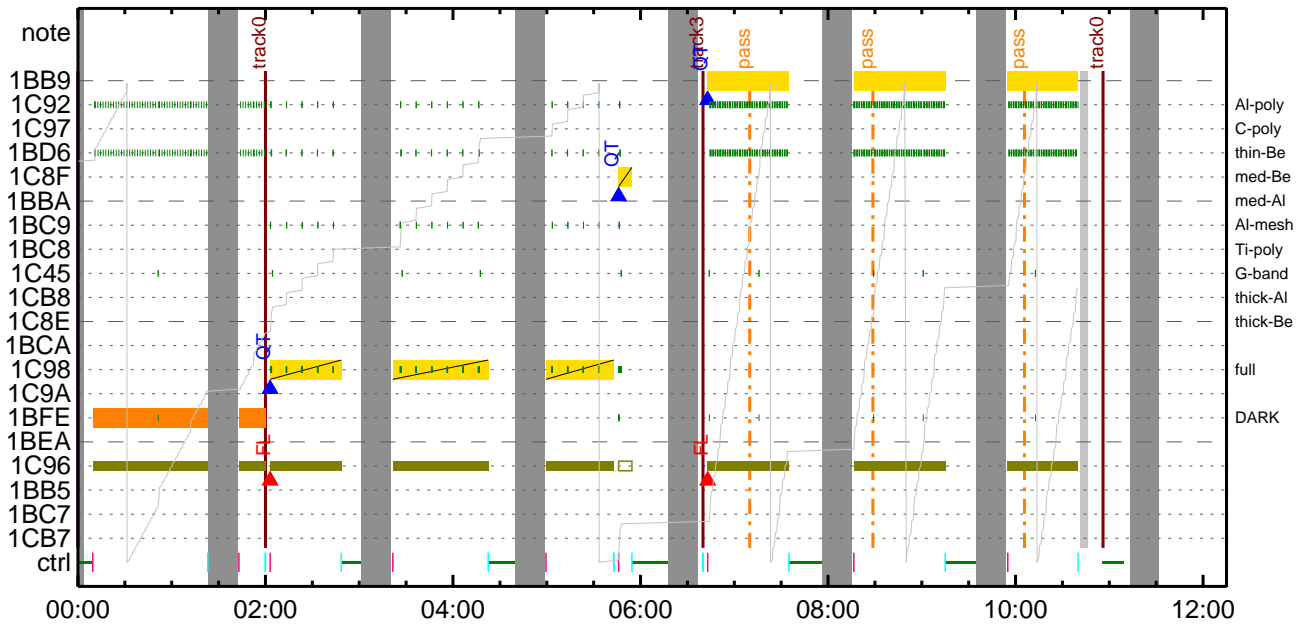
CMDI #0816 2021/08/18



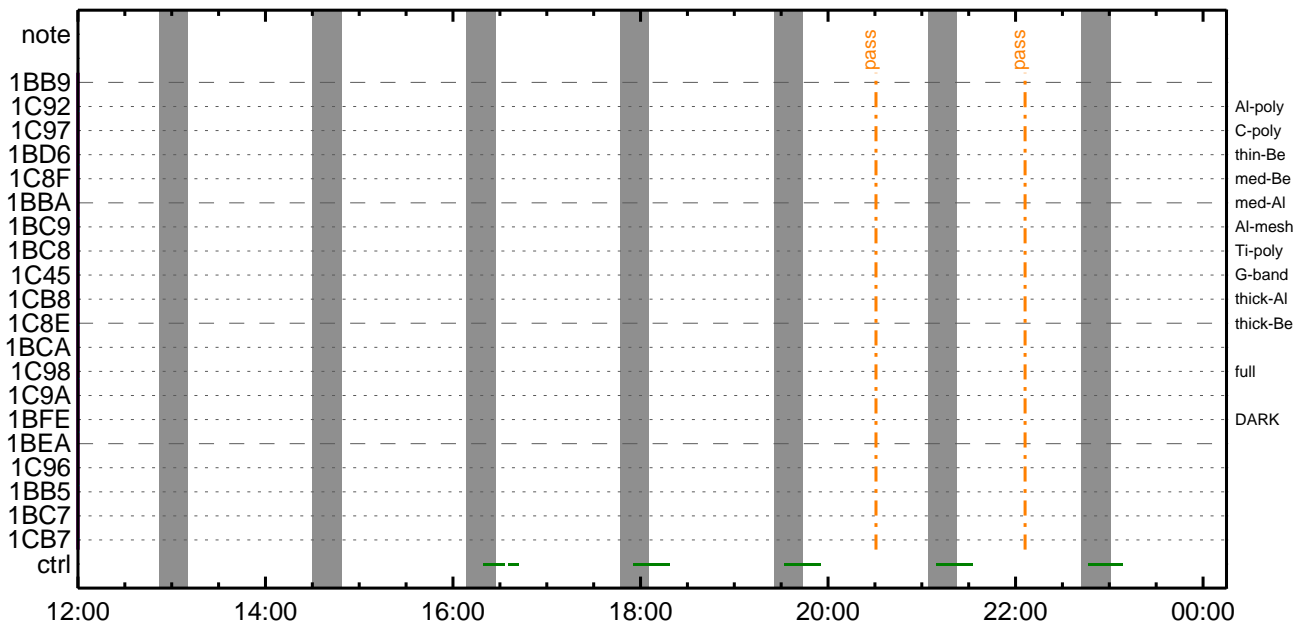
CMDI #0816 2021/08/18



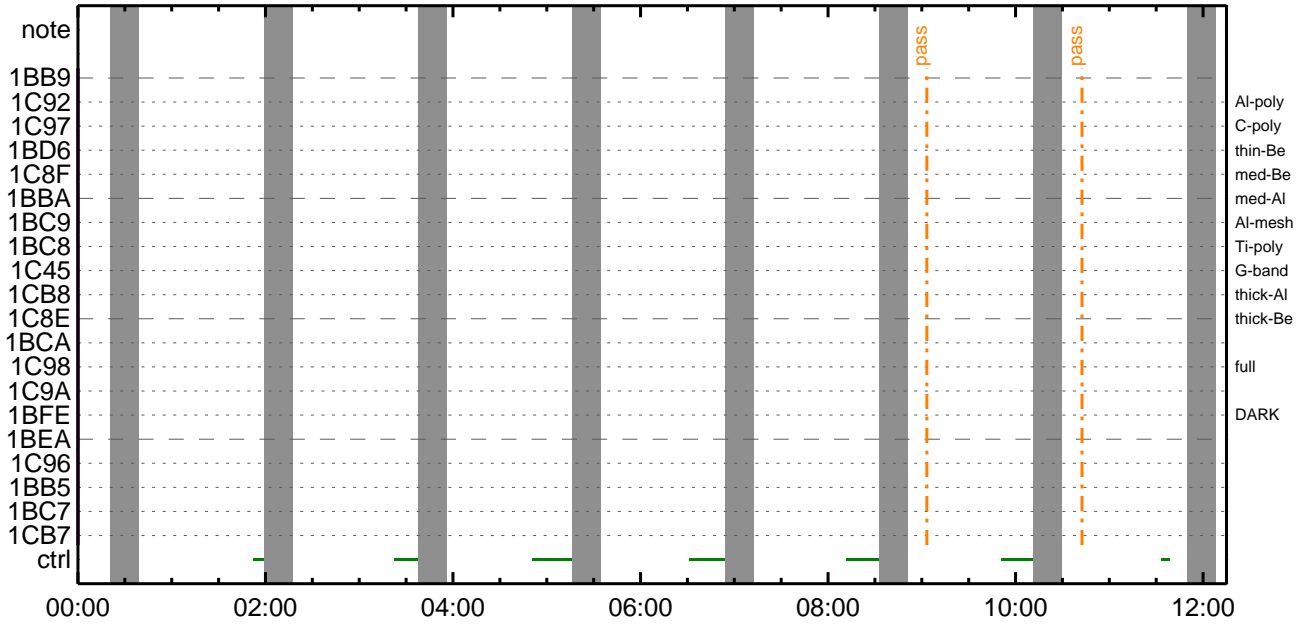
CMDI #0816 2021/08/19



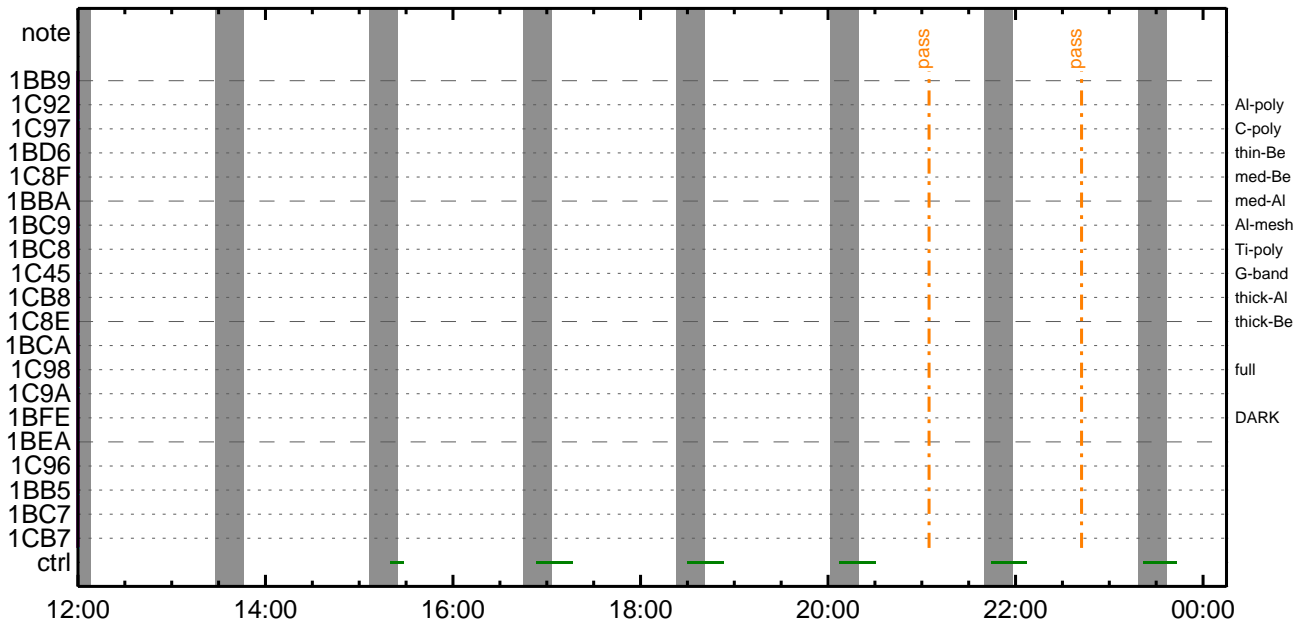
CMDI #0816 2021/08/19



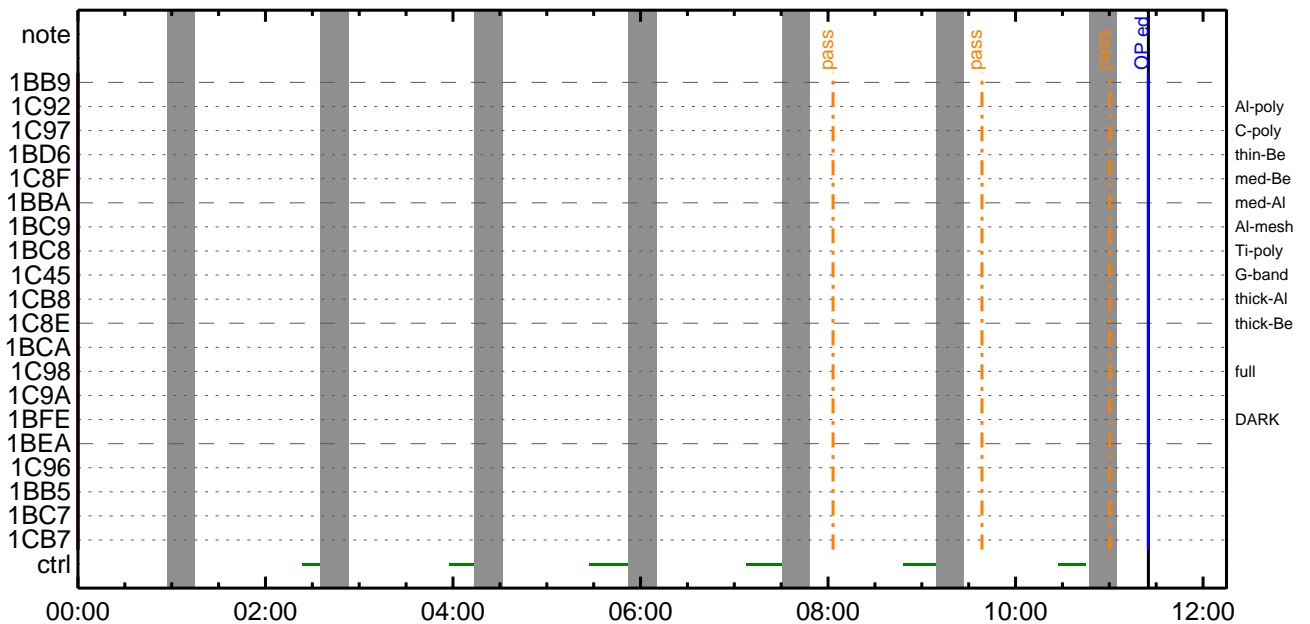
CMDI #0816 2021/08/20

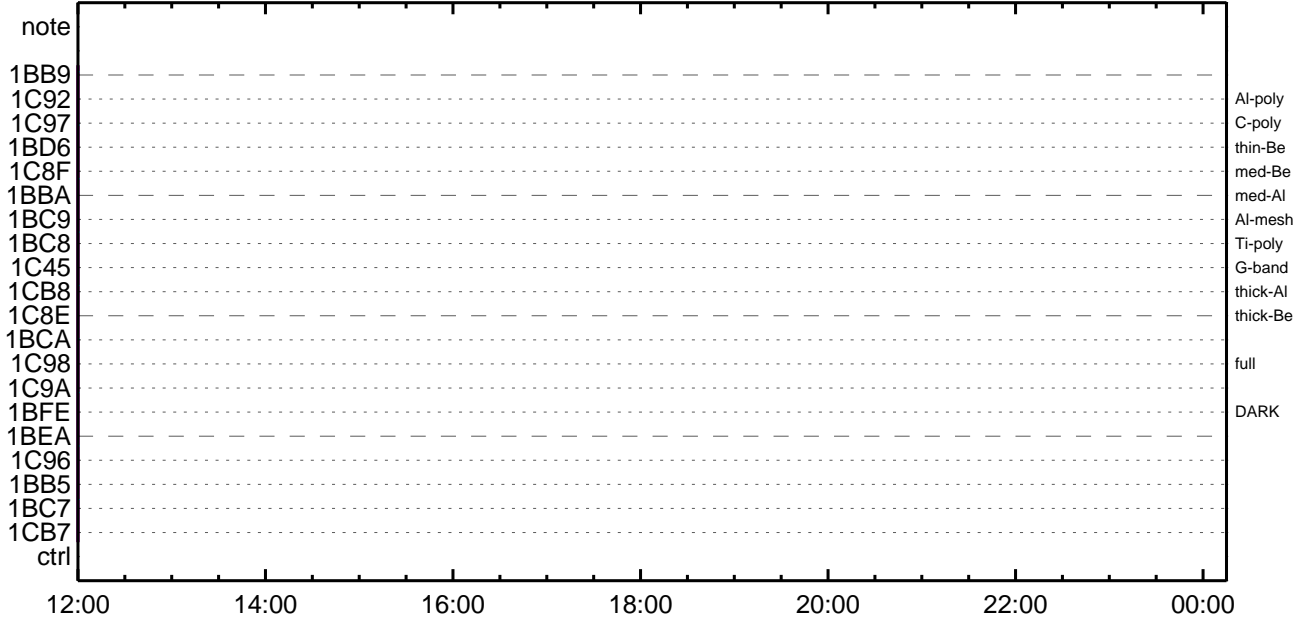


CMDI #0816 2021/08/20



CMDI #0816 2021/08/21





(a) Spacecraft Operation Procedure (real-commands)

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main-643 2021-08-17 11:54:34 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÁÝSÝÄÝ^-¼Ã»Û;ä
0005 C.
0006 C. ÝÄÝÊ;¼Ý³ÝÊÝÓÝÉÄ÷¸®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¸¸Äá«•µ°È»Í×ÁÇáÍÝçÝÄÝ×Ýí;¼ÝÊ;ÈÈÈ¼µ•íÉ;È«È¼°ÇÓ«•¸¼í¹ç«Í;çÄ®, ù«¹«È«È¼çÄ÷¸®«•«È««««ÈÈ; Æ
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¸µ;ON
0016 C. *****
0017 C. ¸°«Ä, Í×ÈÝ«ÄLOS«Èç«Í»¸´Ô«ð¹íí, «•; çÉÔÍ×«ÈXÁÓON«í¹Ô«È«í«È«««ÈÈ; Æ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓÝÉÝíÝÄÝ^-¼ÔÀ««•¸¼«é; ç°È²¼«í°«Ä, ¼½ç«ðð¼Ã¹Ô«¹«ÈÈ; Æ
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°«Ä,
0033 C. *****
0034 C. ¸° RESTART; ÈPT1; È«•¸¼«ð¼í¹ç«Í; ç°È²¼«í°«Ä¹Ô««««°; çDCBC-150«ð¸ÈÈ««; Æ
0035 C.
0036 . C. ;ãPT1°«Ä, ³«»Í; ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ã¹Ô, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ã¹Ô, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹Ô, ;¼Ú)
0045 C.
0046 . C. ;ãÝçÝÓÝÉÝÈÄÚÄØ; ÈÄ•Ä°²ÓÈò; È, á«í°«Ä, °È³«; ä
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ã¹Ô, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ã¹Ô, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹Ô, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°«Ä, «-¼«È°Äá»ß«•¸¼, á; ç°È²¼«ðð¼Ã¹Ô«¹«ÈÈ; Æ
0055 C. ÝçÝÓÝÉÝÈÄÚÄØ«Ä•Ä°²ÓÈò««¼«ð¼í¹ç«Í´°í»«¹«ÈÈ«ÈçÄÓ«Ä; Æ
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°«Ä,
0059 C. *****
0060 C. ¸° RESTART; ÈPT2; È«•¸¼«ð¼í¹ç«Í; ç°È²¼«í°«Ä¹Ô««««°; çDCBC-151«ð¸ÈÈ««; Æ
0061 C.
0062 . C. ;ãPT2°«Ä, ³«»Í; ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ã¹Ô, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ã¹Ô, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹Ô, ;¼Ú)
0071 C.
0072 . C. ;ãÝçÝÓÝÉÝÈÄÚÄØ; ÈÄ•Ä°²ÓÈò; È, á«í°«Ä, °È³«; ä
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ã¹Ô, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ã¹Ô, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ã¹Ô, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°«Ä, Áá»ß; çXÁ÷¸µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°«Ä, Áá»ß; ä
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¸µ;OFF; ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-643:OP
0104 ( )
0105 . S. OG og-643:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPîî°è¥ÅYóYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. ¥ÅYóYx½ªî»ò³îÇ§
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²î½Ê¹Ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. ¥ÅYóYx½ªî»ò³îÇ§
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²î½Ê¹Ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. ¥ÅYóYx½ªî»ò³îÇ§
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG,RAM ID=OP²î½Ê¹Ç•è²îOK²³îÇ§
0165 C.
0166 . C. ***** °Ê²¼²î½Ê¹Ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;½YE;Ê½Y½;¥î;½YE;Êòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½Ê¹Ç•è²îOK²³îÇ§ *****
0180 C. °Ê²¼²î½Ê¹Ç•è²îOK²³îÇ§ *****
0181 C.
0182 . C. TIY³Y½YóYÊ²òðÁDî¿(UT)
0183 +. TI 2021-08-17 11:07:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2021-08-17 11:07:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2021-08-17 11:07:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.
0195 +. TI 2021-08-17 11:11:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îíñαîŷÄŷ§ŷÄŷ-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷÈ¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2021-08-17 11:11:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2021-08-17 11:11:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2021-08-17 11:11:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2021-08-17 11:11:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´úÃîαî»ö¼ŷαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îŷÖŷÄŷÈŷŷŷÈŷáŷçŷèèÈ¼αα¼Ä»Ûα¹αè)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-644 2021-08-17 11:54:34 126 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Ü;ä
0005 C.
0006 C. YÄYß;¼Y³YFÝÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C.  Āí;Ē□¿□Ā□•µ°Ē»Í×ĀÇ□ĬYçYÄY×Yı;¼YÉ;ĒĒ%µ•ĬĒ;Ē□Ē°ÇÖ□•□¿Ĭ¹ç□Ĭ;çĀ®, ù□¹□Ē□□□çĀ+¿®□•□Ē□□□³□Ē;Ē
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_ST51>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 103217.9 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. *****
0055 C. SOT table upload
0056 C. *****
0057 . C. < Stop SP table >
0058 +. DC 07-F0 MDP_SP_CTRL_MANU
0059 BC (61)
0060 C.
0061 C. MDP_SP_CTRL_MODE = MANU [ ]
0062 C.
0063 C.
0064 . C. <Upload SP Observation Table>
0065 . S. RAM ram-283:MDP_OBS_S
0066 ( )
0067 C.
0068 . C. < Dump RAMID=MDP_OBS_S >
0069 +. DC 07-F0 MDP_DUMP_SPTBL
0070 BC (83 07 00 00 00 38 b8)
0071 C.
0072 C. MDP_OBS_S verify = OK/NG [ ]
0073 C.
0074 C.
0075 C. *****
0076 C. SOT TI command set
0077 C. *****
0078 C. Execute, after the success of TBL upload.
0079 +. TI 2021-08-17 11:11:18.0
0080 DC 07-F0 MDP_SOT_MODE_OBSV
0081 BC (40)
0082 . C.
0083 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0084 C.
0085 C.
0086 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0087 +. DC 07-FC EIS_MODE_CHG_ENA
0088 BC (20)
0089 . C. Verify EIS_MODE_CHG_FLG is ENA
0090 +. DC 07-FC EIS_MODE_MANU
0091 BC (21 02)
0092 . C. Verify EIS in MANUAL mode
0093 . C. Estimated OBSTBL upload time is 20s
0094 C. *****
0095 C. EIS START OBSTBL LOAD
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 c0 c0 10 10)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 40 c0 10 10)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 40 40 10 10)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b c0 40 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c 80 80 20 08)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 80 80 08 20)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 + DC 07-F0 MDP_XRT_ARS_DIS
0153 BC (d5)
0154 + DC 07-F0 MDP_XRT_AEC_RESET
0155 BC (d0)
0156 + DC 07-F0 MDP_XRT_FLD_RESET
0157 BC (da)
0158 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159 BC (c4 14)
0160 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0161 BC (c5 04)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C.
0165 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0166 C.
0167 +. DC 07-F0 MDP_XRT_MODE_OBSV
0168 BC (c2)
0169 +. TI 2021-08-17 11:11:02.0
0170 DC 07-F0 MDP_XRT_MODE_OBSV
0171 BC (c2)
0172 . C. ----- Success Verify ? OK / NG ____
0173 C.
0174 C. ***** XRT END *****
0175 C.
0176 . C. ***** MDP 'uÃîqî»ö¼ÝðÊÃð¹ñèDCBC•x²è *****
0177 C. (¼ã°îÿÓÿÄÿËÿÏÿÑÿÑÿÛÿÜÿÝÿÞÿßÿàÿáÿâÿãÿäÿåÿæÿçÿèÿéÿêÿëÿìÿíÿîÿïÿðÿñÿ)
0178 . S. DC-BC dcbc-402:DCBC
0179 (MDP_known_event)
0180 C.
0181 C.
0182 . C. ***** ÝÐÿ¹•Ï Daily±¿ÎÑèË'Øñ¹ñèDCBC•x²è *****
0183 . S. DC-BC dcbc-153:DCBC
0184 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0185 C.
0186 C.
0187 . C. ;ãLOSÿÁÿSÿÄÿ-¼Ã»Û;ã
0188 C.
0189 . C. ***** LOS *****
0190 C.

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Aug 17, 21 11:54

XRT_OGLIST_0816.chk

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*** OP Sequence for XRT ***

2021/08/17	11:22:00.0	AACS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	03	a5	01	f3
2021/08/17	11:24:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/17	11:24:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/17	11:24:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2021/08/17	11:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2021/08/17	11:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/17	11:39:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/17	11:39:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2021/08/17	11:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2021/08/17	13:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/17	13:18:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/17	13:18:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2021/08/17	13:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2021/08/17	14:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/17	14:56:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/17	14:56:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2021/08/17	14:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2021/08/17	16:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/17	16:34:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/17	16:34:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2021/08/17	16:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2021/08/17	17:57:30.0	AACS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2021/08/17	18:07:30.0	AACS_Orе-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	03	a5	01	f3
2021/08/17	18:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/17	18:13:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/17	18:13:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2021/08/17	18:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2021/08/17	19:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/17	19:51:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/17	19:51:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2021/08/17	19:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2021/08/17	21:07:30.0	AACS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	03	a5	01	f3
2021/08/17	21:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/17	21:30:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/17	21:30:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2021/08/17	21:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2021/08/17	23:08:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/17	23:08:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/17	23:08:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2021/08/17	23:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2021/08/18	00:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	00:47:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/18	00:47:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2021/08/18	00:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2021/08/18	02:15:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	02:15:32.0	XRT_FLD_RESET_415_OG [0x19f]							

2021/08/18	02:15:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	02:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	03:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	03:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	03:49:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	03:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	05:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	05:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	05:18:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	05:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	06:08:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_426_OG [0x1aa]	TCIB_XRT_S_HTR_A_DIS	0	04-C0		
2021/08/18	06:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	06:58:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	06:58:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	07:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	08:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	08:39:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	08:39:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	08:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	10:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	10:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	10:18:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	10:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	10:37:01.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	10:37:03.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	10:37:05.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	10:40:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	12:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	12:15:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	12:15:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	12:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	12:20:00.0	XRT_CTRL_MANU_445_OG [0x1bd]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	12:20:10.0	XRT_FOCUS_RECALIBRATE_427_OG [0x1ab]	XRT_FOCUS_RECAL	2	07-F8	78 00	
2021/08/18	12:24:10.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/08/18	12:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	12:34:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	12:34:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/08/18	12:35:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 2e f9 2e f9	
2021/08/18	12:37:52.0	XRT_ARS_DIS_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/08/18	12:37:54.0	XRT_FLRCTRL_DIS_436_OG [0x1b4]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2021/08/18	12:37:56.0	XRT_FLD_DIS_440_OG [0x1b8]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2021/08/18	12:37:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2021/08/18	12:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/08/18	12:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	12:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	12:44:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	

2021/08/18	12:45:00.0	AOCS_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2021/08/18	12:47:52.0	XRT_ARS_DIS_444_OG [0x1bc]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2021/08/18	12:47:54.0	XRT_FLRCTRL_DIS_436_OG [0x1b4]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2021/08/18	12:47:56.0	XRT_FLD_DIS_440_OG [0x1b8]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2021/08/18	12:47:58.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 0d
2021/08/18	12:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2021/08/18	12:54:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	12:54:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	12:54:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2021/08/18	12:55:00.0	AOCS_OrE-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2021/08/18	12:57:52.0	XRT_ARS_DIS_444_OG [0x1bc]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2021/08/18	12:57:54.0	XRT_FLRCTRL_DIS_436_OG [0x1b4]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2021/08/18	12:57:56.0	XRT_FLD_DIS_440_OG [0x1b8]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2021/08/18	12:57:58.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 0e
2021/08/18	12:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2021/08/18	13:04:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	13:04:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	13:04:58.0	XRT_FOCUS_POSITION_437_OG [0x1b5]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2021/08/18	13:05:00.0	AOCS_OrE-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2021/08/18	13:07:52.0	XRT_ARS_DIS_444_OG [0x1bc]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2021/08/18	13:07:54.0	XRT_FLRCTRL_DIS_436_OG [0x1b4]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2021/08/18	13:07:56.0	XRT_FLD_DIS_440_OG [0x1b8]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2021/08/18	13:07:58.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 09
2021/08/18	13:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2021/08/18	13:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	13:14:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	13:14:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2021/08/18	13:15:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2021/08/18	13:15:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2021/08/18	13:15:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2021/08/18	13:15:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2021/08/18	13:17:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 0c
2021/08/18	13:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2021/08/18	13:24:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	13:24:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	13:24:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97		00
2021/08/18	13:25:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	03	a5	01	f3
2021/08/18	13:25:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2021/08/18	13:25:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2021/08/18	13:25:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2021/08/18	13:25:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2021/08/18	13:25:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2021/08/18	13:27:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 06
2021/08/18	13:27:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0					c5 04
2021/08/18	13:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2021/08/18	13:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2021/08/18	13:54:02.0	XRT_FLD_RESET_415_OG [0x19f]							

2021/08/18	13:54:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	13:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	14:13:30.0	XRT_Custom_430_OG [0x1ae]					
2021/08/18	14:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/08/18	15:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	15:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	15:32:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	15:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	15:56:30.0	XRT_Custom_430_OG [0x1ae]					
2021/08/18	15:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/08/18	17:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	17:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	17:11:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	17:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	17:43:30.0	XRT_Custom_430_OG [0x1ae]					
2021/08/18	17:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/08/18	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/08/18	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2021/08/18	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2021/08/18	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2021/08/18	18:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/08/18	18:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2021/08/18	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/08/18	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	18:09:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/08/18	18:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 03 a5 01 f3	
2021/08/18	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2021/08/18	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2021/08/18	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2021/08/18	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/08/18	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	18:12:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2021/08/18	18:12:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2021/08/18	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/08/18	18:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	18:49:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	18:49:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	18:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	19:20:00.0	XRT_Custom_430_OG [0x1ae]					
2021/08/18	19:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/08/18	20:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/08/18	20:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/08/18	20:27:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/08/18	20:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/08/18	20:57:00.0	XRT_Custom_430_OG [0x1ae]					
2021/08/18	20:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					

2021/08/18	21:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/08/18	21:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/08/18	21:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/08/18	21:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2021/08/18	21:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 03 a5 01 f3
2021/08/18	21:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2021/08/18	21:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2021/08/18	21:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2021/08/18	21:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2021/08/18	21:12:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/08/18	21:12:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2021/08/18	21:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2021/08/18	22:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/08/18	22:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/08/18	22:06:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/08/18	22:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/08/18	22:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/08/18	22:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2021/08/18	23:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2021/08/18	23:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/08/18	23:44:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/08/18	23:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/08/19	00:08:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/08/19	00:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/08/19	01:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2021/08/19	01:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/08/19	01:23:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/08/19	01:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/08/19	01:42:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/08/19	01:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/08/19	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]			
2021/08/19	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2021/08/19	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/08/19	02:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/08/19	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/08/19	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/08/19	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/08/19	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	XRT_AEC_RESET_448_OG [0x1c0]			
2021/08/19	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2021/08/19	02:02:56.0	XRT_QT_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/08/19	02:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2021/08/19	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2021/08/19	02:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/08/19	02:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/08/19	02:48:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/08/19	02:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/08/19	03:20:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/08/19	03:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0

2021/08/19	04:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/08/19	04:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/08/19	04:22:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/08/19	04:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/08/19	04:58:30.0	XRT_Custom_430_OG [0x1ae]						
2021/08/19	04:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/08/19	05:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/08/19	05:42:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/08/19	05:42:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2021/08/19	05:43:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2021/08/19	05:43:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2021/08/19	05:43:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/08/19	05:45:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2021/08/19	05:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/08/19	05:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/08/19	05:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/08/19	05:54:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/08/19	05:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/08/19	06:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/08/19	06:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/08/19	06:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2021/08/19	06:40:00.0	AOCs_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 03 a5 01 f3		
2021/08/19	06:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2021/08/19	06:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2021/08/19	06:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2021/08/19	06:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/08/19	06:40:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/08/19	06:42:56.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2021/08/19	06:42:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2021/08/19	06:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/08/19	07:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/08/19	07:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/08/19	07:35:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/08/19	07:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/08/19	08:15:30.0	XRT_Custom_430_OG [0x1ae]						
2021/08/19	08:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/08/19	09:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/08/19	09:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/08/19	09:15:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/08/19	09:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/08/19	09:54:00.0	XRT_Custom_430_OG [0x1ae]						
2021/08/19	09:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/08/19	10:40:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/08/19	10:56:00.0	AOCs_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		