

# XRT Timeline to be uploaded on 2021/09/30

Period: 2021/09/30 11:23:00 - 2021/10/05 11:04:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

XOB #1CBA: HOP421MaGIX - High cadence (8s thin-Be only) 384x384 at 1064 1048												
Term		Pointing (x, y)					Comment					
09/30 11:36:00 - 09/30 16:25:54		Track ( 736.2, -565.6) @ 09/30 11:33:00					# OP start + 10min, HOP361					
PROG= 16 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 67 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 43 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 22 250-time(s) 8.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
Default Filter		Thicker Filter		VLS		mode image		Exp. CCD Bin		ROI: size (center)		Comp. AEC Buffer Interval

XOB #1C50: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms)												
Term		Pointing (x, y)					Comment					
09/30 16:29:00 - 09/30 17:54:24		Track ( -485.7, 360.0) @ 09/30 16:26:00					HOP420					
09/30 18:07:30 - 09/30 21:32:30		Track ( -475.2, 359.2) @ 09/30 18:04:30					HOP420, synoptic					
09/30 22:03:00 - 10/01 01:59:54		Fixed ( 750.0, -550.0)					AR obs.					
10/01 06:13:00 - 10/01 10:59:54		Fixed ( 750.0, -550.0)					AR obs.					
10/01 16:29:00 - 10/01 17:56:24		Track ( -360.9, 352.7) @ 10/01 11:00:00					HOP361, HOP420					
10/01 18:09:30 - 10/01 21:59:54		Track ( -310.3, 350.5) @ 10/01 18:06:30					HOP420					
10/02 06:33:00 - 10/02 10:43:55		Track ( -219.1, 347.8) @ 10/02 06:30:00					AR obs					
PROG= 10 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 62 3-time(s) 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Seqn= 98 15-time(s) 120.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 70.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 70.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 70.0sec												
Default Filter		Thicker Filter		VLS		mode image		Exp. CCD Bin		ROI: size (center)		Comp. AEC Buffer Interval

XOB #1BC0: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Th												
Term		Pointing (x, y)					Comment					
09/30 17:57:30 - 09/30 18:04:24		Fixed ( 0.0, 0.0)					synoptic, shifted -5.5 min					
10/01 06:03:00 - 10/01 06:09:54		Fixed ( 0.0, 0.0)					HOP349, synoptic					
10/01 17:59:30 - 10/01 18:06:24		Fixed ( 0.0, 0.0)					synoptic, shifted -3.5 min					
PROG= 12 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 63 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 15 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 27 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 23 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter		Thicker Filter		VLS		mode image		Exp. CCD Bin		ROI: size (center)		Comp. AEC Buffer Interval

**XOB #1CBD: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[181/2048/11571] with 512x512 G-band+Leak - 45min cad) + C**

Term	Pointing (x, y)		Comment									
10/01 02:03:00 - 10/01 05:59:54	Fixed ( 0.0, 0.0)		HOP349, synoptic									
10/02 02:03:00 - 10/02 05:51:30	Fixed ( 0.0, 0.0)		HOP349, synoptic									
<b>PROG= 18 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 600.0sec												
└─ Seqn= 1 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 99 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 53 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 30 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 5-time(s) 480.0sec												
└─ Seqn= 8 1-time(s) 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec												
└─ Seqn= 6 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec												
└─ Seqn= 29 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

**XOB #1CC1: HOP361- High cadence (10s thin-Be only) 256x256 at 1064 1048**

Term	Pointing (x, y)		Comment									
10/01 11:03:00 - 10/01 16:25:54	Track ( -360.9, 352.7) @ 10/01 11:00:00		HOP361, HOP420									
<b>PROG= 15 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 67 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Seqn= 43 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 28 250-time(s) 10.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 256x256 (1064, 1048) Q=95 3 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

**XOB #1C6E: HOP336 1-filter - Al/poly -384x384, 1024ms and 8192s, 300s-cadence, G-band - 384x384 1ms**

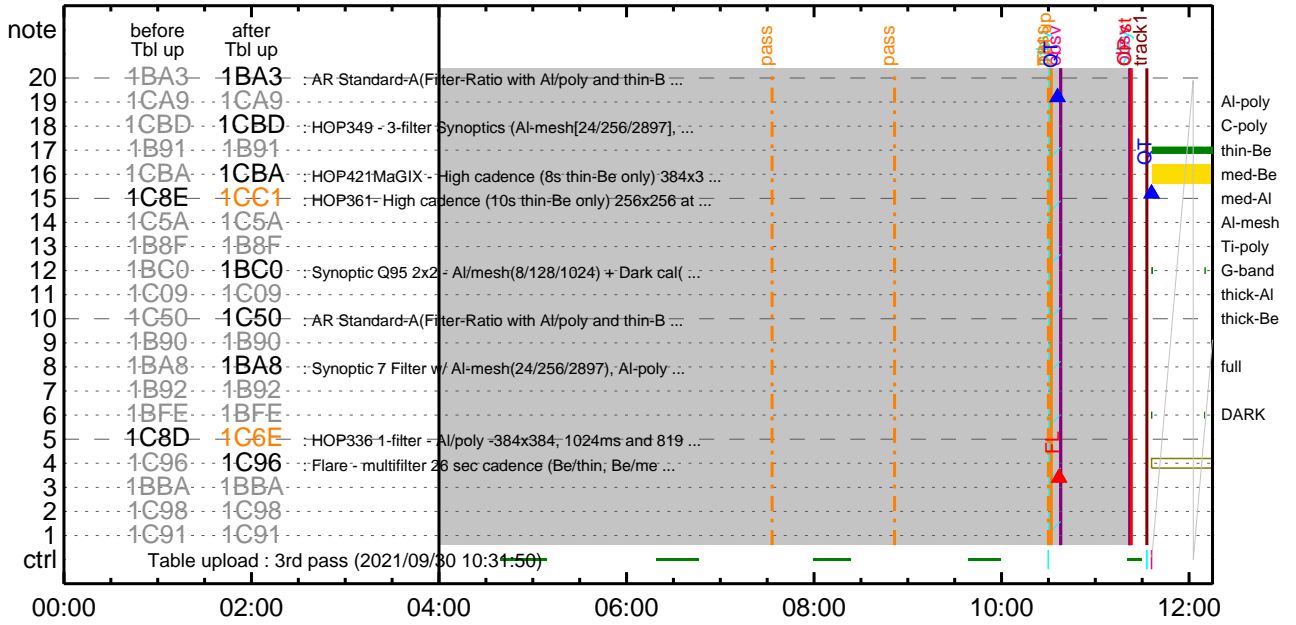
Term	Pointing (x, y)		Comment									
10/01 22:03:00 - 10/02 01:59:54	Track ( -23.0, -35.0) @ 10/01 22:00:00		HOP393									
<b>PROG= 05 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 16 2-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 30.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─ Subr= 3 3-time(s) 2.0sec												
└─ Seqn= 72 1-time(s) 300.0sec												
└─ Al-poly/Open Al-poly/Ti-poly close Safe Norm 8.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

**XOB #1BA8: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-Al**

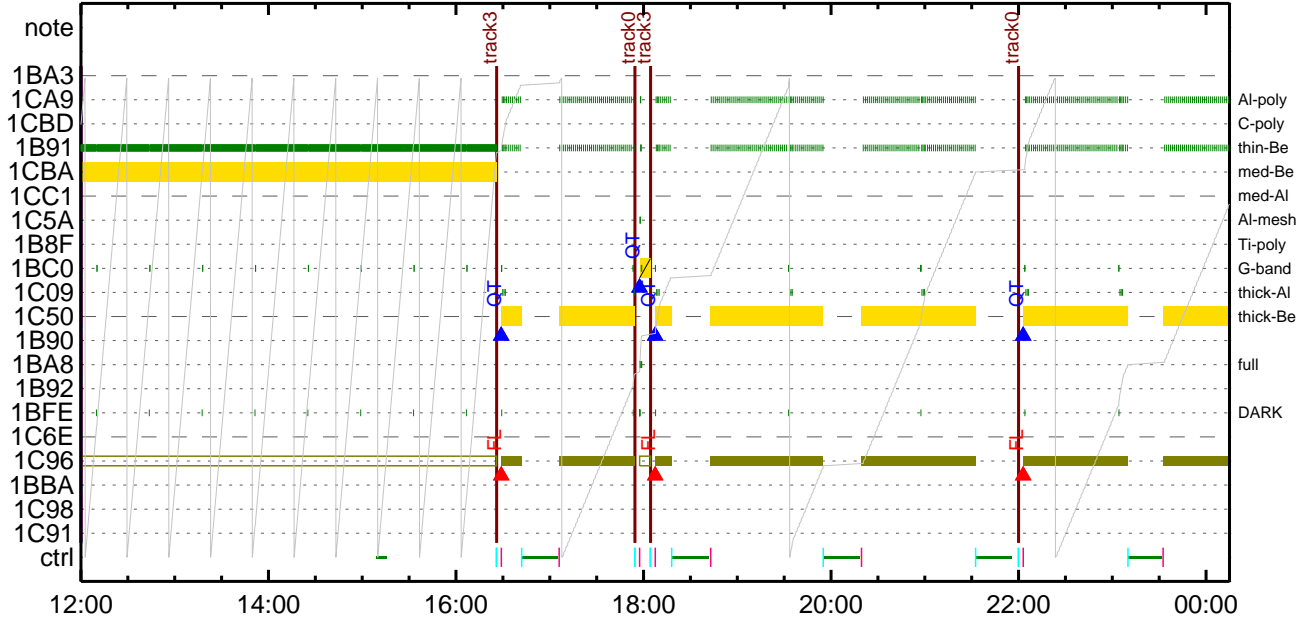
Term	Pointing (x, y)		Comment									
10/02 06:23:00 - 10/02 06:29:54	Fixed ( 0.0, 0.0)		HOP349, synoptic									
<b>PROG= 08 1-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												



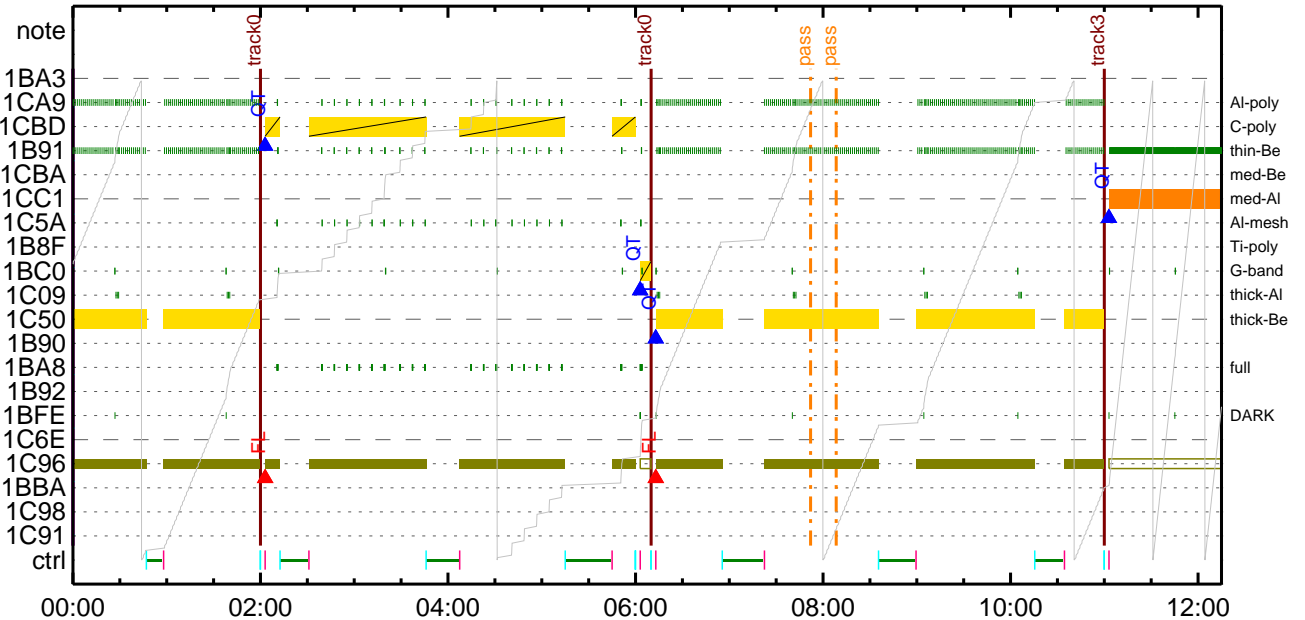
### CMDI #0904 2021/09/30



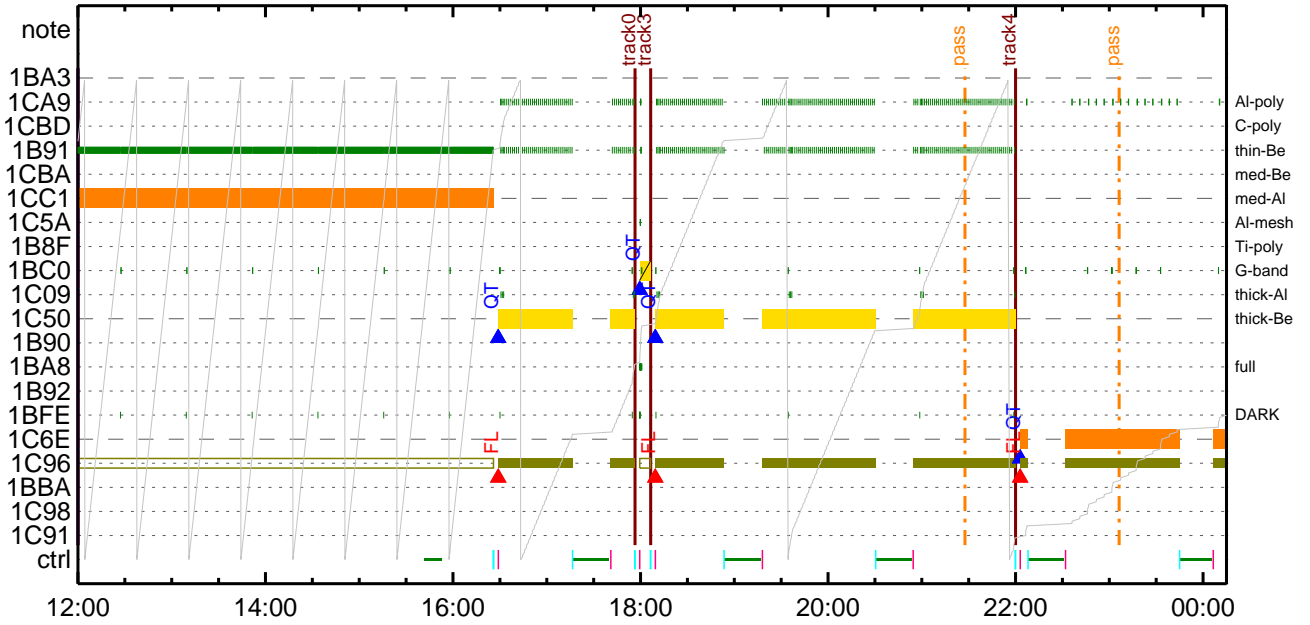
### CMDI #0904 2021/09/30



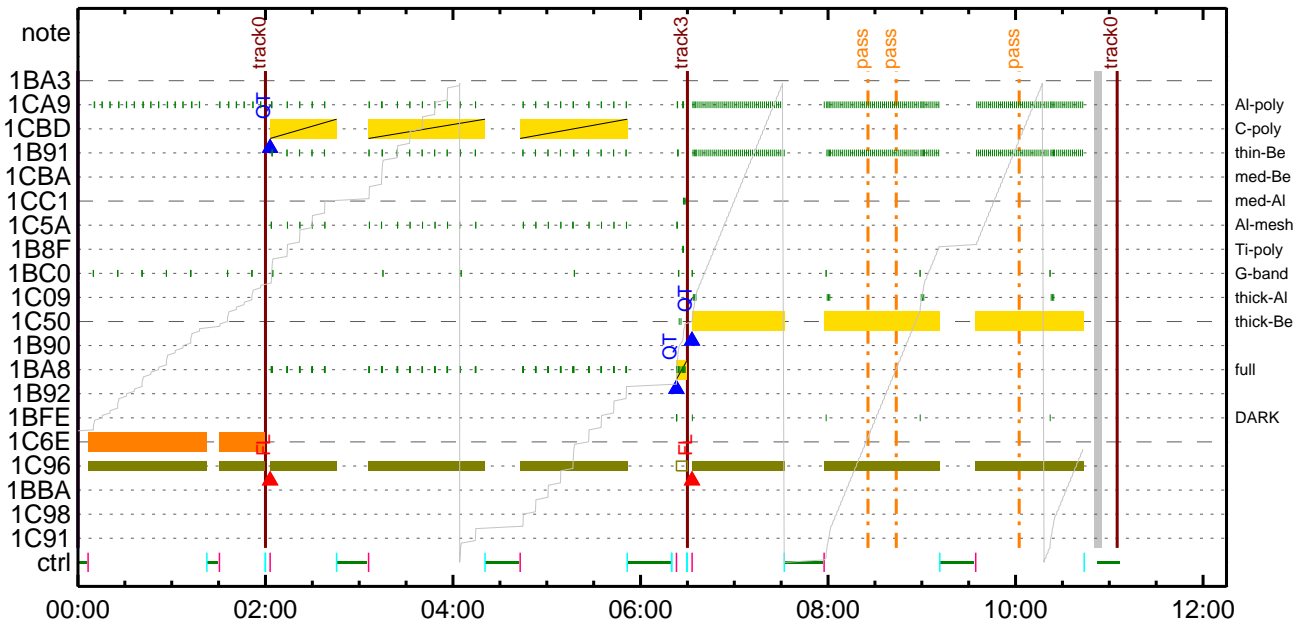
### CMDI #0904 2021/10/01



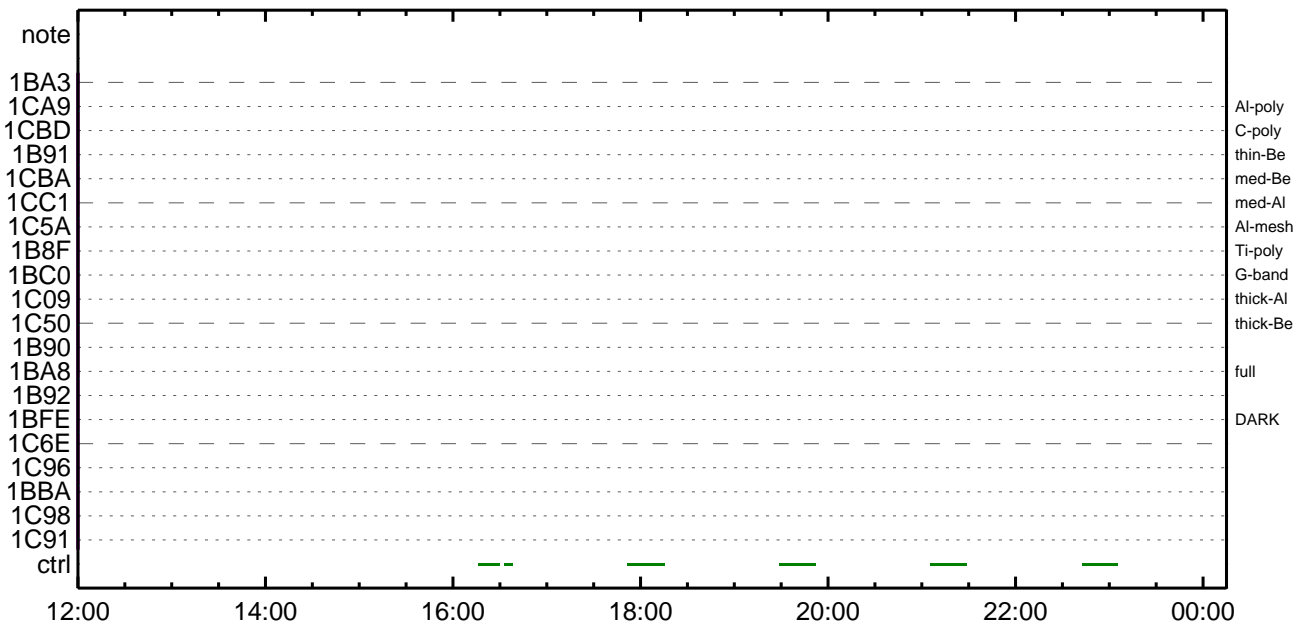
CMDI #0904 2021/10/01



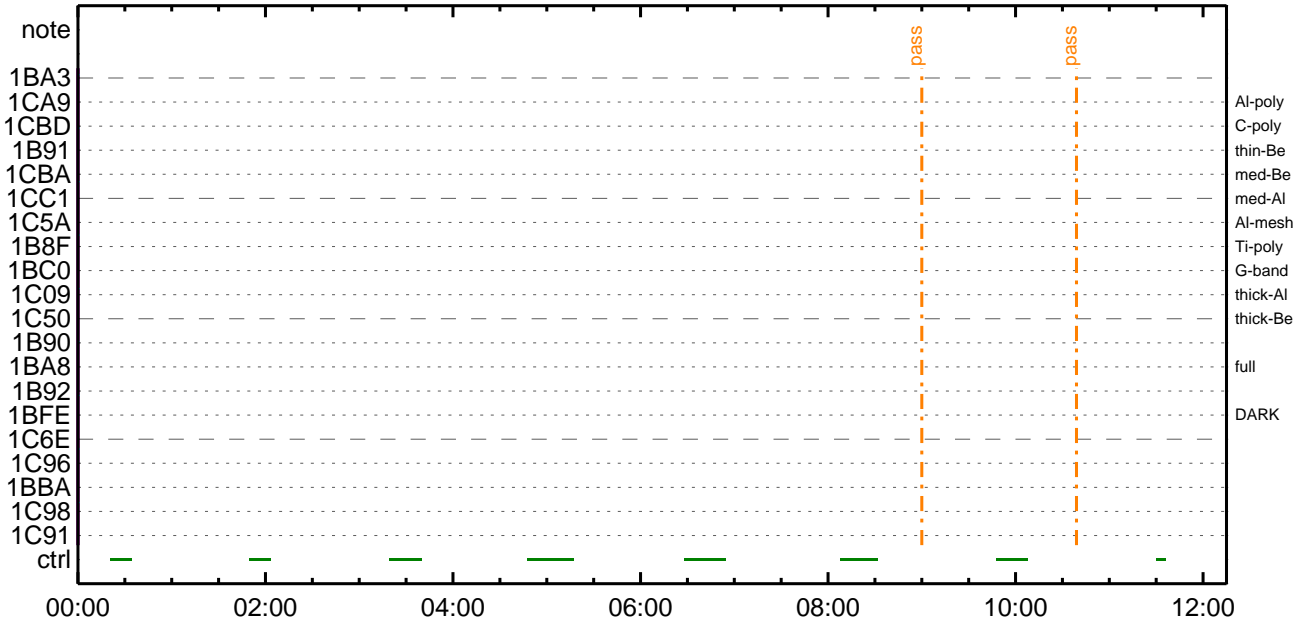
CMDI #0904 2021/10/02



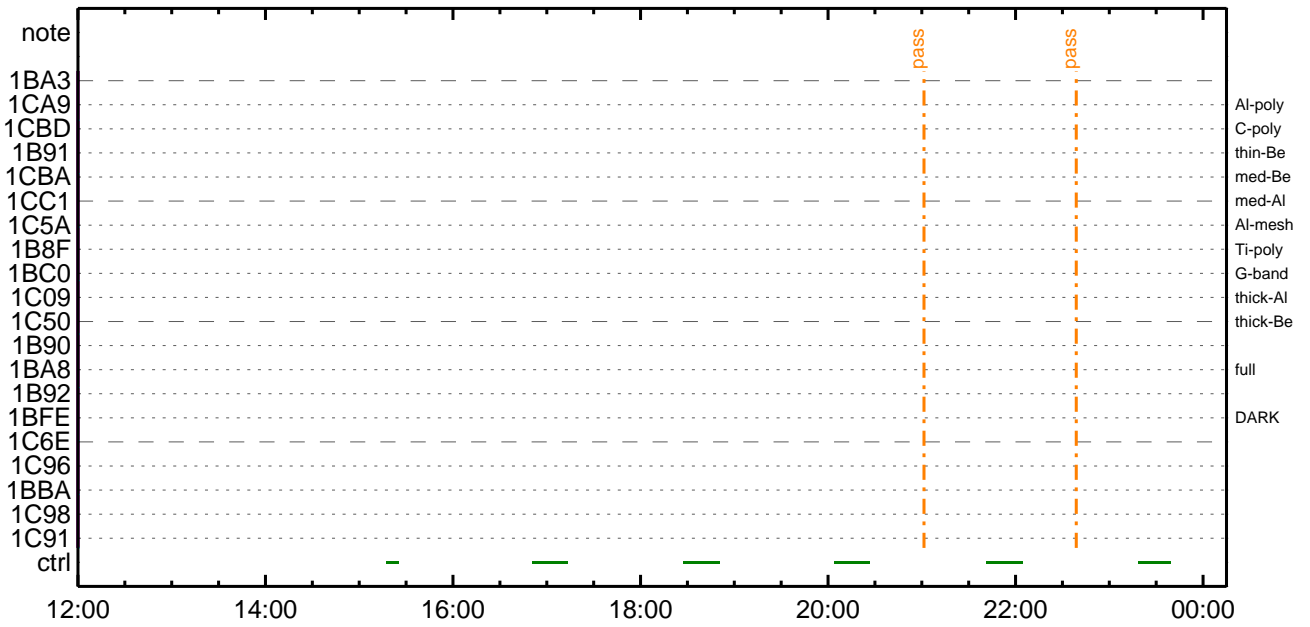
CMDI #0904 2021/10/02



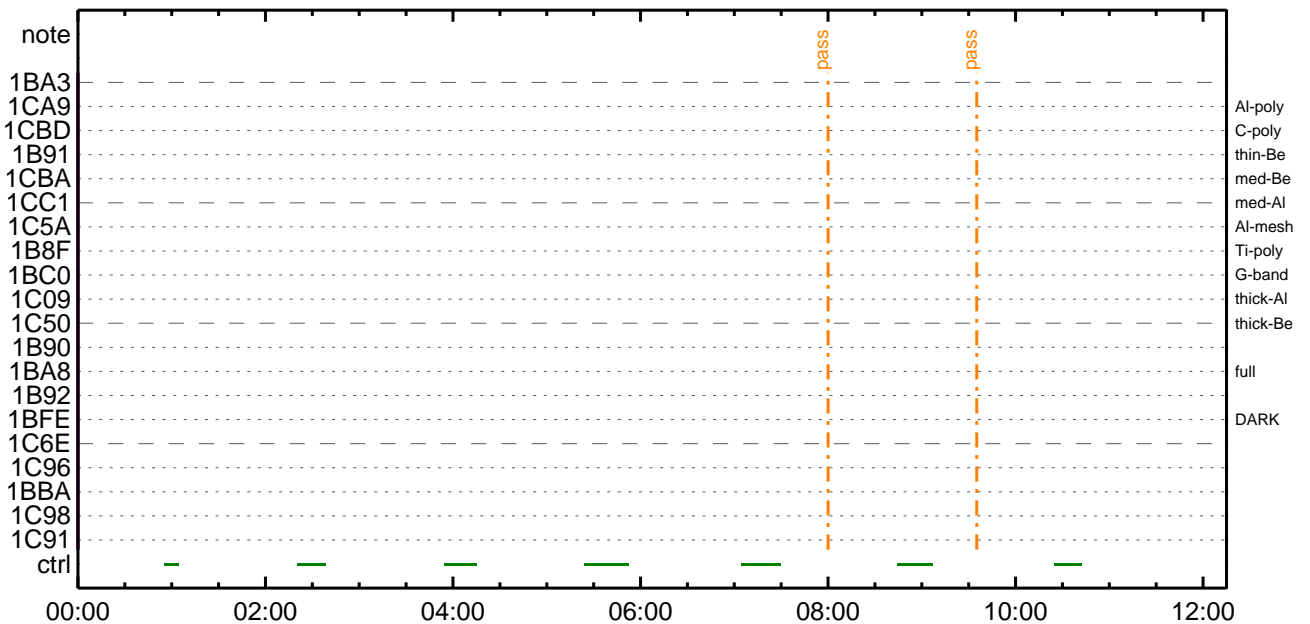
CMDI #0904 2021/10/03



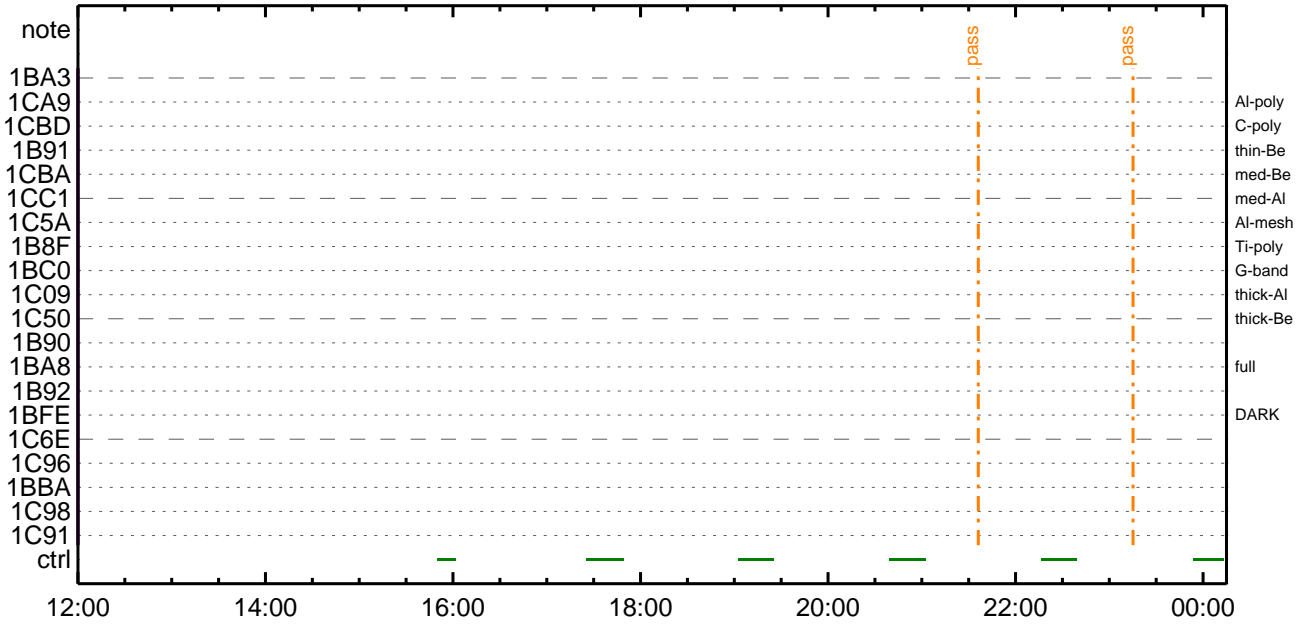
CMDI #0904 2021/10/03



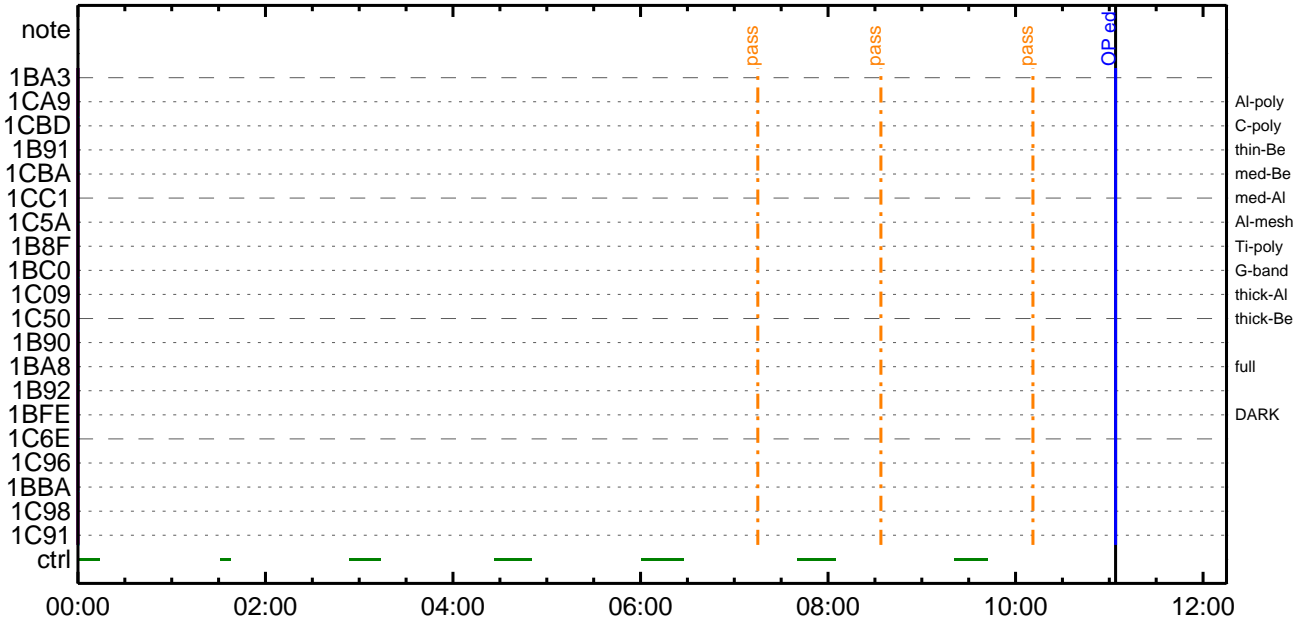
CMDI #0904 2021/10/04



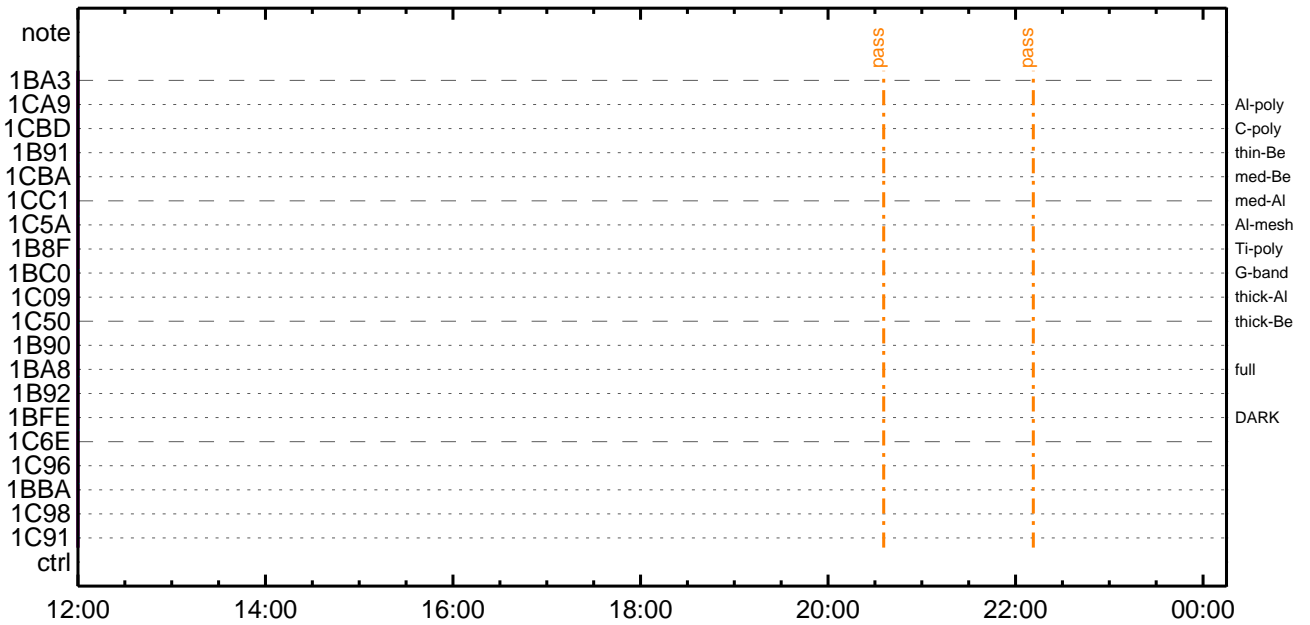
CMDI #0904 2021/10/04



CMDI #0904 2021/10/05



CMDI #0904 2021/10/05



(a) Spacecraft Operation Procedure (real-commands)

```
main-725 2021-09-30 14:14:45 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Ä»Ü;ä
0005 C.
0006 C. YÀYB;¼Y³YBYÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ðÁð•µ°È»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈÈÈ¼°ÇÓð•ø¿¼ì¹ç¿Í;çÄ®, ùñ¹òèððçÄ+¿®ð•òÈððð³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-725:OP
0020 ()
0021 . S. OG og-725:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPî°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYÖY×½ªî»ðð³îç§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGðî¼È¹ç•è²îOKðð³îç§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYÖY×½ªî»ðð³îç§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGðî¼È¹ç•è²îOKðð³îç§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYÖY×½ªî»ðð³îç§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPðî¼È¹ç•è²îOKðð³îç§
0081 C.
0082 . C. ***** øÈ²¼ðî¼Ä´¶¹°òÈÈ-ò°Ä+¿® (¼áµ-YÁYÖY×¼È½çððÄÖÄæç¼ª°ñðÈ¼ì¹çççðâ) *****
0083 C. DHUYâ;¼YÉ;È¼Y½, Yí;¼YÉ;ÈòðÍá¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADð-Ä+¿®NGðî¼È¹ç;ç°È²¼ðî¼TI-CMDÄ+¿®ðî¼Ä¹Òð•òÈððð³òÈ;f
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0096 C.                0303; SET0EDUMP01A±0iYNY¹0Ç¹00|0³0E;E
0097 C.
0098 . C. TIY³YF¥0YÉ000ADÍ¿(UT)
0099 +. TI 2021-09-30 11:18:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2021-09-30 11:18:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2021-09-30 11:18:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2021-09-30 11:22:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. °E²¼0İÄê%îÍÑ0İYÁY$YÁY-¹àìÛ
0116 C.                ÇÇ[HK1_TI_CMD_ENA/DIS]              EQ        ENA
0117 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        4
0118 C.                ÇÇ[HK1_NEXT_EXEC_PIM]              EQ        DHU
0119 C.                ÇÇ[HK1_NEXT_EXEC_DC]              EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÁY0Y×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.                ÇÇ[HK1_DMP_TOP_ADRS_1]              EQ        07
0129 C.                ÇÇ[HK1_DMP_TOP_ADRS_0]              EQ        2B
0130 C.                ÇÇ[HK1_DMP_BLOCK_NUM]               EQ        3
0131 C.                ÇÇ[HK1_DMP_REPEAT_NUM]              EQ        0
0132 C.                ÇÇ[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.                ÇÇ[HK1_PKT_FORM_NO]                EQ        7
0136 C.                ÇÇ[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                ÇÇ[HK1_S_TLM_BIT_RATE]              EQ        32k
0138 C.                ÇÇ[HK1_X_TLM_BIT_RATE]             EQ        4M
0139 C.                ÇÇ[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 . C. YÁY0Y×¼ªİ»00³İÇ$
0142 C.                ÇÇ[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK00³İÇ$
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼,¥î;¼YÈ;È00İá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.                ÇÇ[HK1_PKT_FORM_NO]                EQ        2
0150 C.                ÇÇ[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                ÇÇ[HK1_S_TLM_BIT_RATE]              EQ        32K
0152 C.                ÇÇ[HK1_X_TLM_BIT_RATE]             EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2021-09-30 11:22:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2021-09-30 11:22:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2021-09-30 11:22:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 . C.                [ ] [HK1_TI_CMD_NUM]              EQ        2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2021-09-30 11:22:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]              EQ        1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ´úÃİ0İ»0¼Y0ÈÄ00¹0èDCBC•×²è *****
0192 C. (%â0İY0YÁYÉY0YÉYÁYÇYè0È%¼00¼Ä»Û0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```







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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-287:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2021-09-30 11:22:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 85 83 08 08)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 20)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 20 08)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 80 80 08 20)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0b 80 80 08 08)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 0c 85 83 04 04)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 0f 80 80 06 06)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 10 80 80 08 08)
0176 + DC 07-F0 MDP_XRT_FLD_ENA
0177 BC (d8)
0178 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0179 BC (c8)
0180 + DC 07-F0 MDP_XRT_ARS_DIS
0181 BC (d5)
0182 + DC 07-F0 MDP_XRT_AEC_RESET
0183 BC (d0)
0184 + DC 07-F0 MDP_XRT_FLD_RESET
0185 BC (da)
0186 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0187 BC (c4 14)
0188 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0189 BC (c5 04)
0190 . C. ----- Success Verify ? OK / NG ____
0191 C.
0192 C.
0193 . C. All OK? Yes--> Please Proceed. / No --> Stop here.

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0194      C.
0195 +. DC 07-F0 MDP_XRT_MODE_OBSV
0196      BC          (c2)
0197 +. TI 2021-09-30 11:22:02.0
0198      DC 07-F0 MDP_XRT_MODE_OBSV
0199      BC          (c2)
0200      . C. ----- Success Verify ?             OK / NG ____
0201      C.
0202      C. ***** XRT END *****
0203      C.
0204      . C. ***** MDP 'ûÃîñî»ò¼ŷñÈÂðñ¹ñèDCBC•x²è *****
0205      C. (¼ã°îŷÓŷÃŷÈŷßŷËŷàŷçŷëñÈ¼ñ¼Ã»Ûñ¹ñè)
0206      . S. DC-BC dcbc-402:DCBC
0207      (MDP_known_event)
0208      C.
0209      C.
0210      . C. ***** ŷDŷ¹•ï Daily±¿îÑñÈ´ðñ¹ñèDCBC•x²è *****
0211      . S. DC-BC dcbc-153:DCBC
0212      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0213      C.
0214      C.
0215      . C. ;ãLOŠŷÃŷŠŷÃŷ´¼Ã»Û;ã
0216      C.
0217      . C. ***** LOS *****
0218      C.

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\*\*\* OP Sequence for XRT \*\*\*

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2021/09/30 11:32:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/09/30 11:32:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/09/30 11:32:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2021/09/30 11:33:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 01 03 1a 02 0c
2021/09/30 11:33:18.0 XRT_FLD_DIS_409_OG [0x199]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2021/09/30 11:33:20.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2021/09/30 11:33:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2021/09/30 11:33:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2021/09/30 11:33:26.0 XRT_FLD_RESET_421_OG [0x1a5]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/09/30 11:35:58.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 10
2021/09/30 11:36:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2021/09/30 16:25:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/09/30 16:25:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/09/30 16:25:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2021/09/30 16:26:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 03 03 1a 02 0c
2021/09/30 16:26:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2021/09/30 16:26:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2021/09/30 16:26:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2021/09/30 16:26:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2021/09/30 16:26:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/09/30 16:28:56.0 XRT_QT_PROG_SET_405_OG [0x195]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0a
2021/09/30 16:28:58.0 XRT_FL_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 04
2021/09/30 16:29:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2021/09/30 16:42:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/09/30 16:42:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/09/30 16:42:04.0 XRT_PREFLR_STRT_431_OG [0x1af]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2021/09/30 16:45:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2021/09/30 17:05:00.0 XRT_Custom_430_OG [0x1ae]
2021/09/30 17:06:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2021/09/30 17:54:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/09/30 17:54:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/09/30 17:54:28.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2021/09/30 17:54:30.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 00 00
2021/09/30 17:54:48.0 XRT_FLD_DIS_409_OG [0x199]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2021/09/30 17:54:50.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2021/09/30 17:54:52.0 XRT_ARS_DIS_420_OG [0x1a4]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2021/09/30 17:57:28.0 XRT_QT_PROG_SET_422_OG [0x1a6]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0c
2021/09/30 17:57:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2021/09/30 18:04:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/09/30 18:04:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/09/30 18:04:28.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2021/09/30 18:04:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 03 03 1a 02 0c
2021/09/30 18:04:48.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2021/09/30 18:04:50.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2021/09/30 18:04:52.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2021/09/30 18:04:54.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5

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2021/09/30	18:04:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da				
2021/09/30	18:07:26.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2021/09/30	18:07:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/09/30	18:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/09/30	18:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/09/30	18:18:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2021/09/30	18:18:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/09/30	18:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/09/30	18:42:00.0	XRT_Custom_430_OG [0x1ae]								
2021/09/30	18:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/09/30	19:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/09/30	19:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2021/09/30	19:55:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/09/30	19:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/09/30	20:18:30.0	XRT_Custom_430_OG [0x1ae]								
2021/09/30	20:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/09/30	21:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/09/30	21:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2021/09/30	21:32:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/09/30	21:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/09/30	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/09/30	21:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/09/30	21:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2021/09/30	22:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	30	e5	bd	59
2021/09/30	22:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/09/30	22:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/09/30	22:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/09/30	22:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/09/30	22:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da				
2021/09/30	22:02:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2021/09/30	22:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/09/30	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/09/30	23:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/09/30	23:10:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2021/09/30	23:10:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/09/30	23:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/09/30	23:31:30.0	XRT_Custom_430_OG [0x1ae]								
2021/09/30	23:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/01	00:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	00:47:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/01	00:47:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/01	00:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/01	00:57:00.0	XRT_Custom_430_OG [0x1ae]								
2021/10/01	00:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/01	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2021/10/01	02:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2021/10/01	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]								

2021/10/01	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2021/10/01	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2021/10/01	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/10/01	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/01	02:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2021/10/01	02:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04
2021/10/01	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/01	02:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/01	02:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/01	02:12:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/01	02:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/01	02:30:00.0	XRT_Custom_430_OG [0x1ae]					
2021/10/01	02:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/01	03:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/01	03:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/01	03:46:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/01	03:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/01	04:06:30.0	XRT_Custom_430_OG [0x1ae]					
2021/10/01	04:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/01	05:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/01	05:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/01	05:15:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/01	05:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/01	05:44:00.0	XRT_Custom_430_OG [0x1ae]					
2021/10/01	05:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/01	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/01	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/01	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2021/10/01	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2021/10/01	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2021/10/01	06:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/10/01	06:02:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2021/10/01	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/01	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/01	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/01	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2021/10/01	06:10:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	30 e5 bd 59
2021/10/01	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2021/10/01	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2021/10/01	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2021/10/01	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/10/01	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/01	06:12:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2021/10/01	06:12:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04
2021/10/01	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/01	06:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/01	06:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/01	06:55:34.0	XRT_PREFLR_STRT_431_OG [0x1af]					

2021/10/01	06:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/10/01	07:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/10/01	07:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/10/01	08:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	08:35:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/10/01	08:35:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/10/01	08:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/10/01	08:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/10/01	08:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	10:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/10/01	10:15:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/10/01	10:15:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/10/01	10:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/10/01	10:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/10/01	10:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2021/10/01	11:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 03 1a 02 0c		
2021/10/01	11:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2021/10/01	11:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2021/10/01	11:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2021/10/01	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/10/01	11:00:26.0	XRT_FLD_RESET_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/10/01	11:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2021/10/01	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/10/01	16:25:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	16:25:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	16:25:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2021/10/01	16:26:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2021/10/01	16:26:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2021/10/01	16:26:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2021/10/01	16:26:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2021/10/01	16:26:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/10/01	16:28:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2021/10/01	16:28:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2021/10/01	16:29:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/10/01	17:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	17:16:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2021/10/01	17:16:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2021/10/01	17:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2021/10/01	17:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2021/10/01	17:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	17:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	17:56:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2021/10/01	17:56:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2021/10/01	17:56:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2021/10/01	17:56:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		

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2021/10/01	17:56:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2021/10/01	17:56:52.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/10/01	17:59:28.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2021/10/01	17:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/01	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	18:06:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	18:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2021/10/01	18:06:30.0	AOCs_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03	03	1a	02	0c
2021/10/01	18:06:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/10/01	18:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/10/01	18:06:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/10/01	18:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/10/01	18:06:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/01	18:09:26.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2021/10/01	18:09:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/10/01	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/01	18:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	18:53:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/01	18:53:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/01	18:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/01	19:17:00.0	XRT_Custom_430_OG [0x1ae]							
2021/10/01	19:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/01	20:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	20:30:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/01	20:30:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/01	20:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/01	20:53:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/01	20:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/01	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	21:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	21:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2021/10/01	22:00:00.0	AOCs_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	04	03	1a	02	0c
2021/10/01	22:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/10/01	22:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/10/01	22:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/10/01	22:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/10/01	22:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/01	22:02:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2021/10/01	22:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/10/01	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/01	22:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	22:08:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/01	22:08:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/01	22:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/01	22:31:00.0	XRT_Custom_430_OG [0x1ae]							
2021/10/01	22:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/01	23:45:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/01	23:45:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

2021/10/01	23:45:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/01	23:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/02	00:05:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/02	00:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/02	01:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/02	01:22:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/02	01:22:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/02	01:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/02	01:29:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/02	01:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/02	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/02	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/02	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2021/10/02	02:00:00.5	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2021/10/02	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/10/02	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/10/02	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/10/02	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/10/02	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/02	02:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2021/10/02	02:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2021/10/02	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/02	02:45:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/02	02:45:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/02	02:45:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/02	02:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/02	03:05:00.0	XRT_Custom_430_OG [0x1ae]							
2021/10/02	03:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/02	04:20:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/02	04:20:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/02	04:20:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/02	04:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/02	04:42:00.0	XRT_Custom_430_OG [0x1ae]							
2021/10/02	04:43:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/02	05:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/02	05:51:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/02	05:51:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/02	05:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/02	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/02	06:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/02	06:19:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2021/10/02	06:20:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2021/10/02	06:20:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2021/10/02	06:20:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/10/02	06:22:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2021/10/02	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/02	06:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/02	06:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/02	06:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							

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2021/10/02	06:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			AOCU_NM	5	02-76	03	03	1a	02 0c
2021/10/02	06:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2021/10/02	06:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2021/10/02	06:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2021/10/02	06:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2021/10/02	06:30:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2021/10/02	06:32:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2021/10/02	06:32:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04		
2021/10/02	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/10/02	07:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/02	07:32:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2021/10/02	07:32:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2021/10/02	07:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2021/10/02	07:56:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/02	07:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/10/02	09:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/02	09:11:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2021/10/02	09:11:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2021/10/02	09:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2021/10/02	09:33:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/02	09:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/10/02	10:43:55.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/02	11:05:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00 00