

# XRT Timeline to be uploaded on 2021/10/07

Period: 2021/10/07 10:25:00 - 2021/10/12 11:12:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

XOB #1C09: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 1ms													
Term	Pointing (x, y)					Comment							
10/07 10:51:00 - 10/07 17:19:54	Fixed ( -23.0, 865.0)					# OP start + 10min , HOP81							
<b>PROG= 11 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 16 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 90 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3 60-time(s) 60.0sec													
└─ Seqn= 57 1-time(s) 30.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BC0: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Th													
Term	Pointing (x, y)					Comment							
10/07 17:23:00 - 10/07 17:29:54	Fixed ( 0.0, 0.0)					synoptic, shifted -2.0 min							
10/08 05:45:30 - 10/08 05:52:24	Fixed ( 0.0, 0.0)					HOP349 and synoptic, shifted -17.5 min							
10/08 14:53:00 - 10/08 14:59:54	Fixed ( 0.0, 0.0)					synoptic, shifted manually							
<b>PROG= 12 1-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 63 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 15 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 27 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 23 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C50: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms)													
Term	Pointing (x, y)					Comment							
10/07 17:33:00 - 10/08 01:59:54	Track ( -496.2, 139.4) <sup>Ⓜ</sup> 10/07 17:30:00					AR12882 , HOP420							
10/08 15:05:00 - 10/08 16:32:30	Track ( -380.5, 202.8) <sup>Ⓜ</sup> 10/08 15:00:00					HOP424 Northward							
10/08 16:56:30 - 10/08 17:26:54	Track ( -365.5, 142.2) <sup>Ⓜ</sup> 10/08 16:50:00					HO424							
10/08 18:33:30 - 10/08 19:46:00	Track ( -365.5, 142.2) <sup>Ⓜ</sup> 10/08 16:50:00					HO424							
10/08 20:10:00 - 10/09 05:59:54	Track ( -281.2, 130.2) <sup>Ⓜ</sup> 10/08 20:00:00					AR12882							
10/09 06:13:00 - 10/09 10:37:00	Track ( -193.5, 128.4) <sup>Ⓜ</sup> 10/09 06:10:00					AR12882							
<b>PROG= 10 Inf.-time(s)</b>													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 62 3-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 98 15-time(s) 120.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	70.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	70.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	70.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1CBD: HOP349 - 3-filter Synoptics (Al-mesh[24/256/2897], Al-poly[45/512/4096], thin-Be[181/2048/11571] with 512x512 G-band+Leak - 45min cad) + C**

Term	Pointing (x, y)	Comment
10/08 02:03:00 - 10/08 05:42:24	Fixed ( 0.0, 0.0)	HOP349 and synoptic, shifted -17.5 min
<b>PROG= 18 Inf.-time(s)</b>		
Subr= 1 1-time(s) 600.0sec		
Seqn= 1 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 99 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 53 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 5-time(s) 480.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 29 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1CC3: HOP361- High cadence (10s thin-Be only) 256x256 at 1064 1048-2**

Term	Pointing (x, y)	Comment
10/08 05:55:30 - 10/08 14:49:54	Track ( -398.8, 134.3) <sup>® 10/08 05:52:30</sup>	AR12882 , HO361
<b>PROG= 14 Inf.-time(s)</b>		
Subr= 1 1-time(s) 2.0sec		
Seqn= 12 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 256x256 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 256x256 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 256x256 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 28 250-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 256x256 (1064, 1048) Q=95 3 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1BCB: AR (Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 30s cad**

Term	Pointing (x, y)	Comment
10/08 17:30:00 - 10/08 18:08:30	Track ( -365.5, 142.2) <sup>® 10/08 16:50:00</sup>	HO424
<b>PROG= 19 Inf.-time(s)</b>		
Subr= 1 1-time(s) 2.0sec		
Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Be close	Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 3 0 2.0sec
Subr= 2 90-time(s) 30.0sec		
Seqn= 35 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec
Seqn= 58 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #1BA8: Synoptic 7 Filter w/ Al-mesh(24/256/2897), Al-poly(45/512/4096), Thin-Be(181/2048/11571) - Thick-Be(65536), Al-poly+Ti-poly(256/5795), Med-A**

Term	Pointing (x, y)	Comment
10/09 06:03:00 - 10/09 06:09:54	Fixed ( 0.0, 0.0)	synoptic
<b>PROG= 08 1-time(s)</b>		
Subr= 1 1-time(s) 2.0sec		
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 1</b>			<b>1-time(s)</b>		<b>2.0sec</b>							
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 99</b>			<b>1-time(s)</b>		<b>2.0sec</b>							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 53</b>			<b>1-time(s)</b>		<b>2.0sec</b>							
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 23</b>			<b>1-time(s)</b>		<b>4.0sec</b>							
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2</b>			<b>1-time(s)</b>		<b>2.0sec</b>							
<b>Seqn= 46</b>			<b>2-time(s)</b>		<b>2.0sec</b>							
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 13</b>			<b>2-time(s)</b>		<b>2.0sec</b>							
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 45</b>			<b>2-time(s)</b>		<b>2.0sec</b>							
med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Flare mode

\* \* \* \* \*

### XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GB

Term	Pointing (x, y)	Comment
10/07 10:51:00 - 10/07 17:19:54	Fixed ( -23.0, 865.0)	# OP start + 10min , HOP81
10/07 17:33:00 - 10/08 01:59:54	Track ( -496.2, 139.4) @ 10/07 17:30:00	AR12882 , HOP420
10/08 02:03:00 - 10/08 05:42:24	Fixed ( 0.0, 0.0)	HOP349 and synoptic, shifted -17.5 min
10/08 15:05:00 - 10/08 16:32:30	Track ( -380.5, 202.8) @ 10/08 15:00:00	HOP424 Northward
10/08 16:56:30 - 10/08 17:26:54	Track ( -365.5, 142.2) @ 10/08 16:50:00	HO424
10/08 17:30:00 - 10/08 18:08:30	Track ( -365.5, 142.2) @ 10/08 16:50:00	HO424
10/08 18:33:30 - 10/08 19:46:00	Track ( -365.5, 142.2) @ 10/08 16:50:00	HO424
10/08 20:10:00 - 10/09 05:59:54	Track ( -281.2, 130.2) @ 10/08 20:00:00	AR12882
10/09 06:13:00 - 10/09 10:37:00	Track ( -193.5, 128.4) @ 10/09 06:10:00	AR12882

### PROG= 04 30-time(s)

<b>Subr= 1</b>			<b>20-time(s)</b>		<b>2.0sec</b>							
<b>Seqn= 11</b>			<b>1-time(s)</b>		<b>2.0sec</b>							
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 73</b>			<b>1-time(s)</b>		<b>10.0sec</b>							
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2</b>			<b>1-time(s)</b>		<b>2.0sec</b>							
<b>Seqn= 10</b>			<b>1-time(s)</b>		<b>2.0sec</b>							
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11</b>			<b>1-time(s)</b>		<b>2.0sec</b>							
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 87</b>			<b>1-time(s)</b>		<b>2.0sec</b>							
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

\* \* \* \* \*

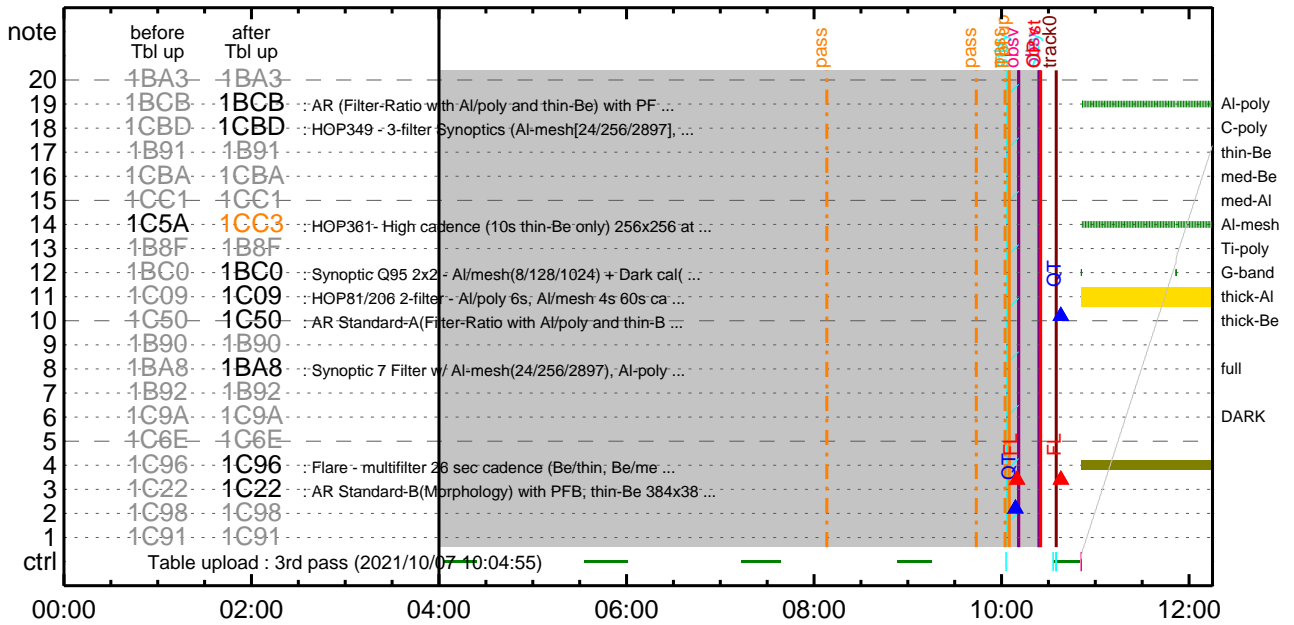
### Flare Detection

\* \* \* \* \*

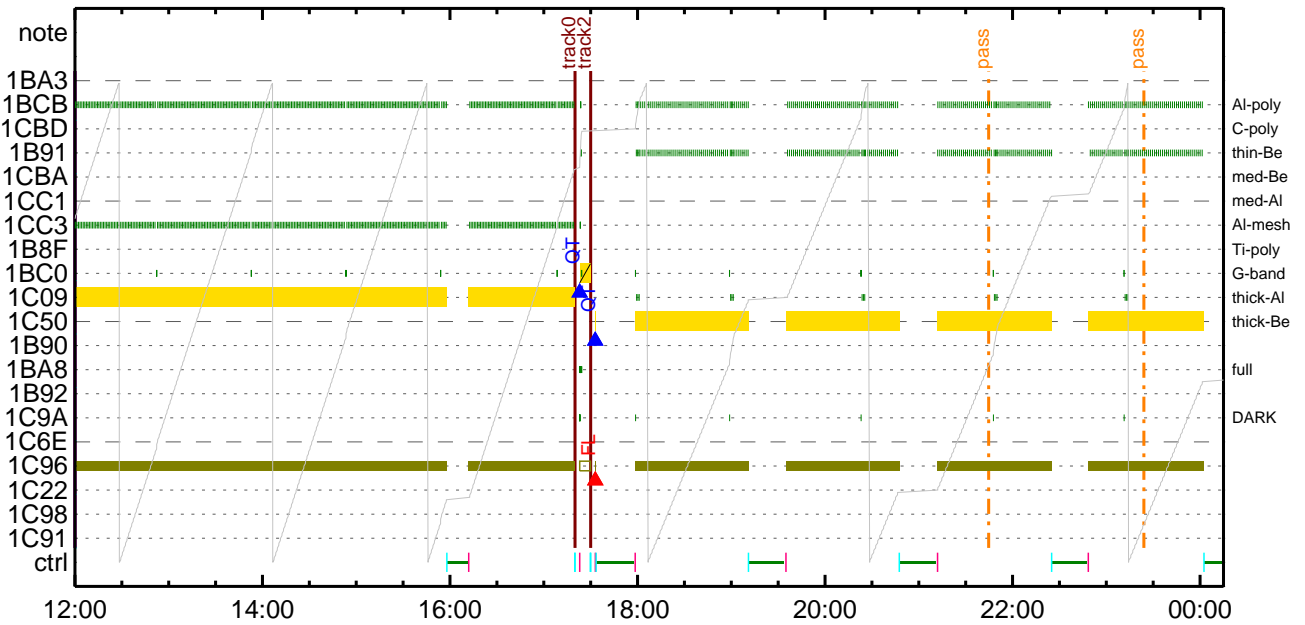
### FLD Patrol

Term	Pointing (x, y)	Comment										
10/07 17:30:18 - 10/08 05:42:48	Track ( -496.2, 139.4) @ 10/07 17:30:00	AR12882 , HOP420										
10/08 15:00:18 - 10/09 06:00:18	Track ( -380.5, 202.8) @ 10/08 15:00:00	HOP424 Northward										
10/09 06:10:18 - 10/12 11:12:00	Track ( -193.5, 128.4) @ 10/09 06:10:00	AR12882										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8	Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

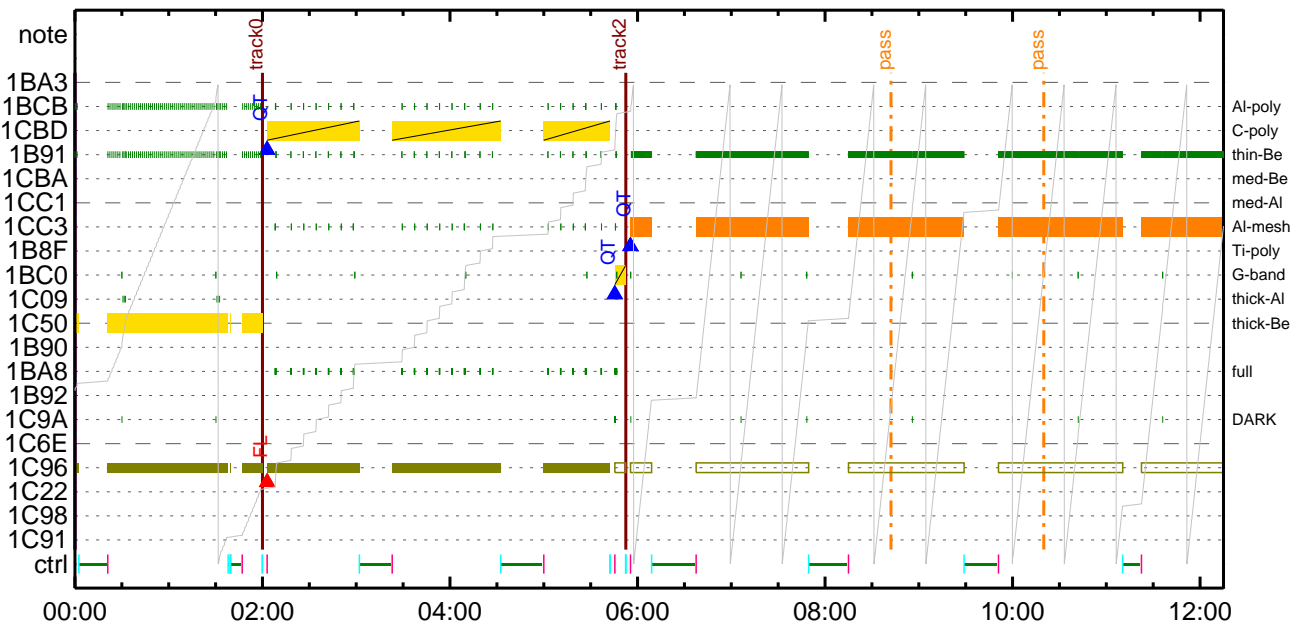
### CMDI #0917 2021/10/07



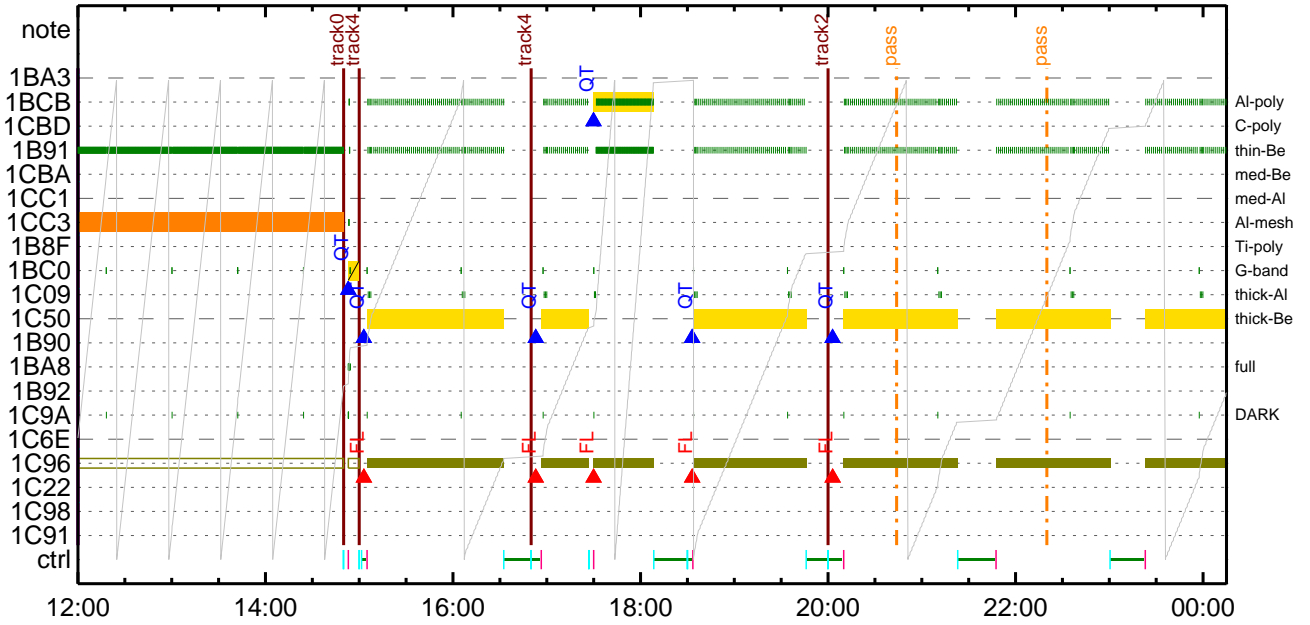
### CMDI #0917 2021/10/07



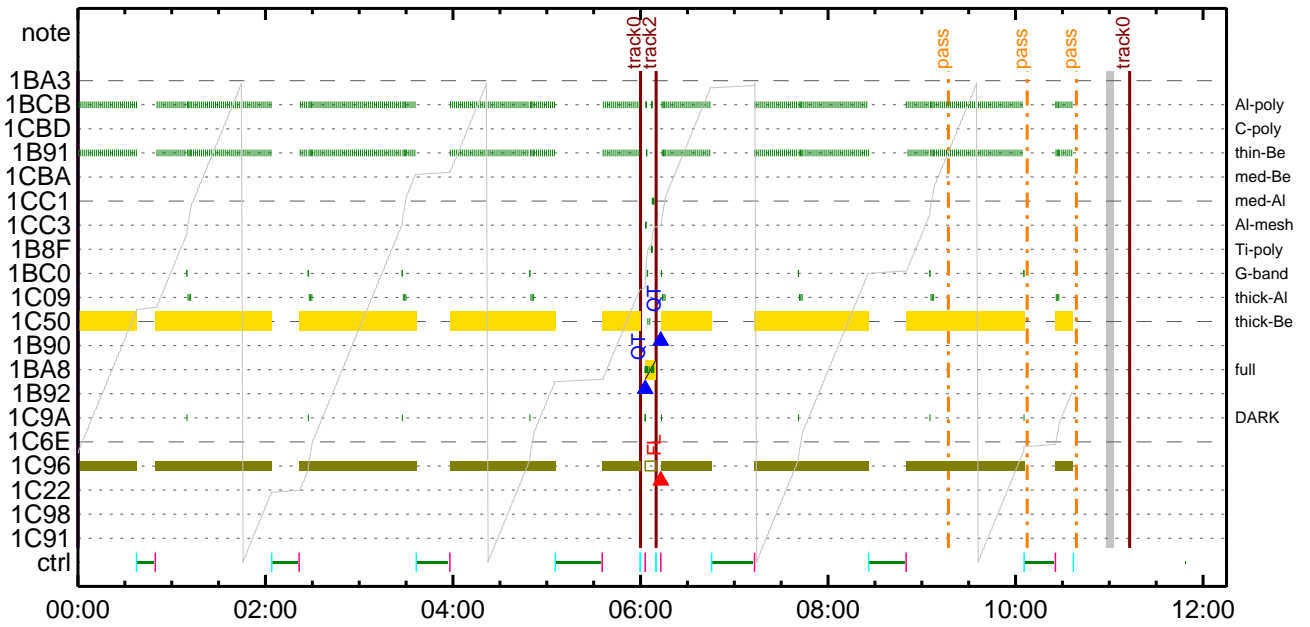
### CMDI #0917 2021/10/08



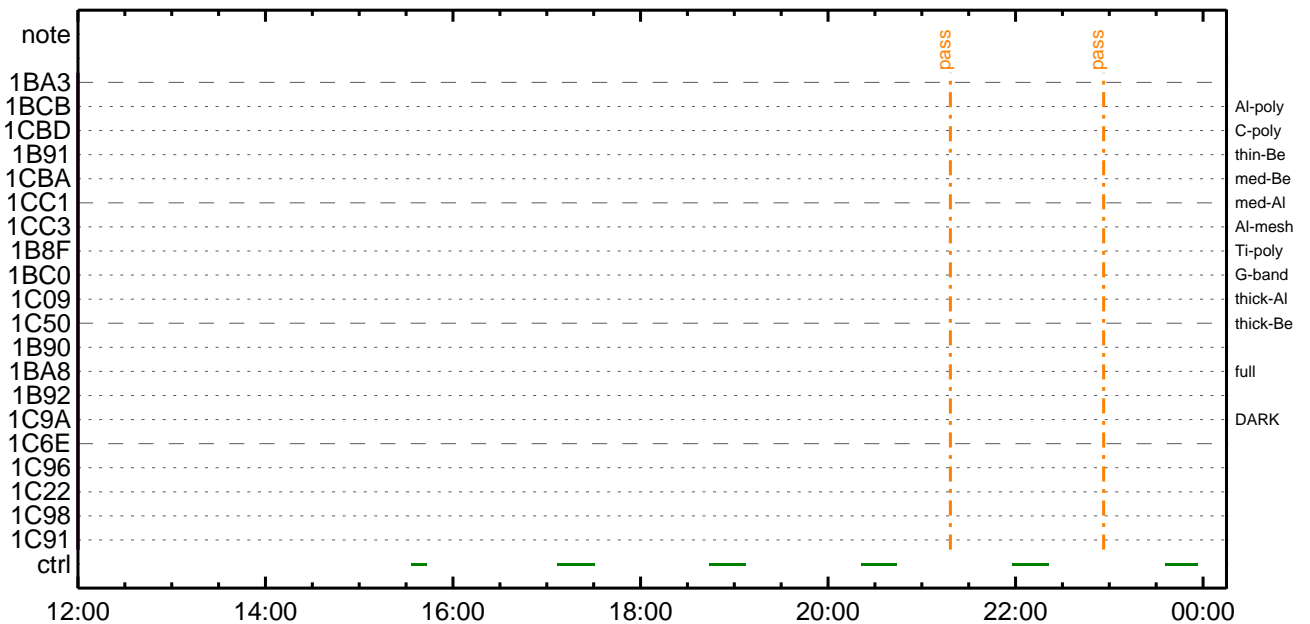
CMDI #0917 2021/10/08



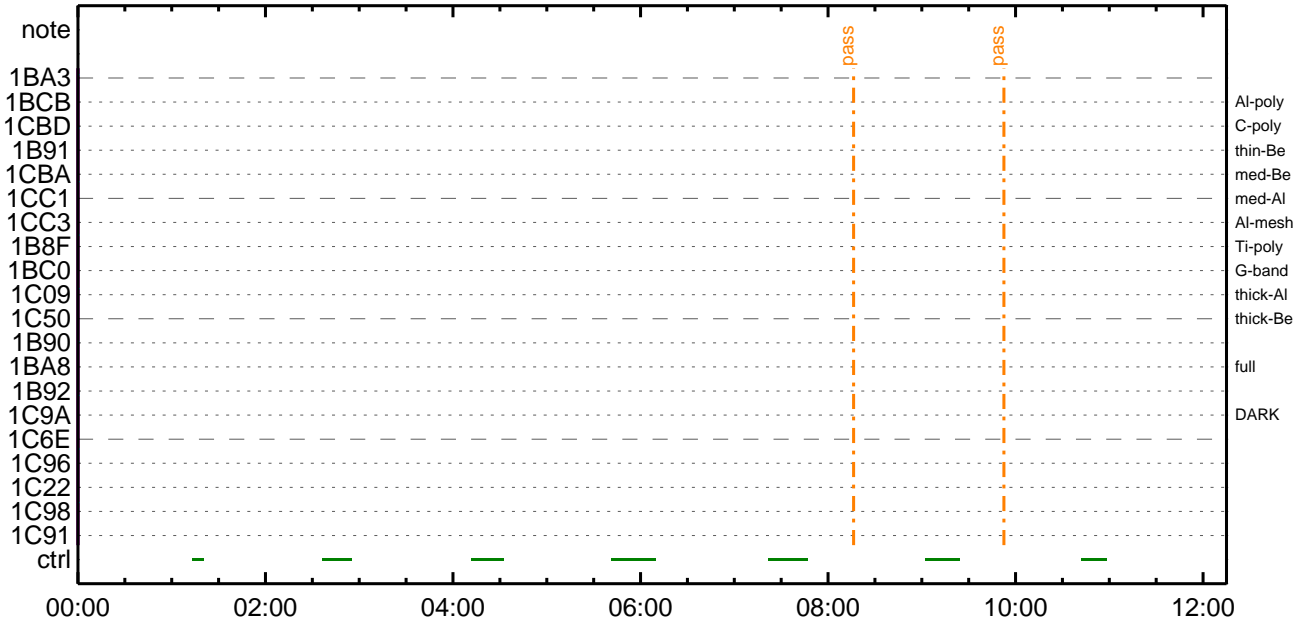
CMDI #0917 2021/10/09



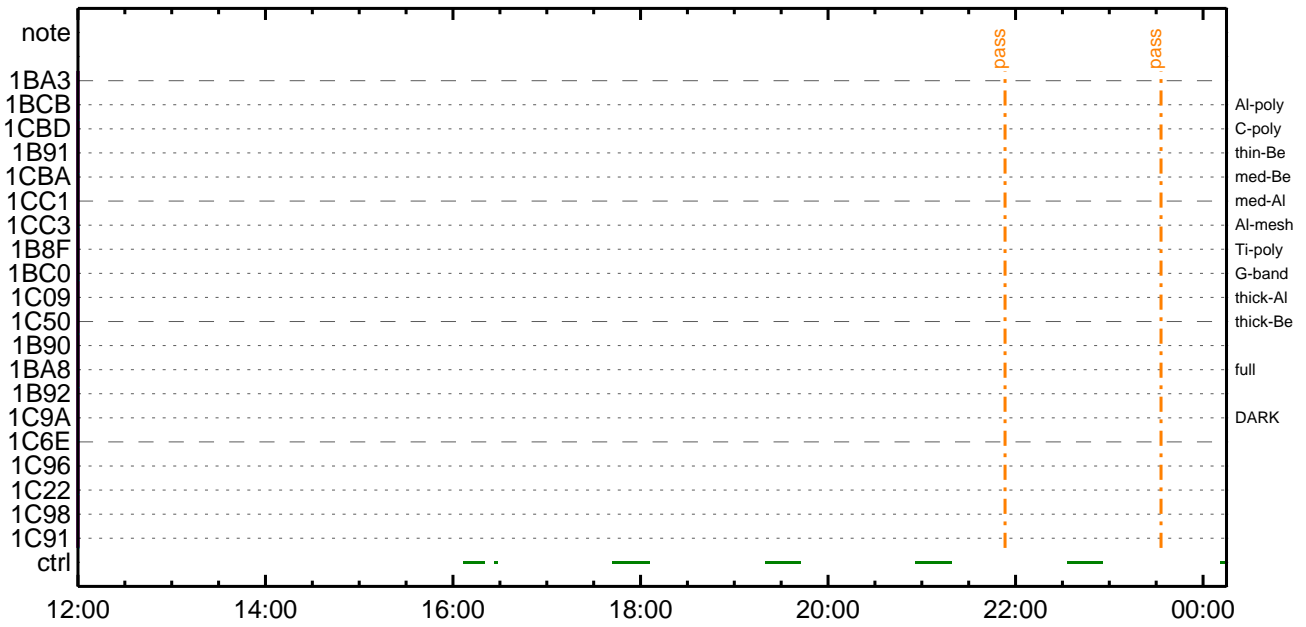
CMDI #0917 2021/10/09



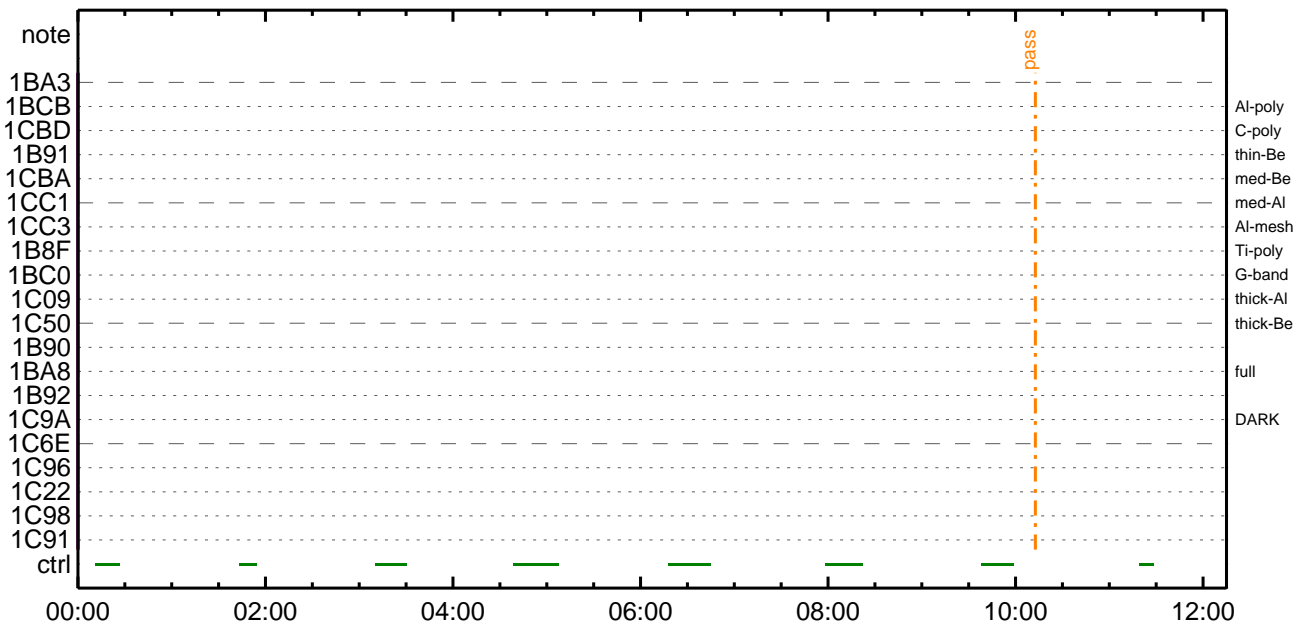
### CMDI #0917 2021/10/10



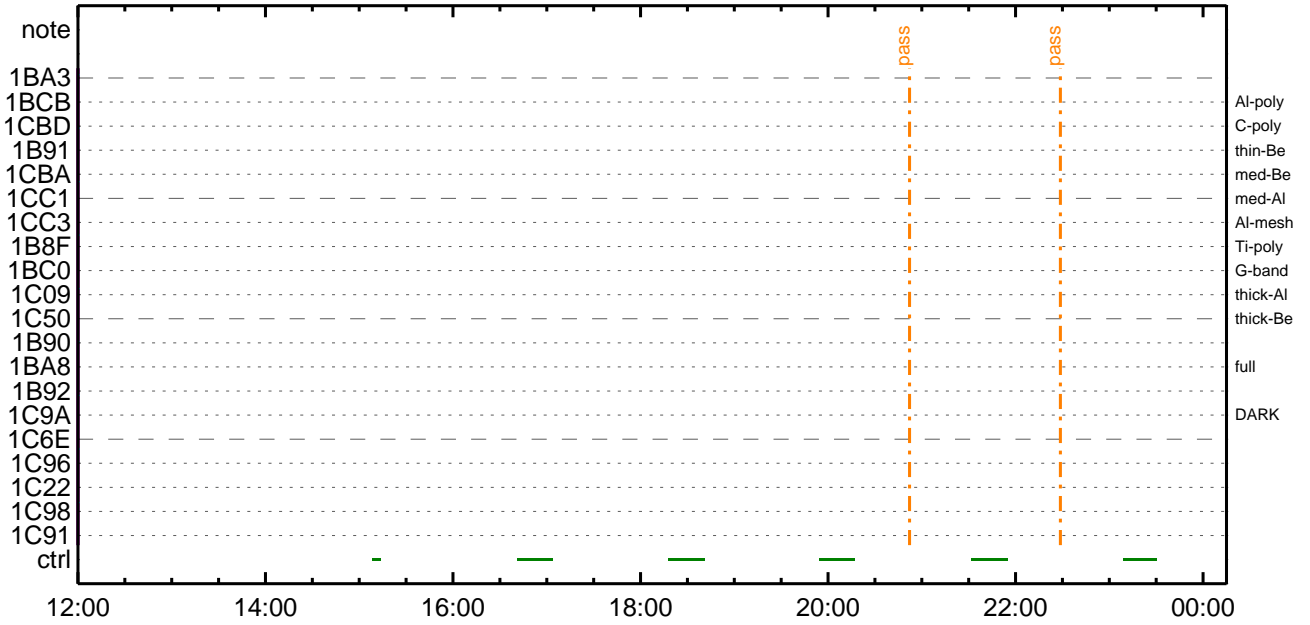
### CMDI #0917 2021/10/10



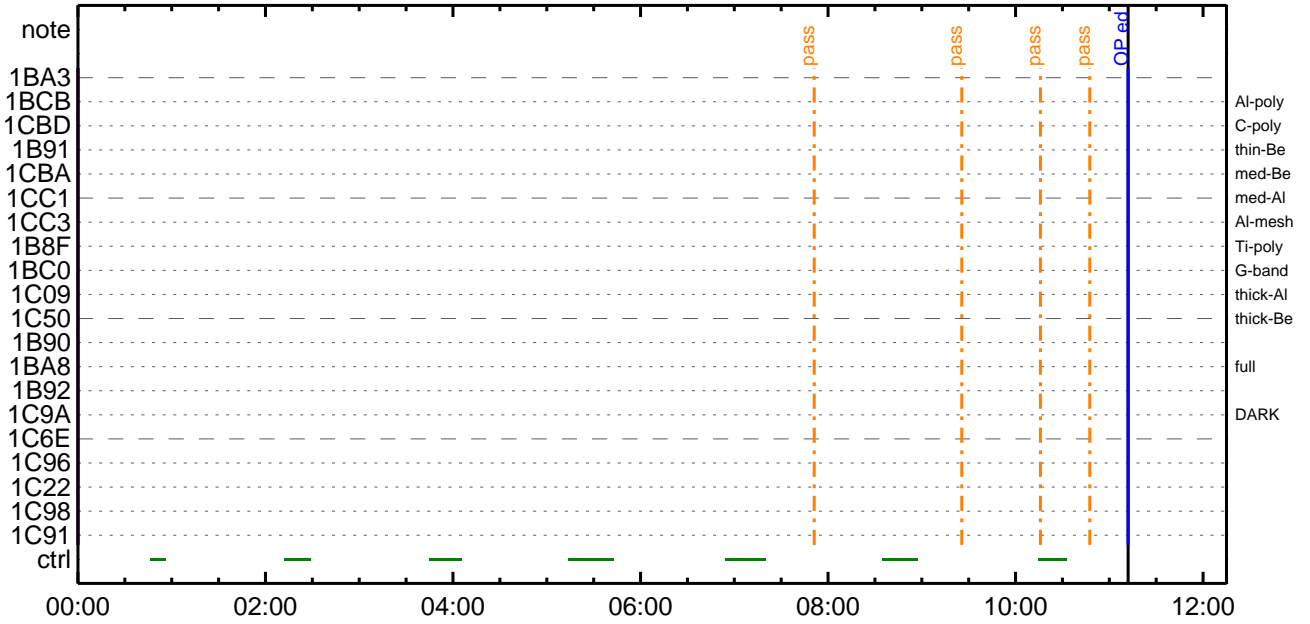
### CMDI #0917 2021/10/11



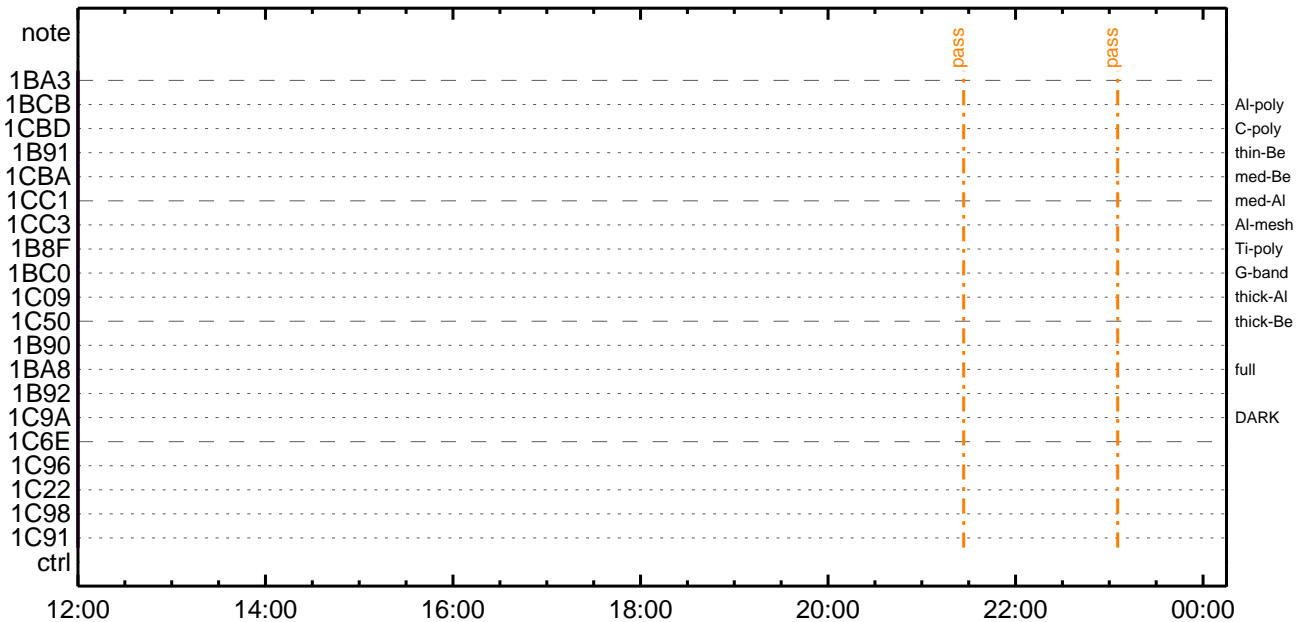
### CMDI #0917 2021/10/11



### CMDI #0917 2021/10/12



### CMDI #0917 2021/10/12







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-738:OP
0104 ( )
0105 S. OG og-738:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°"òè¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°²E²ò²³òE;f
0180 C. ²²ò¿;çSET²E²DUMP²î½±°iYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2021-10-07 10:20:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2021-10-07 10:20:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2021-10-07 10:20:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```







(a) Spacecraft Operation Procedure (real-commands)

```
main-740 2021-10-07 12:13:22 102 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÀø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÖø•ø¿¼í¹çøÍ;çÁ®, ùø¹øèøÈøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 85 83 04 04)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 85 83 08 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 + DC 07-F0 MDP_XRT_FLD_ENA
0061 BC (d8)
0062 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063 BC (c8)
0064 + DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 + DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 + DC 07-F0 MDP_XRT_FLD_RESET
0069 BC (da)
0070 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0071 BC (c4 03)
0072 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0073 BC (c5 04)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C.
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078 C.
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV
0080 BC (c2)
0081 +. TI 2021-10-07 10:24:02.0
0082 DC 07-F0 MDP_XRT_MODE_OBSV
0083 BC (c2)
0084 . C. ----- Success Verify ? OK / NG ____
0085 C.
0086 C. ***** XRT END *****
0087 C.
0088 . C. ***** MDP 'úÁíøí»ø¼YøÈÁøø¹øÈDCBC•x²è *****
0089 C. (%á°íYÖYÁYÈYB¥YÉYáYçYèøÈ¼øø¼Á»Ûø¹øè)
0090 . S. DC-BC dcbc-402:DCBC
0091 (MDP_known_event)
0092 C.
0093 C.
0094 . C. ***** YÐY¹.İ Daily+¿ÍÑøÈ´Øø¹øèDCBC•x²è *****
0095 . S. DC-BC dcbc-153:DCBC
```

```
0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. ;äLOSŸÁŸSŸÄŸ-¼Ä»Û;ä
0100 C.
0101 . C. ***** LOS *****
0102 C.
```

\*\*\* OP Sequence for XRT \*\*\*

```

2021/10/07 10:33:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/10/07 10:33:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/10/07 10:33:04.0 XRT_PREFLR_STRT_431_OG [0x1af]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2021/10/07 10:34:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/10/07 10:34:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/10/07 10:34:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2021/10/07 10:35:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 b3 1b 02 0c
2021/10/07 10:35:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2021/10/07 10:35:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2021/10/07 10:35:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2021/10/07 10:35:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2021/10/07 10:35:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/10/07 10:36:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2021/10/07 10:37:56.0 XRT_QT_PROG_SET_447_OG [0x1bf]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0b
2021/10/07 10:37:58.0 XRT_FL_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 04
2021/10/07 10:50:00.0 XRT_Custom_430_OG [0x1ae]
2021/10/07 10:51:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2021/10/07 15:58:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/10/07 15:58:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/10/07 15:58:04.0 XRT_PREFLR_STRT_431_OG [0x1af]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2021/10/07 16:01:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2021/10/07 16:11:00.0 XRT_Custom_430_OG [0x1ae]
2021/10/07 16:12:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2021/10/07 17:19:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/10/07 17:19:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/10/07 17:19:58.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2021/10/07 17:20:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2021/10/07 17:20:18.0 XRT_FLD_DIS_409_OG [0x199]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2021/10/07 17:20:20.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2021/10/07 17:20:22.0 XRT_ARS_DIS_420_OG [0x1a4]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2021/10/07 17:22:58.0 XRT_QT_PROG_SET_422_OG [0x1a6]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0c
2021/10/07 17:23:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2021/10/07 17:29:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/10/07 17:29:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/10/07 17:29:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2021/10/07 17:30:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 02 03 1a 02 0c
2021/10/07 17:30:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2021/10/07 17:30:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2021/10/07 17:30:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2021/10/07 17:30:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2021/10/07 17:30:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/10/07 17:32:56.0 XRT_QT_PROG_SET_405_OG [0x195]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0a
2021/10/07 17:32:58.0 XRT_FL_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 04
2021/10/07 17:33:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2021/10/07 17:33:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2021/10/07 17:33:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2021/10/07 17:33:34.0 XRT_PREFLR_STRT_431_OG [0x1af]

```

2021/10/07	17:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/07	17:57:30.0	XRT_Custom_430_OG [0x1ae]					
2021/10/07	17:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/07	19:11:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/07	19:11:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/07	19:11:04.0	XRT_PREFLR_STRT_431_OG [0x1af]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/07	19:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/07	19:34:00.0	XRT_Custom_430_OG [0x1ae]					
2021/10/07	19:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/07	20:47:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/07	20:47:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/07	20:47:34.0	XRT_PREFLR_STRT_431_OG [0x1af]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/07	20:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/07	21:11:00.0	XRT_Custom_430_OG [0x1ae]					
2021/10/07	21:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/07	22:25:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/07	22:25:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/07	22:25:04.0	XRT_PREFLR_STRT_431_OG [0x1af]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/07	22:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/07	22:47:30.0	XRT_Custom_430_OG [0x1ae]					
2021/10/07	22:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	00:02:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	00:02:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/08	00:02:34.0	XRT_PREFLR_STRT_431_OG [0x1af]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/08	00:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/08	00:20:00.0	XRT_Custom_430_OG [0x1ae]					
2021/10/08	00:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	01:38:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	01:38:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/08	01:38:04.0	XRT_PREFLR_STRT_431_OG [0x1af]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/08	01:38:30.0	XRT_Custom_430_OG [0x1ae]					
2021/10/08	01:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	01:39:30.5	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	01:39:32.5	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/08	01:39:34.5	XRT_PREFLR_STRT_431_OG [0x1af]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/08	01:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/08	01:42:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/08	01:46:00.0	XRT_Custom_430_OG [0x1ae]					
2021/10/08	01:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2021/10/08	02:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2021/10/08	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2021/10/08	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2021/10/08	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2021/10/08	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/10/08	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/08	02:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2021/10/08	02:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]					



2021/10/08	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	03:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	03:02:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/08	03:02:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/08	03:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/08	03:22:00.0	XRT_Custom_430_OG [0x1ae]					
2021/10/08	03:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	04:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	04:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/08	04:32:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/08	04:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/08	04:59:00.0	XRT_Custom_430_OG [0x1ae]					
2021/10/08	05:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	05:42:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	05:42:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	05:42:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2021/10/08	05:42:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2021/10/08	05:42:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2021/10/08	05:42:52.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/10/08	05:45:28.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2021/10/08	05:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	05:52:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	05:52:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	05:52:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2021/10/08	05:52:30.0	AOCs_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 03 1a	02 0c
2021/10/08	05:52:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2021/10/08	05:52:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2021/10/08	05:52:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2021/10/08	05:52:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2021/10/08	05:52:56.0	XRT_FLD_RESET_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/08	05:55:28.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2021/10/08	05:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	06:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	06:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/08	06:09:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/08	06:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/08	06:36:30.0	XRT_Custom_430_OG [0x1ae]					
2021/10/08	06:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	07:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	07:49:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/08	07:49:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/08	07:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/10/08	08:14:00.0	XRT_Custom_430_OG [0x1ae]					
2021/10/08	08:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/10/08	09:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/10/08	09:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/10/08	09:29:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/10/08	09:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

2021/10/08	09:50:00.0	XRT_Custom_430_OG [0x1ae]						
2021/10/08	09:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/10/08	11:10:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/08	11:10:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2021/10/08	11:10:34.0	XRT_PREFLR_STRT_431_OG [0x1af]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2021/10/08	11:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2021/10/08	11:21:30.0	XRT_Custom_430_OG [0x1ae]						
2021/10/08	11:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/10/08	14:49:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/08	14:49:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/08	14:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2021/10/08	14:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2021/10/08	14:50:18.0	XRT_FLD_DIS_409_OG [0x199]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2021/10/08	14:50:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2021/10/08	14:50:22.0	XRT_ARS_DIS_420_OG [0x1a4]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2021/10/08	14:52:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2021/10/08	14:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/10/08	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/08	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/08	14:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2021/10/08	15:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	04 fd cb 02 0c			
2021/10/08	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2021/10/08	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2021/10/08	15:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2021/10/08	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2021/10/08	15:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2021/10/08	15:01:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/08	15:01:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2021/10/08	15:01:34.0	XRT_PREFLR_STRT_431_OG [0x1af]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2021/10/08	15:02:56.0	XRT_QT_PROG_SET_405_OG [0x195]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2021/10/08	15:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04			
2021/10/08	15:04:00.0	XRT_Custom_430_OG [0x1ae]						
2021/10/08	15:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2021/10/08	15:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2021/10/08	16:32:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/08	16:32:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2021/10/08	16:32:34.0	XRT_PREFLR_STRT_431_OG [0x1af]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2021/10/08	16:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2021/10/08	16:49:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/08	16:49:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2021/10/08	16:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2021/10/08	16:50:00.0	AOCS_Ore-point_Start_5_OG [0x09b]						
		AOCU_NM	5	02-76	04 03 1a 02 0c			
2021/10/08	16:50:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2021/10/08	16:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2021/10/08	16:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2021/10/08	16:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2021/10/08	16:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2021/10/08	16:52:56.0	XRT_QT_PROG_SET_405_OG [0x195]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			

2021/10/08	16:52:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/10/08	16:55:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/08	16:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/08	17:26:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/08	17:26:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/08	17:26:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2021/10/08	17:27:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/10/08	17:27:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/10/08	17:27:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/10/08	17:27:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/10/08	17:27:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/08	17:29:56.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2021/10/08	17:29:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/10/08	17:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/08	18:08:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/08	18:08:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/08	18:08:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/08	18:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/08	18:30:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/08	18:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/08	18:30:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2021/10/08	18:30:24.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/10/08	18:30:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/10/08	18:30:28.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/10/08	18:30:30.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/10/08	18:30:32.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/08	18:32:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/08	18:33:02.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2021/10/08	18:33:05.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/10/08	18:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/08	19:46:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/08	19:46:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/08	19:46:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/08	19:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/08	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/08	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/08	19:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2021/10/08	20:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02	03	1a	02	0c
2021/10/08	20:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/10/08	20:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/10/08	20:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/10/08	20:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/10/08	20:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/08	20:02:56.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2021/10/08	20:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2021/10/08	20:09:00.0	XRT_Custom_430_OG [0x1ae]							
2021/10/08	20:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/08	21:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

2021/10/08	21:23:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/08	21:23:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/08	21:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/08	21:46:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/08	21:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/08	23:00:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/08	23:00:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/08	23:00:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/08	23:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/08	23:22:00.0	XRT_Custom_430_OG [0x1ae]							
2021/10/08	23:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/09	00:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/09	00:37:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/09	00:37:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/09	00:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/09	00:48:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/09	00:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/09	02:04:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/09	02:04:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/09	02:04:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/09	02:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/09	02:20:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/09	02:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/09	03:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/09	03:36:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/09	03:36:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/09	03:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/09	03:57:00.0	XRT_Custom_430_OG [0x1ae]							
2021/10/09	03:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/09	05:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/09	05:05:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/10/09	05:05:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/10/09	05:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/10/09	05:34:30.0	XRT_Custom_430_OG [0x1ae]							
2021/10/09	05:35:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/09	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/09	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/09	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2021/10/09	06:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2021/10/09	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2021/10/09	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2021/10/09	06:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/10/09	06:02:58.0	XRT_QT_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2021/10/09	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/10/09	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/09	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/10/09	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2021/10/09	06:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 03 1a 02 0c				
2021/10/09	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/10/09	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							

2021/10/09	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2021/10/09	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2021/10/09	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2021/10/09	06:12:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/10/09	06:12:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2021/10/09	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2021/10/09	06:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/10/09	06:45:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/10/09	06:45:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/10/09	06:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/10/09	07:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/10/09	07:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2021/10/09	08:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2021/10/09	08:26:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/10/09	08:26:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/10/09	08:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/10/09	08:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/10/09	08:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/10/09	10:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_430_OG [0x1ae]			
2021/10/09	10:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2021/10/09	10:05:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/10/09	10:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/10/09	10:24:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/10/09	10:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/10/09	10:37:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/10/09	11:13:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_CTRL_AUTO_424_OG [0x1a8]			
		AOCU_NM	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
				5	02-76	00 00 00 00 00