

XRT Timeline to be uploaded on 2021/11/02

Period: 2021/11/02 11:07:00 - 2021/11/06 11:18:00

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Normal mode

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XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
11/02 11:28:00 - 11/02 17:51:24	Fixed (800.0, -460.0)	# OP start + 10min AR12887
11/02 18:04:30 - 11/03 05:59:54	Fixed (800.0, -460.0)	# AR12887
11/03 06:13:00 - 11/03 17:53:24	Fixed (800.0, -460.0)	# A12887
11/03 18:06:30 - 11/03 21:59:54	Track (320.1, 176.3) @ 11/03 18:03:30	# AR12891 (HOP386)
11/04 06:38:30 - 11/04 09:58:00	Fixed (800.0, -460.0)	# AR12887

PROG= 02 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec	Seqn= 92 1-time(s) 2.0sec	Open/G-band	Open/G-band	Open/G-band	Open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	

Subr= 2 5-time(s) 2.0sec

Seqn= 47 1-time(s) 2.0sec	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 96 4-time(s) 120.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1CA9: Synoptic Q95 2x2 - Al/mesh(16/181/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(24/362/4096) + T

Term	Pointing (x, y)	Comment
11/02 17:54:30 - 11/02 18:01:24	Fixed (0.0, 0.0)	synoptic, shifted -8.5 min
11/03 06:03:00 - 11/03 06:09:54	Fixed (0.0, 0.0)	synoptic
11/03 17:56:30 - 11/03 18:03:24	Fixed (0.0, 0.0)	synoptic, shifted -6.5 min
11/04 06:28:30 - 11/04 06:35:24	Fixed (0.0, 0.0)	synoptic, shifted 25.5 min

PROG= 16 1-time(s)

Subr= 1 1-time(s) 2.0sec	Seqn= 5 1-time(s) 2.0sec	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 75 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 9 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 53 1-time(s) 2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1C9A: HOP336 1-filter - Al/poly -384x384, 128ms and 2s, 60s-cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
11/03 22:03:00 - 11/04 01:29:54	Fixed (-23.0, 463.7)	HOP336 (North)

PROG= 13 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec	Seqn= 16 2-time(s) 2.0sec	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 90 1-time(s) 30.0sec	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

Subr= 3 15-time(s) 2.0sec

Seqn= 60 1-time(s) 60.0sec	Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
11/04 01:33:00 - 11/04 06:25:24	Fixed (800.0, -460.0)	AR12887
PROG= 09 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 4-time(s) 2.0sec		
Seqn= 47 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 77 4-time(s) 300.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 95.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 95.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GB

Term	Pointing (x, y)	Comment
11/02 11:28:00 - 11/02 17:51:24	Fixed (800.0, -460.0)	# OP start + 10min AR12887
11/02 18:04:30 - 11/03 05:59:54	Fixed (800.0, -460.0)	# AR12887
11/03 06:13:00 - 11/03 17:53:24	Fixed (800.0, -460.0)	# A12887
11/03 18:06:30 - 11/03 21:59:54	Track (320.1, 176.3) @ 11/03 18:03:30	# AR12891 (HOP386)
11/03 22:03:00 - 11/04 01:29:54	Fixed (-23.0, 463.7)	HOP336 (North)
11/04 01:33:00 - 11/04 06:25:24	Fixed (800.0, -460.0)	AR12887
11/04 06:38:30 - 11/04 09:58:00	Fixed (800.0, -460.0)	# AR12887
PROG= 04 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 73 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 87 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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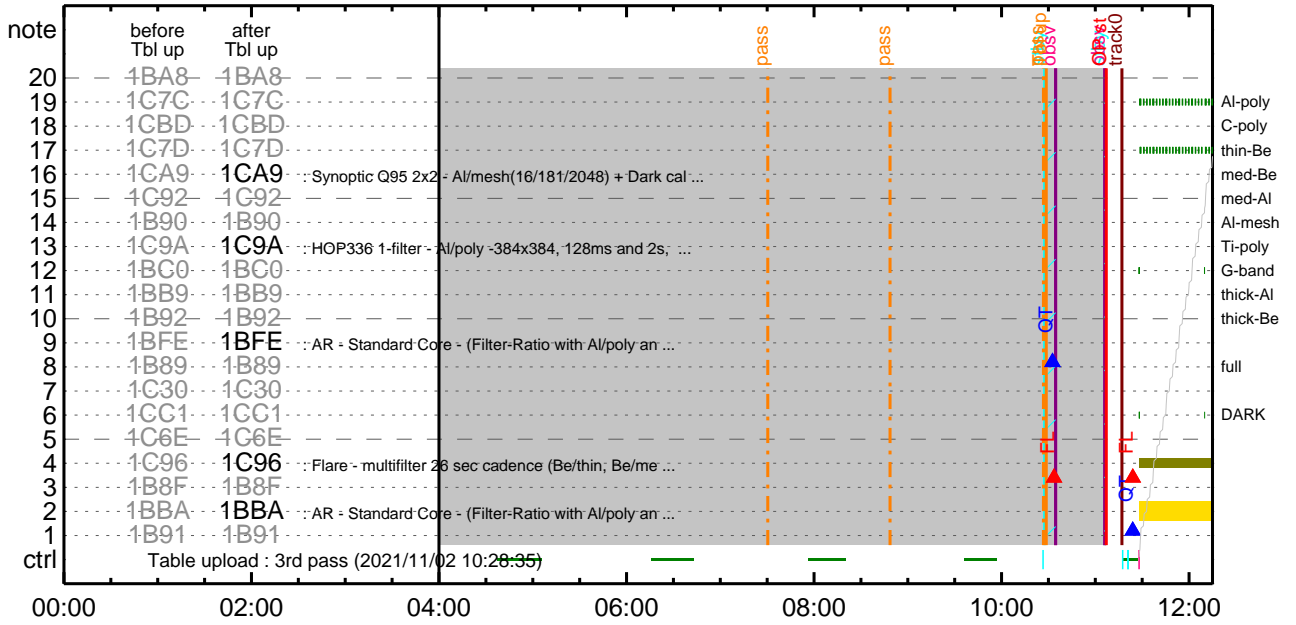
Flare Detection

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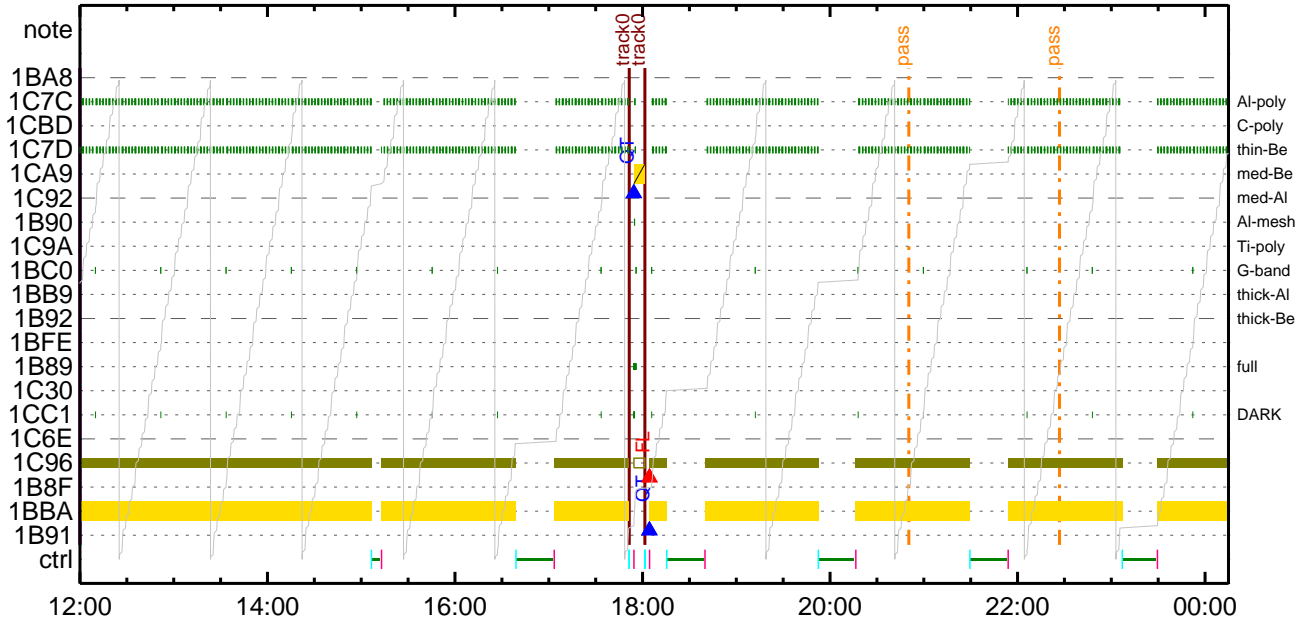
FLD Patrol

Term	Pointing (x, y)	Comment
11/02 18:01:48 - 11/03 06:00:18	Fixed (800.0, -460.0)	# AR12887
11/03 06:10:18 - 11/03 17:53:48	Fixed (800.0, -460.0)	# A12887
11/03 18:03:48 - 11/04 06:25:48	Track (320.1, 176.3) @ 11/03 18:03:30	# AR12891 (HOP386)
11/04 06:35:48 - 11/06 11:18:00	Fixed (800.0, -460.0)	# AR12887
Al-poly/Open	Al-poly/Open close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

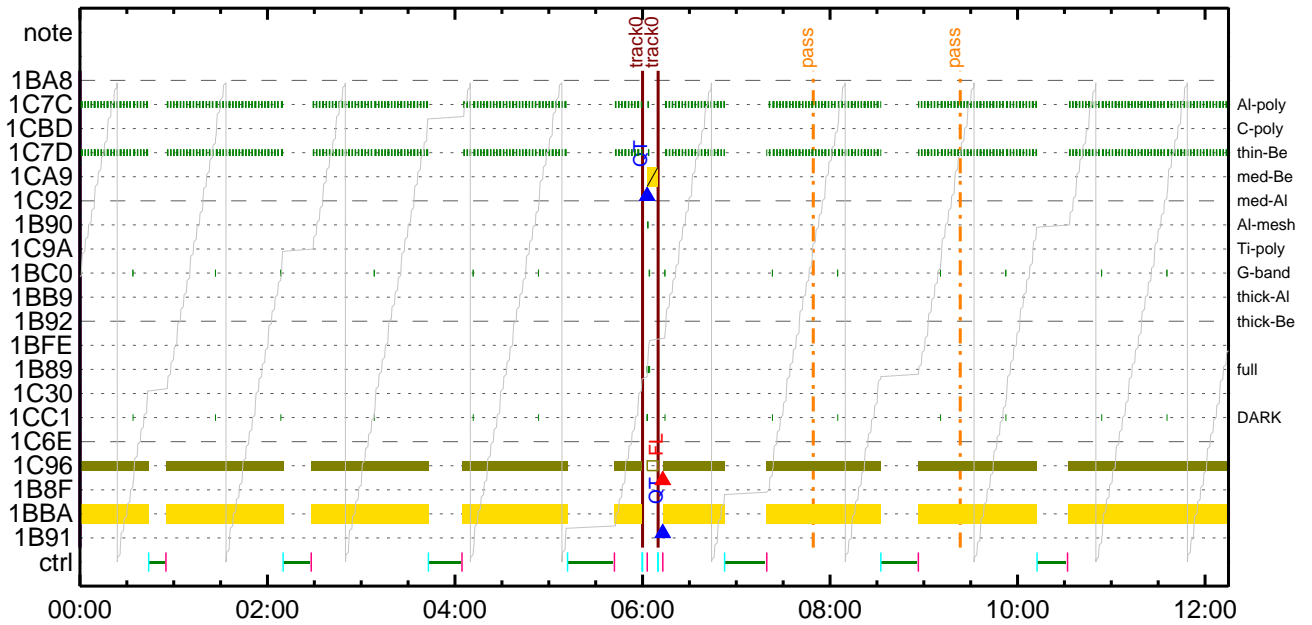
CMDI #0966 2021/11/02



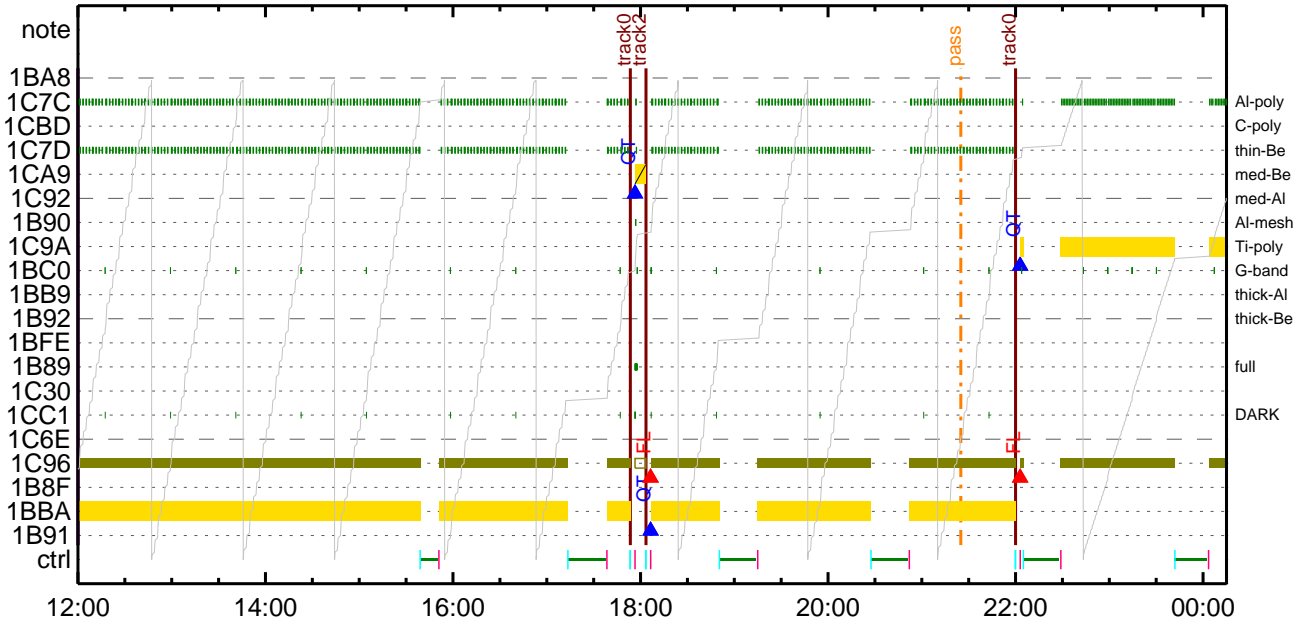
CMDI #0966 2021/11/02



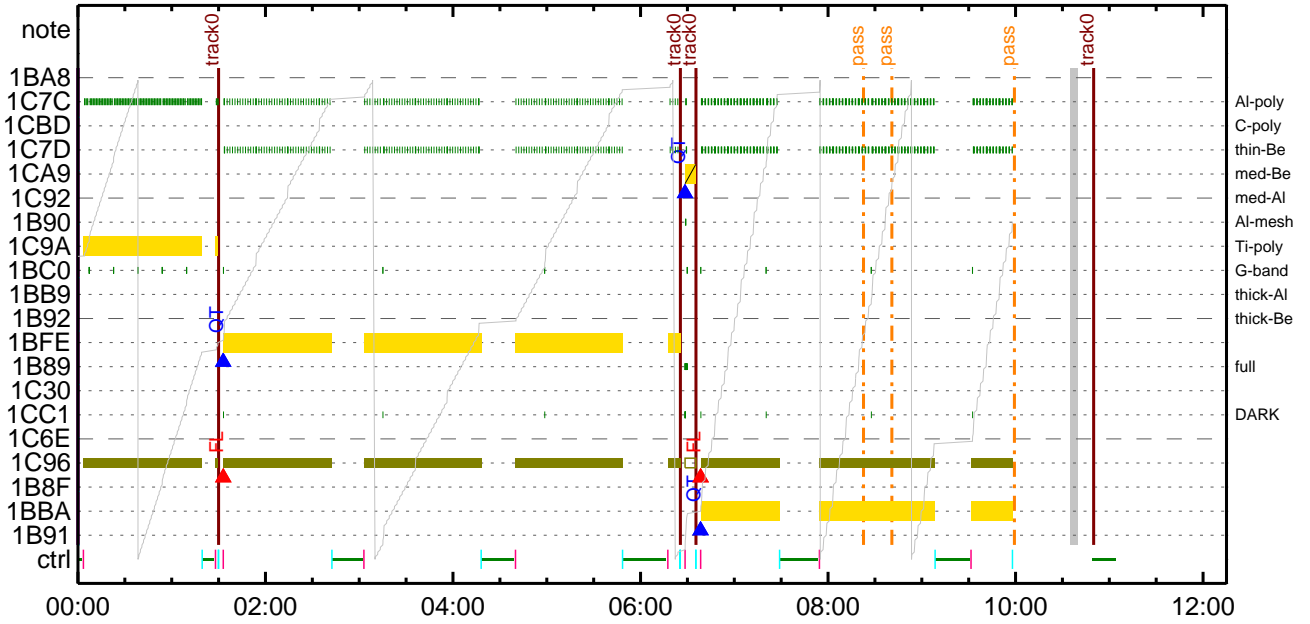
CMDI #0966 2021/11/03



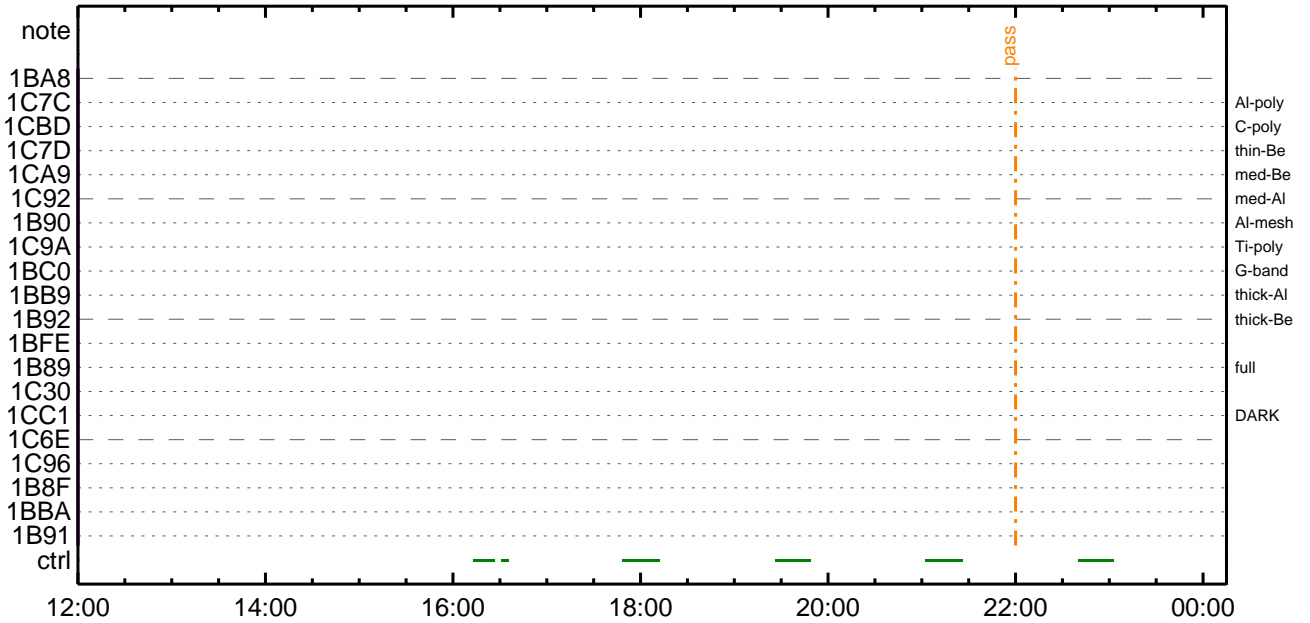
CMDI #0966 2021/11/03



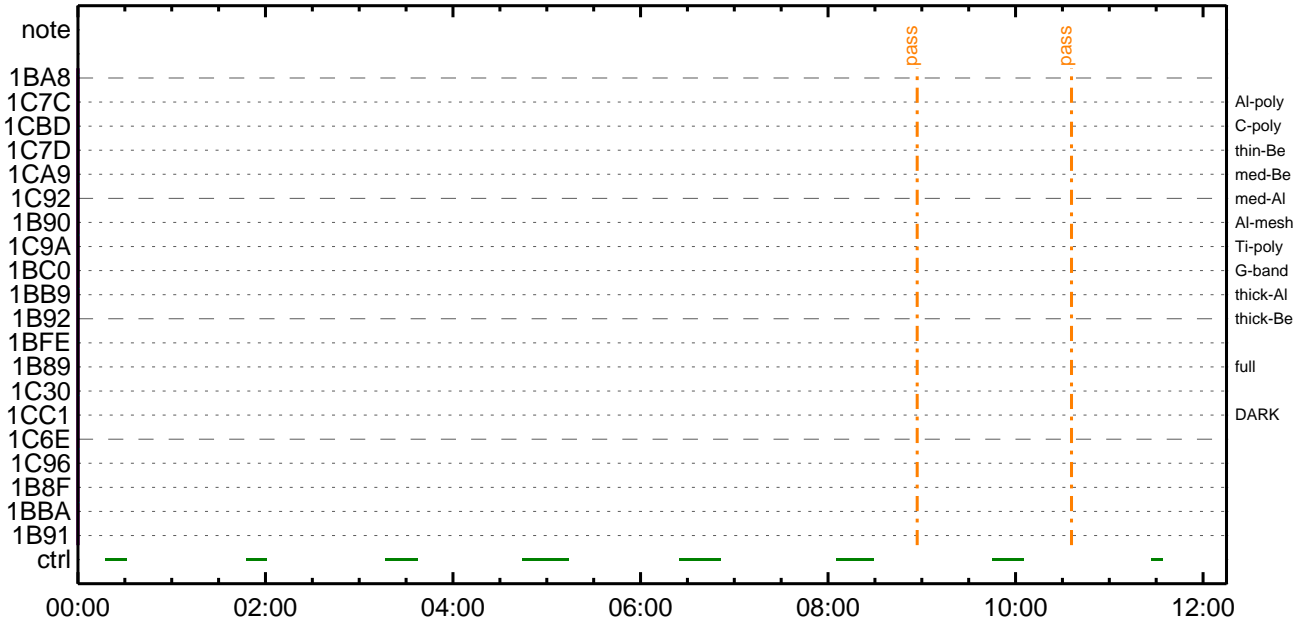
CMDI #0966 2021/11/04



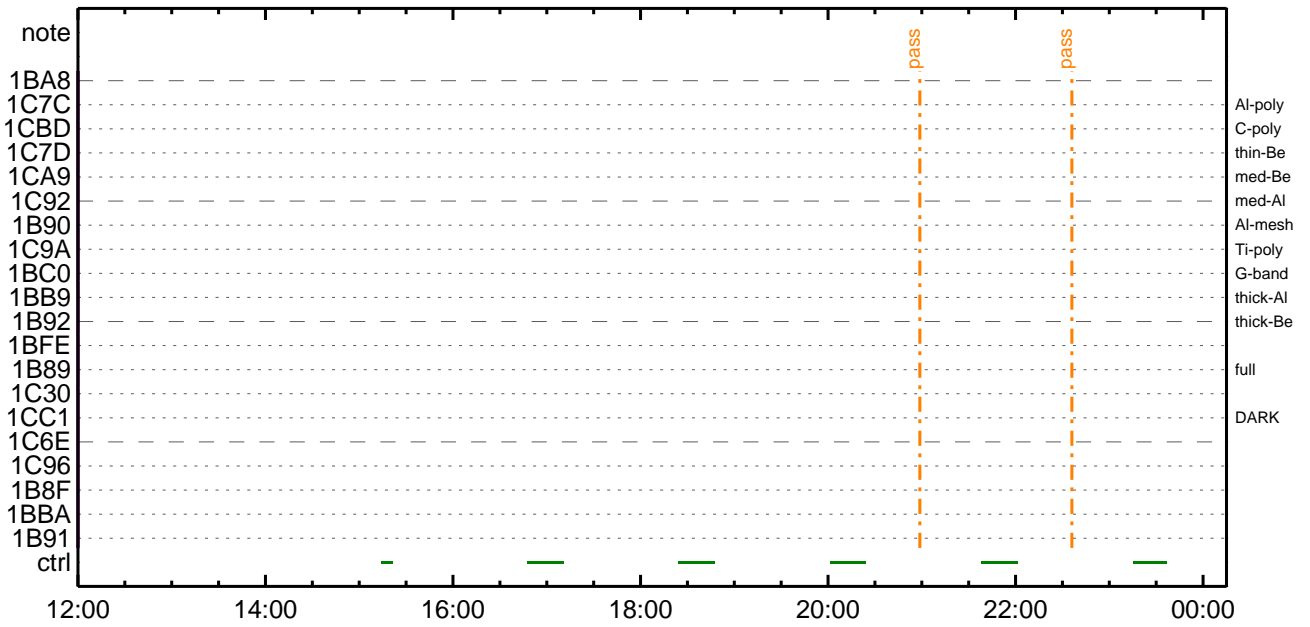
CMDI #0966 2021/11/04



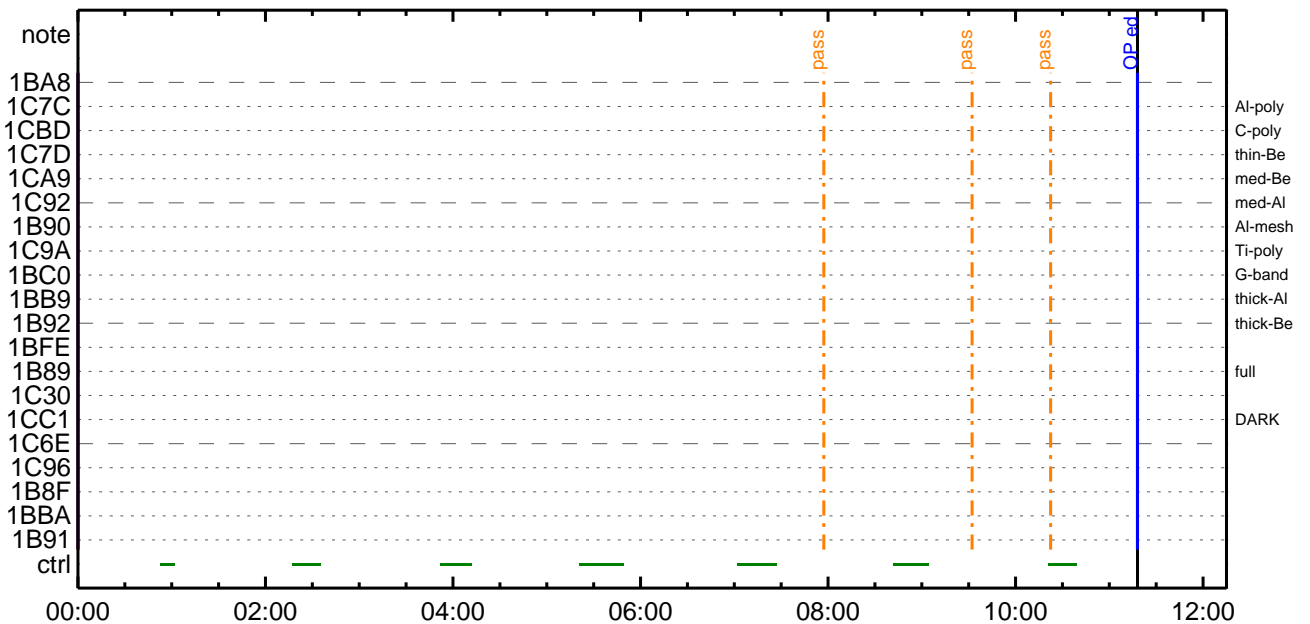
CMDI #0966 2021/11/05

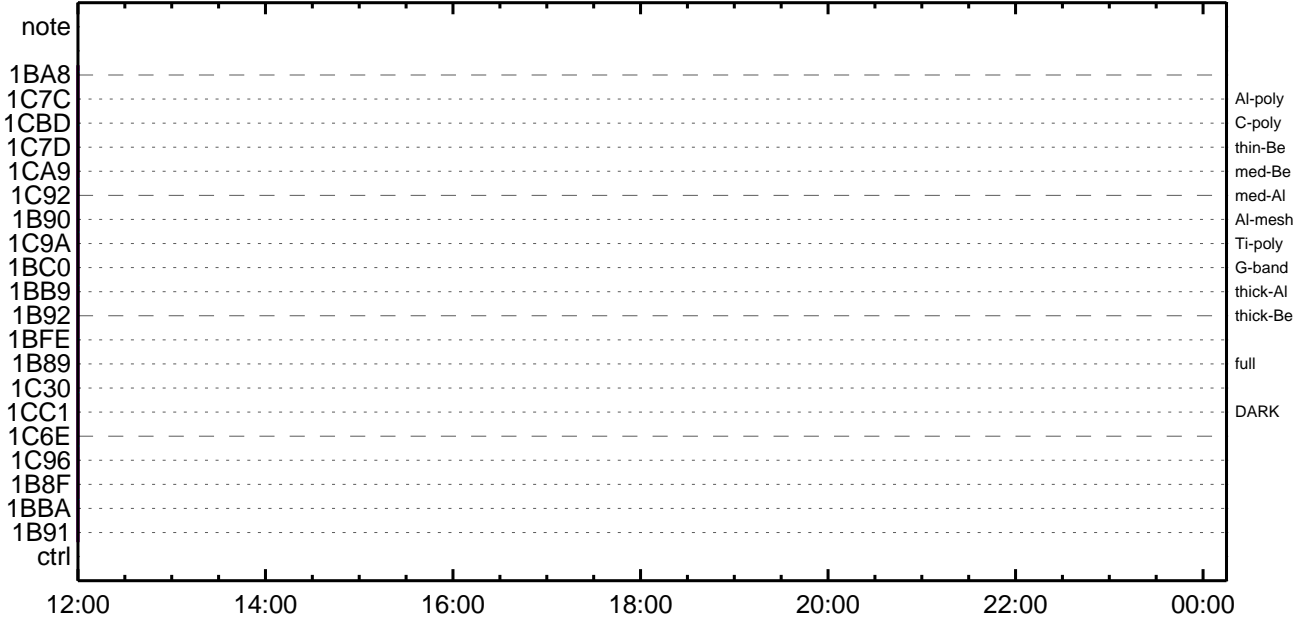


CMDI #0966 2021/11/05



CMDI #0966 2021/11/06






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0096 C.                0302;çSET0EDUMPAİÆ±°iYÑY¹aÇ¹Ôa|a³aE;f
0097 C.
0098 . C. TIY³YF¥ÖYÉ0dÅDİç(UT)
0099 +. TI 2021-11-02 11:02:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2021-11-02 11:02:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2021-11-02 11:02:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2021-11-02 11:06:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.                °E²¼aİÄê%îİÑaİYÁY§YÁY-¹àİÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC                (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC                (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 . C. YÄYÖY×½ªİ»0d³İÇ§
0142 C.                çç[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹ç•è²İOK0d³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼, Yî;¼YÈ;È0dİã0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC                (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2021-11-02 11:06:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC                (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2021-11-02 11:06:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC                (21 02)
0174 +. TI 2021-11-02 11:06:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC                (22)
0177 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2021-11-02 11:06:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC                (c3)
0187 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP `üÃİaİ»ö¼Y0ÈÄD0¹0dCBC•×²è *****
0192 C. (%â°İYÖYÄYÉYÏYËYÁYçYè0E¼0a¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC
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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 6756069.5 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 13s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2021-11-02 11:06:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÀîî»ö¼ÝñÉÁñ¹ñēDCBC•x²è *****
0165 C. (¼á°îÝÖYÁEYþYÉYáçYèñE¼ñ¼Á»Ūñ¹ñé)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** YD¥¹•İ Daily±;İÑñÉ´Øñ¹ñēDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOS¥ÁY$YÄY-¼Á»Ū;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-283:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2021-11-02 11:06:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 20)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 08)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 08 20)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0f 80 80 06 06)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 10 80 80 08 08)
0170 + DC 07-F0 MDP_XRT_FLD_ENA
0171 BC (d8)
0172 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0173 BC (c8)
0174 + DC 07-F0 MDP_XRT_ARS_DIS
0175 BC (d5)
0176 + DC 07-F0 MDP_XRT_AEC_RESET
0177 BC (d0)
0178 + DC 07-F0 MDP_XRT_FLD_RESET
0179 BC (da)
0180 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0181 BC (c4 09)
0182 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0183 BC (c5 04)
0184 . C. ----- Success Verify ? OK / NG ____
0185 C.
0186 C.
0187 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0188 C.
0189 +. DC 07-F0 MDP_XRT_MODE_OBSV
0190 BC (c2)
0191 +. TI 2021-11-02 11:06:02.0
0192 DC 07-F0 MDP_XRT_MODE_OBSV
0193 BC (c2)

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0194 . C. ----- Success Verify ? OK / NG ____
0195 C.
0196 C. ***** XRT END *****
0197 C.
0198 . C. ***** MDP `úÃîñî»ö¼ÿñÈÃðñ¹ñèDCBC•x²è *****
0199 C. (¼ã°îÿÓÿÃÿÈÿPÿEÿãÿçÿéñ¼ñ¼Ã»Ûñ¹ñè)
0200 . S. DC-BC dcbc-402:DCBC
0201 (MDP_known_event)
0202 C.
0203 C.
0204 . C. ***** ÿDÿ¹•Ï Daily±;îÑñÈ'Øñ¹ñèDCBC•x²è *****
0205 . S. DC-BC dcbc-153:DCBC
0206 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0207 C.
0208 C.
0209 . C. ;ãLOSÿÃÿSÿÃÿ-¼Ã»Û;ã
0210 C.
0211 . C. ***** LOS *****
0212 C.

*** OP Sequence for XRT ***

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2021/11/02 11:17:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 00 28 e5 b8 e6
2021/11/02 11:17:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/11/02 11:17:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/11/02 11:17:34.0 XRT_PREFLR_STRT_431_OG [0x1af]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/11/02 11:20:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/11/02 11:20:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/11/02 11:20:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/11/02 11:20:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION          4 07-F8 22 fe 97 00
2021/11/02 11:21:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA            1 07-F0 d8
2021/11/02 11:21:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA         1 07-F0 c8
2021/11/02 11:21:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET          1 07-F0 d0
2021/11/02 11:21:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS            1 07-F0 d5
2021/11/02 11:21:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/11/02 11:23:56.0 XRT_QT_PROG_SET_414_OG [0x19e]
                        MDP_XRT_QT_PROG_SET         2 07-F0 c4 02
2021/11/02 11:23:58.0 XRT_FL_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_FL_PROG_SET         2 07-F0 c5 04
2021/11/02 11:27:00.0 XRT_Custom_430_OG [0x1ae]
2021/11/02 11:28:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2021/11/02 15:06:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/11/02 15:06:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/11/02 15:06:34.0 XRT_PREFLR_STRT_431_OG [0x1af]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/11/02 15:09:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/11/02 15:12:00.0 XRT_Custom_430_OG [0x1ae]
2021/11/02 15:13:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2021/11/02 16:39:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/11/02 16:39:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2021/11/02 16:39:04.0 XRT_PREFLR_STRT_431_OG [0x1af]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2021/11/02 16:42:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2021/11/02 17:02:30.0 XRT_Custom_430_OG [0x1ae]
2021/11/02 17:03:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2021/11/02 17:51:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/11/02 17:51:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/11/02 17:51:28.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION          4 07-F8 22 ff aa 00
2021/11/02 17:51:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2021/11/02 17:51:48.0 XRT_FLD_DIS_409_OG [0x199]
                        MDP_XRT_FLD_DIS            1 07-F0 d9
2021/11/02 17:51:50.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS         1 07-F0 c9
2021/11/02 17:51:52.0 XRT_ARS_DIS_420_OG [0x1a4]
                        MDP_XRT_ARS_DIS            1 07-F0 d5
2021/11/02 17:54:28.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET         2 07-F0 c4 10
2021/11/02 17:54:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2021/11/02 18:01:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/11/02 18:01:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2021/11/02 18:01:28.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION          4 07-F8 22 fe 97 00
2021/11/02 18:01:30.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 00 28 e5 b8 e6
2021/11/02 18:01:48.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA            1 07-F0 d8
2021/11/02 18:01:50.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA         1 07-F0 c8
2021/11/02 18:01:52.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET          1 07-F0 d0
2021/11/02 18:01:54.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS            1 07-F0 d5
2021/11/02 18:01:56.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET          1 07-F0 da

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2021/11/02	18:04:26.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02
2021/11/02	18:04:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04
2021/11/02	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/11/02	18:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/11/02	18:15:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/11/02	18:15:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/11/02	18:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/11/02	18:39:00.0	XRT_Custom_430_OG [0x1ae]					
2021/11/02	18:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/11/02	19:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/11/02	19:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/11/02	19:52:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/11/02	19:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/11/02	20:15:30.0	XRT_Custom_430_OG [0x1ae]					
2021/11/02	20:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/11/02	21:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/11/02	21:29:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/11/02	21:29:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/11/02	21:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/11/02	21:53:00.0	XRT_Custom_430_OG [0x1ae]					
2021/11/02	21:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/11/02	23:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/11/02	23:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/11/02	23:07:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/11/02	23:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/11/02	23:28:30.0	XRT_Custom_430_OG [0x1ae]					
2021/11/02	23:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/11/03	00:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/11/03	00:44:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/11/03	00:44:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/11/03	00:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/11/03	00:54:00.0	XRT_Custom_430_OG [0x1ae]					
2021/11/03	00:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/11/03	02:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/11/03	02:10:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/11/03	02:10:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/11/03	02:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/11/03	02:27:00.0	XRT_Custom_430_OG [0x1ae]					
2021/11/03	02:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/11/03	03:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/11/03	03:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/11/03	03:43:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/11/03	03:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/11/03	04:03:30.0	XRT_Custom_430_OG [0x1ae]					
2021/11/03	04:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2021/11/03	05:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2021/11/03	05:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2021/11/03	05:12:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2021/11/03	05:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2021/11/03	05:41:00.0	XRT_Custom_430_OG [0x1ae]					
2021/11/03	05:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					

2021/11/03	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/03	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/03	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/03	06:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2021/11/03	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00
2021/11/03	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2021/11/03	06:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2021/11/03	06:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_ARS_DIS	1	07-F0	d5
2021/11/03	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2021/11/03	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/03	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/03	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/03	06:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2021/11/03	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 28 e5 b8 e6
2021/11/03	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2021/11/03	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2021/11/03	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2021/11/03	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2021/11/03	06:12:56.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/11/03	06:12:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2021/11/03	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2021/11/03	06:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/03	06:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/03	06:52:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/11/03	06:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/11/03	07:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/11/03	07:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2021/11/03	08:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/03	08:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/03	08:32:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/11/03	08:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/11/03	08:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/11/03	08:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2021/11/03	10:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/03	10:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/03	10:12:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/11/03	10:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/11/03	10:31:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/11/03	10:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2021/11/03	15:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/03	15:39:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/03	15:39:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/11/03	15:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/11/03	15:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/11/03	15:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2021/11/03	17:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/03	17:13:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/03	17:13:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/11/03	17:13:34.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

2021/11/03	17:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/11/03	17:37:30.0	XRT_Custom_430_OG [0x1ae]							
2021/11/03	17:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/03	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/03	17:53:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/03	17:53:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2021/11/03	17:53:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2021/11/03	17:53:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2021/11/03	17:53:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2021/11/03	17:53:52.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/11/03	17:56:28.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2021/11/03	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/03	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/03	18:03:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/03	18:03:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2021/11/03	18:03:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2021/11/03	18:03:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/11/03	18:03:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/11/03	18:03:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/11/03	18:03:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/11/03	18:03:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/03	18:06:26.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2021/11/03	18:06:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2021/11/03	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/03	18:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/03	18:50:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/03	18:50:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/11/03	18:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/11/03	19:14:00.0	XRT_Custom_430_OG [0x1ae]							
2021/11/03	19:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/03	20:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/03	20:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/03	20:27:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/11/03	20:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/11/03	20:51:00.0	XRT_Custom_430_OG [0x1ae]							
2021/11/03	20:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/03	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/03	21:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/03	21:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2021/11/03	22:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 d6 c9 02 0c				
2021/11/03	22:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/11/03	22:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/11/03	22:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/11/03	22:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/11/03	22:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/03	22:02:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2021/11/03	22:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2021/11/03	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

2021/11/03	22:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/03	22:05:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/03	22:05:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/11/03	22:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/11/03	22:28:00.0	XRT_Custom_430_OG [0x1ae]							
2021/11/03	22:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/03	23:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/03	23:42:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/03	23:42:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/11/03	23:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/11/04	00:02:30.0	XRT_Custom_430_OG [0x1ae]							
2021/11/04	00:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/04	01:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/04	01:19:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/04	01:19:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/11/04	01:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/11/04	01:27:00.0	XRT_Custom_430_OG [0x1ae]							
2021/11/04	01:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/04	01:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/04	01:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/04	01:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2021/11/04	01:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 28 e5 b8 e6				
2021/11/04	01:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2021/11/04	01:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2021/11/04	01:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2021/11/04	01:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2021/11/04	01:30:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/04	01:32:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2021/11/04	01:32:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2021/11/04	01:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/04	02:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/04	02:42:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/04	02:42:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/11/04	02:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/11/04	03:02:00.0	XRT_Custom_430_OG [0x1ae]							
2021/11/04	03:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/04	04:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/04	04:18:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/04	04:18:04.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/11/04	04:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/11/04	04:39:00.0	XRT_Custom_430_OG [0x1ae]							
2021/11/04	04:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/04	05:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/04	05:48:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2021/11/04	05:48:34.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2021/11/04	05:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2021/11/04	06:16:30.0	XRT_Custom_430_OG [0x1ae]							
2021/11/04	06:17:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2021/11/04	06:25:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2021/11/04	06:25:26.0	XRT_CTRL_MANU_402_OG [0x192]							

2021/11/04	06:25:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/04	06:25:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2021/11/04	06:25:48.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00
2021/11/04	06:25:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2021/11/04	06:25:52.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2021/11/04	06:28:28.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_ARS_DIS	1	07-F0	d5
2021/11/04	06:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2021/11/04	06:35:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/04	06:35:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/04	06:35:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/04	06:35:30.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2021/11/04	06:35:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 28 e5 b8 e6
2021/11/04	06:35:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2021/11/04	06:35:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2021/11/04	06:35:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2021/11/04	06:35:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2021/11/04	06:38:26.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/11/04	06:38:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2021/11/04	06:38:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2021/11/04	07:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/04	07:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/04	07:29:04.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/11/04	07:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/11/04	07:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/11/04	07:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2021/11/04	09:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/04	09:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2021/11/04	09:08:34.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2021/11/04	09:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2021/11/04	09:30:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2021/11/04	09:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2021/11/04	09:58:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2021/11/04	10:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00