

# XRT Timeline to be uploaded on 2022/03/01

Period: 2022/03/01 11:50:00 - 2022/03/05 10:57:00

\* \* \* \* \*

## Normal mode

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XOB #1CC7: Synoptic Q95 2x2 - Al/mesh(2/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Thi												
Term	Pointing (x, y)							Comment				
03/01 12:10:00 - 03/01 12:40:01 cannot be identified												
<b>PROG= 17 1-time(s)</b>												
└─ <b>Subr= 1 1-time(s) 2.0sec</b>												
└─ <b>Seqn= 5 1-time(s) 2.0sec</b>												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ <b>Seqn= 55 1-time(s) 2.0sec</b>												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ <b>Seqn= 15 1-time(s) 2.0sec</b>												
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ <b>Seqn= 79 1-time(s) 2.0sec</b>												
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ <b>Seqn= 23 1-time(s) 2.0sec</b>												
└─ Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs 1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
└─ Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs 1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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## Flare mode

\* \* \* \* \*

NOT USED

\* \* \* \* \*

## Active Region Search

\* \* \* \* \*

NOT USED

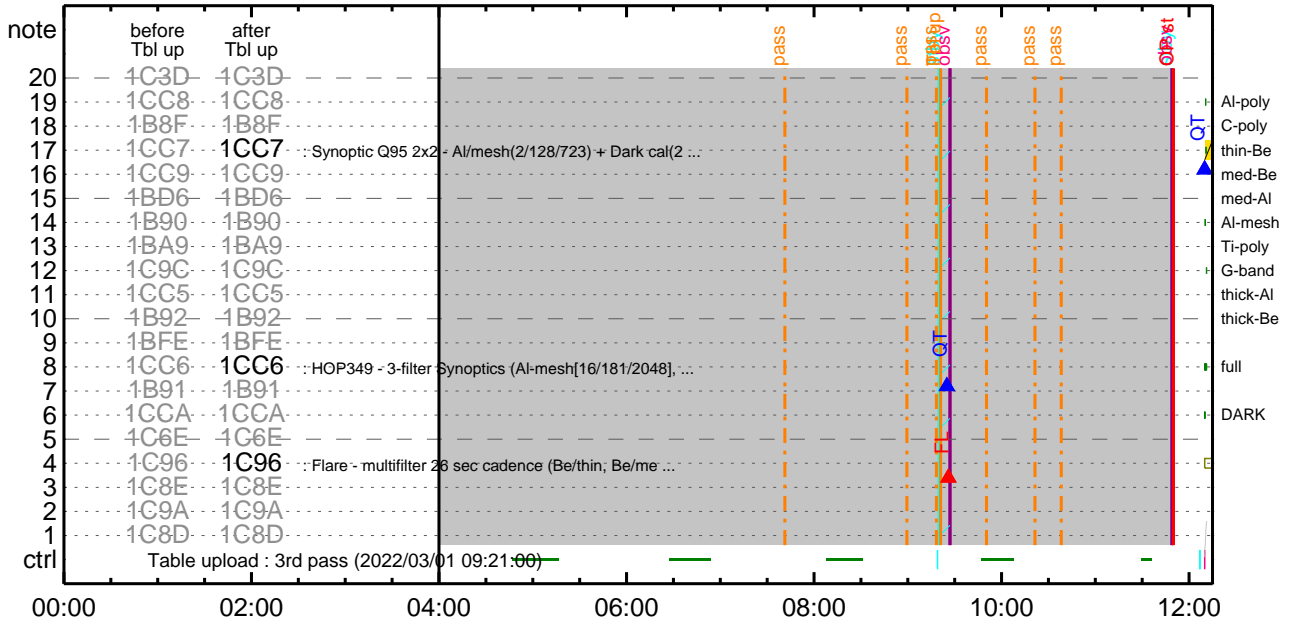
\* \* \* \* \*

## Flare Detection

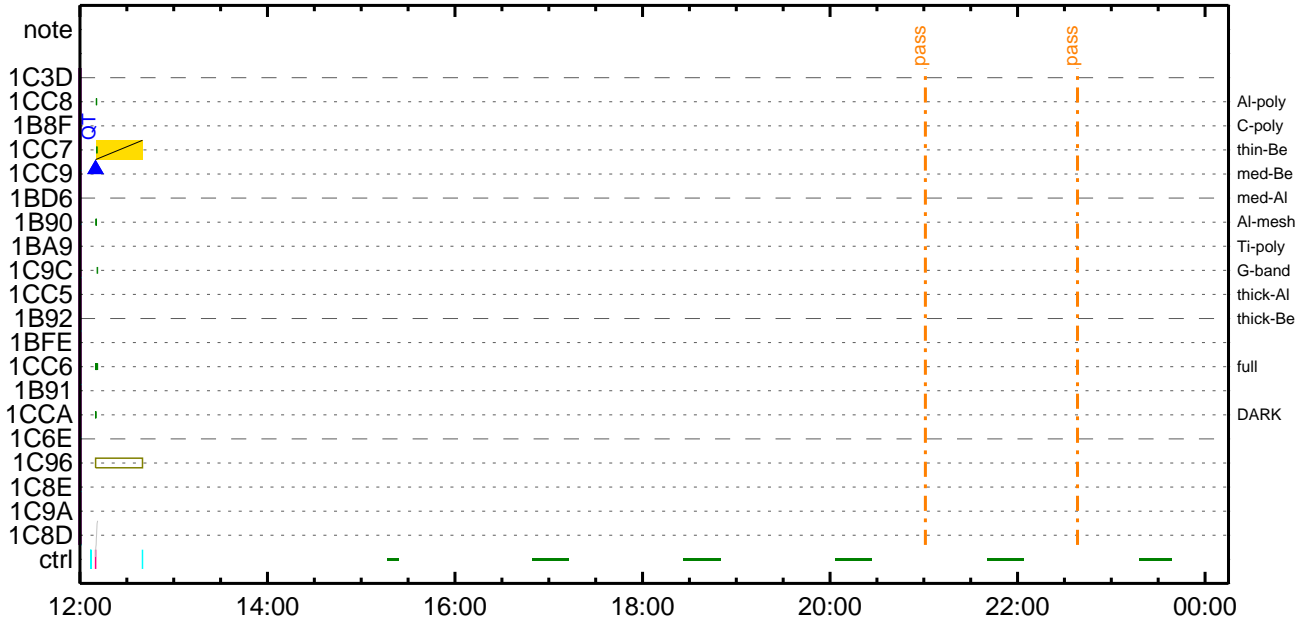
\* \* \* \* \*

FLD Patrol												
Term	Pointing (x, y)							Comment				
03/01 09:22:00 - 03/05 10:57:00 cannot be identified												
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs 8x8		Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

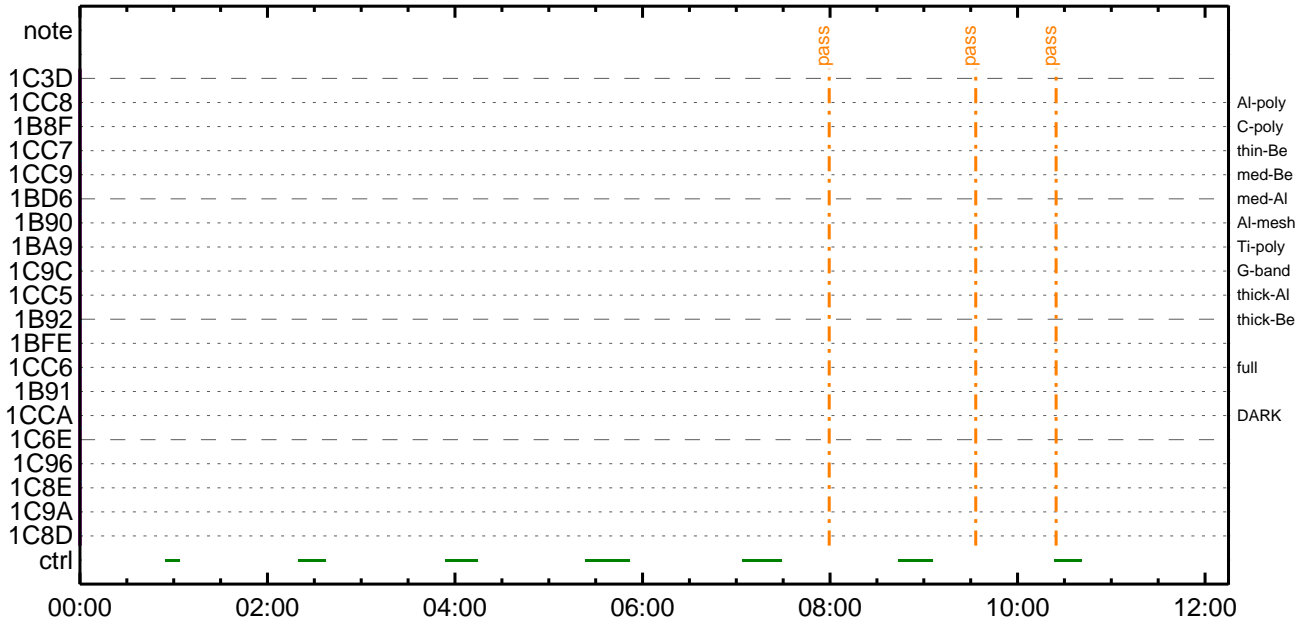
### CMDI #0153 2022/03/01



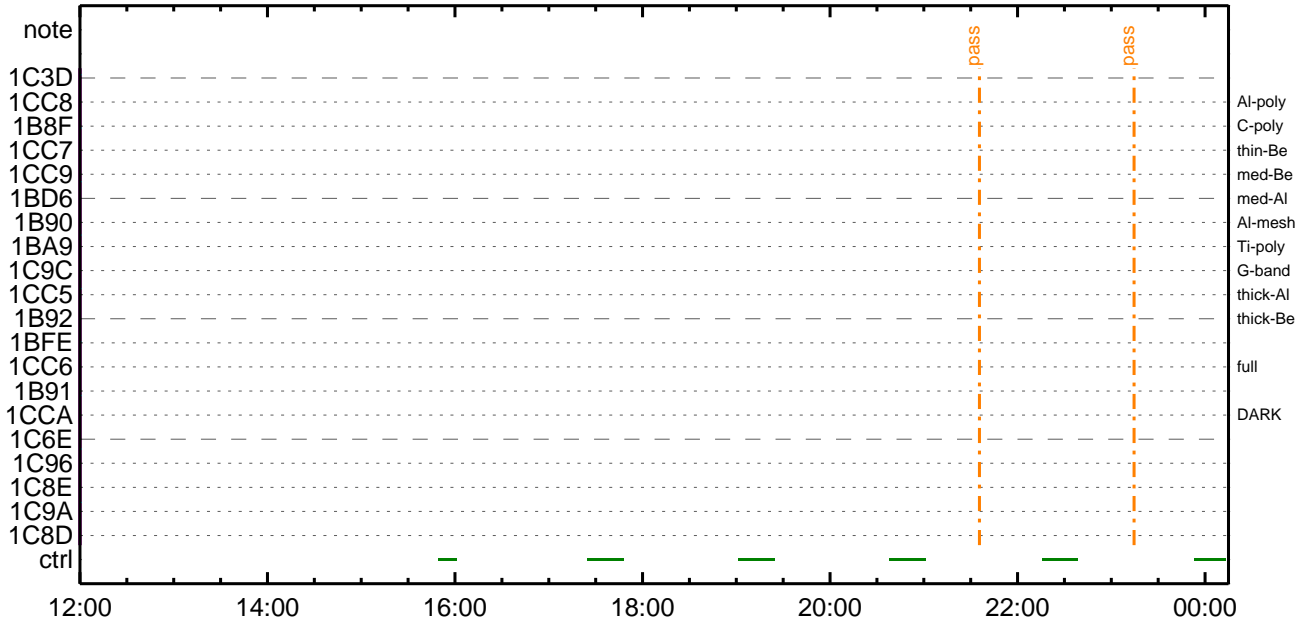
### CMDI #0153 2022/03/01



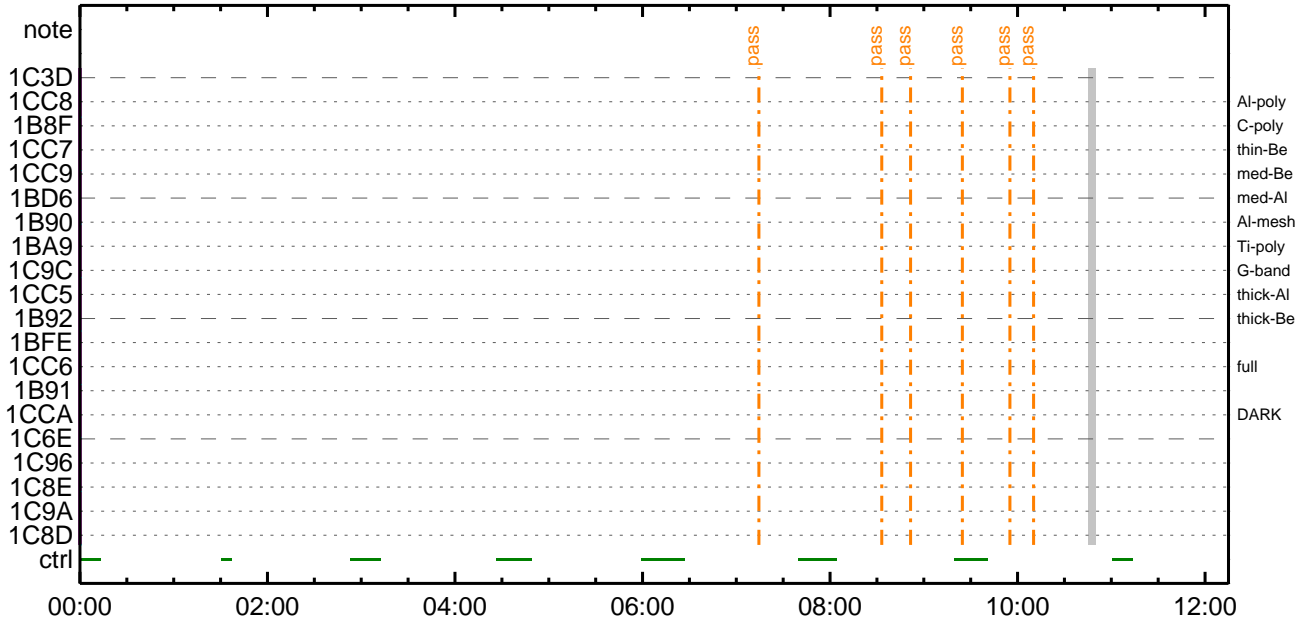
### CMDI #0153 2022/03/02



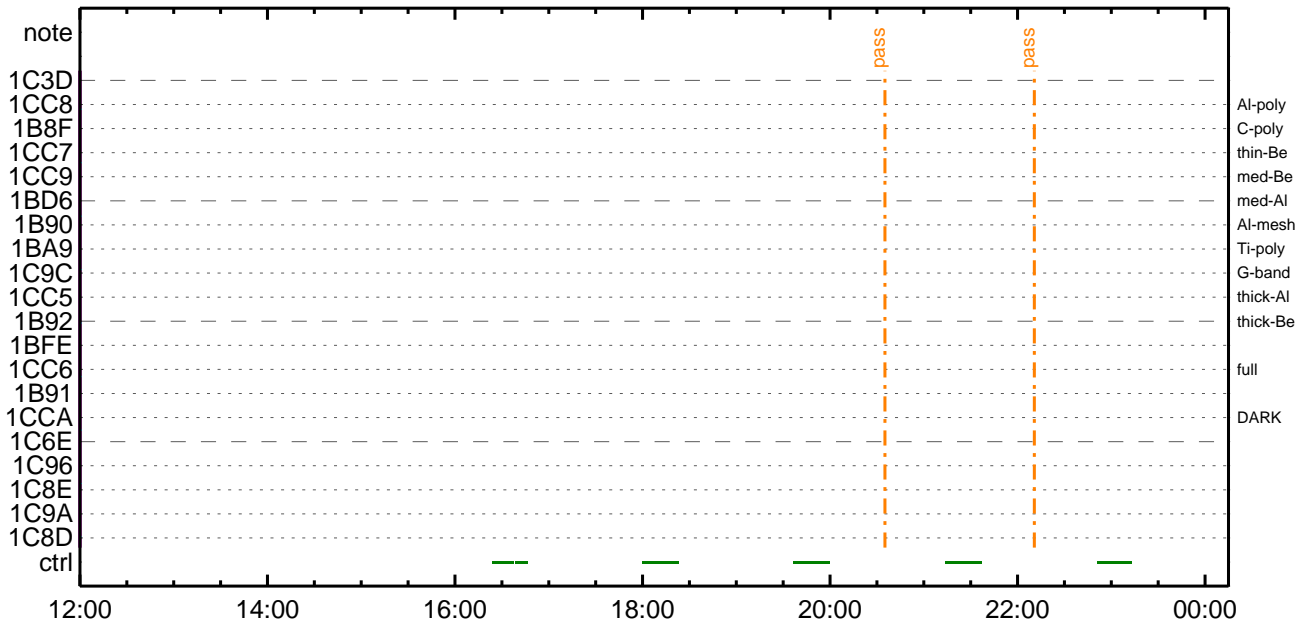
CMDI #0153 2022/03/02



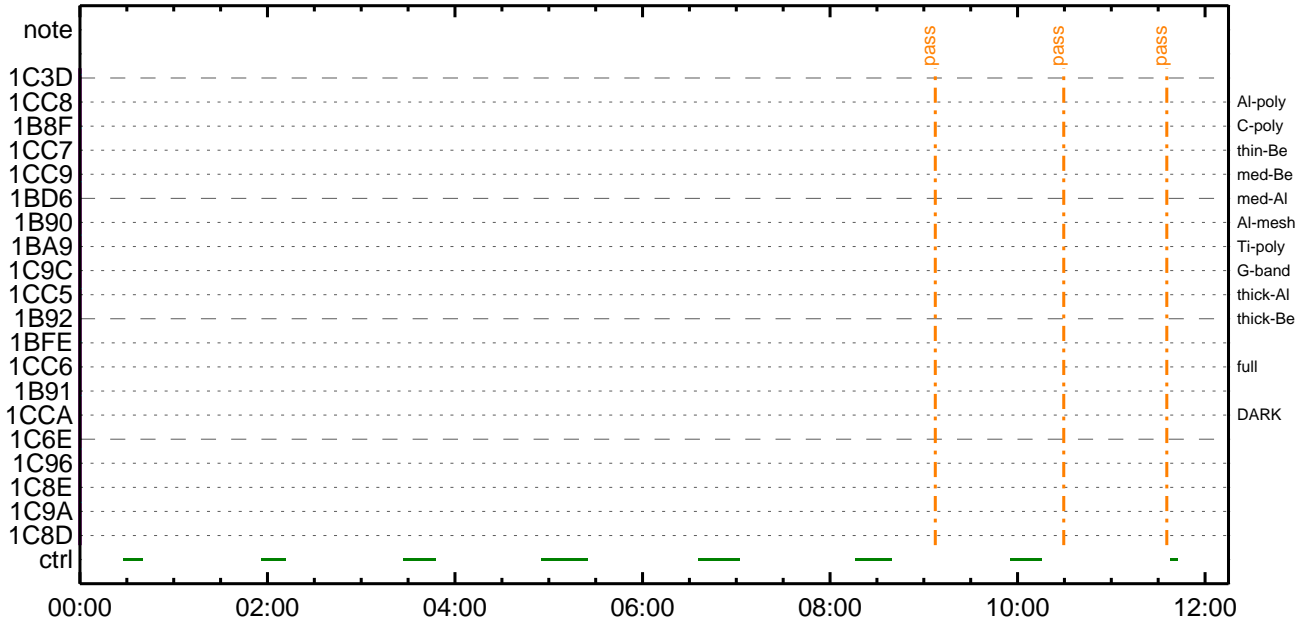
CMDI #0153 2022/03/03



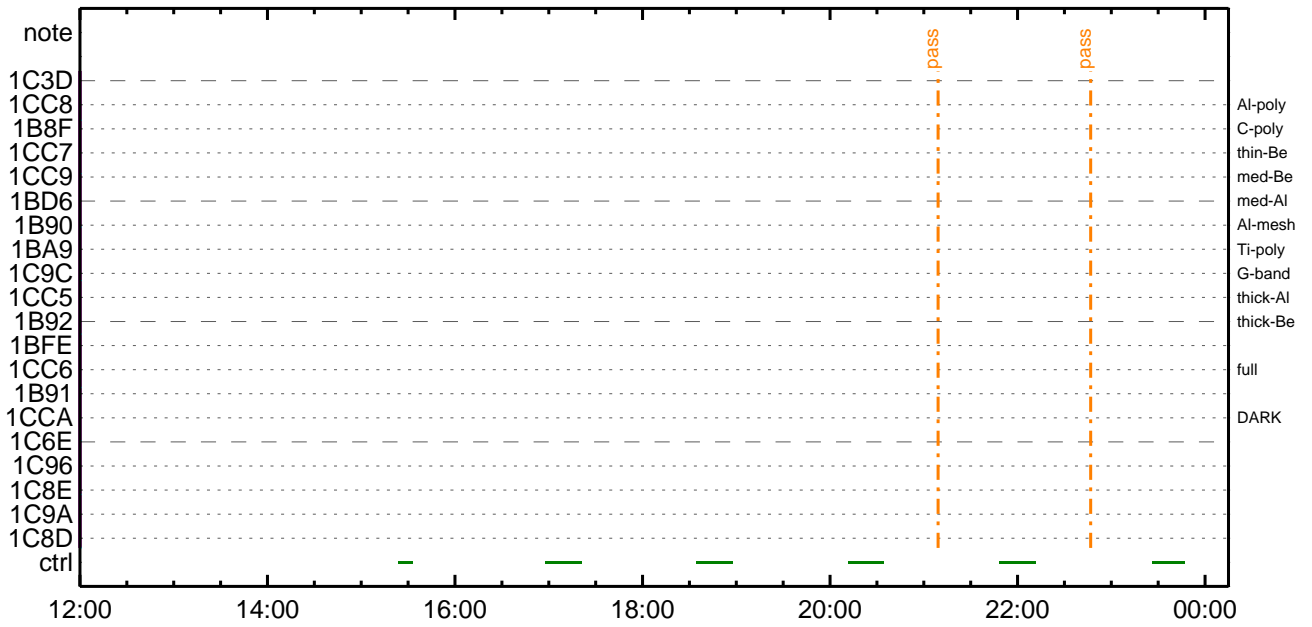
CMDI #0153 2022/03/03



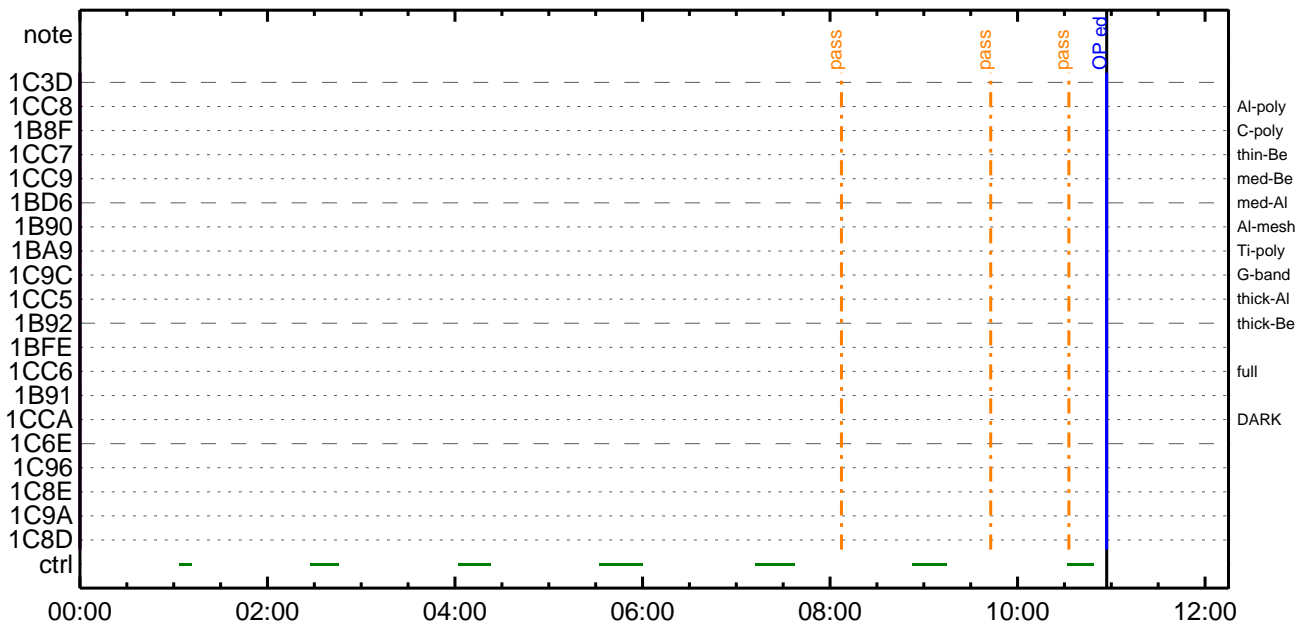
CMDI #0153 2022/03/04



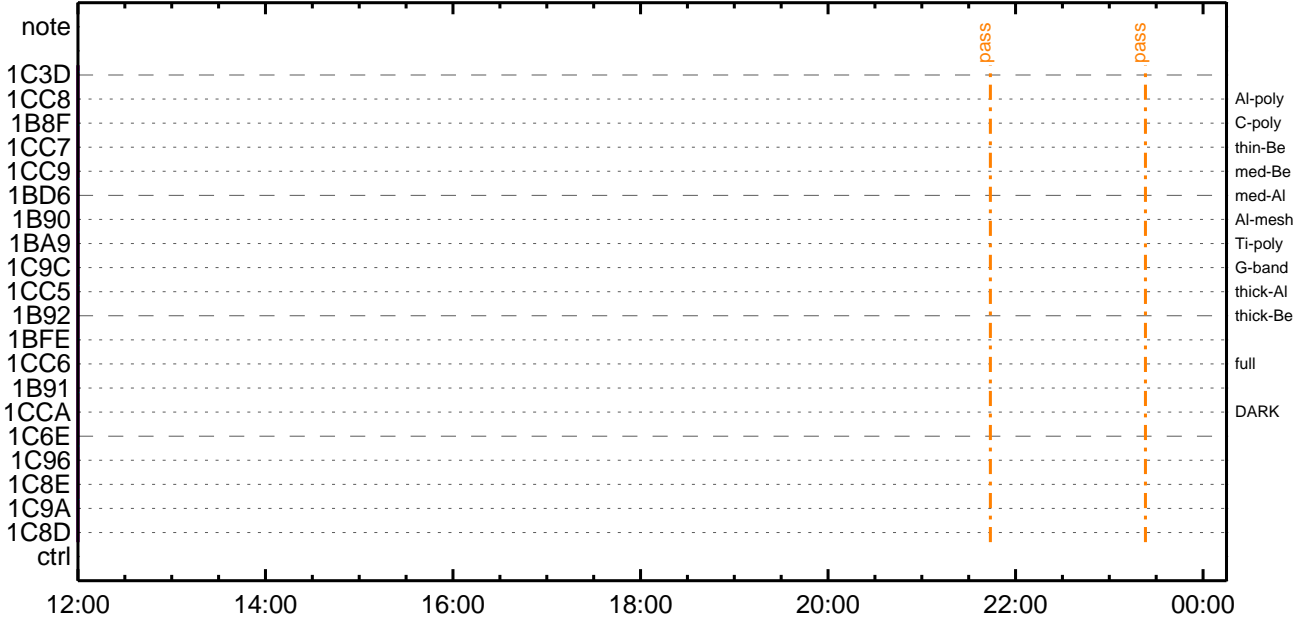
CMDI #0153 2022/03/04



CMDI #0153 2022/03/05



CMDI #0153 2022/03/05



(a) Spacecraft Operation Procedure (real-commands)

```
main-022 2022-03-01 11:25:49 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYóYÉA+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçòÁâ•µ°È»Í×ÁÇçóÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈèÈ¼°ÇÓâ•âç¼l¹ççí;çÀ®, ùâ¹âèçâçÁ+ç®â•âÈççççè;é
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-022:OP
0020 ()
0021 . S. OG og-022:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÁYóY×¼ªªí»òð³íç§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGç¼È¹ç•è²íOKçð³íç§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÁYóY×¼ªªí»òð³íç§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGç¼È¹ç•è²íOKçð³íç§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÁYóY×¼ªªí»òð³íç§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPç¼È¹ç•è²íOKçð³íç§
0081 C.
0082 . C. ***** óÈ²¼ç¼í¼Á´¶¼°çÈÈ-ç¼Á+ç® (¼áµ-YÁYóY×¼è¼ççðÁóÁçç¼ªªç¼°ç¼í¹ççççâ) *****
0083 C. DHUYã;¼YÉ;ÈY¼;Yí;¼YÉ;Èçðíáç¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADç¼Á+ç®ç¼È¹ç¼ç;ç°È²¼ç¼í¼TI-CMDÁ+ç®ç¼È¹ç¼ççççè;é
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0096 C.      0300; SET EDUMP I±°iYNY¹qÇ¹Ôa|a³aE;f
0097 C.
0098 . C.  TIY³YF¥ÖYÉaðÀDİ¿(UT)
0099 +. TI 2022-03-01 11:45:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.      çç[HK1_TI_CMD_NUM]      EQ      1COUNTUP
0102 C.
0103 +. TI 2022-03-01 11:45:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.      çç[HK1_TI_CMD_NUM]      EQ      1COUNTUP
0106 C.
0107 +. TI 2022-03-01 11:45:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.      çç[HK1_TI_CMD_NUM]      EQ      1COUNTUP
0110 C.
0111 +. TI 2022-03-01 11:49:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.      çç[HK1_TI_CMD_NUM]      EQ      1COUNTUP
0114 C.
0115 C.  °E²¼aİÄê%îÍÑaİYÁY§YÁY-¹àİÜ
0116 C.      çç[HK1_TI_CMD_ENA/DIS]    EQ      ENA
0117 C.      çç[HK1_TI_CMD_NUM]      EQ      4
0118 C.      çç[HK1_NEXT_EXEC_PIM]    EQ      DHU
0119 C.      çç[HK1_NEXT_EXEC_DC]    EQ      0xB3
0120 C.
0121 . C.  *****
0122 C.  TIİî°èYÁYÖY×
0123 C.  *****
0124 C.
0125 C.  TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.      çç[HK1_DMP_TOP_ADRS_1]    EQ      07
0129 C.      çç[HK1_DMP_TOP_ADRS_0]    EQ      2B
0130 C.      çç[HK1_DMP_BLOCK_NUM]    EQ      3
0131 C.      çç[HK1_DMP_REPEAT_NUM]   EQ      0
0132 C.      çç[HK1_DMA_DMP_PIM]     EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.      çç[HK1_PKT_FORM_NO]      EQ      7
0136 C.      çç[HK1_PKT_GEN_TIME]     EQ      0.25 s
0137 C.      çç[HK1_S_TLM_BIT_RATE]   EQ      32k
0138 C.      çç[HK1_X_TLM_BIT_RATE]  EQ      4M
0139 C.      çç[HK1_DMP_CHK_FLG]     EQ      EXEC
0140 C.
0141 . C.  YÁYÖY×¼ª¹İ»að³İÇ§
0142 C.      çç[HK1_DMP_CHK_FLG]     EQ      NON
0143 C.
0144 . C.  RAM ID=TI_TBLaİ%È¹Ç•è²İOKað³İÇ§
0145 C.
0146 . C.  DHU¥â;¼YÉ;È¼Y¼,¥İ;¼YÈ;Èaðİáa¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.      çç[HK1_PKT_FORM_NO]      EQ      2
0150 C.      çç[HK1_PKT_GEN_TIME]     EQ      0.5S
0151 C.      çç[HK1_S_TLM_BIT_RATE]   EQ      32K
0152 C.      çç[HK1_X_TLM_BIT_RATE]  EQ      4M
0153 C.
0154 C.  *****
0155 C.  SOT TI command set
0156 C.  *****
0157 C.  Execute, after the success of OP upload.
0158 +. TI 2022-03-01 11:49:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC      (41)
0161 . C.  -----
0162 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0163 C.  -----
0164 C.  ***** SOT END *****
0165 C.
0166 C.  ***** XRT START *****
0167 C.  Execute, after the success of OP upload.
0168 +. TI 2022-03-01 11:49:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC      (c3)
0171 . C.      [ ] [HK1_TI_CMD_NUM]    EQ      1COUNTUP
0172 C.
0173 C.  ***** XRT END *****
0174 . C.  Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C.  ***** Start EIS operation (TI set) *****
0178 C.  Execute, after the success of OP upload.
0179 C.  Set EIS TI-commands
0180 +. TI 2022-03-01 11:49:30.0
0181 DC 07-FC EIS_MODE_MANU
0182 BC      (21 02)
0183 +. TI 2022-03-01 11:49:40.0
0184 DC 07-FC EIS_MODE_CHG_DIS
0185 BC      (22)
0186 . C.      [ ] [HK1_TI_CMD_NUM]    EQ      2 COUNTUP
0187 C.  ***** End EIS operation (TI set) *****
0188 C.
0189 C.
0190 C.
0191 . C.  ***** MDP `ÜÄİaİ»ö¼YaÈÄa¹aèDCBC•x²è *****
0192 C.  (%â°İYÖYÁYÉY¥YáYçYèaÈ¼a¼A»Üa¹aé)
0193 . S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 260571.3 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 12s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2022-03-01 11:49:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÃîî»ö¼ÝðËÄð¹¹èDCBC•x²è *****
0165 C. (¼°îÏÓÿÄÿËÿÏÿËÿÄÿ¼ÿËÿËÿËÿ¼ÿÄÿ»Û¹¹è)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝÐÿ¹•İ Daily±;İÑðË'Ø¹¹èDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLO$ÿÁÿ$ÿÄÿÿ¼Ä»Û;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-024 2022-03-01 11:25:49 126 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁ•µ°È»Í×ÁÇ¿ÍY¿Y×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈÈ¿°ÇÓã•¿¿¼í¹¿¿Í;¿Á®, ù¿¹ãÈ¿ã¿ÇÁ+¿®ã•¿È¿ã¿³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-289:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2022-03-01 11:49:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 06 80 80 20 20)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 07 80 80 08 08)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 08 80 80 20 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 09 80 80 08 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 0f 80 80 06 06)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 10 80 80 08 08)
0084 + DC 07-F0 MDP_XRT_FLD_ENA
0085 BC (d8)
0086 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0087 BC (c8)
0088 + DC 07-F0 MDP_XRT_ARS_DIS
0089 BC (d5)
0090 + DC 07-F0 MDP_XRT_AEC_RESET
0091 BC (d0)
0092 + DC 07-F0 MDP_XRT_FLD_RESET
0093 BC (da)
0094 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0095 BC (c4 08)
```

```
0096 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0097 BC (c5 04)
0098 . C. ----- Success Verify ? OK / NG ____
0099 C.
0100 C.
0101 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0102 C.
0103 +. DC 07-F0 MDP_XRT_MODE_OBSV
0104 BC (c2)
0105 +. TI 2022-03-01 11:49:02.0
0106 DC 07-F0 MDP_XRT_MODE_OBSV
0107 BC (c2)
0108 . C. ----- Success Verify ? OK / NG ____
0109 C.
0110 C. ***** XRT END *****
0111 C.
0112 . C. ***** MDP `ûÃîñî»ö¼ÝñÈÃÐñ¹ñèDCBC•x²è *****
0113 C. (¼ã°îÿÓÿÃÿÈÿPÿÿÈÿãÿçÿèñ¼¼ã¼Ã»Ûñ¹ñè)
0114 . S. DC-BC dcbc-402:DCBC
0115 (MDP_known_event)
0116 C.
0117 C.
0118 . C. ***** ÝDÿ¹•İ Daily±;îññÈ´Øñ¹ñèDCBC•x²è *****
0119 . S. DC-BC dcbc-153:DCBC
0120 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0121 C.
0122 C.
0123 . C. ;ãLOSÿÃÿSÿÿÃÿ-¼Ã»Û;ã
0124 C.
0125 . C. ***** LOS *****
0126 C.
```





(a) Spacecraft Operation Procedure (real-commands)

```
main-026 2022-03-01 11:25:49 137 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 . C.
0006 . C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 . C. Áí;È¿µÁµ•µ°È»Í×ÁÇµÍYÇYÁY×YÍ;¼YÉ;ÈÈ%µ•íÉ;ÈÈ¼°ÇÓµ•µ¿¼í¹ÇµÍ;ÇÀ®, ùµ¹µÈµµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. ===== Begin of AOCs CMD Sequence =====
0015 . C.
0016 . C. *****
0017 . C. ***** No. 2: YDÝµYÇY¹YÁ;¼YÁYÓY¿YàÀBÁÈ-2 *****
0018 . C. *****
0019 . C.
0020 . C. *****
0021 . C. MWÛÛÉ,²óÁ¼¿òÁpÁf³ÍÇŞ
0022 . C. *****
0023 . C.
0024 . C. ;ú ²¼µ-¾òÁÔµÇµçµÈµ³µÈµò³ÍÇŞµ¹µÈµ³µÈ;f
0025 . C.
0026 . C. [ ] <STS4> [MW]<SPEED> A = 1800 +- 20 ?
0027 . C. [ ] <STS4> [MW]<SPEED> B = 1800 +- 20 ?
0028 . C. [ ] <STS4> [MW]<SPEED> C = -1800 +- 20 ?
0029 . C. [ ] <STS4> [MW]<SPEED> D = -1800 +- 20 ?
0030 . C.
0031 . C.
0032 . C. *****
0033 . C. MSAÁ«°Û
0034 . C. *****
0035 . C.
0036 +. DC 02-75 AOCU_MSA
0037 BC (00 00 00 00 00)
0038 . C. [ ] <A_AOS> [CNT MODE] = MSA ?
0039 . C. ;ú»ÑÀªÀ°ÀÈÁÔµÁ
0040 . C.
0041 . C.
0042 . C. *****
0043 . C. IRU-B HIÁÛµèÊÁµµ
0044 . C. *****
0045 . C.
0046 +. DC 02-AD AOCU_IRU-B1_HI
0047 +. DC 02-B2 AOCU_IRU-B2_HI
0048 . C. [ ] <A_AOS> [COMPONENT STS] <IRU> B1 = ON/HI ?
0049 . C. [ ] <A_AOS> [COMPONENT STS] <IRU> B2 = ON/HI ?
0050 . C. ;ú»ÑÀªÀ°ÀÈÁÔµÁ
0051 . C.
0052 . C.
0053 . C. *****
0054 . C. FDIR ÀBÁÈÛáµ•
0055 . C. *****
0056 . C.
0057 +. DC 02-50 AOCU_SUNANG_CHK_ENA
0058 BC (86)
0059 +. DC 02-50 AOCU_MW_SPDCHK_ENA
0060 BC (8c)
0061 +. DC 02-50 AOCU_MWA_HCHK_ENA
0062 BC (96)
0063 +. DC 02-50 AOCU_RATE_CHK_ENA
0064 BC (8e)
0065 . C. [ ] <A_AOS> [FDIR] SANG = ENA ?
0066 . C. [ ] <A_AOS> [FDIR] MWSP = ENA ?
0067 . C. [ ] <A_AOS> [FDIR] MW_H = ENA ?
0068 . C. [ ] <A_AOS> [FDIR] RATE = ENA ?
0069 . C.
0070 . C.
0071 . C. *****
0072 . C. LV-2,4 CLOSE;çVDRV OFF
0073 . C. *****
0074 . C.
0075 +. DC 02-3B AOCU_LV-2_CLOSE
0076 +. DC 02-3F AOCU_LV-4_CLOSE
0077 +. DC 02-37 AOCU_VDRV_OFF
0078 . C. [ ] <A_AOS> [COMPONENT STS] LV2 = CLOSE ?
0079 . C. [ ] <A_AOS> [COMPONENT STS] LV4 = CLOSE ?
0080 . C. [ ] <A_AOS> [COMPONENT STS] <VDRV> = OFF ?
0081 . C.
0082 . C.
0083 . C. *****
0084 . C. TLM FMT = 2Ûáµ•
0085 . C. *****
0086 . C.
0087 . C. çf²¼µ-2CMDµÍµÈµÁµéµ«ÁªÁðµ•µÈÁ÷¿®µ¹µÈµ³µÈ
0088 . C. <USCÍÑ>
0089 +. DC 01-22 DHU_MODE_CHNG
0090 BC (02 0a fb)
0091 . C. <GNÍÑ>
0092 +. DC 01-22 DHU_MODE_CHNG
0093 BC (02 0a f8)
0094 . C. [ ] <PRM2> [TLM STS] FMT = 2?
0095 . C.
```

```

0096 . C.
0097 . C. *****
0098 . C. Z»ÑÀª•èÄéíÑÇ;¼ÿ¿¼èÈÀ
0099 . C. *****
0100 . C.
0101 . C. ***** GASÿàÿËÿ¿¼î¼¼á MTQ¶íÈººi»þÃã»ß *****
0102 +. DC 02-33 AOCU_MDRV-X_OFF
0103 +. DC 02-34 AOCU_MDRV-Y_OFF
0104 +. DC 02-35 AOCU_MDRV-Z_OFF
0105 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> X = OFF ?
0106 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = OFF ?
0107 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = OFF ?
0108 . C.
0109 . C.
0110 . C. ¡üÿÇ;¼ÿ¿¼èÈÀ¼î¼¼á;çÌó¼minÂÔµ¡
0111 . C.
0112 . C.
0113 . C. ***** MTQ¶íÈººE³« *****
0114 +. DC 02-32 AOCU_MDRV_ON
0115 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> X = ON ?
0116 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = ON ?
0117 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = ON ?
0118 . C.
0119 . C.
0120 . C. ===== End of AOCs CMD Sequence =====
0121 . C.
0122 . C.
0123 . C. ***** MDP `úÃí¼í»ö¼ÿ¼èÈÀ¼¼¼èDCBC•x²è *****
0124 . C. (¼áºìÿÓÿÃÿÈÿ¼ÿËÿ¼ÿáÿçÿè¼¼¼¼¼»¼¼¼¼¼»¼¼¼¼¼)
0125 . S. DC-BC dcbc-402:DCBC
0126 (MDP_known_event)
0127 . C.
0128 . C.
0129 . C. ***** ÿDÿ¹•Ï Daily¼¼íÑ¼è´Ø¼¼¼èDCBC•x²è *****
0130 . S. DC-BC dcbc-153:DCBC
0131 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0132 . C.
0133 . C.
0134 . C. ¡ãLOSÿÃÿÿÿÿÿ¼¼¼»¼¼¼¼¼»¼¼¼¼¼
0135 . C.
0136 . C. ***** LOS *****
0137 . C.

```





```
0096 . C.
0097 . C.
0098 . C. ===== Begin of APCS CMD Sequence =====
0099 . C.
0100 . C. *****
0101 . C. ***** GASŸÇ;¼Ÿç¼èÆÀ¼Ä»Ü *****
0102 . C. *****
0103 . C.
0104 . C. *****
0105 . C. MDRV OFF
0106 . C. *****
0107 . C.
0108 . C. ***** GASŸâŸÈŸç¼î¼ç¼á MTQŸ¼Ÿ°°i»þÃã»ß *****
0109 +. DC 02-33 AOCU_MDRV-X_OFF
0110 +. DC 02-34 AOCU_MDRV-Y_OFF
0111 +. DC 02-35 AOCU_MDRV-Z_OFF
0112 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> X = OFF ?
0113 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = OFF ?
0114 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = OFF ?
0115 . C.
0116 . C.
0117 . C. ;úŸÇ;¼Ÿç¼èÆÀ¼î¼ç¼á;ç¼î¼minÃÔµ;
0118 . C.
0119 . C. *****
0120 . C. MDRV ON
0121 . C. *****
0122 . C.
0123 . C. ***** MTQŸ¼Ÿ°°E³« *****
0124 +. DC 02-32 AOCU_MDRV_ON
0125 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> X = ON ?
0126 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = ON ?
0127 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = ON ?
0128 . C.
0129 . C.
0130 . C. ===== End of APCS CMD Sequence =====
0131 . C.
0132 . C.
0133 . C. ***** MDP ´ûÃî¼î»ô¼Ÿ¼èÆÀ¼¼èDCBC•x²è *****
0134 . C. (¼á°îŸÓŸÄŸÈŸþŸÈŸáŸçŸè¼¼ç¼á»Ü¼¼è)
0135 . S. DC-BC dcbc-402:DCBC
0136 (MDP_known_event)
0137 . C.
0138 . C.
0139 . C. ***** ŸDŸ¹•İ Daily±;¼î¼è´Ø¼¼èDCBC•x²è *****
0140 . S. DC-BC dcbc-153:DCBC
0141 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0142 . C.
0143 . C.
0144 . C. ;ãLOSŸÄŸSŸÄŸ¼Ä»Ü;ã
0145 . C.
0146 . C. ***** LOS *****
0147 . C.
```

Mar 01, 22 11:25

XRT\_OGLIST\_0153.chk

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\*\*\* OP Sequence for XRT \*\*\*

2022/03/01	12:06:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/03/01	12:06:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/03/01	12:06:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2022/03/01	12:07:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2022/03/01	12:07:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2022/03/01	12:07:22.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2022/03/01	12:09:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2022/03/01	12:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/03/01	12:40:01.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1