XRT Timeline to be uploaded on 2022/03/03

Period: 2022/03/03 10:50:00 - 2022/03/08 11:05:00

Normal mode

XOB #1CC7: Synoptic Q95 2x2 - Al/mesh(2/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Thi

Term	Pointing (x, y)	Comment
03/03 11:30:00 - 03/03 12:00:00	cannot be identified	

PROG= 17 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 55 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 15 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 79 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Flare mode

NOT USED

Active Region Search

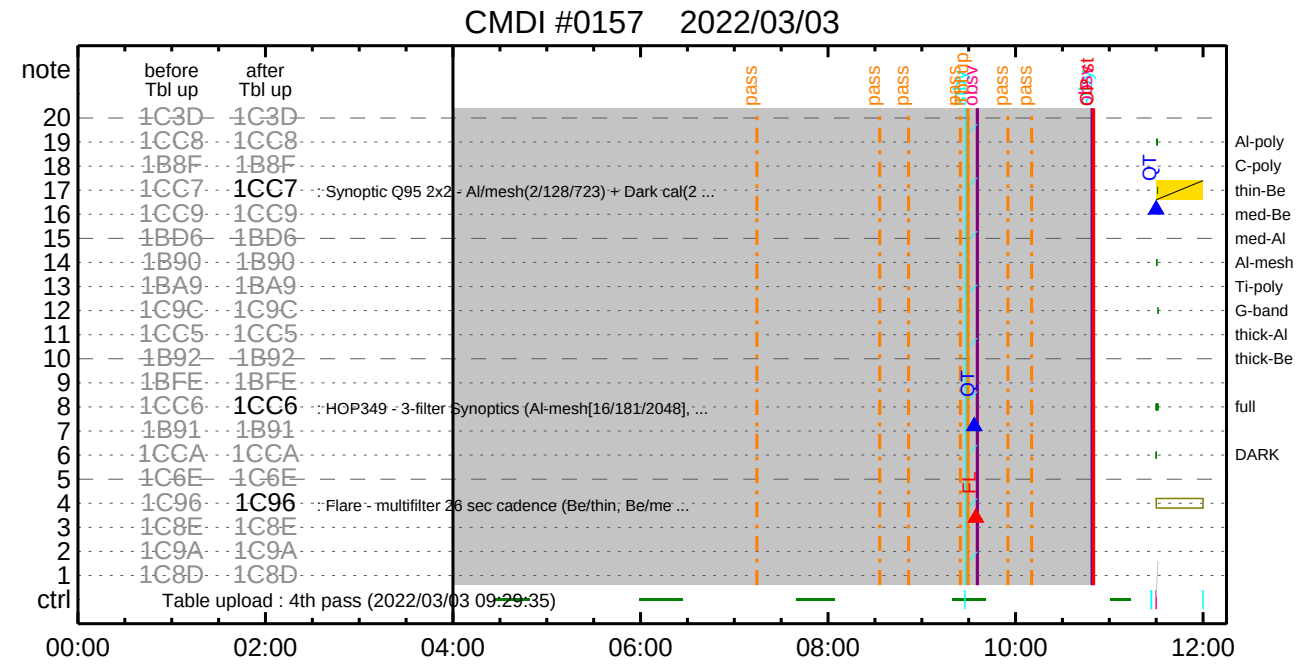
NOT USED

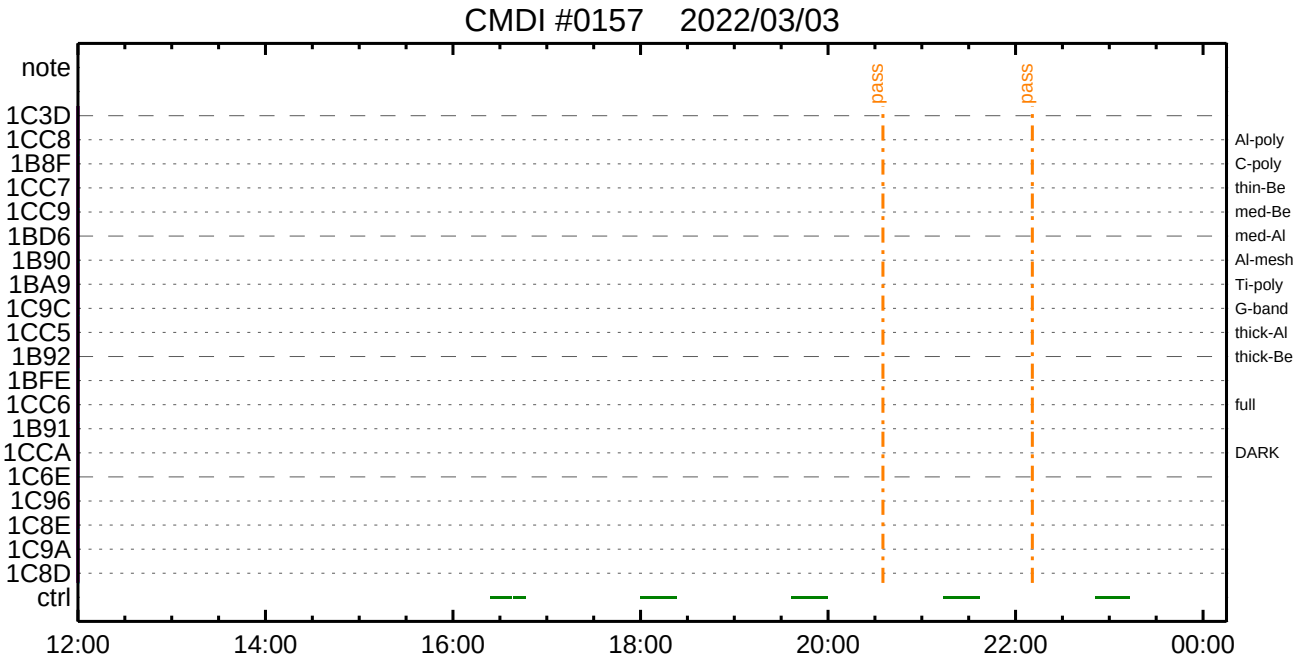
Flare Detection

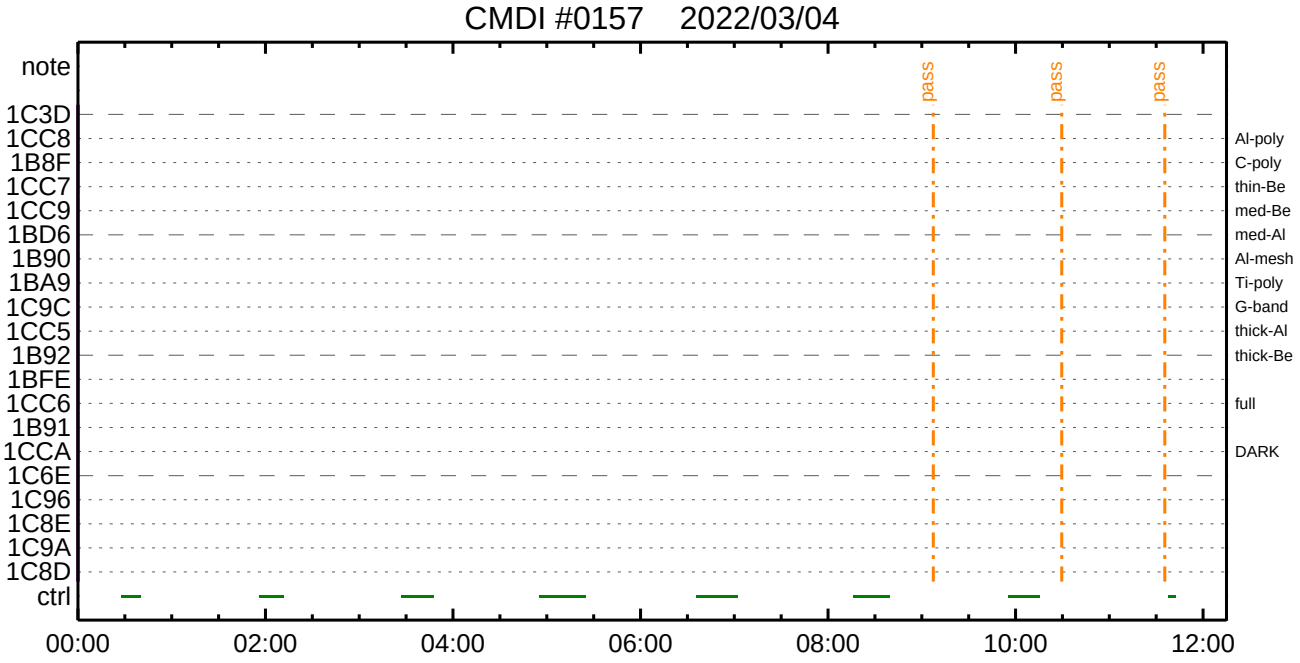
FLD Patrol

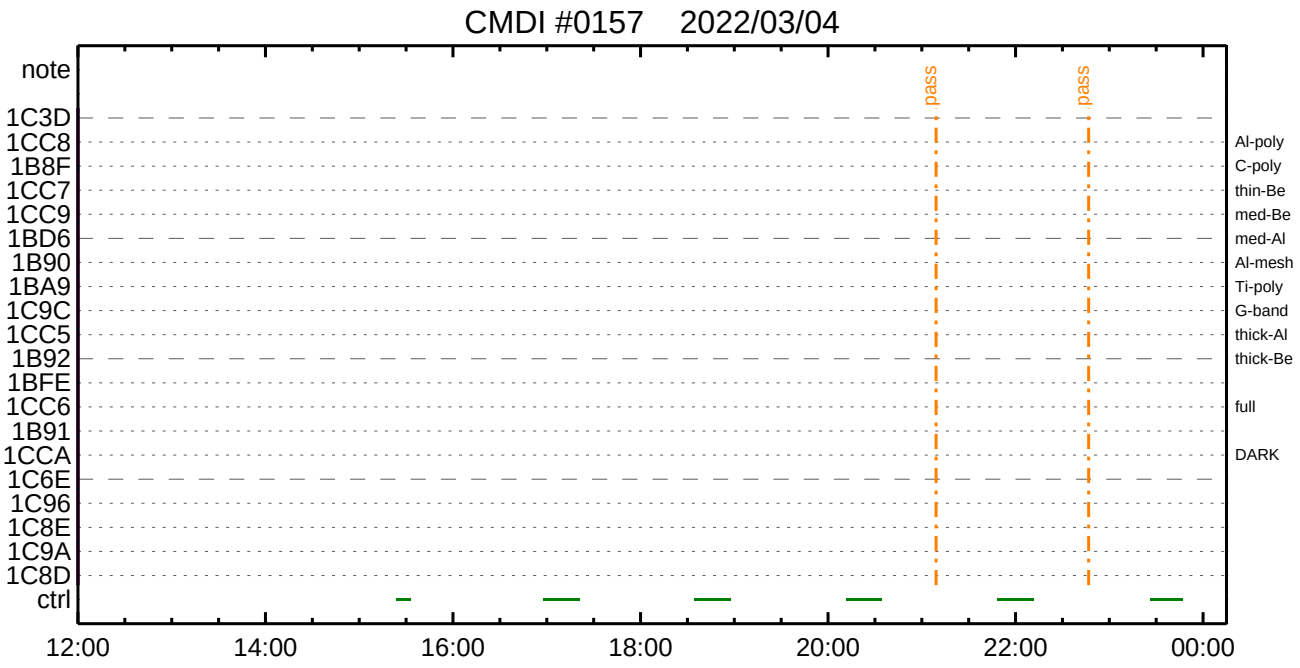
Term	Pointing (x, y)	Comment
03/03 09:30:35 - 03/08 11:05:00	cannot be identified	

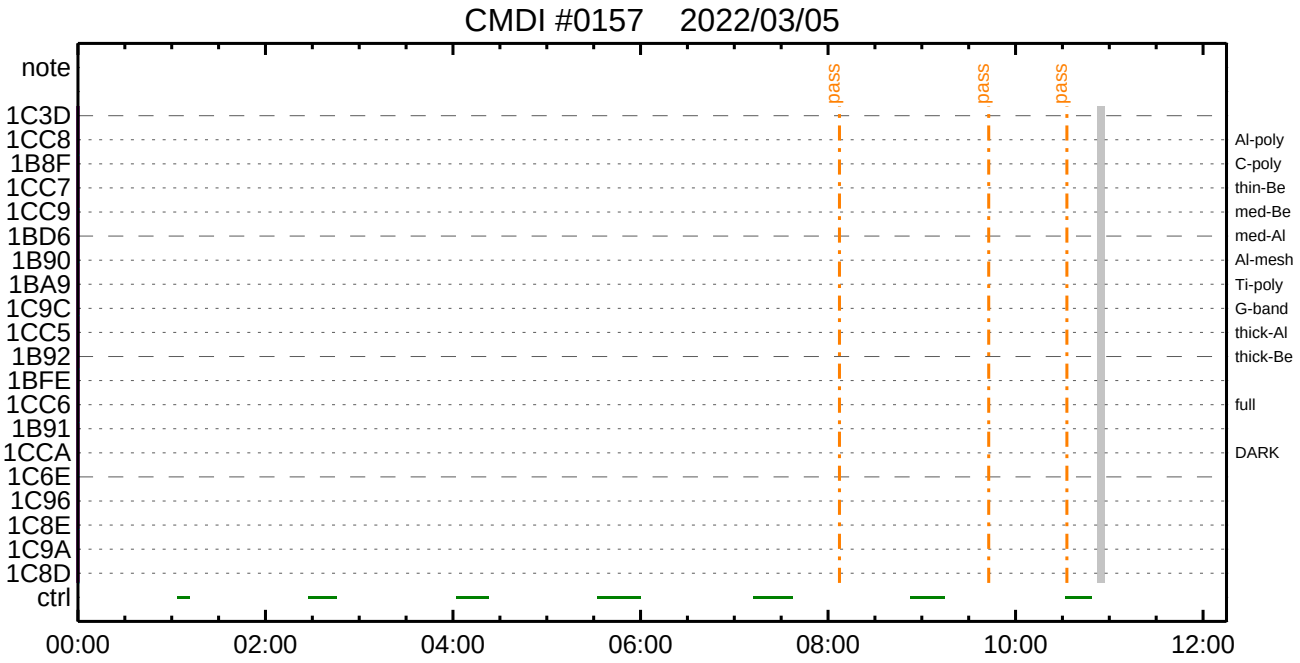
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec

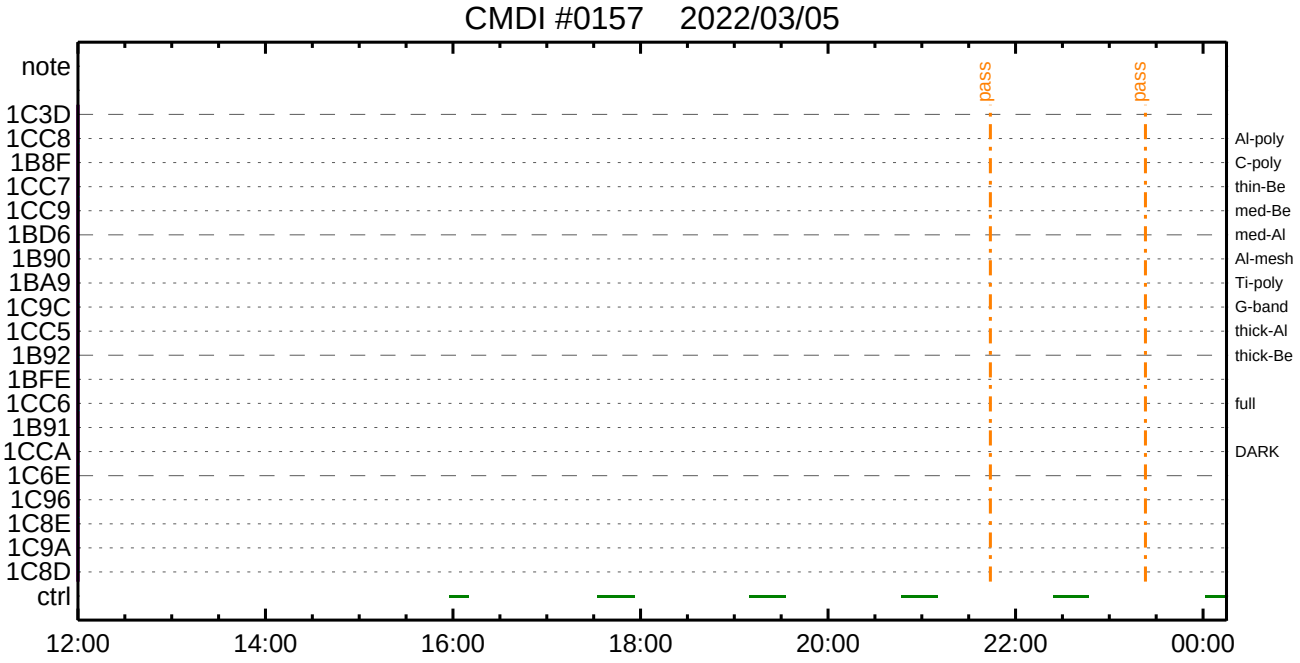

CMDI #0157 2022/03/03
note
before Tbl up
after Tbl up
20 1C3D 1C3D
19 1CC8 1CC8
18 1B8F 1B8F
17 1CC7 1CC7
16 1CC9 1CC9
15 1BD6 1BD6
14 1B90 1B90
13 1BA9 1BA9
12 1C9C 1C9C
11 1CC5 1CC5
10 1B92 1B92
9 1BFE 1BFE
8 1CC6 1CC6
7 1B91 1B91
6 1CCA 1CCA
5 1C6E 1C6E
4 1C96 1C96
3 1C8E 1C8E
2 1C9A 1C9A
1 1C8D 1C8D
ctrl
: Synoptic Q95 2x2 - Al/mesh(2/128/723) + Dark cal(2 ...
: HOP349 - 3-filter Synoptics (Al-mesh[16/181/2048], ...
: Flare - multifilter 26 sec cadence (Be/thin; Be/me ...
Table upload : 4th pass (2022/03/03 09:29:35)
pass
obsv
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

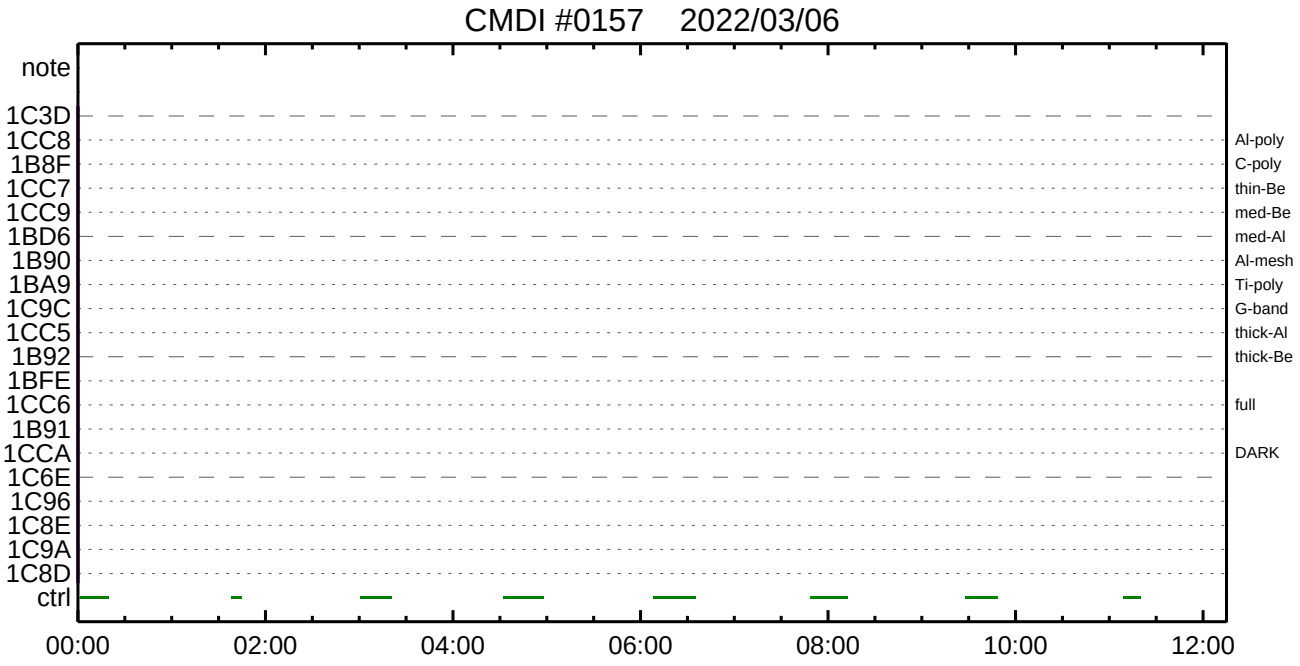

CMDI #0157 2022/03/03
note
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

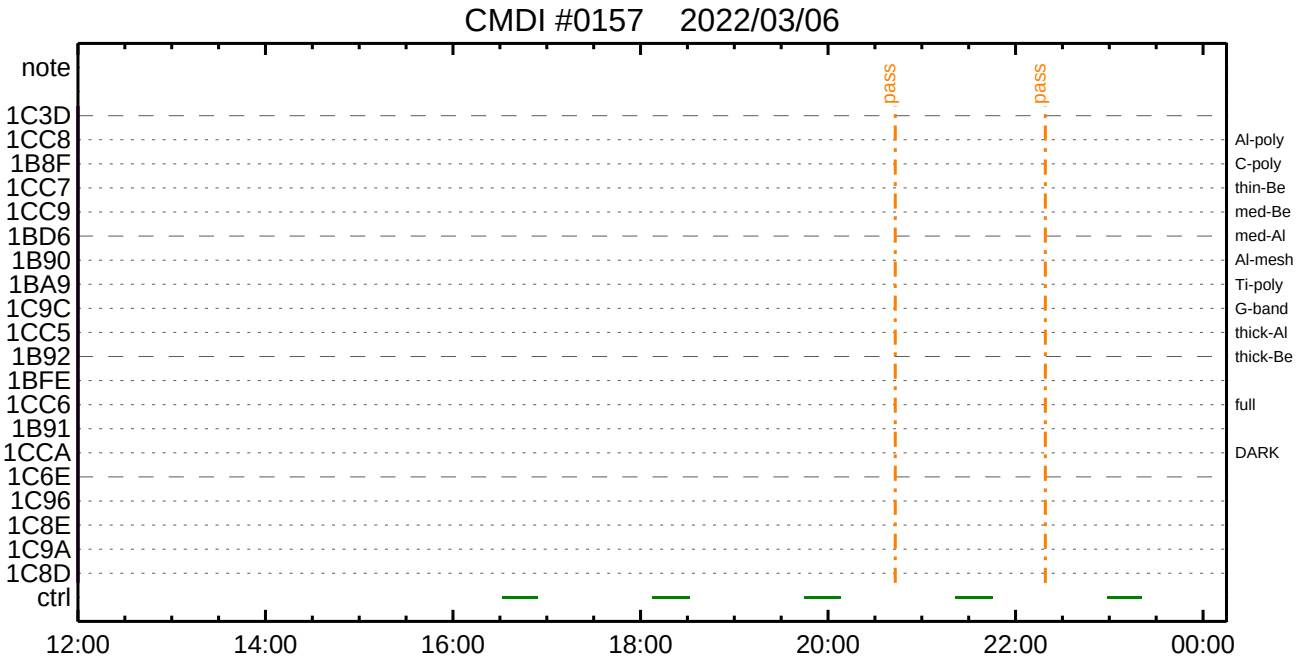

CMDI #0157 2022/03/04
note
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
pass
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

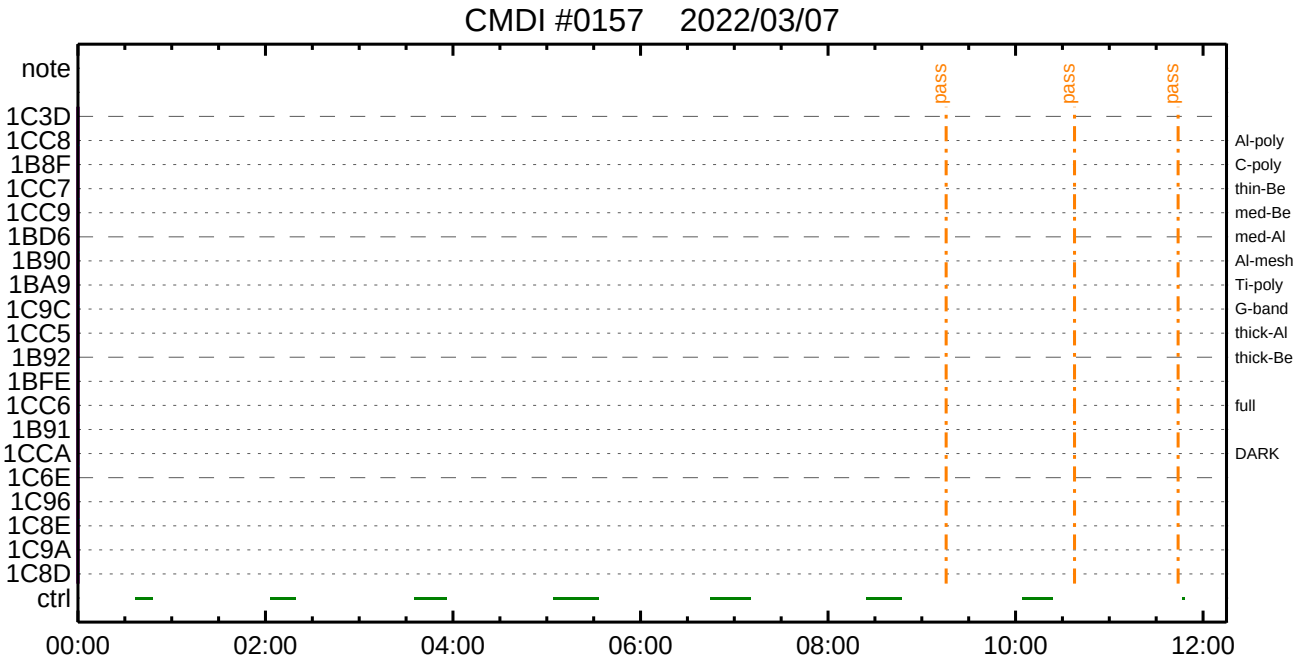

CMDI #0157 2022/03/04
note
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

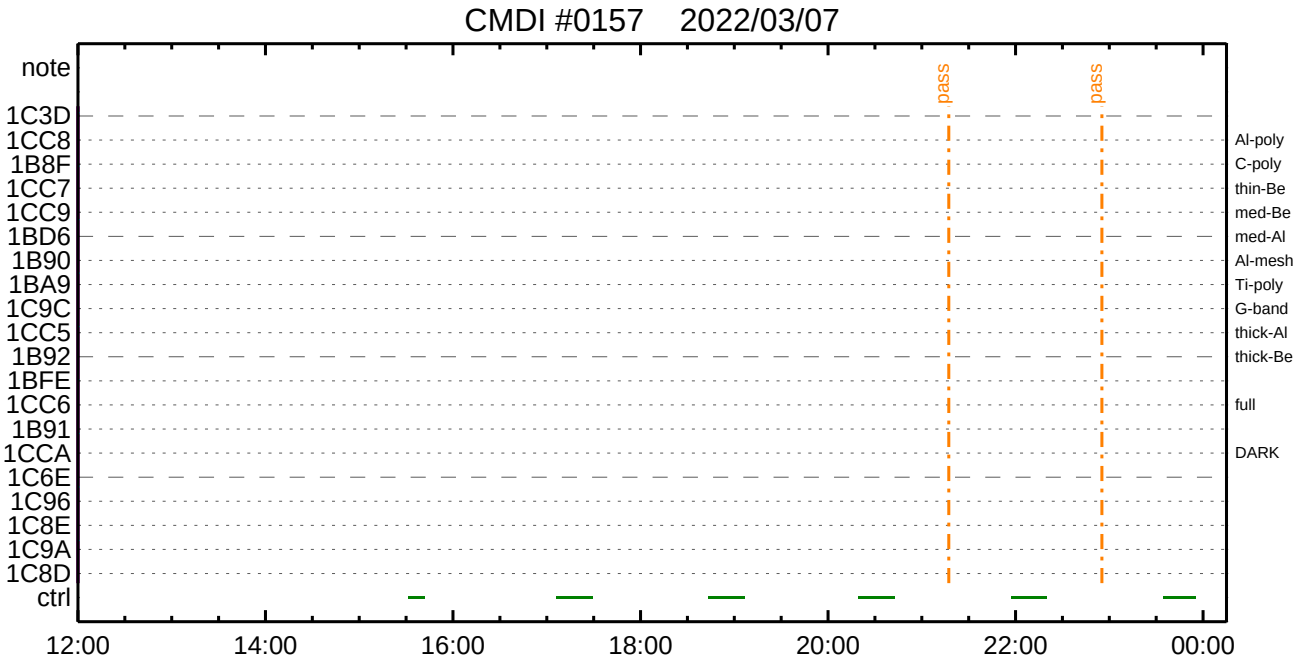

CMDI #0157 2022/03/05
note
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
pass
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

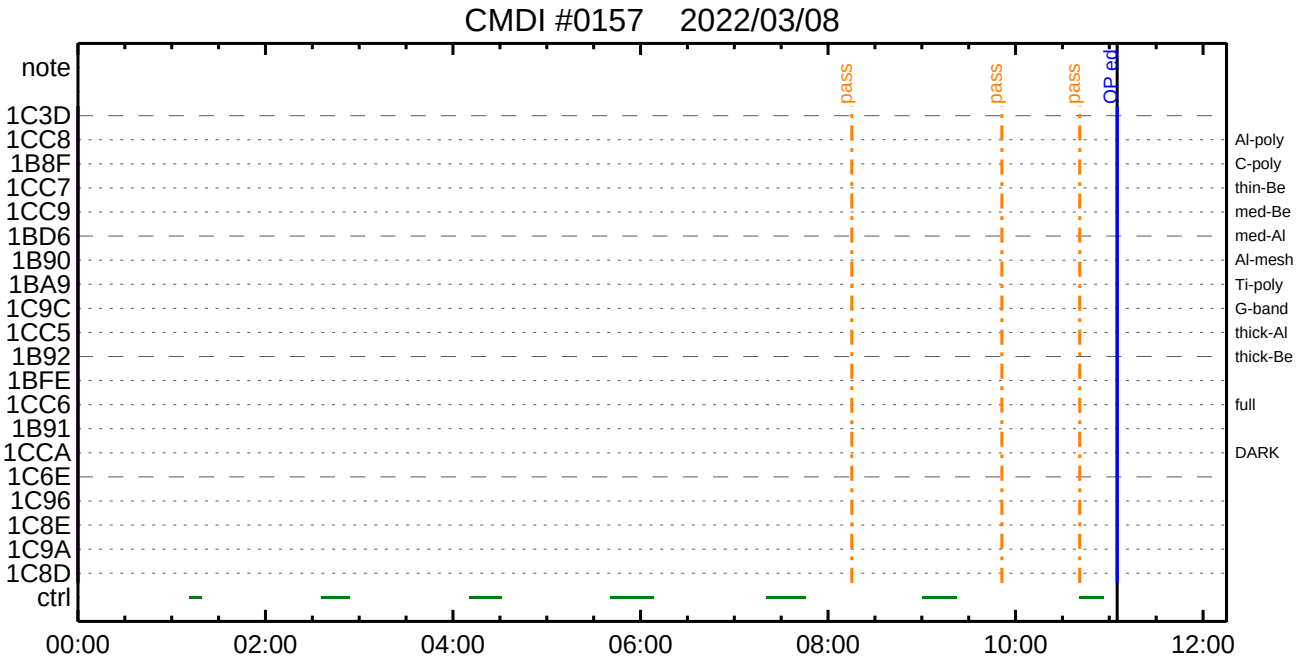

CMDI #0157 2022/03/05
note
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

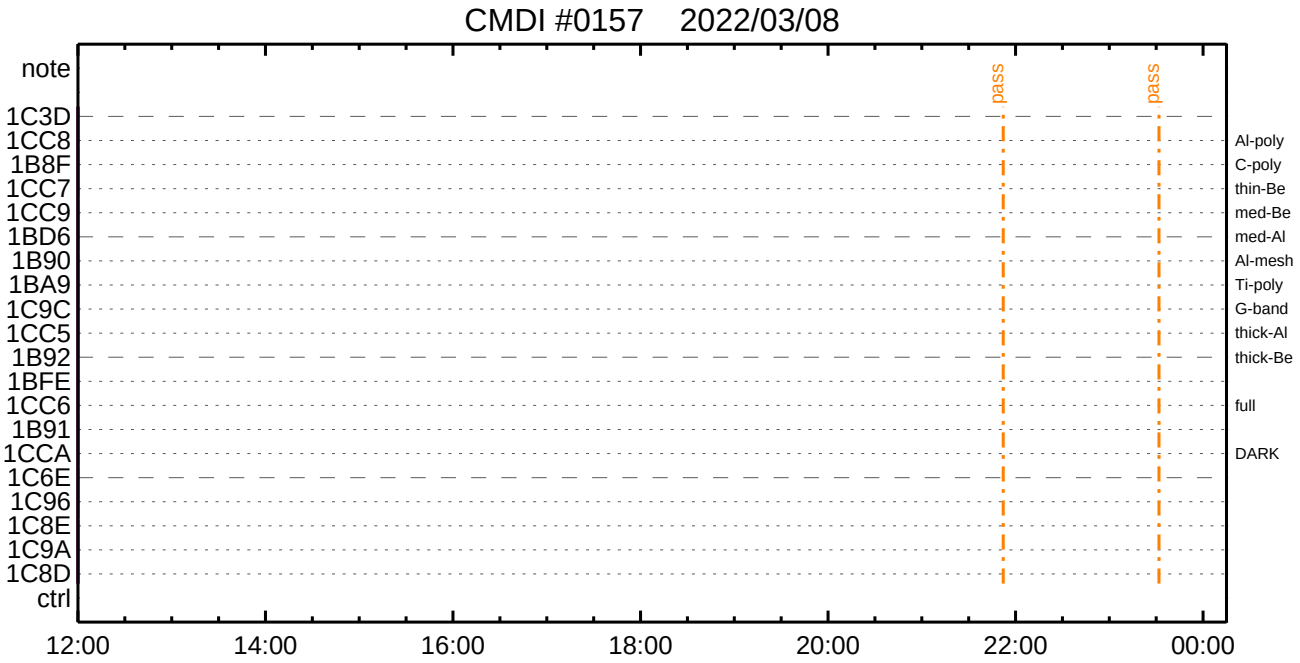

CMDI #0157 2022/03/06
note
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0157 2022/03/06
note
pass
pass
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0157 2022/03/07
note
pass
pass
pass
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0157 2022/03/07
note
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0157 2022/03/08
note
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0157 2022/03/08
note
1C3D
1CC8
1B8F
1CC7
1CC9
1BD6
1B90
1BA9
1C9C
1CC5
1B92
1BFE
1CC6
1B91
1CCA
1C6E
1C96
1C8E
1C9A
1C8D
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-031 2022-03-03 11:21:33 194 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-031:OP
0020     ()
0021   . S. OG    og-031:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
    C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
    C.
  . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
 +. TI 2022-03-03 10:45:00.0
    DC 01-B3 DHU_OP_STOP
    C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2022-03-03 10:45:01.0
    DC 01-B4 DHU_OP_COPY
    C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2022-03-03 10:45:01.0
    DC 01-B5 DHU_OPOG_COPY
    C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2022-03-03 10:49:59.5
    DC 01-B2 DHU_OP_START
    C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
    C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
    C.                       ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
    C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     4
    C.                       ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
    C.                       ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
    C.
  . C. ****************************
    C. TIÎÎ°è¥À¥ó¥×
    C. ****************************
    C.
    C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC        (03 ab 03 01 02)
    C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
    C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
    C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
    C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC        (07 0b f8)
    C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
    C.
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
    C.
  . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
  . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
 +. DC 01-22 DHU_MODE_CHNG
    BC        (02 0a f8)
    C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
    C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
    C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
    C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.
    C.
    C. ************ XRT START ***********
    C. Execute, after the success of OP upload.
 +. TI 2022-03-03 10:49:00.0
    DC 07-F0 MDP_XRT_MODE_STBY
    BC        (c3)
  . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
    C.
    C. ************ XRT END ***********
  . C. Stop EIS observation and temporarily disable EIS mode changes
    C.
    C.
    C. ********** Start EIS operation (TI set) **********
    C. Execute, after the success of OP upload.
    C. Set EIS TI-commands
 +. TI 2022-03-03 10:49:30.0
    DC 07-FC EIS_MODE_MANU
    BC        (21 02)
 +. TI 2022-03-03 10:49:40.0
    DC 07-FC EIS_MODE_CHG_DIS
    BC        (22)
  . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
    C. *********** End EIS operation (TI set) ***********
    C.
    C.
    C.
  . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
    C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
  . S. DC-BC dcbc-402:DCBC
    (MDP_known_event)
    C.
    C.
  . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
  . S. DC-BC dcbc-153:DCBC
    (SPECIAL-CMD_DAILY_OPERATIN_DCB)
    C.
    C.
  . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
    C.
  . C. *********** LOS ***********
```

0194 C.

```
(a) Spacecraft Operation Procedure (real-commands)

main-032 2022-03-03 11:21:33 169 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106  +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113  +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122  +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127  +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130  +. DC 07-FC EIS_MODE_CHG_ENA
0131     BC       (20)
0132   . C. Verify EIS_MODE_CHG_FLG is ENA
0133  +. DC 07-FC EIS_MODE_MANU
0134     BC       (21 02)
0135   . C. Verify EIS in MANUAL mode
0136   . C. Estimated OBSTBL upload time is 4s
0137     C. ********************************
0138     C. EIS START OBSTBL LOAD
0139     C. ********************************
0140   . S. RAM   ram-820:EIS_OBSTBL
0141     ()
0142  +. DC 07-FC EIS_DUMP_OBSTBL
0143     BC       (07 07 07 00 00 70 00)
0144     C.
0145     C. Execute, after the success of OBSTBL upload.
0146     C. Set EIS TI-commands
0147  +. TI 2022-03-03 10:49:50.0
0148     DC 07-FC EIS_MODE_CHG_ENA
0149     BC       (20)
0150   . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0151     C. ********************************
0152     C. EIS END OBSTBL LOAD
0153     C. ********************************
0154     C.
0155   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0156     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0157   . S. DC-BC dcbc-402:DCBC
0158     (MDP_known_event)
0159     C.
0160     C.
0161   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0162   . S. DC-BC dcbc-153:DCBC
0163     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164     C.
0165     C.
0166   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0167     C.
0168   . C. ********** LOS **********
0169     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-033 2022-03-03 11:21:33 168 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. =====  Begin of AOCS CMD Sequence =====
0015   . C.
0016   . C. ****************************************
0017   . C. ***** No.1: NM°Ü¹Ô                  *****
0018   . C. ****************************************
0019   . C.
0020   . C. ***************************
0021   . C.  Z¥É¥ê¥Õ¥È¥ì¡¼¥ÈÀßÄê
0022   . C. ***************************
0023   . C.
0024   . C.  ¡ú É¬Í×»þ¤Ë•×²è³°¤Ë¤Æ¼Â»Ü¤¹¤ë¡£
0025   . C.
0026   . C.  ¡ÚAOCU_SET_DRIFT_RATE 07 01 ** **¡Û
0027   . C.
0028   . C.
0029   . C. ***************************
0030   . C.  FDIR °ìÉô²ò½ü
0031   . C. ***************************
0032   . C.
0033   +. DC 02-50 AOCU_RATE_CHK_DIS
0034     BC       (8f)
0035   . C.                                        [   ] <A_AOS> [FDIR] RATE = DIS ?
0036   . C.
0037   . C.
0038   . C. ***************************
0039   . C.  IRU-B LOÀÚ¤êÂØ¤¨
0040   . C. ***************************
0041   . C.
0042   +. DC 02-AE AOCU_IRU-B1_LO
0043   +  DC 02-B3 AOCU_IRU-B2_LO
0044   . C.                                        [   ] <A_AOS> [COMPONENT STS] <IRU> B1 = ON/LO ?
0045   . C.                                        [   ] <A_AOS> [COMPONENT STS] <IRU> B2 = ON/LO ?
0046   . C.  ¡ú»ÑÀªÀ°ÄêÂÔ¤Á
0047   . C.
0048   . C.
0049   . C. ¢£¢£¢£¢£  ¶ÛµÞ»þCMD  ¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£
0050   . C. ¡ú  »ÑÀªÀ©¸æ°Û¾ïÈ¯À¸»þ¤Ë¤Ï²¼µ-CMD¤òÁ÷¿®¤•¤ÆIRU-B HI¥â¡¼¥É¤ØÌá¤¹¡£
0051   . C.
0052   +. DC 02-AD AOCU_IRU-B1_HI
0053   +  DC 02-B2 AOCU_IRU-B2_HI
0054   . C.                                        [   ] <A_AOS> [COMPONENT STS] <IRU> B1 = ON/HI ?
0055   . C.                                        [   ] <A_AOS> [COMPONENT STS] <IRU> B2 = ON/HI ?
0056   +. DC 02-50 AOCU_RATE_CHK_ENA
0057     BC       (8e)
0058   . C.                                        [   ] <A_AOS> [FDIR] RATE = ENA ?
0059   . C.
0060   . C. ¡ú  ¶ÛµÞÂÐ±þ»þ¤Ï¡¢°Ê¹ß¤Î¼ê½ç¤Ï¥-¥ã¥ó¥»¥ë¡£
0061   . C. ¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£
0062   . C.
0063   . C.
0064   . C. ***************************
0065   . C.  UFSS USEÀÚ¤êÂØ¤¨
0066   . C. ***************************
0067   . C.
0068   +. DC 02-50 AOCU_NSAS_NO_USE
0069     BC       (0a)
0070   +  DC 02-50 AOCU_UFSS-A_USE
0071     BC       (05)
0072   +  DC 02-50 AOCU_UFSS-B_USE
0073     BC       (07)
0074   . C.                                        [   ] <A&CS>[ADS UPD] UFSS-A = USE ?
0075   . C.                                        [   ] <A&CS>[ADS UPD] UFSS-B = USE ?
0076   . C.                                        [   ] <A&CS>[ADS UPD] NSAS = NO_USE ?
0077   . C.  ¡ú»ÑÀªÀ°ÄêÂÔ¤Á
0078   . C.
0079   . C.
0080   . C. ¢£¢£¢£¢£  ¶ÛµÞ»þCMD  ¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£
0081   . C. ¡ú  »ÑÀªÀ©¸æ°Û¾ïÈ¯À¸»þ¤Ë¤Ï²¼µ-CMD¤òÁ÷¿®¤•¤ÆNSAS»ÈÍÑ¤ØÌá¤¹¡£
0082   . C.
0083   +. DC 02-50 AOCU_NSAS_USE
0084     BC       (09)
0085   +  DC 02-50 AOCU_UFSS-A_NO_USE
0086     BC       (06)
0087   +  DC 02-50 AOCU_UFSS-B_NO_USE
0088     BC       (08)
0089   . C.                                        [   ] <A&CS>[ADS UPD] UFSS-A = NO_USE ?
0090   . C.                                        [   ] <A&CS>[ADS UPD] UFSS-B = NO_USE ?
0091   . C.                                        [   ] <A&CS>[ADS UPD] NSAS = USE ?
0092   +. DC 02-50 AOCU_RATE_CHK_ENA
0093     BC       (8e)
0094   . C.                                        [   ] <A_AOS> [FDIR] RATE = ENA ?
0095   . C.
```

```
  . C. ¡ú  ¶ÛµÞÂÐ±þ»þ¤Ï¡¢°Ê¹ß¤Î¼ê½ç¤Ï¥-¥ã¥ó¥»¥ë¡£
  . C. ¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£
  . C.
  . C.
  . C. ***************************
  . C.  NMÁ«°Ü
  . C. ***************************
  . C.
 +. DC 02-76 AOCU_NM
    BC       (00 00 00 00 00)
  . C.                                          [   ] <¡½> [CNT MODE]   =  NM ?
  . C.  ¡ú»ÑÀªÀ°ÄêÂÔ¤Á
  . C.
  . C.
  . C. ¢£¢£¢£¢£¢£  ¶ÛµÞ»þCMD  ¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£
  . C. ¡ú  »ÑÀªÀ©¸æ°Û¾ïÈ¯À¸»þ¤Ë¤Ï²¼µ-CMD¤òÁ÷¿®¤•¤ÆMSA¤ØÌá¤¹¡£
  . C.
 +. DC 02-75 AOCU_MSA
    BC       (00 00 00 00 00)
  . C.                                          [   ] <A_AOS> [CNT MODE]   =  MSA ?
  . C.
  . C. ¡ú  ¶ÛµÞÂÐ±þ»þ¤Ï¡¢°Ê¹ß¤Î¼ê½ç¤Ï¥-¥ã¥ó¥»¥ë¡£
  . C. ¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£
  . C.
  . C.
  . C. ***************************
  . C.  Àº»ÑÀª•èÄê•ÏÀÚ¤êÂØ¤¨
  . C. ***************************
  . C.
 +. DC 02-50 AOCU_ADS_UPD_ENA
    BC       (03)
  . C.                                          [   ] <A&CS>[ADS UPD]  = ENA ?
  . C.  ¡ú»ÑÀªÀ°ÄêÂÔ¤Á
  . C.
  . C.
  . C. ¢£¢£¢£¢£¢£  ¶ÛµÞ»þCMD  ¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£
  . C. ¡ú  »ÑÀªÀ©¸æ°Û¾ïÈ¯À¸»þ¤Ë¤Ï²¼µ-CMD¤òÁ÷¿®¤•¤ÆÁÆ»ÑÀª•èÄê•Ï¤ØÌá¤¹¡£
  . C.
 +. DC 02-50 AOCU_ADS_UPD_DIS
    BC       (04)
  . C.                                          [   ] <A&CS>[ADS UPD]  = DIS ?
  . C.
  . C. ¡ú  ¶ÛµÞÂÐ±þ»þ¤Ï¡¢°Ê¹ß¤Î¼ê½ç¤Ï¥-¥ã¥ó¥»¥ë¡£
  . C. ¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£¢£
  . C.
  . C.
  . C. ***************************
  . C.  FDIR ÀßÄêÌá¤•
  . C. ***************************
  . C.
 +. DC 02-50 AOCU_RATE_CHK_ENA
    BC       (8e)
  . C.                                          [   ] <A_AOS> [FDIR] RATE = ENA ?
  . C.
  . C.
  . C. =====  End of AOCS CMD Sequence =====
  . C.
    C.
  . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
    C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
  . S. DC-BC dcbc-402:DCBC
    (MDP_known_event)
    C.
    C.
  . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
  . S. DC-BC dcbc-153:DCBC
    (SPECIAL-CMD_DAILY_OPERATIN_DCB)
    C.
    C.
  . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
    C.
  . C. *********** LOS ***********
    C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-034 2022-03-03 11:21:33 94 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +  DC 07-F0 MDP_XRT_MODE_STBY
0020     BC       (c3)
0021   . C. ----------- Success Verify ?              OK / NG____
0022     C.
0023     C. XRT Obs. Table Upload
0024   . S. RAM   ram-291:MDP_OBS_X
0025     ()
0026     C.
0027   +. DC 07-F0 MDP_DUMP_XRTTBL
0028     BC       (84 07 00 00 00 3a d4)
0029   . C. ----------- Comparison Check ?                        OK / ERR ____
0030     C.
0031     C.
0032   +. DC 07-F0 MDP_XRT_ROI_SET
0033     BC       (cd 01 b1 b1 04 04)
0034   +  DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 02 b1 b1 08 08)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 03 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 04 b1 b1 06 06)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 06 80 80 20 20)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 07 80 80 08 08)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 08 80 80 20 08)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 09 80 80 08 20)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 0f 80 80 06 06)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 10 80 80 08 08)
0052   +  DC 07-F0 MDP_XRT_FLD_ENA
0053     BC       (d8)
0054   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0055     BC       (c8)
0056   +  DC 07-F0 MDP_XRT_ARS_DIS
0057     BC       (d5)
0058   +  DC 07-F0 MDP_XRT_AEC_RESET
0059     BC       (d0)
0060   +  DC 07-F0 MDP_XRT_FLD_RESET
0061     BC       (da)
0062   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0063     BC       (c4 08)
0064   +. DC 07-F0 MDP_XRT_FL_PROG_SET
0065     BC       (c5 04)
0066   . C. ---------- Success Verify ?            OK / NG ____
0067     C.
0068     C.
0069   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0070     C.
0071   +. DC 07-F0 MDP_XRT_MODE_OBSV
0072     BC       (c2)
0073   +. TI 2022-03-03 10:49:02.0
0074     DC 07-F0 MDP_XRT_MODE_OBSV
0075     BC       (c2)
0076   . C. ---------- Success Verify ?            OK / NG ____
0077     C.
0078     C. ************ XRT END ***********
0079     C.
0080   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0081     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0082   . S. DC-BC dcbc-402:DCBC
0083     (MDP_known_event)
0084     C.
0085     C.
0086   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0087   . S. DC-BC dcbc-153:DCBC
0088     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089     C.
0090     C.
0091   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0092     C.
0093   . C. *********** LOS ***********
0094     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-035 2022-03-03 11:21:33 130 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. =====  Begin of AOCS CMD Sequence =====
0015   . C.
0016   . C. ****************************************
0017   . C. ***** No.2: À©¸æÀßÄê(°ìÉô)Ìá¤•     *****
0018   . C. ****************************************
0019   . C.
0020   . C. ***************************
0021   . C.  ÃÏ¿ªÈ½ÄêAUTOÀÚ¤êÂØ¤¨
0022   . C. ***************************
0023   . C.
0024   +. DC 02-50 AOCU_TISHOK_AUTO
0025     BC       (1d)
0026   . C.                                        [   ] <OOT> [S/W TIMER JDG] TSK = AUTO ?
0027   . C.
0028   . C.
0029   . C. ***************************
0030   . C.  ³Æ¼ïSW¥¿¥¤¥ÞÀßÄê
0031   . C. ***************************
0032   . C.
0033   . C. ***** Æü¾ÈÆü±¢¥¿¥¤¥ÞÃÍ *****
0034   +. DC 02-53 AOCU_NIGHT_START_S/W
0035     BC       (56 f9 99)
0036   +  DC 02-53 AOCU_NIGHT_END_S/W
0037     BC       (57 06 66)
0038   +  DC 02-50 AOCU_N/D_UPD
0039     BC       (1c)
0040   . C.                                        [   ] <OOT> [S/W TIMER] N/D_S =  370 +- 1 ?
0041   . C.                                        [   ] <OOT> [S/W TIMER] N/D_E =  -10 +- 1 ?
0042   . C.                                        [   ] <OOT> [SW T STS] N/D  =  DAY ?
0043   . C. ***** ÃÏ¿ª¥¿¥¤¥ÞÃÍ *****
0044   +. DC 02-53 AOCU_TISHOK_START
0045     BC       (58 eb 85)
0046   +  DC 02-53 AOCU_TISHOK_END
0047     BC       (59 a4 7a)
0048   +  DC 02-50 AOCU_TISHOK_UPD
0049     BC       (1f)
0050   . C.                                        [   ] <OOT> [S/W TIMER] TSK_S =  348 +- 1 ?
0051   . C.                                        [   ] <OOT> [S/W TIMER] TSK_E =  237 +- 1 ?
0052   . C. ***** ¶Ë°èÈ½ÄêÃÍ *****
0053   +. DC 02-53 AOCU_POLAR_N_START
0054     BC       (5a 43 33)
0055   +  DC 02-53 AOCU_POLAR_N_END
0056     BC       (5b 49 99)
0057   +  DC 02-53 AOCU_POLAR_S_START
0058     BC       (5c b6 66)
0059   +  DC 02-53 AOCU_POLAR_S_END
0060     BC       (5d bc cc)
0061   +  DC 02-50 AOCU_POLAR_UPD
0062     BC       (22)
0063   . C.                                        [   ] <OOT> [S/W TIMER] PLN_S =  85 +- 1 ?
0064   . C.                                        [   ] <OOT> [S/W TIMER] PLN_E =  95 +- 1 ?
0065   . C.                                        [   ] <OOT> [S/W TIMER] PLS_S =  265 +- 1 ?
0066   . C.                                        [   ] <OOT> [S/W TIMER] PLS_E =  275 +- 1 ?
0067   . C.
0068   . C.
0069   . C. ***************************
0070   . C.  ³Æ¼ïÀ©¸æÀßÄê
0071   . C. ***************************
0072   . C.
0073   +. DC 02-50 AOCU_MTQ_FF_ON
0074     BC       (4b)
0075   +  DC 02-50 AOCU_MW_SAT_NARROW
0076     BC       (3e)
0077   . C.                                        [   ] <A&CS> [CNT SEL] MFF =  ON ?
0078   . C.                                        [   ] <A&CS> [CNT SEL] MWS =  NARROW ?
0079   . C.
0080   . C.
0081   . C. ***************************
0082   . C.  FDIR ÀßÄê
0083   . C. ***************************
0084   . C.
0085   +. DC 02-50 AOCU_UFSS_SCHK_ENA
0086     BC       (82)
0087   . C.                                        [   ] <A_AOS> [FDIR] UFSS = ENA ?
0088   . C.
0089   . C.
0090   . C. *********************************
0091   . C.  Z»ÑÀª•èÄêÍÑ¥Ç¡¼¥¿¼èÆÀ
0092   . C. *********************************
0093   . C.
0094   . C. ***** GAS¥â¥Ë¥¿¤Î¤¿¤á MTQ¶îÆ°°ì»þÄä»ß *****
0095   +. DC 02-33 AOCU_MDRV-X_OFF
```

```
0096  +  DC 02-34 AOCU_MDRV-Y_OFF
0097  +  DC 02-35 AOCU_MDRV-Z_OFF
0098   . C.                                          [   ] <A_AOS> [COMPONENT STS] <MDRV> X = OFF ?
0099   . C.                                          [   ] <A_AOS> [COMPONENT STS] <MDRV> Y = OFF ?
0100   . C.                                          [   ] <A_AOS> [COMPONENT STS] <MDRV> Z = OFF ?
0101   . C.
0102   . C.
0103   . C.  ¡ú¥Ç¡¼¥¿¼èÆÀ¤Î¤¿¤á¡¢Ìó1minÂÔµ¡
0104   . C.
0105   . C.
0106   . C. ***** MTQ¶îÆ°ºÆ³« *****
0107  +. DC 02-32 AOCU_MDRV_ON
0108   . C.                                          [   ] <A_AOS> [COMPONENT STS] <MDRV> X = ON ?
0109   . C.                                          [   ] <A_AOS> [COMPONENT STS] <MDRV> Y = ON ?
0110   . C.                                          [   ] <A_AOS> [COMPONENT STS] <MDRV> Z = ON ?
0111   . C.
0112   . C.
0113   . C. =====  End of AOCS CMD Sequence =====
0114   . C.
0115     C.
0116   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0117     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0118   . S. DC-BC dcbc-402:DCBC
0119     (MDP_known_event)
0120     C.
0121     C.
0122   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0123   . S. DC-BC dcbc-153:DCBC
0124     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125     C.
0126     C.
0127   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0128     C.
0129   . C. ********** LOS **********
0130     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-036 2022-03-03 11:21:33 113 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007    +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011    +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019    +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021    +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023    +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029    . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031    . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036    . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037    +. DC 01-29 DHU_S/X_VC4_OFF
0038    +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040    +  DC 06-B3 DR_REP_START
0041    +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046    . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047    +. DC 06-B3 DR_REP_START
0048    +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054    . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057    . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062    . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063    +. DC 01-29 DHU_S/X_VC4_OFF
0064    +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066    +  DC 06-B3 DR_REP_START
0067    +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072    . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073    +. DC 06-B3 DR_REP_START
0074    +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079    . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083    . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084    +. DC 06-B4 DR_REP_STOP
0085    +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090    . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091    +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093    +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0100     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0101   . S. DC-BC dcbc-402:DCBC
0102     (MDP_known_event)
0103     C.
0104     C.
0105   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0106   . S. DC-BC dcbc-153:DCBC
0107     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108     C.
0109     C.
0110   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0111     C.
0112   . C. ********** LOS **********
0113     C.
```

```
*** OP Sequence for XRT ***

2022/03/03  11:26:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/03/03  11:26:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/03/03  11:26:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2022/03/03  11:27:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2022/03/03  11:27:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2022/03/03  11:27:22.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2022/03/03  11:29:58.0  XRT_QT_PROG_SET_433_OG [0x1b1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2022/03/03  11:30:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/03/03  12:00:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
```