

# XRT Timeline to be uploaded on 2022/07/05

Period: 2022/07/05 11:33:00 - 2022/07/09 10:06:00

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Normal mode

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## XOB #1BC7: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh(2048ms), Al/Poly(4096ms) - w leak image-1ms

Term	Pointing (x, y)	Comment
07/06 12:03:00 - 07/06 12:09:54	Fixed ( -528.4, -528.4)	4-Quadrant pointings Q1
<b>PROG= 10 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 51 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec	
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 88 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 34 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1BC8: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
07/06 12:13:00 - 07/06 12:19:54	Fixed ( 528.4, -528.4)	Q2
<b>PROG= 01 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 38 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec	
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 88 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 34 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1BC9: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
07/06 12:23:00 - 07/06 12:29:54	Fixed ( 528.4, 528.4)	Q3
<b>PROG= 11 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 21 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec	
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 88 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Al-mesh close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ Al-poly/Open	Al-poly/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 34 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

## XOB #1BCA: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (2048ms), Al/Poly (4096ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
07/06 12:33:00 - 07/06 12:39:54	Fixed ( -528.4, 528.4)	Q4
<b>PROG= 20 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 14 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec	
└─ Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec	
└─ Open/thick-Be	Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec	
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		

Seqn= 88		2-time(s)		2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Subr= 3		2-time(s)		2.0sec												
Seqn= 34		1-time(s)		2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval			

**XOB #1CC7: Synoptic Q95 2x2 - Al/mesh(2/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Thin**

Term	Pointing (x, y)	Comment
07/06 12:43:00 - 07/06 12:49:54	Fixed ( 0.0, 0.0)	synoptic
07/06 17:03:00 - 07/06 17:09:54	Fixed ( 0.0, 0.0)	synoptic, shifted -18.0 min
07/07 06:03:00 - 07/07 06:10:24	Fixed ( 0.0, 0.0)	HOP349 and synoptic, shifted 0.5 min

Prog= 17		1-time(s)		2.0sec												
Subr= 1		1-time(s)		2.0sec												
Seqn= 5		1-time(s)		2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec			
Seqn= 55		1-time(s)		2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 15		1-time(s)		2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 79		1-time(s)		2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 23		1-time(s)		2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval			

**XOB #1BDE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

Term	Pointing (x, y)	Comment
07/06 12:53:00 - 07/06 16:59:54	Track ( -102.7, 161.4) <sup>@ 07/06 12:50:00</sup>	AR13046 obs
07/06 17:13:00 - 07/06 22:51:30	Track ( -12.1, 152.6) <sup>@ 07/06 17:10:00</sup>	HOP420 AR
07/06 23:26:00 - 07/07 03:39:00	Track ( -15.2, 160.4) <sup>@ 07/06 23:00:00</sup>	AR13046 obs

Prog= 09		Inf.-time(s)		2.0sec												
Subr= 1		1-time(s)		2.0sec												
Seqn= 92		1-time(s)		2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec			
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec			
Subr= 2		5-time(s)		2.0sec												
Seqn= 47		1-time(s)		2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	2	0	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	2	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec			
Seqn= 96		4-time(s)		72.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval			

**XOB #1CCF: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 72min cad) + CME with**

Term	Pointing (x, y)	Comment
07/07 04:17:30 - 07/07 05:59:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic, shifted 0.5 min

Prog= 12		Inf.-time(s)		300.0sec												
Subr= 1		1-time(s)		2.0sec												
Seqn= 55		1-time(s)		2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 15		1-time(s)		2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec			

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 79 1-time(s) 2.0sec</b>												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 30 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 15-time(s) 360.0sec</b>												
<b>Seqn= 8 1-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 74 1-time(s) 2.0sec</b>												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 29 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1C92: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 600s cad (G-band/Leak first)**

Term	Pointing (x, y)	Comment
07/07 06:13:30 - 07/07 11:15:54	Fixed ( -16.0, -71.0)	HOP437 with SUNRISE-3
<b>PROG= 14 Inf-time(s)</b>		
<b>Subr= 1 1-time(s) 2.0sec</b>		
<b>Seqn= 30 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
<b>Subr= 2 5-time(s) 720.0sec</b>		
<b>Seqn= 8 1-time(s) 2.0sec</b>		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G-band**

Term	Pointing (x, y)	Comment
07/06 12:53:00 - 07/06 16:59:54	Track ( -102.7, 161.4) <sup>Ⓢ 07/06 12:50:00</sup>	AR13046 obs
07/06 17:13:00 - 07/06 22:51:30	Track ( -12.1, 152.6) <sup>Ⓢ 07/06 17:10:00</sup>	HOP420 AR
07/06 23:26:00 - 07/07 03:39:00	Track ( -15.2, 160.4) <sup>Ⓢ 07/06 23:00:00</sup>	AR13046 obs
07/07 04:17:30 - 07/07 05:59:54	Fixed ( 0.0, 0.0)	HOP349 and synoptic, shifted 0.5 min
07/07 06:13:30 - 07/07 11:15:54	Fixed ( -16.0, -71.0)	HOP437 with SUNRISE-3
<b>PROG= 04 30-time(s)</b>		
<b>Subr= 1 20-time(s) 2.0sec</b>		
<b>Seqn= 11 1-time(s) 2.0sec</b>		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn= 73 1-time(s) 10.0sec</b>		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>		
<b>Seqn= 10 1-time(s) 2.0sec</b>		
med-Al/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn= 87 1-time(s) 2.0sec</b>		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

\* \* \* \* \*

NOT USED

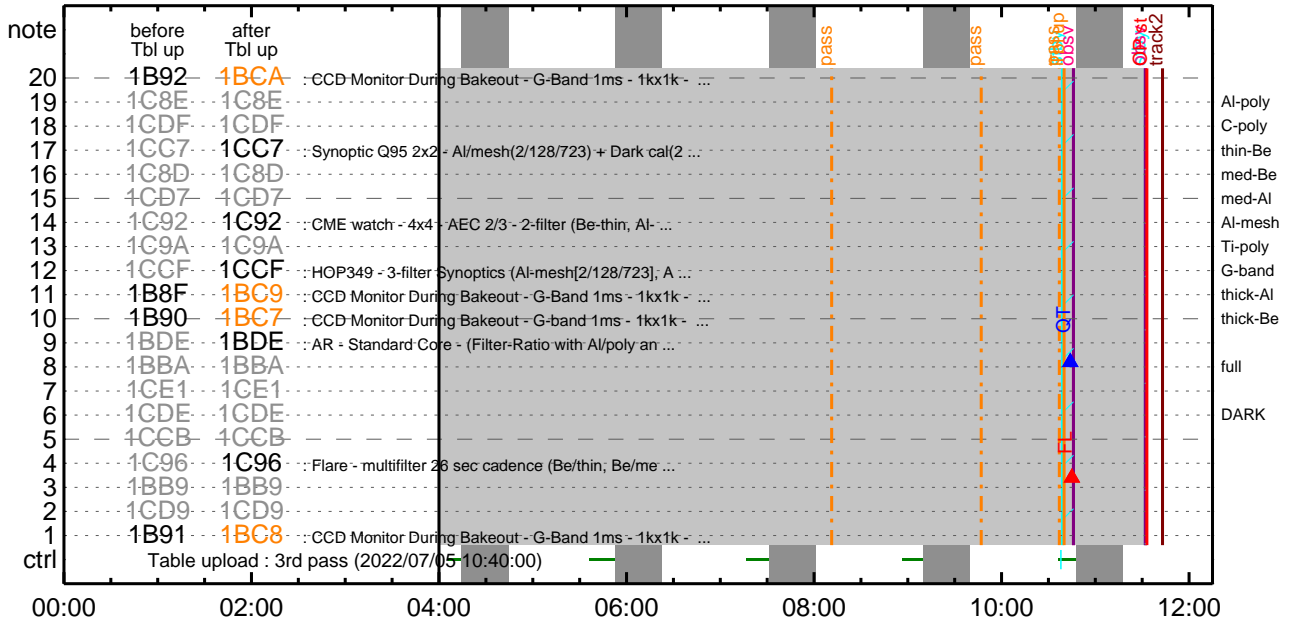
\* \* \* \* \*

**Flare Detection**

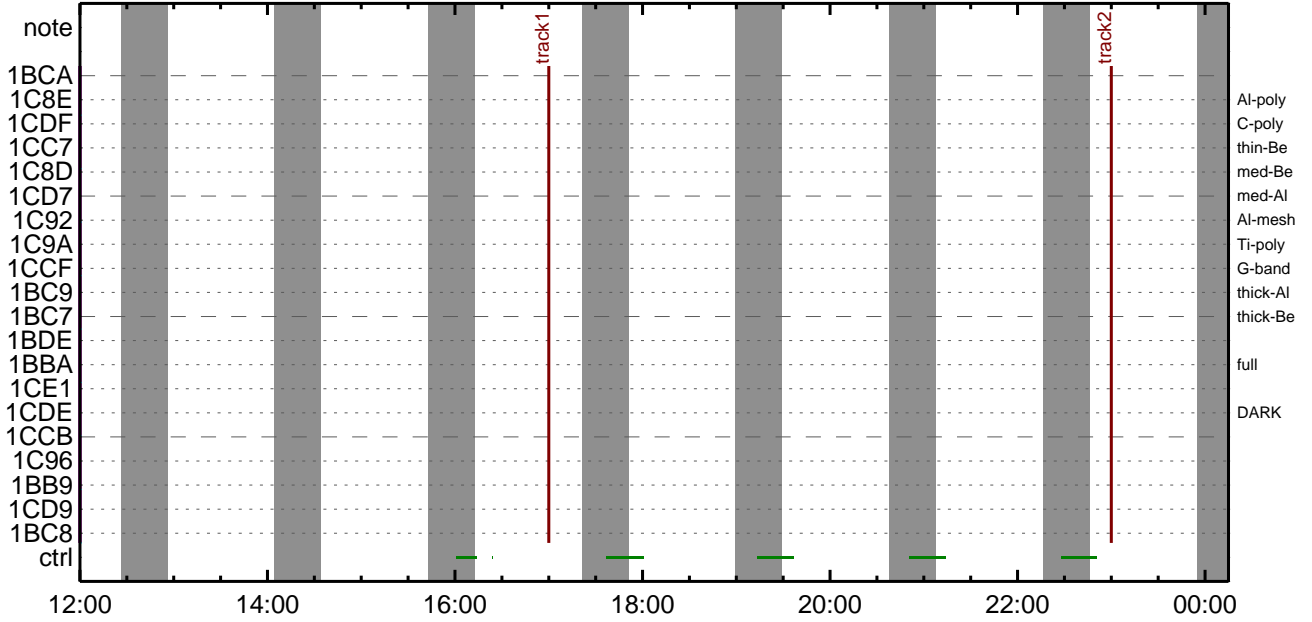
\* \* \* \* \*

Term	Pointing (x, y)		Comment									
07/05 10:41:00 - 07/06 12:02:56	cannot be identified											
07/06 12:50:18 - 07/06 17:00:18	Track ( -102.7, 161.4)	@ 07/06 12:50:00	AR13046 obs									
07/06 17:10:18 - 07/07 06:00:18	Track ( -12.1, 152.6)	@ 07/06 17:10:00	HOP420 AR									
07/07 06:10:48 - 07/09 10:06:00	Fixed ( -16.0, -71.0)		HOP437 with SUNRISE-3									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp. AEC Buffer Interval	

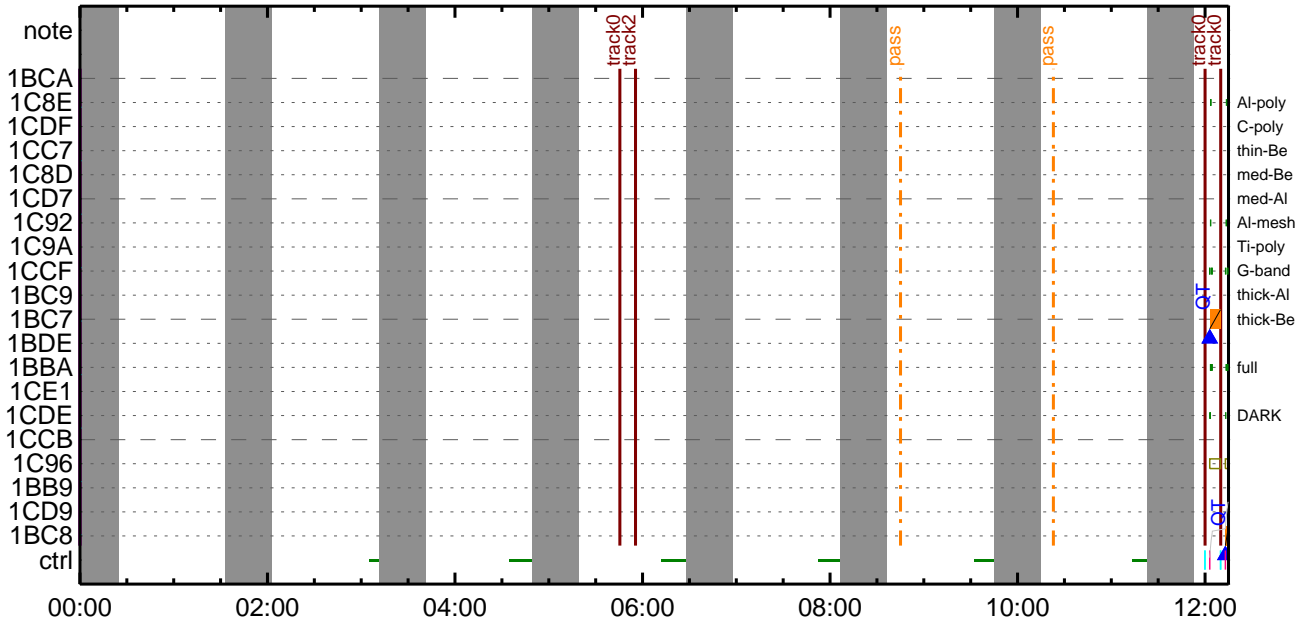
CMDI #0430 2022/07/05



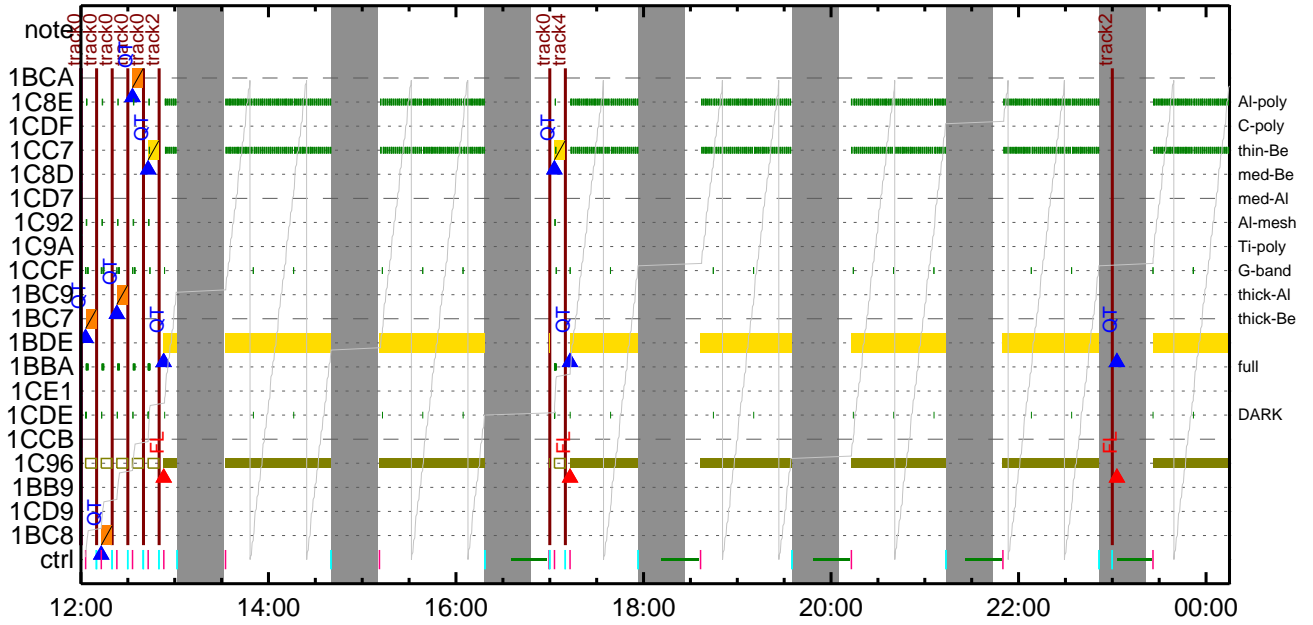
CMDI #0430 2022/07/05



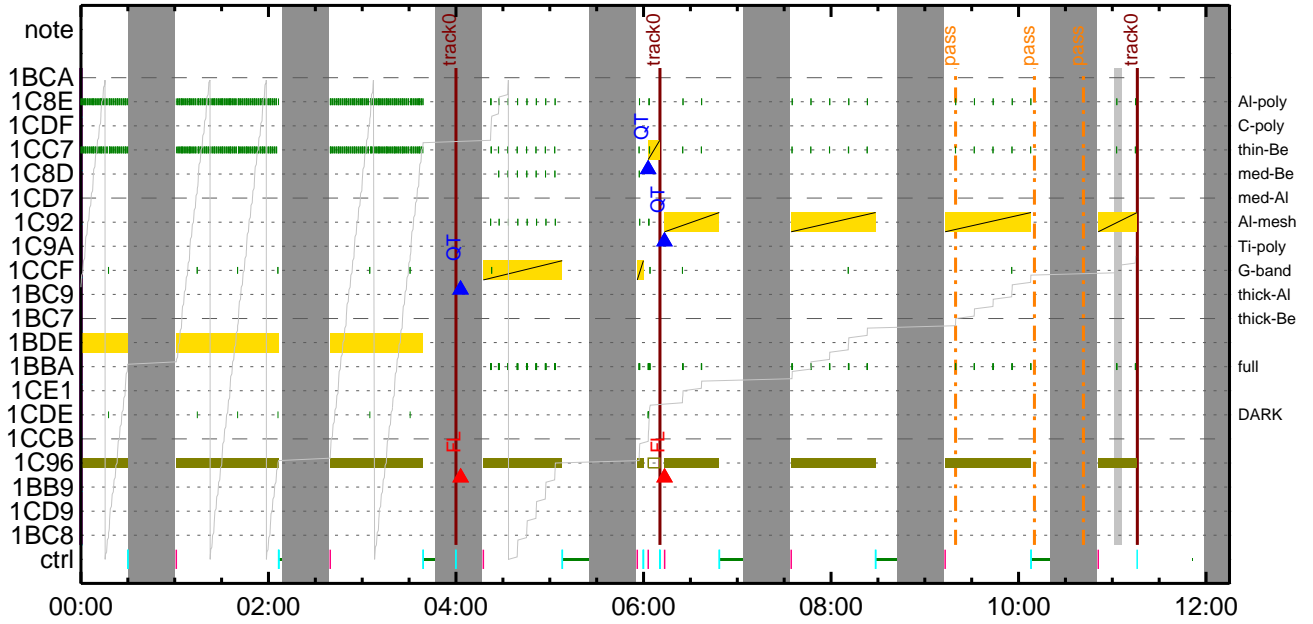
CMDI #0430 2022/07/06



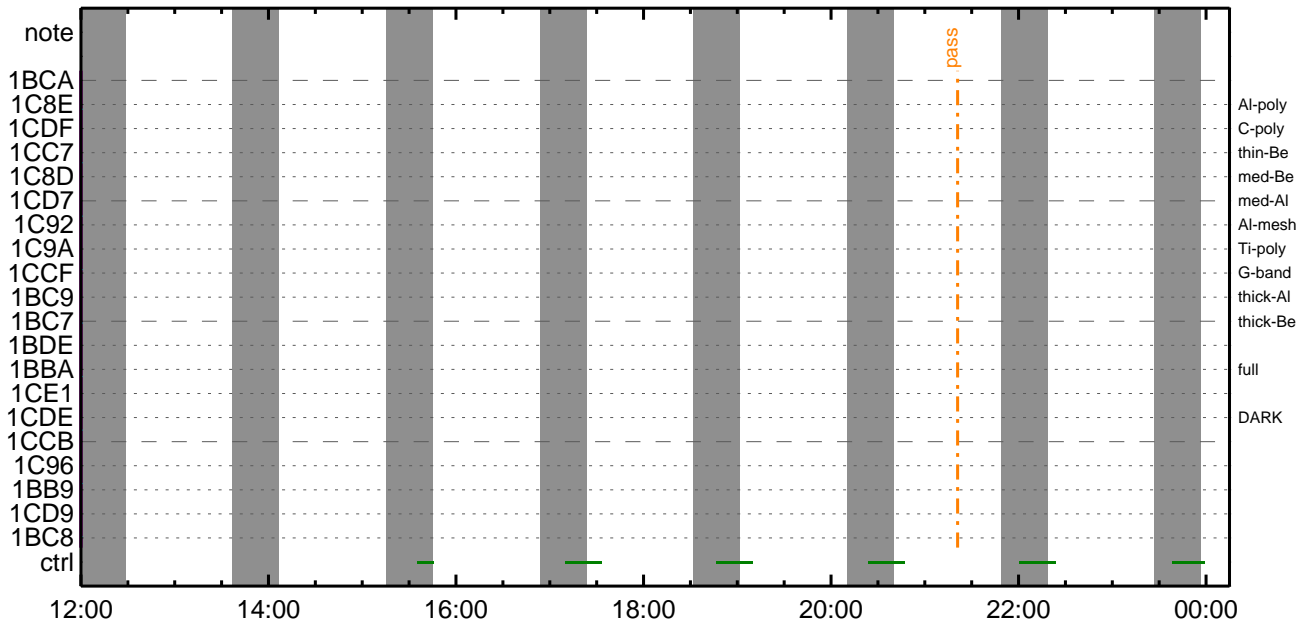
CMDI #0430 2022/07/06



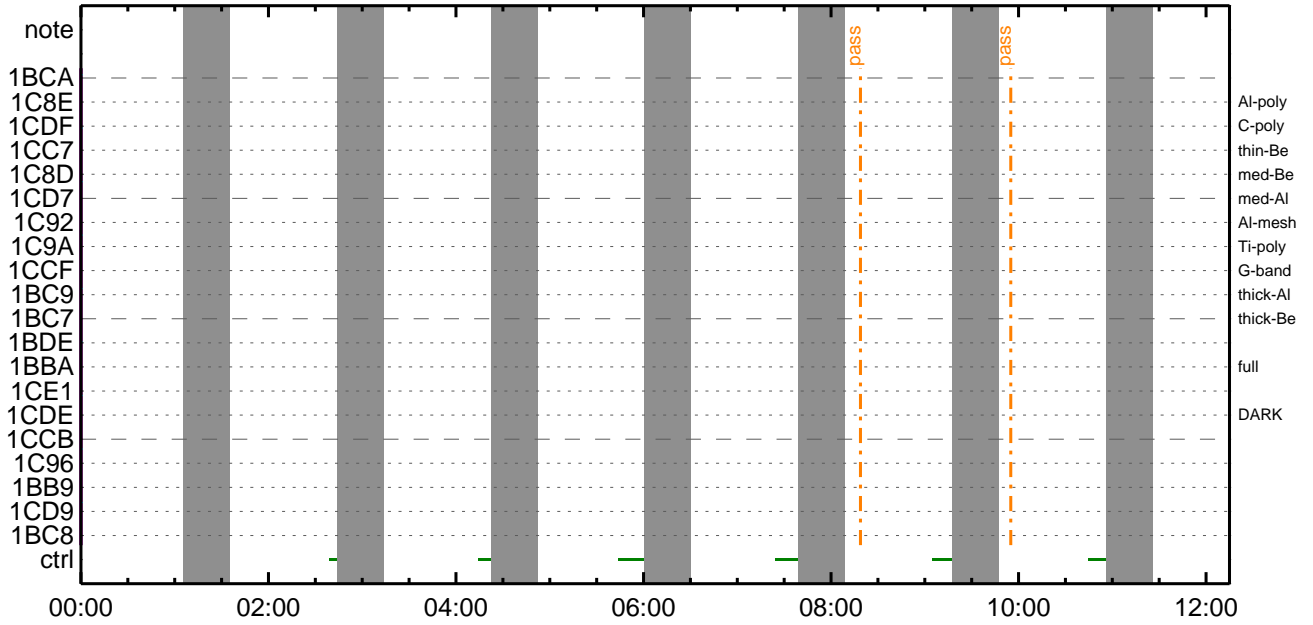
CMDI #0430 2022/07/07



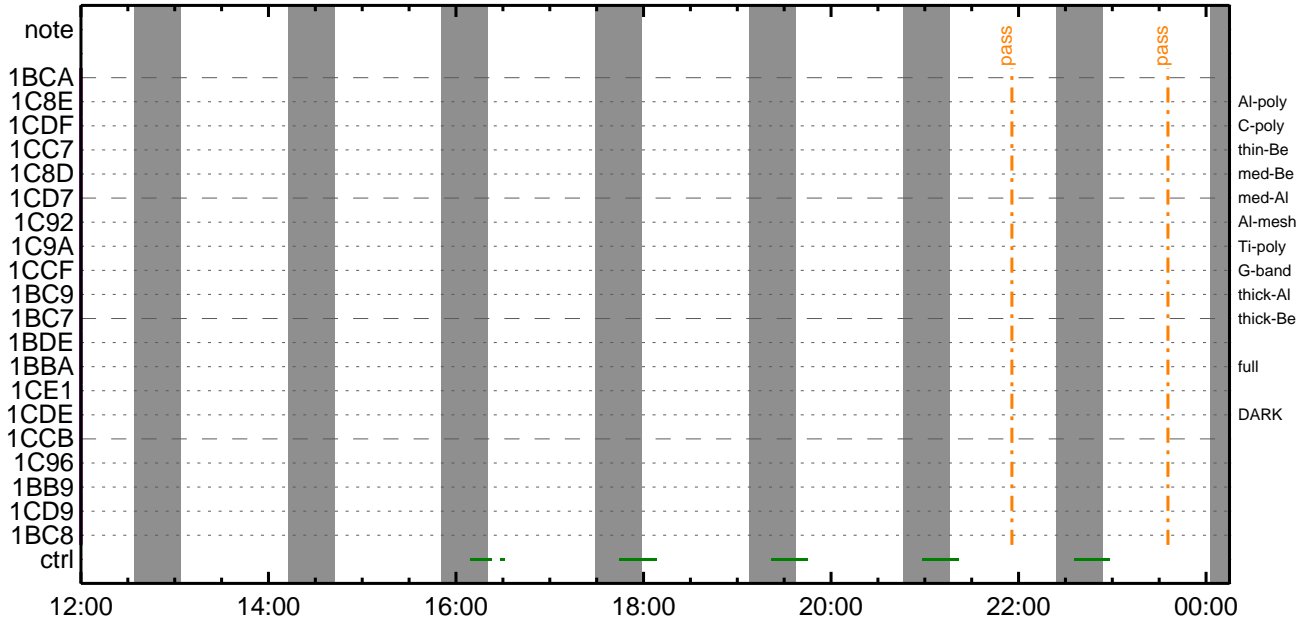
CMDI #0430 2022/07/07



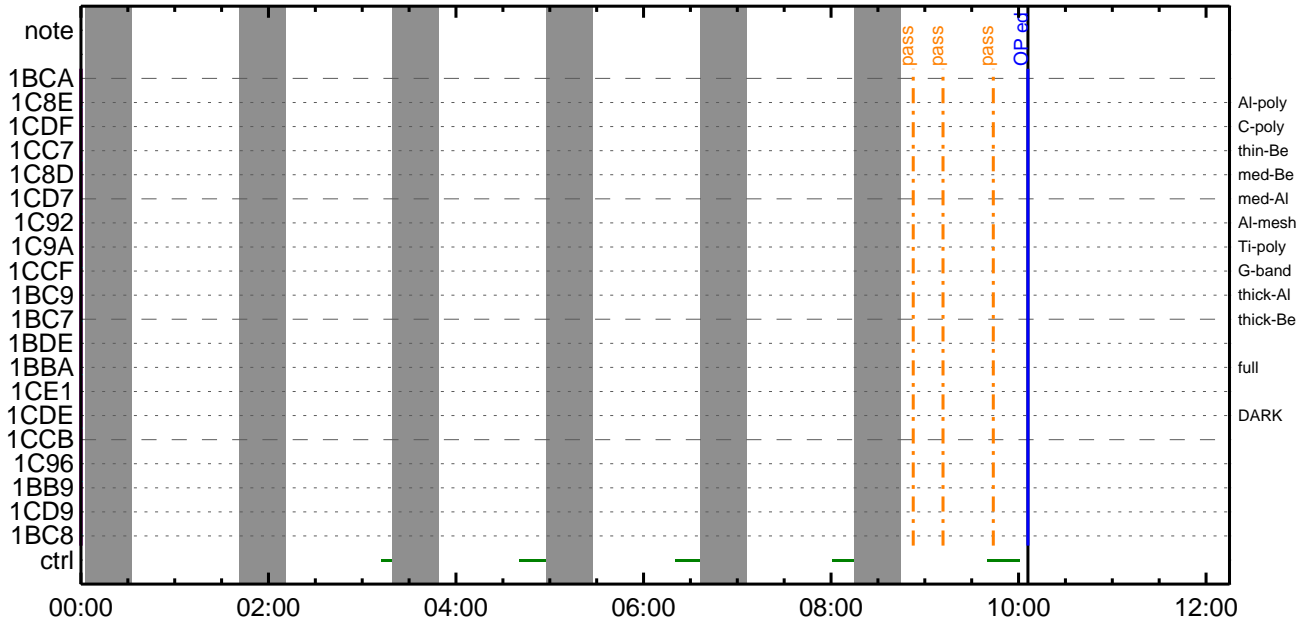
CMDI #0430 2022/07/08



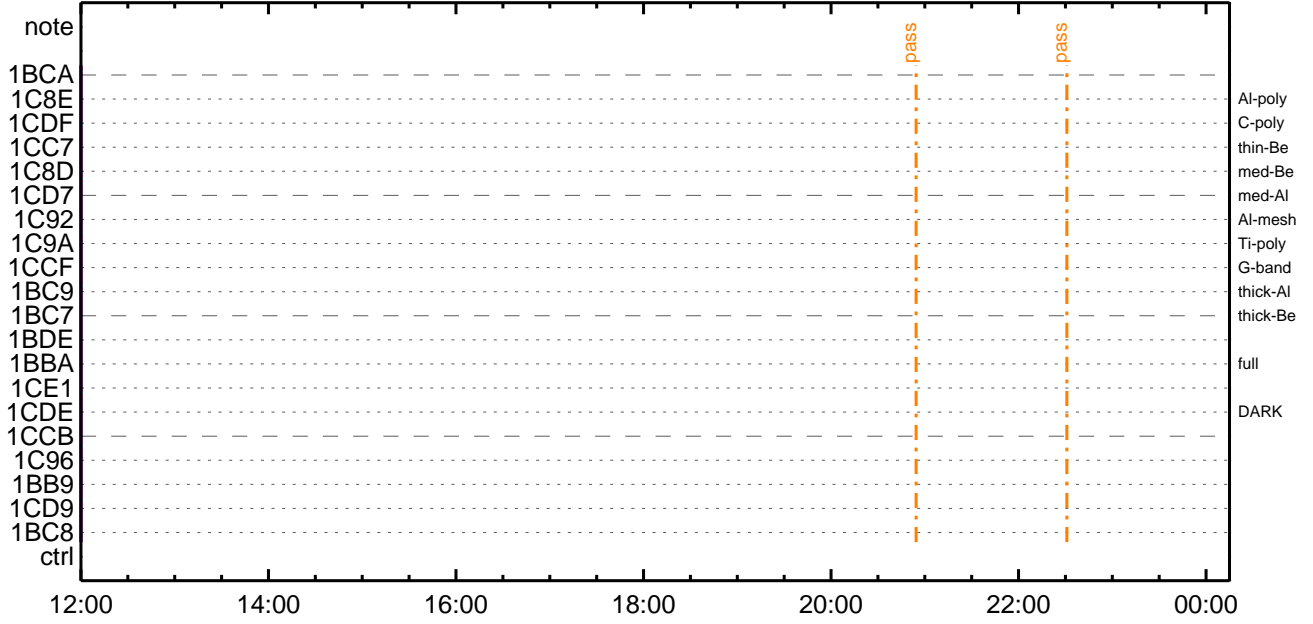
CMDI #0430 2022/07/08



CMDI #0430 2022/07/09



CMDI #0430 2022/07/09







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-265:OP
0104 ( )
0105 S. OG og-265:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî¼Ã´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê¼çòðÁÔæòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E¼Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼î¹ç;ç°E²¼òî¼TI-CMDÁ÷¿@²î¼Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EDUMPAîE±°îYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YOYEòðÁDî¿(UT)
0183 +. TI 2022-07-05 11:28:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2022-07-05 11:28:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2022-07-05 11:28:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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```
0194 C.
0195 +. TI 2022-07-05 11:32:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄè%îíñαîîŸÄŸ§ŸÄŸ-¹àîÜ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]           EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŸÄŸÖŸ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŸÄŸÖŸ×½ªî»αò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¼Ê¹ç•è²îOKαò³îÇ§
0229 C.
0230 C. DHUŸâ;¼ŸÊ;Ê¼Ÿ¼.Ÿî;¼ŸÊ;Êαòîäα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2022-07-05 11:32:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C.
0250 C. ***** XRT START *****
0251 C. Execute, after the success of OP upload.
0252 +. TI 2022-07-05 11:32:00.0
0253 DC 07-F0 MDP_XRT_MODE_STBY
0254 BC      (c3)
0255 C.          [ ] [HK1_TI_CMD_NUM]     EQ      1COUNTUP
0256 C.
0257 C. ***** XRT END *****
0258 C. Stop EIS observation and temporarily disable EIS mode changes
0259 C.
0260 C.
0261 C. ***** Start EIS operation (TI set) *****
0262 C. Execute, after the success of OP upload.
0263 C. Set EIS TI-commands
0264 +. TI 2022-07-05 11:32:30.0
0265 DC 07-FC EIS_MODE_MANU
0266 BC      (21 02)
0267 +. TI 2022-07-05 11:32:40.0
0268 DC 07-FC EIS_MODE_CHG_DIS
0269 BC      (22)
0270 C.          [ ] [HK1_TI_CMD_NUM]     EQ      2 COUNTUP
0271 C. ***** End EIS operation (TI set) *****
0272 C.
0273 C.
0274 C.
0275 C. ***** MDP `ûÃîαî»ö¼ŸαÊÄα¹αèDCBC•x²è *****
0276 C. (¼ª°îŸÖŸÄŸÊŸŸŸŸÄŸçŸèè¼αα¼Ä»Ûα¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŸDŸ¹•î Daily±çîñèË'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŸÄŸ§ŸÄŸ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 2754841.8 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 25s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2022-07-05 11:32:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 . C. ===== Begin of AOCS CMD Sequence =====
0164 . C.
0165 C. *****
0166 . C. ***** GASŸÇ;¼Ÿ;¼èÈÀŸÀ»Û *****
0167 . C. *****
0168 . C.
0169 C. *****
0170 . C. MDRV OFF
0171 . C. *****
0172 . C.
0173 . C. ***** GASŸâŸÈŸ;¼îŸ;¼á MTQŸîŸ°°i»pââ»Ÿ *****
0174 +. DC 02-33 AOCU_MDRV-X_OFF
0175 +. DC 02-34 AOCU_MDRV-Y_OFF
0176 +. DC 02-35 AOCU_MDRV-Z_OFF
0177 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> X = OFF ?
0178 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = OFF ?
0179 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = OFF ?
0180 . C.
0181 . C.
0182 . C. ;úŸÇ;¼Ÿ;¼èÈÀŸîŸ;¼á;çîŸlminÂŸô;
0183 . C.
0184 C. *****
0185 . C. MDRV ON
0186 . C. *****
0187 . C.
0188 . C. ***** MTQŸîŸ°°E³« *****
0189 +. DC 02-32 AOCU_MDRV_ON
0190 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> X = ON ?
0191 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = ON ?
0192 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = ON ?
0193 . C.

```

```
0194 . C.
0195 . C. ===== End of AOCS CMD Sequence =====
0196 . C.
0197 . C.
0198 . C. ***** MDP 'ûÃîñî»ð¼ÿñËÂðñ¹ñèDCBC•×²è *****
0199 . C. (¼ã°îÿÓÿÃÿÈÿÞÿÈÿãÿçÿéñ¼ã¼Ã»Ûñ¹ñè)
0200 . S. DC-BC dcbc-402:DCBC
0201 (MDP_known_event)
0202 . C.
0203 . C.
0204 . C. ***** ÿDÿ¹•ï Daily±;îññË'Øñ¹ñèDCBC•×²è *****
0205 . S. DC-BC dcbc-153:DCBC
0206 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0207 . C.
0208 . C.
0209 . C. ;ãLOSÿÃÿSÿÿÿ-¼Ã»Û;ã
0210 . C.
0211 . C. ***** LOS *****
0212 . C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-267 2022-07-05 12:49:50 138 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁ•µ°È»Í×ÁÇ¿ÍY¿Y×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈÈ¼°ÇÓã•¿¿¼í¹¿ãÍ;¿À®, ù¿¹ãÈãÈã¿Á+¿®ã•¿Èãã¿ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-288:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2022-07-05 11:32:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 c0 c0 10 10)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 40 c0 10 10)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 40 40 10 10)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b c0 40 10 10)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0c 80 80 20 08)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0d 80 80 08 20)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0e 80 80 08 08)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 0f 80 80 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 10 80 80 08 08)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_ENA
0097 BC (d8)
0098 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0099 BC (c8)
0100 + DC 07-F0 MDP_XRT_ARS_DIS
0101 BC (d5)
0102 + DC 07-F0 MDP_XRT_AEC_RESET
0103 BC (d0)
0104 + DC 07-F0 MDP_XRT_FLD_RESET
0105 BC (da)
0106 + DC 07-F0 MDP_XRT_QT_PROG_SET
0107 BC (c4 09)
0108 + DC 07-F0 MDP_XRT_FL_PROG_SET
0109 BC (c5 04)
0110 . C. ----- Success Verify ? OK / NG ____
0111 C.
0112 C.
0113 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0114 C.
0115 + DC 07-F0 MDP_XRT_MODE_OBSV
0116 BC (c2)
0117 + TI 2022-07-05 11:32:02.0
0118 DC 07-F0 MDP_XRT_MODE_OBSV
0119 BC (c2)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C. ***** XRT END *****
0123 C.
0124 . C. ***** MDP 'ûÃîñî»ö¼ÿñÊÂðñ¹ñèDCBC•x²è *****
0125 C. (¼â°îÿÓÿÃÿÈÿÞÿËÿàÿçÿèñ¼¼ñ¼Ã»Ûñ¹ñè)
0126 . S. DC-BC dcbc-402:DCBC
0127 (MDP_known_event)
0128 C.
0129 C.
0130 . C. ***** ÿDÿ¹•î Daily±;îññÊ'Øñ¹ñèDCBC•x²è *****
0131 . S. DC-BC dcbc-153:DCBC
0132 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133 C.
0134 C.
0135 . C. ;ãLOSÿÃÿSÿËÿ-¼Ã»Û;ã
0136 C.
0137 . C. ***** LOS *****
0138 C.
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\*\*\* OP Sequence for XRT \*\*\*

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2022/07/05 11:43:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 02 06 4d 01 68
2022/07/05 17:00:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 01 06 4d 01 68
2022/07/05 23:00:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 02 06 4d 01 68
2022/07/06 05:45:30.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2022/07/06 05:55:30.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 02 06 4d 01 68
2022/07/06 06:00:00.0 XRT_TCIB_XRT_S_HTR_A_DIS_435_OG [0x1b3]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2022/07/06 11:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2022/07/06 11:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2022/07/06 11:59:58.0 XRT_FOCUS_POSITION_443_OG [0x1bb]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2022/07/06 12:00:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 2e f9 2e f9
2022/07/06 12:02:52.0 XRT_ARS_DIS_426_OG [0x1aa]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2022/07/06 12:02:54.0 XRT_FLRCTRL_DIS_449_OG [0x1c1]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2022/07/06 12:02:56.0 XRT_FLD_DIS_425_OG [0x1a9]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2022/07/06 12:02:58.0 XRT_QT_PROG_SET_439_OG [0x1b7]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 0a
2022/07/06 12:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2022/07/06 12:09:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2022/07/06 12:09:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2022/07/06 12:09:58.0 XRT_FOCUS_POSITION_443_OG [0x1bb]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2022/07/06 12:10:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 2e f9 d1 07
2022/07/06 12:12:52.0 XRT_ARS_DIS_426_OG [0x1aa]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2022/07/06 12:12:54.0 XRT_FLRCTRL_DIS_449_OG [0x1c1]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2022/07/06 12:12:56.0 XRT_FLD_DIS_425_OG [0x1a9]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2022/07/06 12:12:58.0 XRT_QT_PROG_SET_447_OG [0x1bf]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 01
2022/07/06 12:13:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2022/07/06 12:19:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2022/07/06 12:19:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2022/07/06 12:19:58.0 XRT_FOCUS_POSITION_443_OG [0x1bb]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2022/07/06 12:20:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCU_NM                    5 02-76 00 d1 07 d1 07
2022/07/06 12:22:52.0 XRT_ARS_DIS_426_OG [0x1aa]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2022/07/06 12:22:54.0 XRT_FLRCTRL_DIS_449_OG [0x1c1]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2022/07/06 12:22:56.0 XRT_FLD_DIS_425_OG [0x1a9]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2022/07/06 12:22:58.0 XRT_QT_PROG_SET_446_OG [0x1be]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 0b
2022/07/06 12:23:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2022/07/06 12:29:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2022/07/06 12:29:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2022/07/06 12:29:58.0 XRT_FOCUS_POSITION_443_OG [0x1bb]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2022/07/06 12:30:00.5 AOCs_OrE-point_Start_7_OG [0x09d]
                        AOCU_NM                    5 02-76 00 d1 07 2e f9
2022/07/06 12:32:52.0 XRT_ARS_DIS_426_OG [0x1aa]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2022/07/06 12:32:54.0 XRT_FLRCTRL_DIS_449_OG [0x1c1]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2022/07/06 12:32:56.0 XRT_FLD_DIS_425_OG [0x1a9]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2022/07/06 12:32:58.0 XRT_QT_PROG_SET_441_OG [0x1b9]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 14
2022/07/06 12:33:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2022/07/06 12:39:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2022/07/06 12:39:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2022/07/06 12:39:58.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2022/07/06 12:40:00.0 AOCs_OrE-point_Start_3_OG [0x099]

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2022/07/06	12:40:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2022/07/06	12:40:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2022/07/06	12:40:22.0	XRT_ARS_DIS_442_OG [0x1ba]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2022/07/06	12:42:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]		2	07-F0	c4	11			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11			
2022/07/06	12:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2022/07/06	12:49:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2022/07/06	12:49:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2022/07/06	12:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2022/07/06	12:50:00.0	AOCS_Ore-point_Start_1_OG [0x097]		5	02-76	02	06	4d	01	68
		AOCU_NM		5	02-76	02	06	4d	01	68
2022/07/06	12:50:18.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2022/07/06	12:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2022/07/06	12:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2022/07/06	12:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2022/07/06	12:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2022/07/06	12:52:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]		2	07-F0	c4	09			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	09			
2022/07/06	12:52:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]		2	07-F0	c5	04			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	04			
2022/07/06	12:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2022/07/06	13:01:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2022/07/06	13:01:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2022/07/06	13:01:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2022/07/06	13:01:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2022/07/06	13:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2022/07/06	13:31:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
2022/07/06	13:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2022/07/06	14:40:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2022/07/06	14:40:02.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2022/07/06	14:40:04.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2022/07/06	14:40:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2022/07/06	14:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2022/07/06	15:10:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
2022/07/06	15:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2022/07/06	16:18:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2022/07/06	16:18:32.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2022/07/06	16:18:34.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2022/07/06	16:18:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2022/07/06	16:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2022/07/06	16:58:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
2022/07/06	16:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2022/07/06	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2022/07/06	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2022/07/06	16:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]		4	07-F8	22	ff	aa	00	
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2022/07/06	17:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]		5	02-76	00	00	00	00	00
		AOCU_NM		5	02-76	00	00	00	00	00
2022/07/06	17:00:18.0	XRT_FLD_DIS_409_OG [0x199]		1	07-F0	d9				
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2022/07/06	17:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2022/07/06	17:00:22.0	XRT_ARS_DIS_442_OG [0x1ba]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2022/07/06	17:02:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]		2	07-F0	c4	11			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11			
2022/07/06	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2022/07/06	17:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				

2022/07/06	17:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	17:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	17:10:00.0	AOCs_Orе-point_Start_8_OG [0x09e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2022/07/06	17:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 06 4d 01 68	
2022/07/06	17:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2022/07/06	17:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2022/07/06	17:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2022/07/06	17:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2022/07/06	17:12:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/06	17:12:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2022/07/06	17:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2022/07/06	17:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/06	17:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	17:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	17:56:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/06	17:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/06	18:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/06	18:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2022/07/06	19:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/06	19:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	19:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	19:35:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/06	19:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/06	20:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/06	20:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2022/07/06	21:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/06	21:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	21:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	21:13:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/06	21:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/06	21:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/06	21:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2022/07/06	22:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/06	22:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	22:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	22:51:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/06	22:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/06	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/06	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_MANU_402_OG [0x192]				
2022/07/06	22:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/06	23:00:00.0	AOCs_Orе-point_Start_1_OG [0x097]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2022/07/06	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 06 4d 01 68	
2022/07/06	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2022/07/06	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2022/07/06	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2022/07/06	23:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2022/07/06	23:02:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/06	23:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	

2022/07/06	23:25:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04
2022/07/06	23:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2022/07/07	00:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/07	00:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	00:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	00:30:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/07	00:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/07	01:00:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/07	01:01:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
2022/07/07	02:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/07	02:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	02:06:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	02:06:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/07	02:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/07	02:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/07	02:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2022/07/07	03:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/07	03:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	03:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	03:39:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/07	03:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/07	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/07	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
2022/07/07	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	04:00:00.0	AOCs_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2022/07/07	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2022/07/07	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2022/07/07	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2022/07/07	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2022/07/07	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2022/07/07	04:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/07	04:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2022/07/07	04:16:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2022/07/07	04:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2022/07/07	05:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/07	05:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	05:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	05:08:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/07	05:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/07	05:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/07	05:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2022/07/07	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/07	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/07	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2022/07/07	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2022/07/07	06:00:22.0	XRT_ARS_DIS_442_OG [0x1ba]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2022/07/07	06:02:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2022/07/07	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	

2022/07/07	06:10:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/07/07	06:10:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/07/07	06:10:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/07/07	06:10:30.0	AOCS_ORe-point_Start_9_OG [0x09f]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2022/07/07	06:10:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 06 4d 01 68
2022/07/07	06:10:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2022/07/07	06:10:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2022/07/07	06:10:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2022/07/07	06:10:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2022/07/07	06:13:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/07/07	06:13:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2022/07/07	06:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2022/07/07	06:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/07/07	06:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/07/07	06:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/07/07	06:48:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/07/07	06:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/07/07	07:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/07/07	07:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2022/07/07	08:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/07/07	08:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/07/07	08:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/07/07	08:28:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/07/07	08:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/07/07	09:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/07/07	09:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2022/07/07	10:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/07/07	10:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/07/07	10:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/07/07	10:08:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/07/07	10:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/07/07	10:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/07/07	10:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2022/07/07	11:15:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/07/07	11:16:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00