

XRT Timeline to be uploaded on 2022/07/23

Period: 2022/07/23 11:28:00 - 2022/07/28 10:55:00

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Normal mode

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XOB #1B89: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
07/23 12:05:30 - 07/23 17:56:24	Track (584.4, 93.1) @ 07/23 11:38:00	# OP start + 10min and AR 13060 obs
07/23 18:48:30 - 07/24 03:51:00	Track (626.6, 96.3) @ 07/23 18:06:30	AR 13060 obs
PROG= 11 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 92 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
└─ Open/G-band Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Subr= 2 5-time(s) 2.0sec		
└─ Seqn= 47 1-time(s) 2.0sec		
└─ Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
└─ Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Seqn= 96 4-time(s) 60.0sec		
└─ Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 2.0sec
└─ Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 2.0sec
└─ Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1BC0: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Th

Term	Pointing (x, y)	Comment
07/23 17:59:30 - 07/23 18:06:24	Fixed (0.0, 0.0)	synoptic, shifted -3.5 min
PROG= 03 1-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 5 1-time(s) 2.0sec		
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024)	DPCM 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024)	DPCM 0 0 2.0sec
└─ Seqn= 63 1-time(s) 2.0sec		
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 15 1-time(s) 2.0sec		
└─ Al-poly/Open Al-poly/Open close	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open Al-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 27 1-time(s) 2.0sec		
└─ thin-Be/Open thin-Be/Open close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ thin-Be/Open thin-Be/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ thin-Be/Open thin-Be/Open close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 23 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1CCF: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 72min cad) + CME wa

Term	Pointing (x, y)	Comment
07/24 04:29:00 - 07/24 05:20:30	Fixed (0.0, 0.0)	HOP 349 and synoptic, shifted 11.0 min
PROG= 12 Inf.-time(s)		
└─ Subr= 1 1-time(s) 300.0sec		
└─ Seqn= 55 1-time(s) 2.0sec		
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 15 1-time(s) 2.0sec		
└─ Al-poly/Open Al-poly/Open close	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open Al-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Al-poly/Open Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 79 1-time(s) 2.0sec		
└─ thin-Be/Open thin-Be/Open close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ thin-Be/Open thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ thin-Be/Open thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 30 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024)	Q=90 0 0 2.0sec

Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 15-time(s) 360.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74 1-time(s) 2.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CCB: Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(12/181/1443), Thin-Be(64/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(64/2048), Med-Al(2048)

Term	Pointing (x, y)	Comment
07/24 06:09:26 - 07/24 06:19:02	Fixed (0.0, 0.0)	HOP 349 and synoptic, shifted 11.0 min

PROG= 05 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 63 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 27 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 46 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 93 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 56 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GB

Term	Pointing (x, y)	Comment
07/23 12:05:30 - 07/23 17:56:24	Track (584.4, 93.1) @ 07/23 11:38:00	# OP start + 10min and AR 13060 obs
07/23 18:48:30 - 07/24 03:51:00	Track (626.6, 96.3) @ 07/23 18:06:30	AR 13060 obs
07/24 04:29:00 - 07/24 05:20:30	Fixed (0.0, 0.0)	HOP 349 and synoptic, shifted 11.0 min

PROG= 04 30-time(s)												
Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

L	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

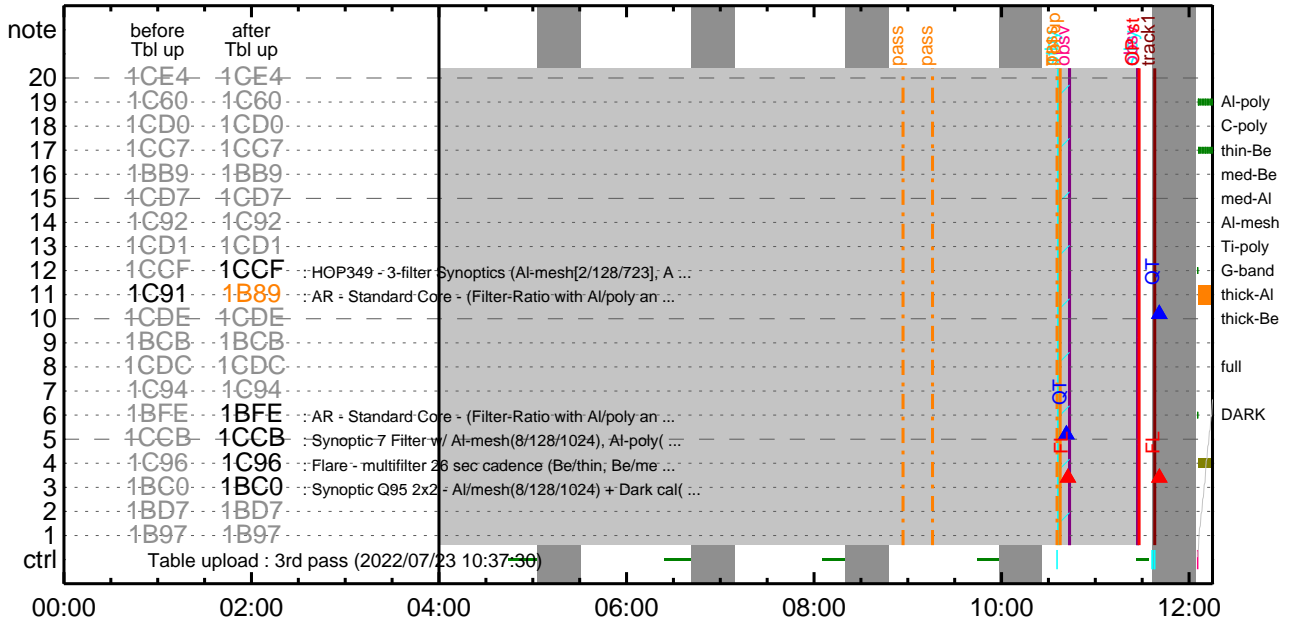
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Flare Detection

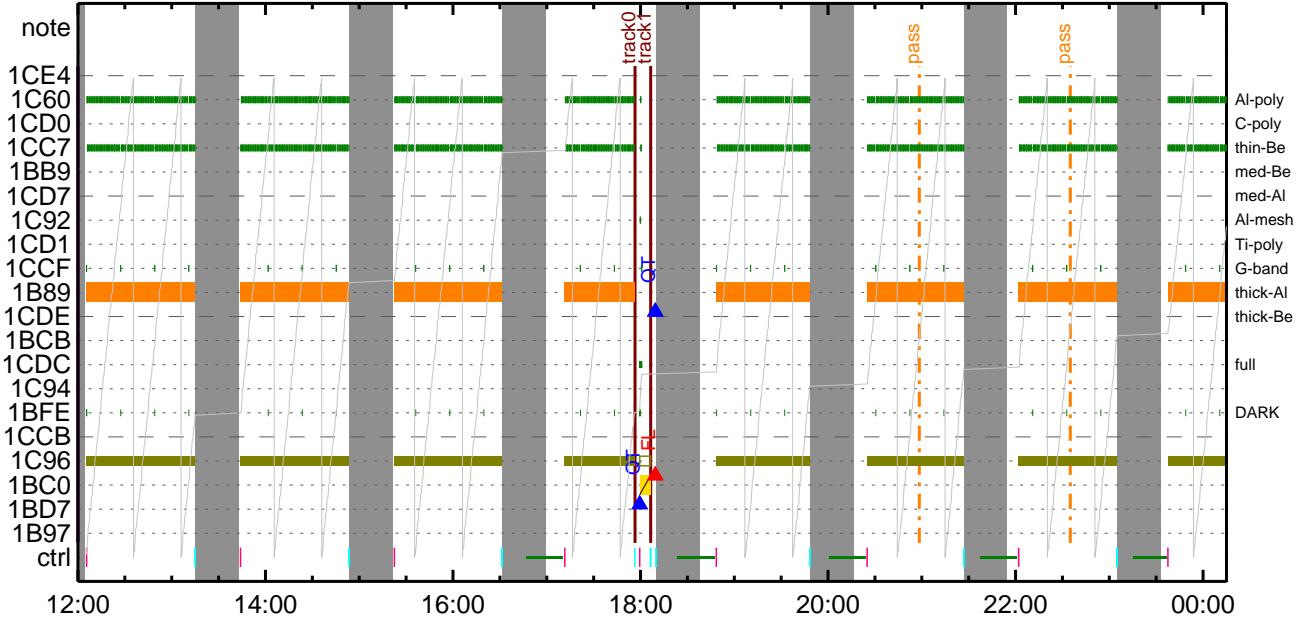
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FLD Patrol												
Term		Pointing (x, y)						Comment				
07/23 10:38:30 - 07/23 17:56:46		cannot be identified										
07/23 18:06:48 - 07/24 06:09:18		Track (626.6, 96.3) @ 07/23 18:06:30						AR 13060 obs				
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

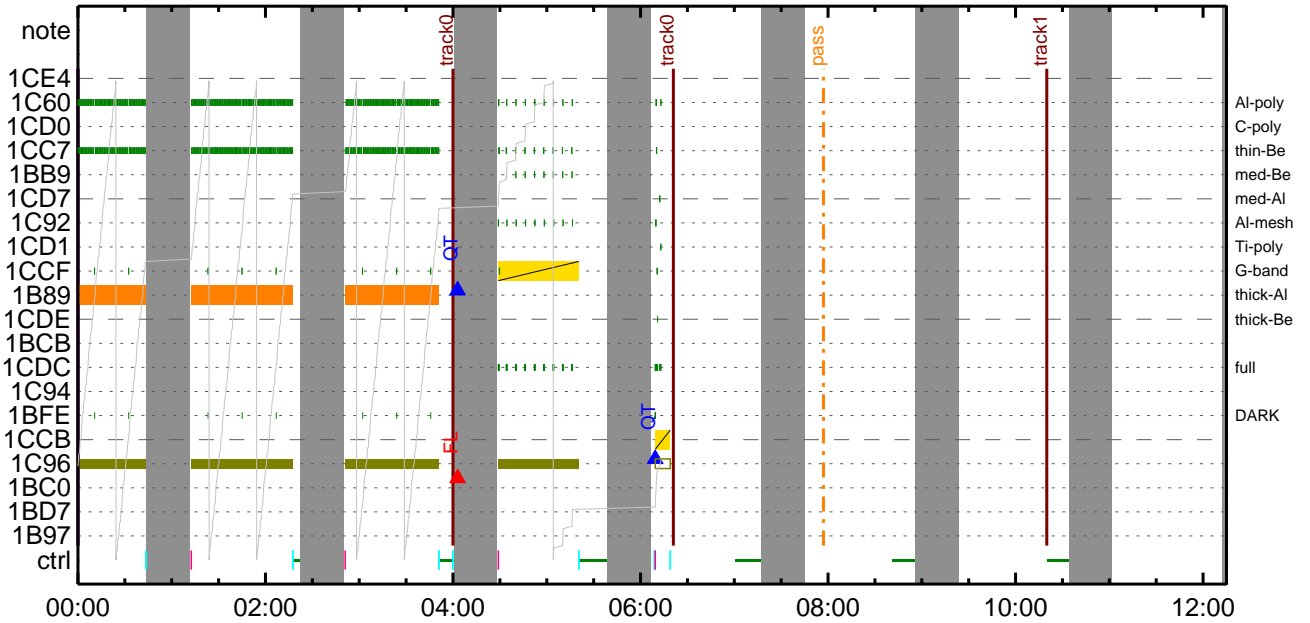
CMDI #0472 2022/07/23



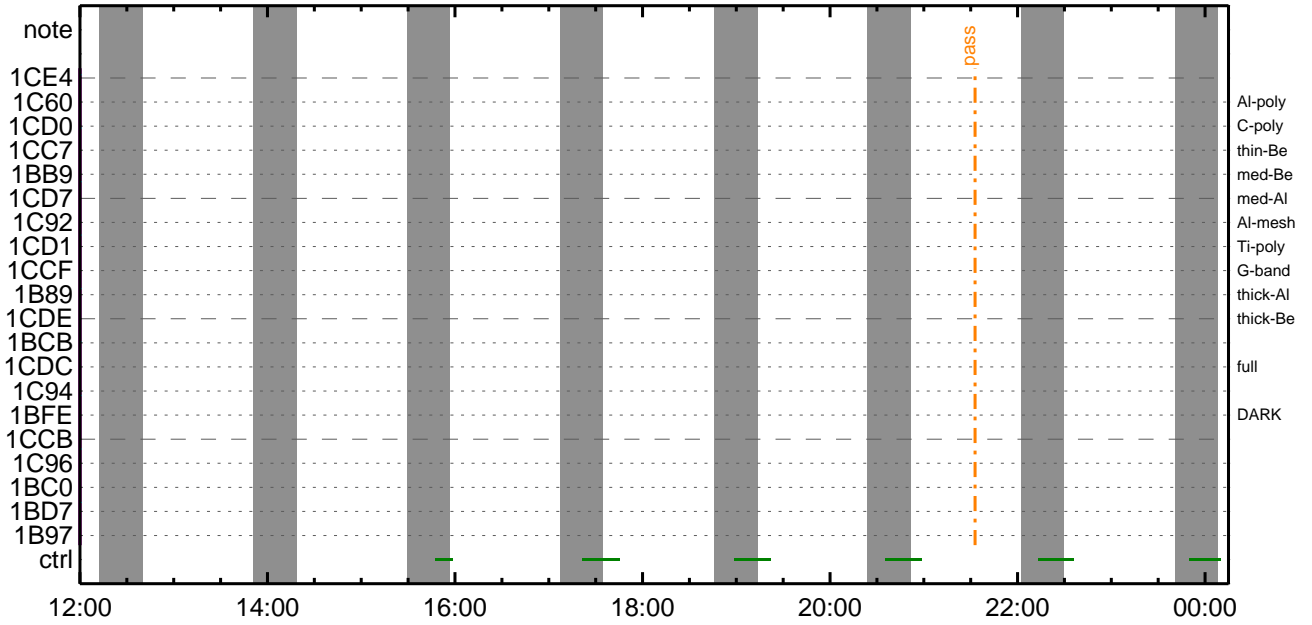
CMDI #0472 2022/07/23



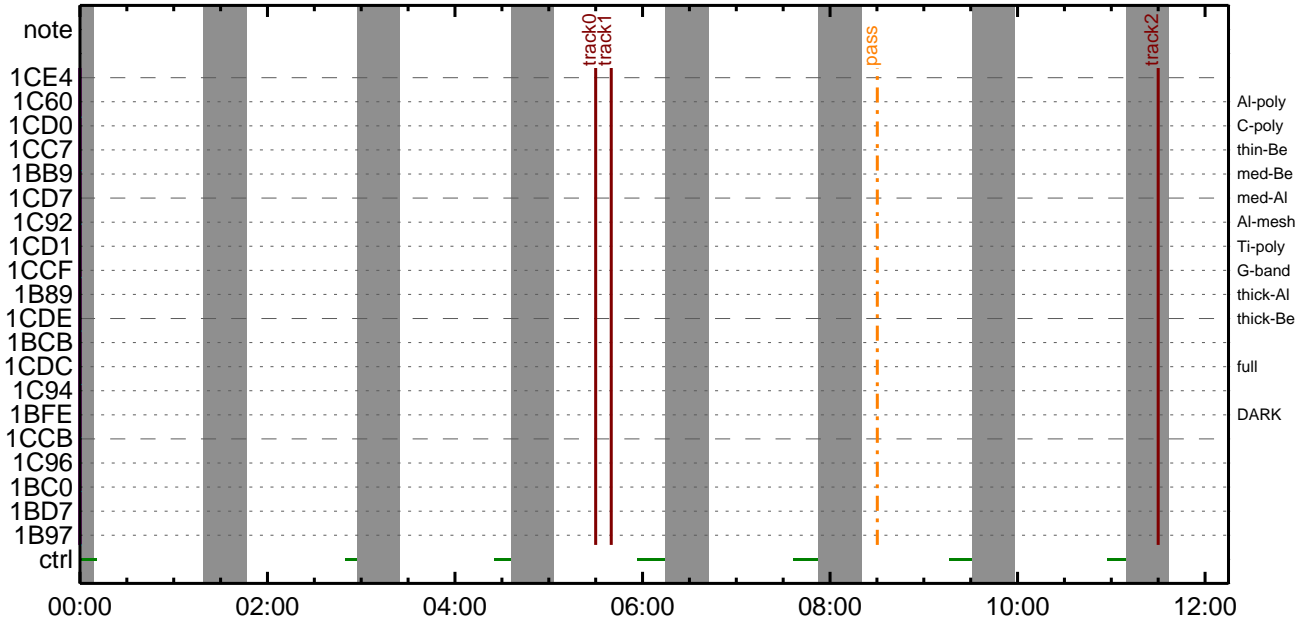
CMDI #0472 2022/07/24



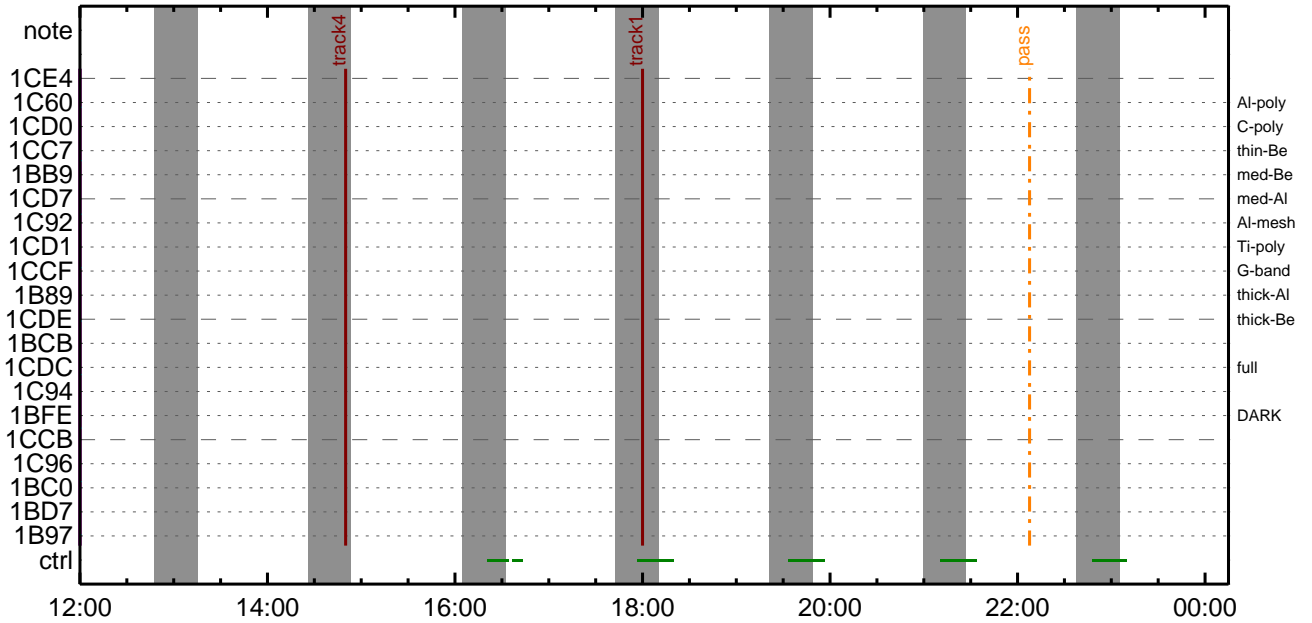
CMDI #0472 2022/07/24



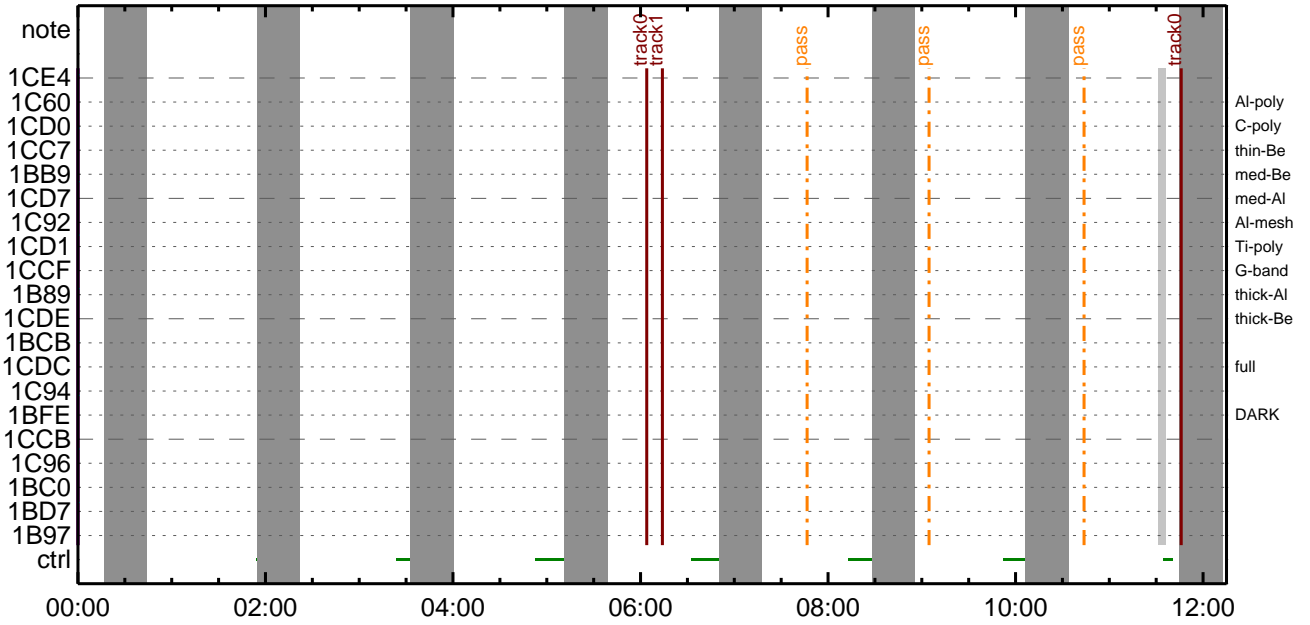
CMDI #0472 2022/07/25



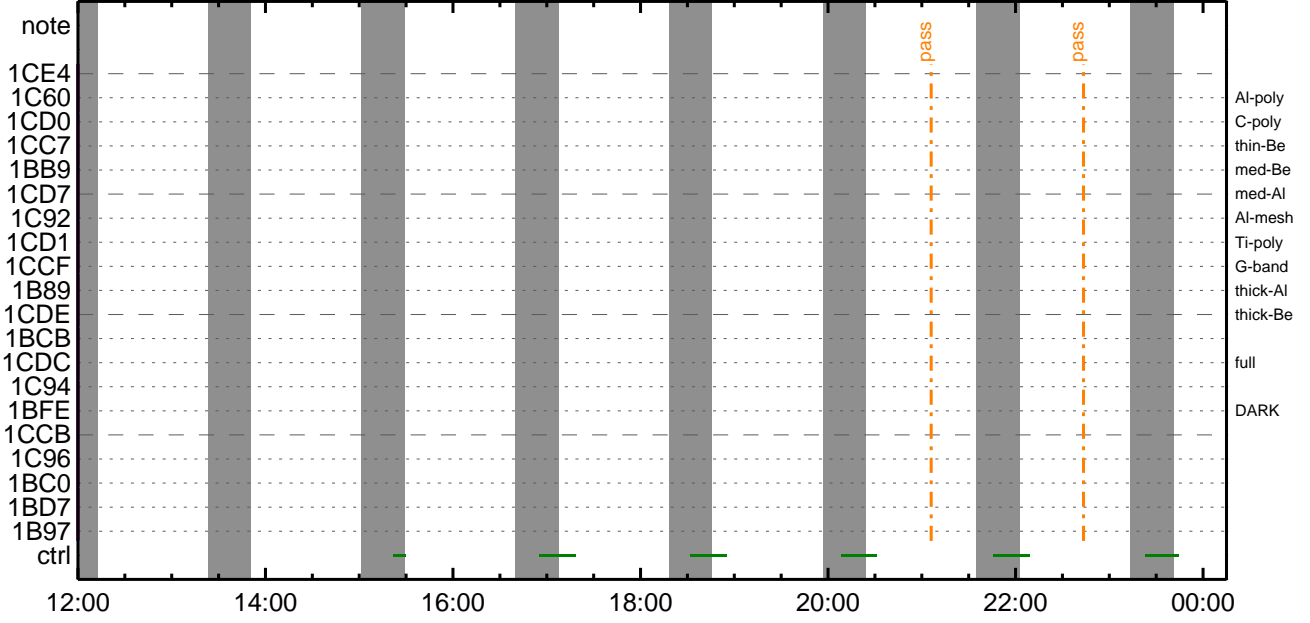
CMDI #0472 2022/07/25



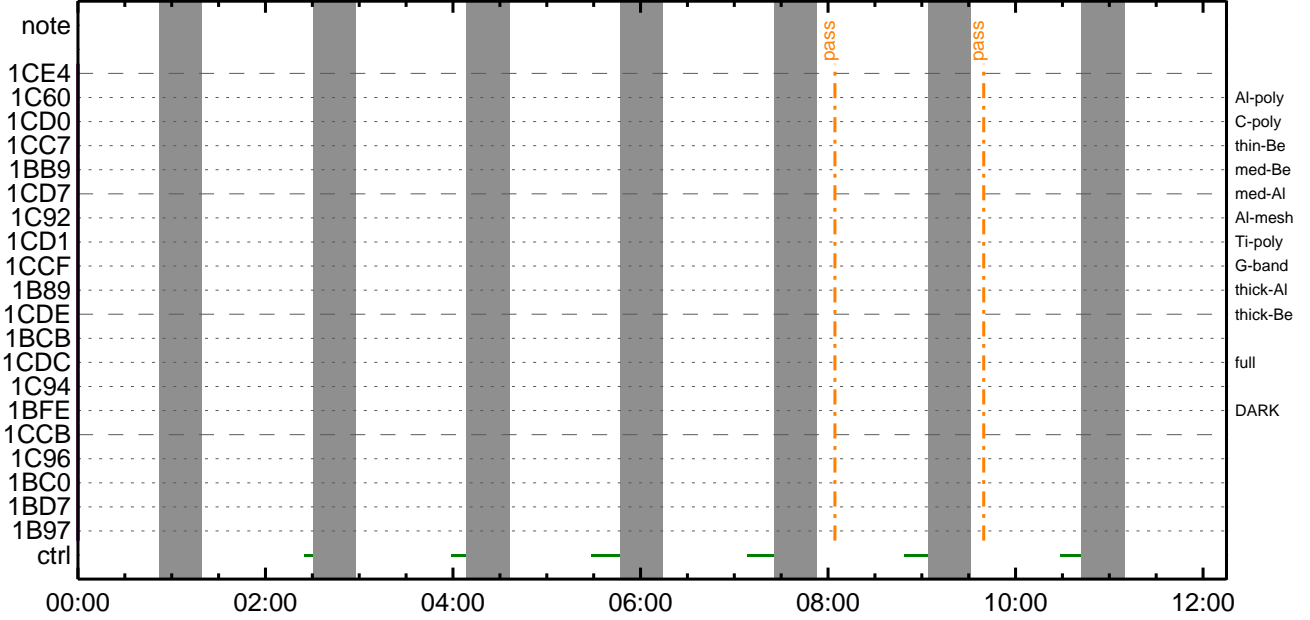
CMDI #0472 2022/07/26



CMDI #0472 2022/07/26



CMDI #0472 2022/07/27



(a) Spacecraft Operation Procedure (real-commands)

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main-301 2022-07-23 12:32:26 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY~¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YÞYÓYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçòÄð•µ°Æ»Í×ÁÇçÍYçYÄY×YÍ;¼YÉ;ÈÈè¼µ•ííÈ;ÈòÈ¼°ÇÒá•ç¼í¹ççí;çÄ®, ùñ¹äððßçÄ+ç®á•ðÈäðð³äÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç@µ;ON
0016 C. *****
0017 C. ç" °ÆÀ, Í×ÈYðÄLOSðßççí»p´Òäð¹íí, ñ•; çÉÖÍ×ðÈXÄÓONáí¹ÒðÈíðÈäðð³äÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYÍYÄY~¼ÖÄÖð~°ÄÄèá•çç; ç°È²¼ççí°ÆÀ, ¼è¼ççð¼Ä¹Òçç;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÆÄ,
0033 C. *****
0034 C. ç" RESTART;ÈPT1;Èá•çç¼¼í¹ççí; ç°È²¼ççí°ÆÄ¹Òçç»°; çDCBC-150ççÈä;ç
0035 C.
0036 . C. ;ãPT1°ÆÄ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ò, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò, ;¼Ú)
0045 C.
0046 . C. ;ãYçYóYÄYÈÄÙÄØ;ÈÄ•Ä°²óÈð;È, äçí°ÆÄ, °Æ³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ò, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ç~¼«Æ°Ää»ßçç; ä; ç°È²¼ççí°ÆÄ¹Òçç;ç
0055 C. YçYóYÄYÈÄÙÄØðäÄ•Ä°²óÈð~¼¼äçç¼í¹ççí°ÆÄ¹Òçç»°; çDCBC-150ççÈä;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÆÄ,
0059 C. *****
0060 C. ç" RESTART;ÈPT2;Èá•çç¼¼í¹ççí; ç°È²¼ççí°ÆÄ¹Òçç»°; çDCBC-151ççÈä;ç
0061 C.
0062 . C. ;ãPT2°ÆÄ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ò, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò, ;¼Ú)
0071 C.
0072 . C. ;ãYçYóYÄYÈÄÙÄØ;ÈÄ•Ä°²óÈð;È, äçí°ÆÄ, °Æ³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ò, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ß;çXÁ+ç@µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÄ, Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç@µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```



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0096 . C.
0097 . C.
0098 . C. *****
0099 . C. OP/OGY1;4YE;ã
0100 . C. *****
0101 . C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-301:OP
0104 . ( )
0105 . S. OG og-301:OG
0106 . ( )
0107 . C.
0108 . C. ;ãNMOG&OPÎÎ°èYÅYôYx;ã
0109 . C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 . C. çç[HK1_PKT_FORM_NO] EQ 7
0120 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YÅYôYx½ªÎ»ð³ÎÇ§
0125 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²Î¼Ë¹ç•ë²ÏOK²³ÎÇ§
0127 . C.
0128 . C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 . C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 . C. çç[HK1_PKT_FORM_NO] EQ 7
0139 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YÅYôYx½ªÎ»ð³ÎÇ§
0144 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²Î¼Ë¹ç•ë²ÏOK²³ÎÇ§
0146 . C.
0147 . C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 . C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 . C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 . C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 . C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 . C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 . C. çç[HK1_PKT_FORM_NO] EQ 7
0158 . C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 . C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YÅYôYx½ªÎ»ð³ÎÇ§
0163 . C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OP²Î¼Ë¹ç•ë²ÏOK²³ÎÇ§
0165 . C.
0166 . C. ***** òË²¼òÎ¼Ã´¶Á°òËË¬ò°Ã÷¿® (¼âµ-YÅYôYx½ªË½ç²ðÁÔÃæ²ç¼ª°²ðË¼¹ç²ç²â) *****
0167 . C. DHU¥â;¼YE;Ë½Y½;¥i;¼YE;Ë²ðÎã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 . C. çç[HK1_PKT_FORM_NO] EQ 2
0171 . C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 . C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 . C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 . C.
0175 . C. *****
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)
0177 . C. *****
0178 . C.
0179 . C. NOTICE ;§ OPOG UPLOAD²-Á÷¿®NG²Î¼¹ç;ç°Ë²¼òÎTI-CMDÁ÷¿®²Î¼¹Ô²°²Ë²²²³²Ë;£
0180 . C. ò²²¿;çSET²EDUMP²ÎÆ²±²iYÑY¹²ç¹Ô²³²²²;£
0181 . C.
0182 . C. TIY³Y²YôYË²ððÁDÎ¿(UT)
0183 +. TI 2022-07-23 11:23:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 . C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 . C.
0187 +. TI 2022-07-23 11:23:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 . C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 . C.
0191 +. TI 2022-07-23 11:23:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 . C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
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0194 C.
0195 +. TI 2022-07-23 11:27:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.               꺽꺽[HK1_TI_CMD_NUM]                               EQ      1COUNTUP
0198 C.
0199 C. °Ê¼ºíÄè%îíñºîîŷÅŷŷÅŷ-¹àîÛ
0200 C.               꺽꺽[HK1_TI_CMD_ENA/DIS]                       EQ      ENA
0201 C.               꺽꺽[HK1_TI_CMD_NUM]                             EQ      4
0202 C.               꺽꺽[HK1_NEXT_EXEC_PIM]                           EQ      DHU
0203 C.               꺽꺽[HK1_NEXT_EXEC_DC]                           EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÅŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C.               꺽꺽[HK1_DMP_TOP_ADRS_1]                       EQ      07
0213 C.               꺽꺽[HK1_DMP_TOP_ADRS_0]                       EQ      2B
0214 C.               꺽꺽[HK1_DMP_BLOCK_NUM]                          EQ      3
0215 C.               꺽꺽[HK1_DMP_REPEAT_NUM]                       EQ      0
0216 C.               꺽꺽[HK1_DMA_DMP_PIM]                           EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C.               꺽꺽[HK1_PKT_FORM_NO]                           EQ      7
0220 C.               꺽꺽[HK1_PKT_GEN_TIME]                           EQ      0.25 s
0221 C.               꺽꺽[HK1_S_TLM_BIT_RATE]                        EQ      32k
0222 C.               꺽꺽[HK1_X_TLM_BIT_RATE]                        EQ      4M
0223 C.               꺽꺽[HK1_DMP_CHK_FLG]                           EQ      EXEC
0224 C.
0225 C. ŷÅŷÖŷ×½ªî»º³îÇŞ
0226 C.               꺽꺽[HK1_DMP_CHK_FLG]                           EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLºîî¼È¹Çªë²ìOKºº³îÇŞ
0229 C.
0230 C. DHUŷª;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èººîªº¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C.               꺽꺽[HK1_PKT_FORM_NO]                           EQ      2
0234 C.               꺽꺽[HK1_PKT_GEN_TIME]                           EQ      0.5S
0235 C.               꺽꺽[HK1_S_TLM_BIT_RATE]                        EQ      32K
0236 C.               꺽꺽[HK1_X_TLM_BIT_RATE]                        EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2022-07-23 11:27:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2022-07-23 11:27:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2022-07-23 11:27:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C.               [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2022-07-23 11:27:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C.               [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `úÃîîï»ö¼ŷºëÄº¹ºèDCBC•x²è *****
0276 C. (¼ªºîîŷÅŷÈŷŷŷÈŷªŷÇŷèºÈ¼ºº¼ª»Ûº¹ºè)
0277 C. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹.ï Daily±çîñºë'ºº¹ºèDCBC•x²è *****
0282 C. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÅŷŷŷÅŷ-¼ª»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-281:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2022-07-23 11:27:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 20)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 08)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 08 20)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 80 80 08 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0f 80 80 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 10 80 80 08 08)
0172 + DC 07-F0 MDP_XRT_FLD_ENA
0173 BC (d8)
0174 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0175 BC (c8)
0176 + DC 07-F0 MDP_XRT_ARS_DIS
0177 BC (d5)
0178 + DC 07-F0 MDP_XRT_AEC_RESET
0179 BC (d0)
0180 + DC 07-F0 MDP_XRT_FLD_RESET
0181 BC (da)
0182 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0183 BC (c4 06)
0184 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0185 BC (c5 04)
0186 . C. ----- Success Verify ? OK / NG ____
0187 C.
0188 C.
0189 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0190 C.
0191 +. DC 07-F0 MDP_XRT_MODE_OBSV
0192 BC (c2)
0193 +. TI 2022-07-23 11:27:02.0

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0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 . C. ----- Success Verify ? OK / NG _____
0197 . C.
0198 . C. ***** XRT END *****
0199 . C. ===== Begin of AOCs CMD Sequence =====
0200 . C.
0201 . C. *****
0202 . C. ***** GAsYÇ;¼Yç¼èEÄ¼À»Ü *****
0203 . C. *****
0204 . C.
0205 . C. *****
0206 . C. MDRV OFF
0207 . C. *****
0208 . C.
0209 . C. ***** GAsYâYËYç¼ñíñçá MTQ¶íÆ°°i»pÄä»ß *****
0210 +. DC 02-33 AOCU_MDRV-X_OFF
0211 +. DC 02-34 AOCU_MDRV-Y_OFF
0212 +. DC 02-35 AOCU_MDRV-Z_OFF
0213 . C. [] <A_AOS> [COMPONENT STS] <MDRV> X = OFF ?
0214 . C. [] <A_AOS> [COMPONENT STS] <MDRV> Y = OFF ?
0215 . C. [] <A_AOS> [COMPONENT STS] <MDRV> Z = OFF ?
0216 . C.
0217 . C.
0218 . C. ;úYÇ;¼Yç¼èEÄñíñçá;çÌòlminÂÔµ;
0219 . C.
0220 . C. *****
0221 . C. MDRV ON
0222 . C. *****
0223 . C.
0224 . C. ***** MTQ¶íÆ°°E³« *****
0225 +. DC 02-32 AOCU_MDRV_ON
0226 . C. [] <A_AOS> [COMPONENT STS] <MDRV> X = ON ?
0227 . C. [] <A_AOS> [COMPONENT STS] <MDRV> Y = ON ?
0228 . C. [] <A_AOS> [COMPONENT STS] <MDRV> Z = ON ?
0229 . C.
0230 . C.
0231 . C. ===== End of AOCs CMD Sequence =====
0232 . C.
0233 . C.
0234 . C. ***** MDP ´ûÄîñí»ò¼YñEÄñíñçáèDCBC•x²è *****
0235 . C. (¼á°íYÖYÄYËYç¼èEÄ¼À»Ü¼ñíñçáè)
0236 . S. DC-BC dcbc-402:DCBC
0237 (MDP_known_event)
0238 . C.
0239 . C.
0240 . C. ***** YD¥¹·Ï Daily±;çíñçá´øñíñçáèDCBC•x²è *****
0241 . S. DC-BC dcbc-153:DCBC
0242 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0243 . C.
0244 . C.
0245 . C. ;äLOS¥Ä¥S¥Ä¥´¼Ä»Ü;ä
0246 . C.
0247 . C. ***** LOS *****
0248 . C.

*** OP Sequence for XRT ***

2022/07/23	11:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	11:36:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	11:36:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/23	11:36:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/23	11:37:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	11:37:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	11:37:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2022/07/23	11:38:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 05 81 01 99				
2022/07/23	11:38:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2022/07/23	11:38:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2022/07/23	11:38:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2022/07/23	11:38:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2022/07/23	11:38:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/23	11:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/23	11:40:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2022/07/23	11:40:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2022/07/23	12:04:30.0	XRT_Custom_430_OG [0x1ae]							
2022/07/23	12:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/23	13:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	13:15:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	13:15:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/23	13:15:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/23	13:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/23	13:43:00.0	XRT_Custom_430_OG [0x1ae]							
2022/07/23	13:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/23	14:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	14:53:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	14:53:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/23	14:53:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/23	14:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/23	15:21:30.0	XRT_Custom_430_OG [0x1ae]							
2022/07/23	15:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/23	16:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	16:31:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	16:31:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/23	16:31:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/23	16:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/23	17:10:31.0	XRT_Custom_430_OG [0x1ae]							
2022/07/23	17:11:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/23	17:56:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/23	17:56:26.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2022/07/23	17:56:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2022/07/23	17:56:46.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2022/07/23	17:56:48.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2022/07/23	17:56:50.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2022/07/23	17:59:28.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2022/07/23	17:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/23	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]							

2022/07/23	18:06:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/23	18:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/23	18:06:30.0	AOCs_Orе-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2022/07/23	18:06:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01	05	81 01 99
2022/07/23	18:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2022/07/23	18:06:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2022/07/23	18:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2022/07/23	18:06:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2022/07/23	18:09:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/23	18:09:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2022/07/23	18:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04	
2022/07/23	18:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/23	18:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/23	18:10:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/23	18:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/07/23	18:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/07/23	18:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2022/07/23	19:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/23	19:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/23	19:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/23	19:48:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/23	19:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/07/23	20:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/07/23	20:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2022/07/23	21:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/23	21:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/23	21:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/23	21:27:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/23	21:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/07/23	22:01:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/07/23	22:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2022/07/23	23:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/23	23:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/23	23:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/23	23:05:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/23	23:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/07/23	23:36:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/07/23	23:37:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2022/07/24	00:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/24	00:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/24	00:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/24	00:43:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/24	00:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/07/24	01:11:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/07/24	01:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2022/07/24	02:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/24	02:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/24	02:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/24	02:17:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da		

2022/07/24	02:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/24	02:50:00.0	XRT_Custom_430_OG [0x1ae]					
2022/07/24	02:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/24	03:51:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/24	03:51:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/24	03:51:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/24	03:51:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/24	03:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/24	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/24	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/24	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2022/07/24	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2022/07/24	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2022/07/24	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2022/07/24	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2022/07/24	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2022/07/24	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/24	04:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2022/07/24	04:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2022/07/24	04:28:00.0	XRT_Custom_430_OG [0x1ae]					
2022/07/24	04:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/24	05:20:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/24	05:20:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/24	05:20:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/24	05:20:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/24	05:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/24	06:08:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/24	06:08:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/24	06:08:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2022/07/24	06:09:18.0	XRT_FLD_DIS_409_OG [0x199]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2022/07/24	06:09:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2022/07/24	06:09:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2022/07/24	06:09:24.0	XRT_QT_PROG_SET_417_OG [0x1a1]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2022/07/24	06:09:26.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/24	06:19:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/24	06:19:04.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/24	06:20:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_425_OG [0x1a9]					
			TCIB_XRT_S_HTR_A_ENA	0	04-BC		
2022/07/24	06:21:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 b3 8e 00 00	
2022/07/24	10:20:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	01 05 81 01 99	
2022/07/25	05:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2022/07/25	05:40:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	01 05 81 01 99	
2022/07/25	11:30:00.5	AOCS_ORe-point_Start_4_OG [0x09a]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2022/07/25	14:50:00.0	AOCS_ORe-point_Start_5_OG [0x09b]					
			AOCU_NM	5	02-76	04 00 00 00 00	
2022/07/25	18:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	01 05 81 01 99	
2022/07/26	06:04:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2022/07/26	06:14:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	01 05 81 01 99	
2022/07/26	11:46:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	