

XRT Timeline to be uploaded on 2022/07/28

Period: 2022/07/28 10:55:00 - 2022/08/02 10:36:00

* * * * *

Normal mode

* * * * *

XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
07/28 13:33:00 - 07/28 17:38:24	Track (-671.7, 205.0) ^{Ⓢ 07/28 13:30:00}	AR 13067 obs
07/28 18:29:00 - 07/29 03:31:30	Track (-646.6, 202.4) ^{Ⓢ 07/28 17:48:30}	AR 13067 obs
07/29 12:21:00 - 07/29 17:59:54	Track (-531.0, 192.4) ^{Ⓢ 07/29 11:45:00}	AR 13067 obs
07/29 19:04:00 - 07/29 23:59:54	Track (-485.9, 189.3) ^{Ⓢ 07/29 18:10:00}	AR 13067 obs

PROG= 09 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec										
Seqn= 92 1-time(s) 2.0sec										
Open/G-band Open/G-band open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band Open/G-band close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly Open/thick-Al close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec										
Seqn= 47 1-time(s) 2.0sec										
Al-poly/Open thin-Be/Open close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open thin-Be/Open close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open med-Be/Open close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open med-Be/Open close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96 4-time(s) 120.0sec										
Al-poly/Open thin-Be/Open close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open med-Be/Open close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open thin-Be/Open close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open med-Be/Open close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open thin-Be/Open close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open med-Be/Open close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
----------------	----------------	-----	------	-------	------	-----	-----	--------------------	-------	------------	----------

XOB #1BC0: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Th

Term	Pointing (x, y)	Comment
07/28 17:41:30 - 07/28 17:48:24	Fixed (0.0, 0.0)	synoptic, shifted -21.5 min
07/29 06:01:00 - 07/29 06:07:54	Fixed (0.0, 0.0)	HOP 349 and synoptic, shifted -2.0 min
07/29 18:03:00 - 07/29 18:09:54	Fixed (0.0, 0.0)	synoptic
07/30 05:28:00 - 07/30 05:36:30	Fixed (0.0, 0.0)	HOP349 and synoptic

PROG= 03 1-time(s)

Subr= 1 1-time(s) 2.0sec										
Seqn= 5 1-time(s) 2.0sec										
Open/Ti-poly Open/thick-Al close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly Open/thick-Al close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly Open/thick-Al close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 63 1-time(s) 2.0sec										
Open/Al-mesh Open/Al-mesh close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh Open/Al-mesh close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh Open/Al-mesh close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15 1-time(s) 2.0sec										
Al-poly/Open Al-poly/Open close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open Al-poly/Open close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open Al-poly/thick-Al close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 27 1-time(s) 2.0sec										
thin-Be/Open thin-Be/Open close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open thin-Be/Open close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open thin-Be/Open close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 2.0sec										
Open/G-band Open/G-band open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band Open/G-band close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
----------------	----------------	-----	------	-------	------	-----	-----	--------------------	-------	------------	----------

XOB #1CCF: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 72min cad) + CME wa

Term	Pointing (x, y)	Comment
07/29 04:09:30 - 07/29 05:57:54	Fixed (0.0, 0.0)	HOP 349 and synoptic, shifted -2.0 min
07/30 04:44:30 - 07/30 05:24:54	Fixed (0.0, 0.0)	HOP349 and synoptic

PROG= 12 Inf.-time(s)

Subr= 1 1-time(s) 300.0sec										
Seqn= 55 1-time(s) 2.0sec										
Open/Al-mesh Open/Al-mesh close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh Open/Al-mesh close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh Open/Al-mesh close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15 1-time(s) 2.0sec										
Al-poly/Open Al-poly/Open close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open Al-poly/Open close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open Al-poly/thick-Al close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 79		1-time(s)		2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 30		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec			
Subr= 2		15-time(s)		360.0sec											
Seqn= 8		1-time(s)		2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec			
Seqn= 74		1-time(s)		2.0sec											
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec			
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec			
Seqn= 6		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec			
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec			
Seqn= 29		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec			
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

XOB #1B89: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with												
Term		Pointing (x, y)				Comment						
07/29 06:11:00 - 07/29 11:43:00	Track (402.8, -398.0)	⑥ 07/29 06:08:00		HOP439								
07/30 06:23:00 - 07/30 08:57:00	Track (577.2, -387.3)	⑥ 07/30 06:20:00		HOP439								

PROG= 11 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 92		1-time(s)		2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Subr= 2		5-time(s)		2.0sec									
Seqn= 47		1-time(s)		2.0sec									
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Seqn= 96		4-time(s)		60.0sec									
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1CDE: HOP393/336 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 7												
Term		Pointing (x, y)				Comment						
07/30 00:03:00 - 07/30 03:59:54	Track (-8.5, -217.3)	⑥ 07/30 00:00:00		HOP393								

PROG= 10 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 30		1-time(s)		2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 52		1-time(s)		2.0sec									
Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Subr= 2		30-time(s)		720.0sec									
Seqn= 97		2-time(s)		2.0sec									
Al-poly/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec	
Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

* * * * *

Flare mode

* * * * *

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G												
Term		Pointing (x, y)				Comment						
07/28 13:33:00 - 07/28 17:38:24	Track (-671.7, 205.0)	⑥ 07/28 13:30:00		AR 13067 obs								
07/28 18:29:00 - 07/29 03:31:30	Track (-646.6, 202.4)	⑥ 07/28 17:48:30		AR 13067 obs								
07/29 04:09:30 - 07/29 05:57:54	Fixed (0.0, 0.0)	HOP 349 and synoptic, shifted -2.0 min										
07/29 06:11:00 - 07/29 11:43:00	Track (402.8, -398.0)	⑥ 07/29 06:08:00		HOP439								
07/29 12:21:00 - 07/29 17:59:54	Track (-531.0, 192.4)	⑥ 07/29 11:45:00		AR 13067 obs								
07/29 19:04:00 - 07/29 23:59:54	Track (-485.9, 189.3)	⑥ 07/29 18:10:00		AR 13067 obs								
07/30 00:03:00 - 07/30 03:59:54	Track (-8.5, -217.3)	⑥ 07/30 00:00:00		HOP393								
07/30 04:44:30 - 07/30 05:24:54	Fixed (0.0, 0.0)	HOP349 and synoptic										
07/30 06:23:00 - 07/30 08:57:00	Track (577.2, -387.3)	⑥ 07/30 06:20:00		HOP439								
PROG= 04 30-time(s)												

Subr= 1		20-time(s)	2.0sec										
Seqn= 11		1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73		1-time(s)	10.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)	2.0sec										
Seqn= 10		1-time(s)	2.0sec										
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87		1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

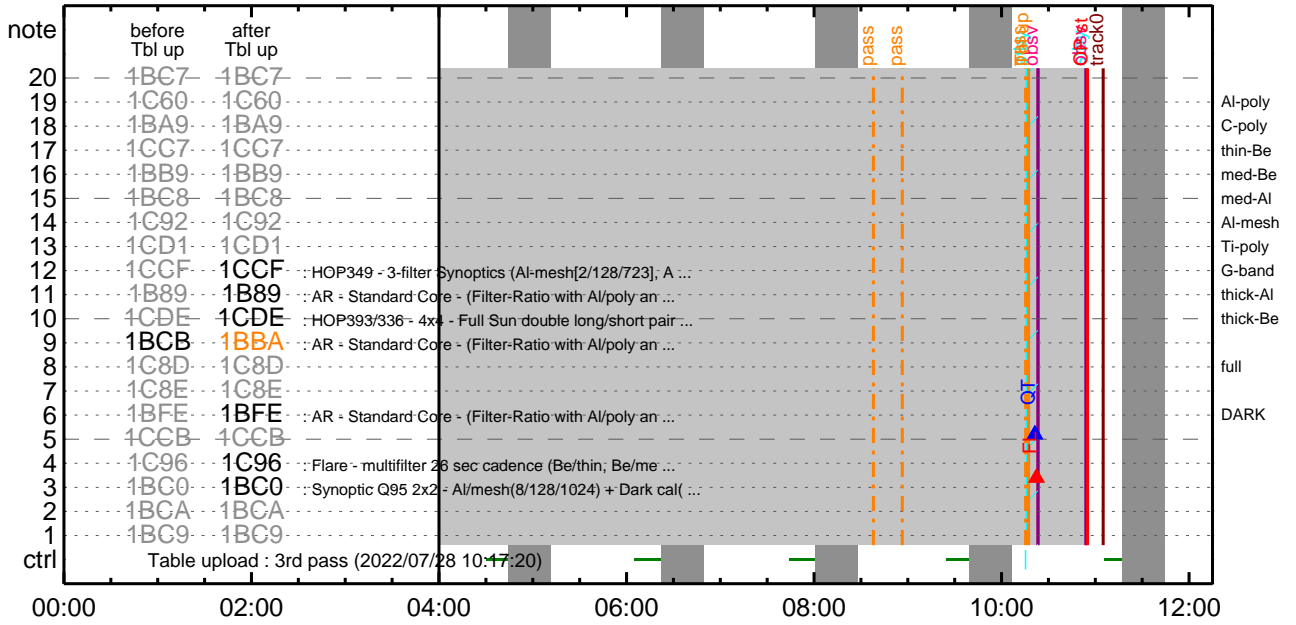
* * * * *

Flare Detection

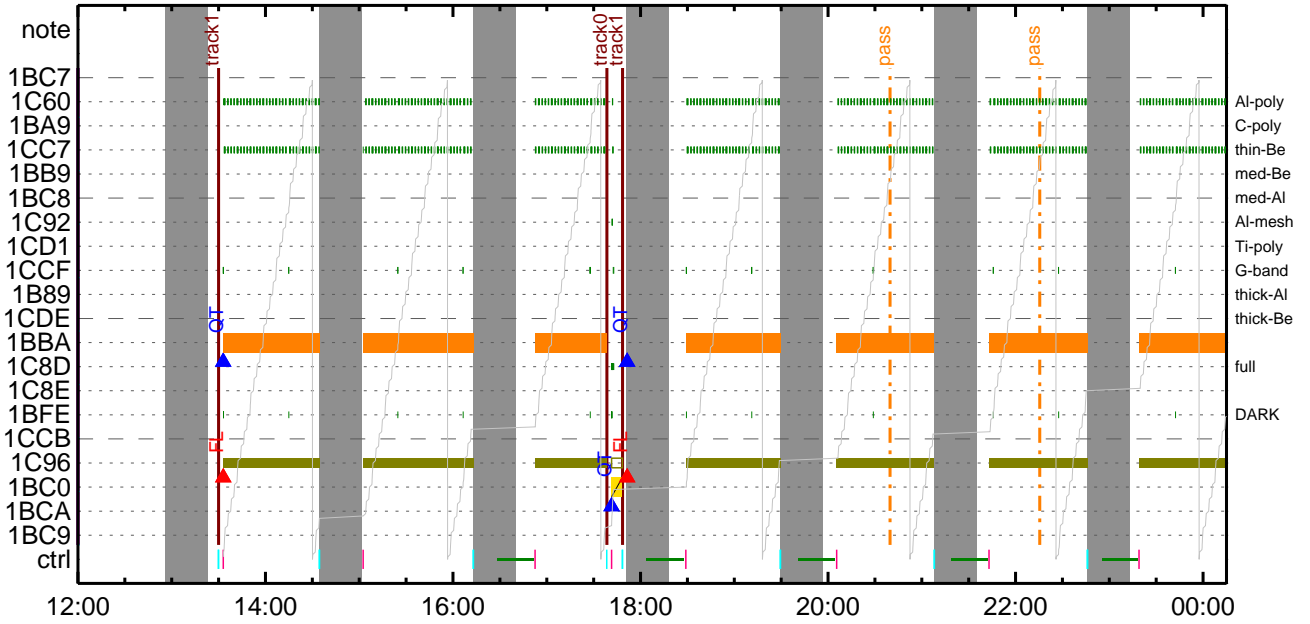
* * * * *

FLD Patrol												
Term	Pointing (x, y)								Comment			
07/28 10:18:20 - 07/28 17:38:46	cannot be identified											
07/28 17:48:48 - 07/29 05:58:16	Track (-646.6,	202.4)	[Ⓢ] 07/28 17:48:30	AR 13067	obs						
07/29 06:08:18 - 07/29 18:00:16	Track (402.8,	-398.0)	[Ⓢ] 07/29 06:08:00	HOP439							
07/29 18:10:18 - 07/30 05:25:16	Track (-485.9,	189.3)	[Ⓢ] 07/29 18:10:00	AR 13067	obs						
07/30 06:20:18 - 08/02 10:36:00	Track (577.2,	-387.3)	[Ⓢ] 07/30 06:20:00	HOP439							
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50	30sec		
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

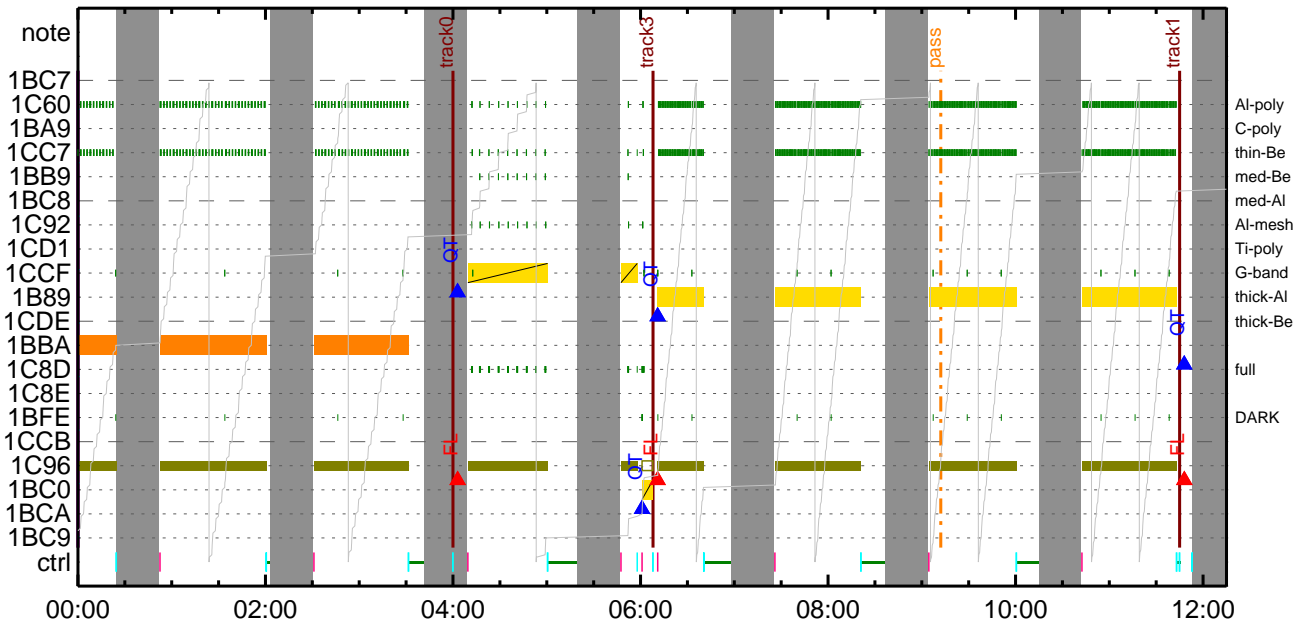
CMDI #0481 2022/07/28



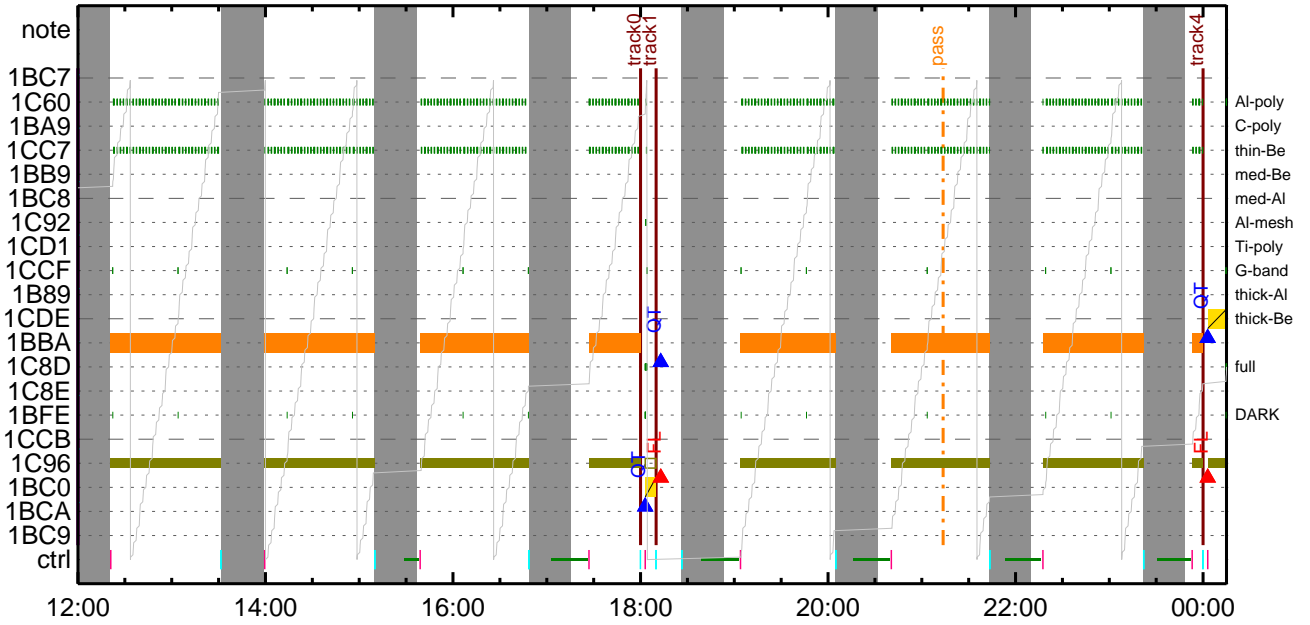
CMDI #0481 2022/07/28



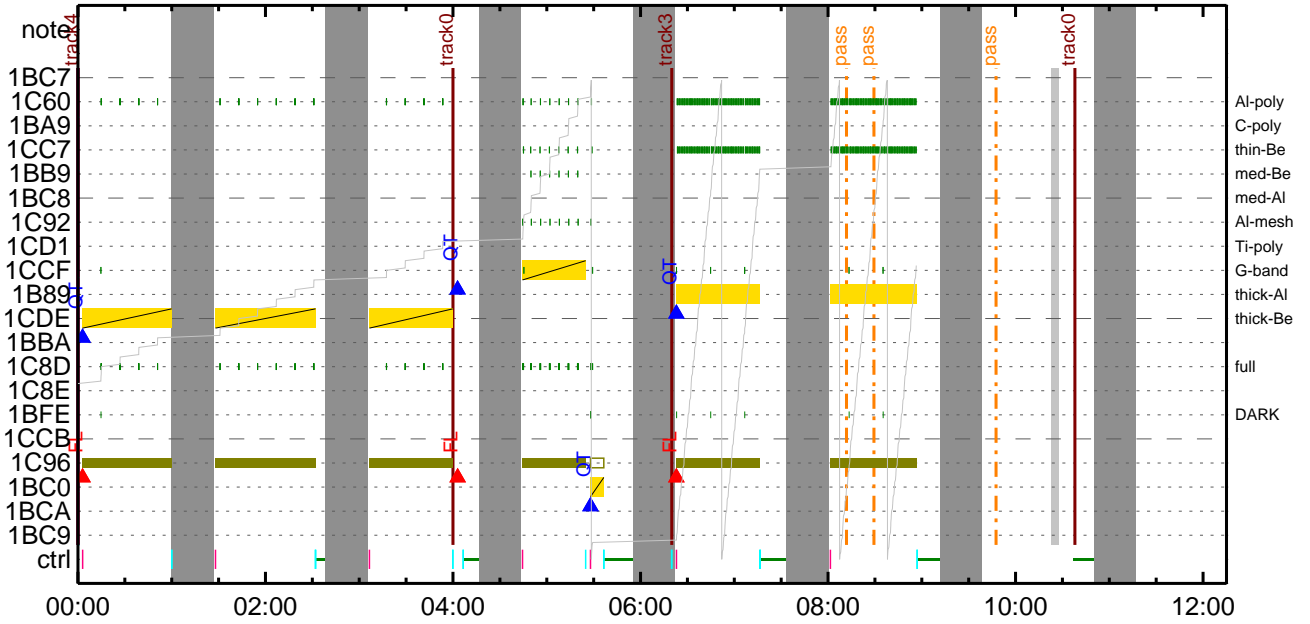
CMDI #0481 2022/07/29



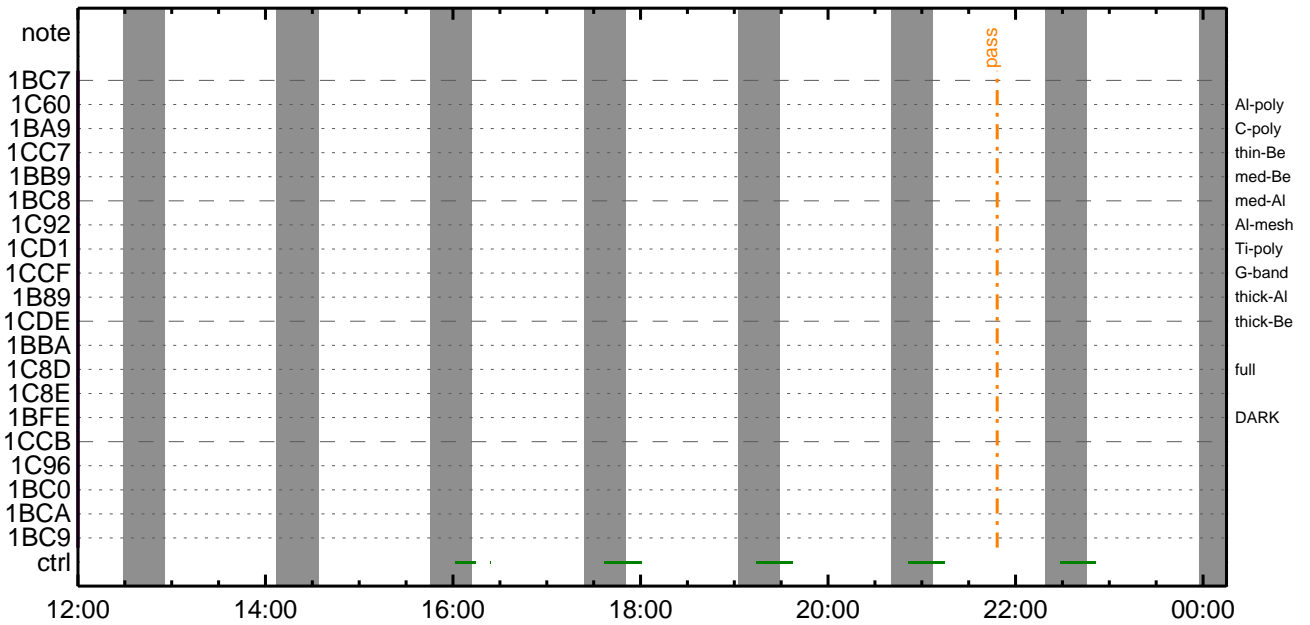
CMDI #0481 2022/07/29



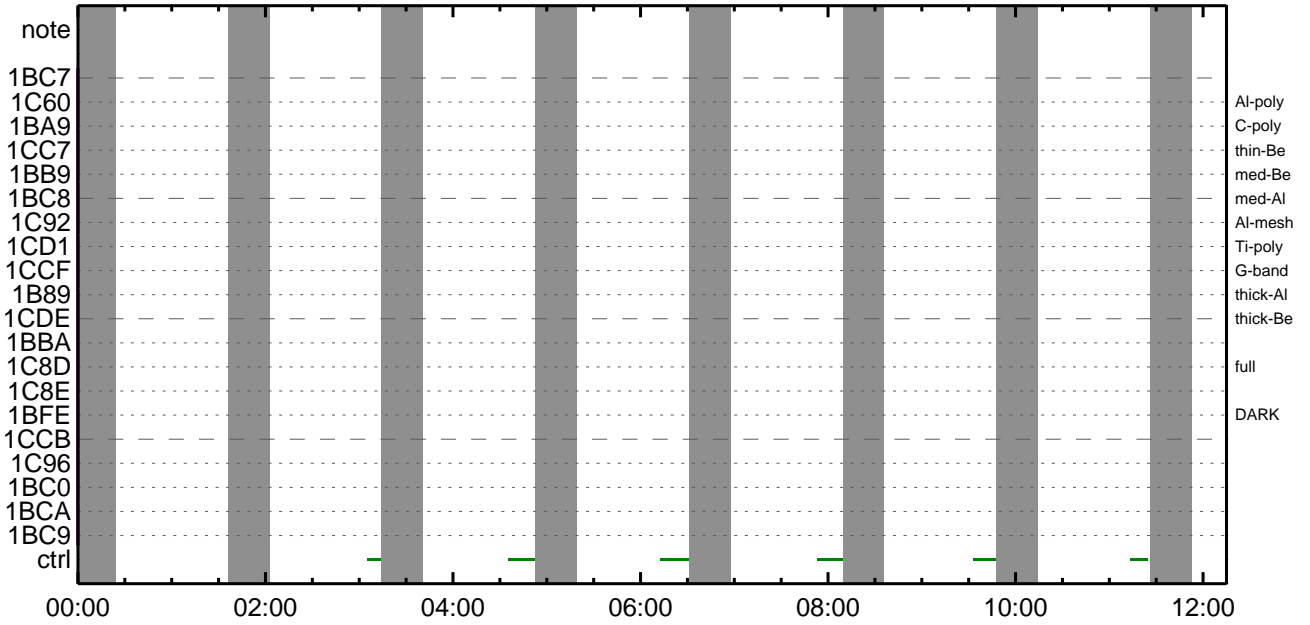
CMDI #0481 2022/07/30



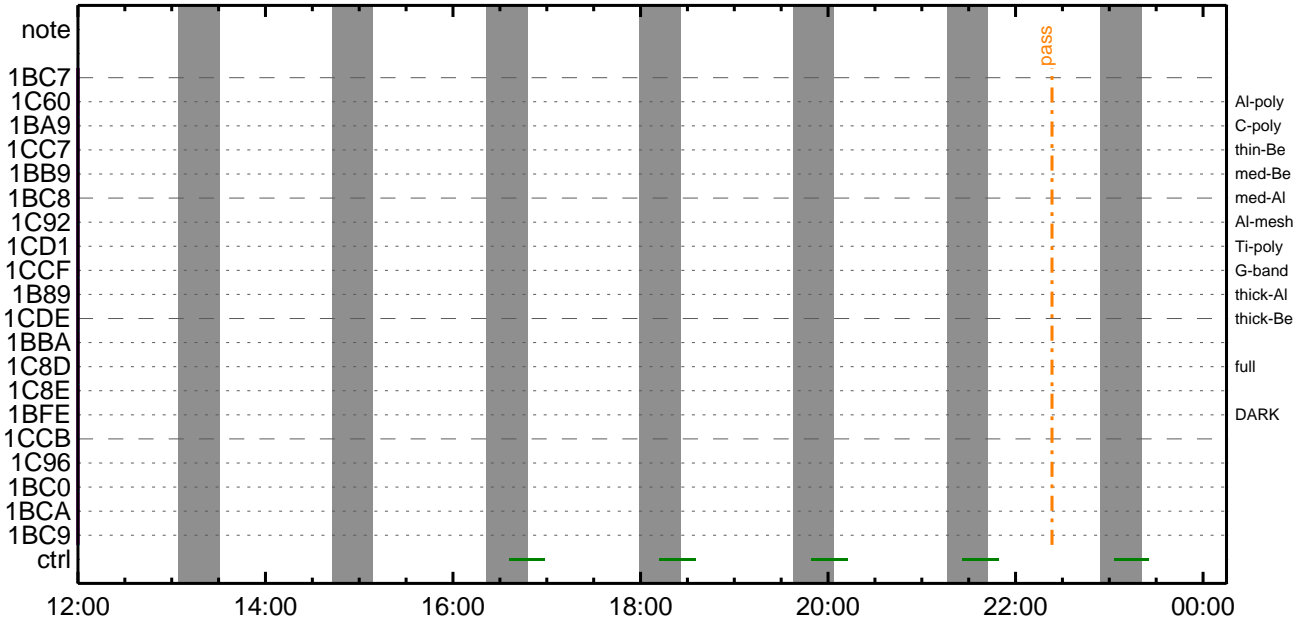
CMDI #0481 2022/07/30



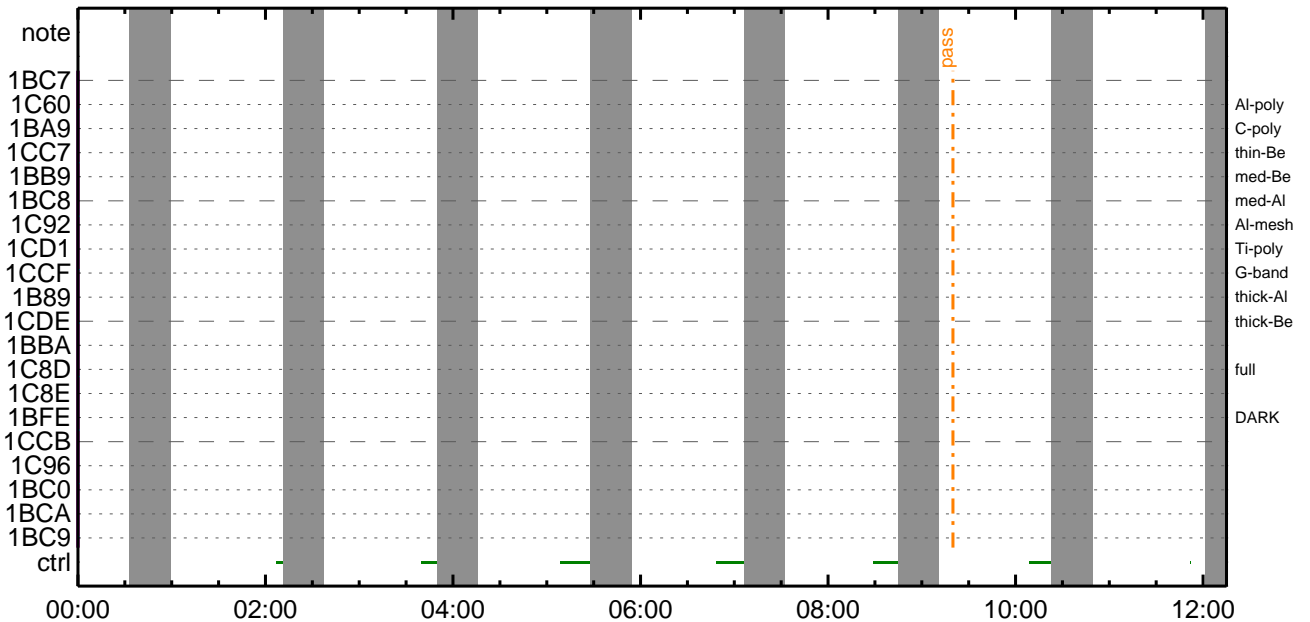
CMDI #0481 2022/07/31



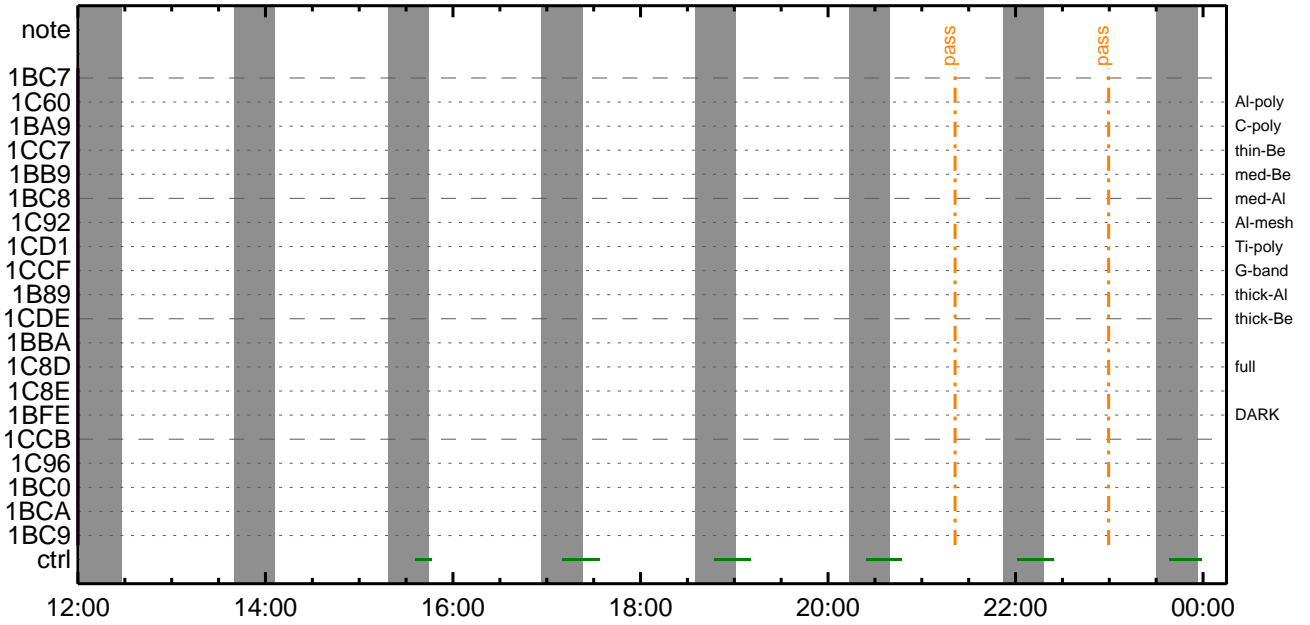
CMDI #0481 2022/07/31



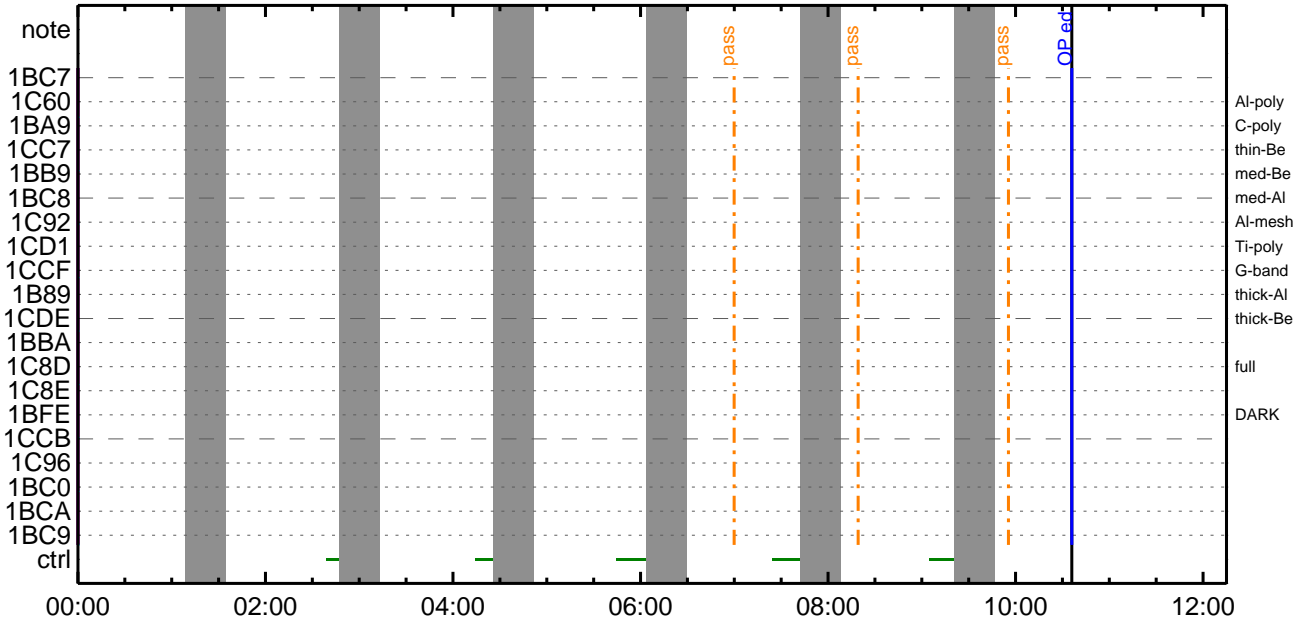
CMDI #0481 2022/08/01



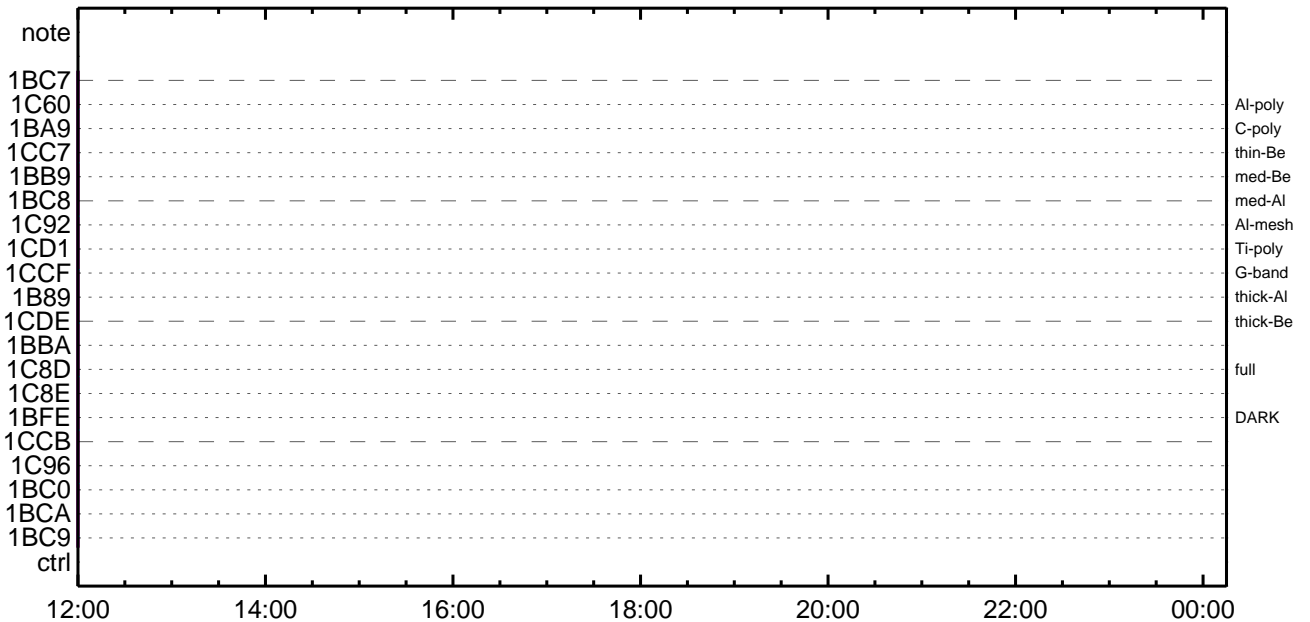
CMDI #0481 2022/08/01



CMDI #0481 2022/08/02



CMDI #0481 2022/08/02




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-310:OP
0104 ( )
0105 S. OG og-310:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°è¥ÅYó¥x;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥ÅYó¥x½ªî»òð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½Ê¹ç•è²îOK²ð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥ÅYó¥x½ªî»òð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½Ê¹ç•è²îOK²ð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥ÅYó¥x½ªî»òð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½Ê¹ç•è²îOK²ð³îÇ§
0165 C.
0166 C. ***** °Ê²¼²î½Ê¹ç•è²îOK²ð³îÇ§ *****
0167 C. DHU¥â;½¥Ê;Ê½¥½, ¥î;½¥Ê;Ê²ðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷çîNG²î½Ê¹ç•è²îOK²ð³îÇ§
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³¥P¥ó¥Ê²ðîã¹ç(UT)
0183 +. TI 2022-07-28 10:50:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2022-07-28 10:50:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2022-07-28 10:50:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```

0194 C.
0195 +. TI 2022-07-28 10:54:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C.      °Ê²¼αİÄê%İİñαİŷÄŷ§ŷÄŷ-¹àİŭ
0200 C.      çç[HK1_TI_CMD_ENA/DIS]             EQ      ENA
0201 C.      çç[HK1_TI_CMD_NUM]                EQ      4
0202 C.      çç[HK1_NEXT_EXEC_PIM]             EQ      DHU
0203 C.      çç[HK1_NEXT_EXEC_DC]             EQ      0xB3
0204 C.
0205 C.      *****
0206 C.      TIİİ°èŷÄŷÖŷ×
0207 C.      *****
0208 C.
0209 C.      TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      çç[HK1_DMP_TOP_ADRS_1]           EQ      07
0213 C.      çç[HK1_DMP_TOP_ADRS_0]           EQ      2B
0214 C.      çç[HK1_DMP_BLOCK_NUM]            EQ      3
0215 C.      çç[HK1_DMP_REPEAT_NUM]          EQ      0
0216 C.      çç[HK1_DMA_DMP_PIM]             EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      çç[HK1_PKT_FORM_NO]              EQ      7
0220 C.      çç[HK1_PKT_GEN_TIME]             EQ      0.25 s
0221 C.      çç[HK1_S_TLM_BIT_RATE]          EQ      32k
0222 C.      çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0223 C.      çç[HK1_DMP_CHK_FLG]             EQ      EXEC
0224 C.
0225 C.      ŷÄŷÖŷ×½ªİ»αò³İÇ§
0226 C.      çç[HK1_DMP_CHK_FLG]             EQ      NON
0227 C.
0228 C.      RAM ID=TI_TBLαİİ½Ê¹ç•è²İOKαò³İÇ§
0229 C.
0230 C.      DHUŷâ;¼ŷÉ;Ê¼ŷ¼.ŷİ;¼ŷÈ;Êαòİãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      çç[HK1_PKT_FORM_NO]              EQ      2
0234 C.      çç[HK1_PKT_GEN_TIME]             EQ      0.5S
0235 C.      çç[HK1_S_TLM_BIT_RATE]          EQ      32K
0236 C.      çç[HK1_X_TLM_BIT_RATE]          EQ      4M
0237 C.
0238 C.      *****
0239 C.      SOT TI command set
0240 C.      *****
0241 C.      Execute, after the success of OP upload.
0242 +. TI 2022-07-28 10:54:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C.      -----
0246 C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C.      -----
0248 C.      ***** SOT END *****
0249 C.      Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C.      ***** Start EIS operation (TI set) *****
0253 C.      Execute, after the success of OP upload.
0254 C.      Set EIS TI-commands
0255 +. TI 2022-07-28 10:54:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2022-07-28 10:54:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.      [ ] [HK1_TI_CMD_NUM]           EQ      2 COUNTUP
0262 C.      ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C.      ***** XRT START *****
0267 C.      Execute, after the success of OP upload.
0268 +. TI 2022-07-28 10:54:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]           EQ      1COUNTUP
0272 C.
0273 C.      ***** XRT END *****
0274 C.
0275 C.      ***** MDP ´úÃİαİ»ö¼ŷαÊÄα¹αèDCBC•x²è *****
0276 C.      (¼ã°İŷÖŷÄŷÈŷŷŷÈŷáŷçŷèèÊ¼αα¼Ã»Ūα¹è)
0277 C.      S. DC-BC dcbc-402:DCBC
0278 C.      (MDP_known_event)
0279 C.
0280 C.
0281 C.      ***** ŷĐŷ¹•İ Daily±çİñèË'Øα¹αèDCBC•x²è *****
0282 C.      S. DC-BC dcbc-153:DCBC
0283 C.      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C.      ;ãLOSŷÄŷ§ŷÄŷ-¼Ã»Ū;ã
0287 C.
0288 C.      ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-311 2022-07-28 11:38:15 85 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿òÁò•µ°È»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòÍ;çÁ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_CHG_ENA
0047 BC (20)
0048 . C. Verify EIS_MODE_CHG_FLG is ENA
0049 +. DC 07-FC EIS_MODE_MANU
0050 BC (21 02)
0051 . C. Verify EIS in MANUAL mode
0052 . C. Estimated OBSTBL upload time is 31s
0053 C. *****
0054 C. EIS START OBSTBL LOAD
0055 C. *****
0056 . S. RAM ram-820:EIS_OBSTBL
0057 ( )
0058 +. DC 07-FC EIS_DUMP_OBSTBL
0059 BC (07 07 07 00 00 70 00)
0060 C.
0061 C. Execute, after the success of OBSTBL upload.
0062 C. Set EIS TI-commands
0063 +. TI 2022-07-28 10:54:50.0
0064 DC 07-FC EIS_MODE_CHG_ENA
0065 BC (20)
0066 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0067 C. *****
0068 C. EIS END OBSTBL LOAD
0069 C. *****
0070 C.
0071 . C. ***** MDP `ûÁîòí»ò¼YòÈÁò¹òèDCBC•x²è *****
0072 C. (¼á°íYÓYÁYÈYpYÈYáYçYèò¼¼ò¼¼Á»Ûò¹òè)
0073 . S. DC-BC dcbc-402:DCBC
0074 (MDP_known_event)
0075 C.
0076 C.
0077 . C. ***** YDÝ¹.Ï Daily±¿íÑòÈ`Øò¹òèDCBC•x²è *****
0078 . S. DC-BC dcbc-153:DCBC
0079 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080 C.
0081 C.
0082 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0083 C.
0084 . C. ***** LOS *****
0085 C.
```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-283:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2022-07-28 10:54:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 20)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 08)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 08 20)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 80 80 08 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0f 80 80 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 10 80 80 08 08)
0172 + DC 07-F0 MDP_XRT_FLD_ENA
0173 BC (d8)
0174 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0175 BC (c8)
0176 + DC 07-F0 MDP_XRT_ARS_DIS
0177 BC (d5)
0178 + DC 07-F0 MDP_XRT_AEC_RESET
0179 BC (d0)
0180 + DC 07-F0 MDP_XRT_FLD_RESET
0181 BC (da)
0182 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0183 BC (c4 06)
0184 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0185 BC (c5 04)
0186 . C. ----- Success Verify ? OK / NG ____
0187 C.
0188 C.
0189 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0190 C.
0191 +. DC 07-F0 MDP_XRT_MODE_OBSV
0192 BC (c2)
0193 +. TI 2022-07-28 10:54:02.0

```

```

0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 . C. ----- Success Verify ? OK / NG ____
0197 . C.
0198 . C. ***** XRT END *****
0199 . C. ===== Begin of APCS CMD Sequence =====
0200 . C.
0201 . C. *****
0202 . C. ***** GASYSÇ;¼Yç¼èEÀ¼Á»Ü *****
0203 . C. *****
0204 . C.
0205 . C. *****
0206 . C. MDRV OFF
0207 . C. *****
0208 . C.
0209 . C. ***** GASYSâYËYç¼î¼ç¼á MTQ¼îE°°i»pÄä»ß *****
0210 +. DC 02-33 AOCU_MDRV-X_OFF
0211 +. DC 02-34 AOCU_MDRV-Y_OFF
0212 +. DC 02-35 AOCU_MDRV-Z_OFF
0213 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> X = OFF ?
0214 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = OFF ?
0215 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = OFF ?
0216 . C.
0217 . C.
0218 . C. ;úYÇ;¼Yç¼èEÀ¼î¼ç¼á;ç¼î¼minÄÔµ;
0219 . C.
0220 . C. *****
0221 . C. MDRV ON
0222 . C. *****
0223 . C.
0224 . C. ***** MTQ¼îE°°E³« *****
0225 +. DC 02-32 AOCU_MDRV_ON
0226 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> X = ON ?
0227 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = ON ?
0228 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = ON ?
0229 . C.
0230 . C.
0231 . C. ===== End of APCS CMD Sequence =====
0232 . C.
0233 . C.
0234 . C. ***** MDP `ûÄî¼î»ò¼Y¼èEÄ¼¼¼¼DCBC•x²è *****
0235 . C. (¼á°î¼Y¼Ä¼YË¼P¼YË¼á¼ç¼Yè¼E¼¼¼¼¼»Ü¼¼¼¼¼)
0236 . S. DC-BC dcbc-402:DCBC
0237 (MDP_known_event)
0238 . C.
0239 . C.
0240 . C. ***** YDÿ¹•İ Daily±;¼Î¼è´Ø¼¼¼¼DCBC•x²è *****
0241 . S. DC-BC dcbc-153:DCBC
0242 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0243 . C.
0244 . C.
0245 . C. ;ãLOS¼Y¼S¼Y¼Ä¼Y¼¼¼¼»Ü¼¼¼¼¼
0246 . C.
0247 . C. ***** LOS *****
0248 . C.

```

*** OP Sequence for XRT ***

2022/07/28	11:05:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	00	00
2022/07/28	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	13:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	13:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2022/07/28	13:30:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	05	81	01	99
2022/07/28	13:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2022/07/28	13:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2022/07/28	13:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2022/07/28	13:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2022/07/28	13:30:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/28	13:32:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2022/07/28	13:32:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2022/07/28	13:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/28	14:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	14:34:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	14:34:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/28	14:34:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/28	14:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/28	15:01:30.0	XRT_Custom_430_OG [0x1ae]							
2022/07/28	15:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/28	16:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	16:13:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	16:13:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/28	16:13:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/28	16:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/28	16:51:30.0	XRT_Custom_430_OG [0x1ae]							
2022/07/28	16:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/28	17:38:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	17:38:26.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2022/07/28	17:38:30.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	00	00	00	00
2022/07/28	17:38:46.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2022/07/28	17:38:48.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2022/07/28	17:38:50.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2022/07/28	17:41:28.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2022/07/28	17:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/28	17:48:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	17:48:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	17:48:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2022/07/28	17:48:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	05	81	01	99
2022/07/28	17:48:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2022/07/28	17:48:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2022/07/28	17:48:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2022/07/28	17:48:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2022/07/28	17:48:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/28	17:51:26.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2022/07/28	17:51:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2022/07/28	18:28:00.0	XRT_Custom_430_OG [0x1ae]							

2022/07/28	18:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/28	19:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	19:29:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	19:29:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/28	19:29:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/28	19:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/28	20:04:30.0	XRT_Custom_430_OG [0x1ae]							
2022/07/28	20:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/28	21:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	21:08:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	21:08:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/28	21:08:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/28	21:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/28	21:42:00.0	XRT_Custom_430_OG [0x1ae]							
2022/07/28	21:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/28	22:46:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	22:46:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/28	22:46:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/28	22:46:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/28	22:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/28	23:18:00.0	XRT_Custom_430_OG [0x1ae]							
2022/07/28	23:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/29	00:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/29	00:24:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/29	00:24:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/29	00:24:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/29	00:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/29	00:51:30.0	XRT_Custom_430_OG [0x1ae]							
2022/07/29	00:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/29	02:00:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/29	02:00:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/29	02:00:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/29	02:00:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/29	02:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/29	02:30:00.0	XRT_Custom_430_OG [0x1ae]							
2022/07/29	02:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/29	03:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/29	03:31:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/29	03:31:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/29	03:31:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/29	03:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/29	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/29	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/29	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2022/07/29	04:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2022/07/29	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2022/07/29	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2022/07/29	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2022/07/29	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				

2022/07/29	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/29	04:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c	
2022/07/29	04:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04	
2022/07/29	04:08:30.0	XRT_Custom_430_OG [0x1ae]						
2022/07/29	04:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/29	05:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	05:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	05:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/29	05:00:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/07/29	05:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/07/29	05:46:30.0	XRT_Custom_430_OG [0x1ae]						
2022/07/29	05:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/29	05:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	05:57:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2022/07/29	05:58:16.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2022/07/29	05:58:18.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2022/07/29	05:58:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2022/07/29	06:00:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2022/07/29	06:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/29	06:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	06:07:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	06:07:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2022/07/29	06:08:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 05 81 01 99		
2022/07/29	06:08:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2022/07/29	06:08:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2022/07/29	06:08:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2022/07/29	06:08:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2022/07/29	06:08:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/29	06:10:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b	
2022/07/29	06:10:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04	
2022/07/29	06:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/29	06:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	06:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	06:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/29	06:40:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/07/29	06:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/07/29	07:25:00.0	XRT_Custom_430_OG [0x1ae]						
2022/07/29	07:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/29	08:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	08:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	08:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/29	08:21:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/07/29	08:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/07/29	09:03:30.0	XRT_Custom_430_OG [0x1ae]						
2022/07/29	09:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/07/29	10:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	10:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/07/29	10:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/07/29	10:00:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]						

2022/07/29	10:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/29	10:41:30.0	XRT_Custom_430_OG [0x1ae]					
2022/07/29	10:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/29	11:43:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	11:43:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	11:43:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/29	11:43:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/29	11:44:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	11:44:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	11:44:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2022/07/29	11:45:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	01 05 81 01 99	
2022/07/29	11:45:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2022/07/29	11:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2022/07/29	11:45:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2022/07/29	11:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2022/07/29	11:45:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/29	11:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/29	11:47:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2022/07/29	11:47:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2022/07/29	11:53:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	11:53:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	11:53:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/29	11:53:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/29	11:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/29	12:20:00.0	XRT_Custom_430_OG [0x1ae]					
2022/07/29	12:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/29	13:31:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	13:31:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	13:31:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/29	13:31:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/29	13:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/29	13:58:30.0	XRT_Custom_430_OG [0x1ae]					
2022/07/29	13:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/29	15:10:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	15:10:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	15:10:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/29	15:10:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/29	15:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/29	15:38:00.0	XRT_Custom_430_OG [0x1ae]					
2022/07/29	15:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/29	16:48:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	16:48:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	16:48:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2022/07/29	16:48:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/07/29	16:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/07/29	17:26:00.0	XRT_Custom_430_OG [0x1ae]					
2022/07/29	17:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/07/29	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/07/29	17:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]					

2022/07/29	18:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2022/07/29	18:00:16.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2022/07/29	18:00:18.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2022/07/29	18:00:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2022/07/29	18:02:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2022/07/29	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/07/29	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2022/07/29	18:10:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01	05	81	01
2022/07/29	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2022/07/29	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2022/07/29	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2022/07/29	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2022/07/29	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2022/07/29	18:12:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2022/07/29	18:12:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04		
2022/07/29	18:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	18:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	18:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2022/07/29	18:26:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2022/07/29	18:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2022/07/29	19:03:00.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2022/07/29	19:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/07/29	20:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	20:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	20:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2022/07/29	20:05:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2022/07/29	20:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2022/07/29	20:39:30.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2022/07/29	20:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/07/29	21:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	21:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	21:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2022/07/29	21:43:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2022/07/29	21:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2022/07/29	22:16:30.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2022/07/29	22:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/07/29	23:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	23:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	23:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2022/07/29	23:22:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2022/07/29	23:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2022/07/29	23:52:00.0	XRT_Custom_430_OG [0x1ae]	XRT_Custom_430_OG [0x1ae]						
2022/07/29	23:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/07/29	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/07/29	23:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00

2022/07/30	05:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/30	05:36:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/30	05:36:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/30	05:36:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/30	05:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/30	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/30	06:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/30	06:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2022/07/30	06:20:00.0	AOCS_Or-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03 05 81 01 99				
2022/07/30	06:20:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2022/07/30	06:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2022/07/30	06:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2022/07/30	06:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2022/07/30	06:20:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/30	06:22:00.0	XRT_Custom_430_OG [0x1ae]							
2022/07/30	06:22:56.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2022/07/30	06:22:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2022/07/30	06:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/30	07:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/30	07:16:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/30	07:16:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/30	07:16:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/30	07:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/30	08:00:30.0	XRT_Custom_430_OG [0x1ae]							
2022/07/30	08:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/07/30	08:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/30	08:57:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/07/30	08:57:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/07/30	08:57:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/07/30	09:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/07/30	10:38:00.0	AOCS_Or-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 00 00 00 00				