XRT Timeline to be uploaded on 2022/09/29

Period: 2022/09/29 10:57:00 - 2022/10/04 12:44:00

* * * * * **Normal mode** * * * * *

XOB #1B8F: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant -Al/mesh(512ms), Al/Poly(1443ms) - w leak image-1msCCD

Term	Pointing (x, y)	Comment
09/29 16:53:00 - 09/29 16:59:54	Fixed (-528.4, -528.4)	Post bakeout Q1

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 11 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 51 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 19 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec												
Seqn= 34 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1B90: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
09/29 17:03:00 - 09/29 17:09:54	Fixed (528.4, -528.4)	Post bakeout Q2

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 03 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 38 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 19 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec												
Seqn= 34 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1B91: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
09/29 17:13:00 - 09/29 17:19:54	Fixed (528.4, 528.4)	Post bakeout Q3

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 15 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 21 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 19 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec												
Seqn= 34 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1B92: CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Al/Poly (1443ms) - w leak image-1 ms

Term	Pointing (x, y)	Comment
09/29 17:23:00 - 09/29 17:32:30	Fixed (-528.4, 528.4)	Post bakeout Q4

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 19 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 14 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												

Seqn= 19 2-time(s) 2.0sec													
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 3 2-time(s) 2.0sec													
Seqn= 34 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

XOB #1CCB: Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(12/181/1443), Thin-Be(64/1024/5795) - Thick-Be(65536), Al-poly+Ti-poly(64/2048), Med-Al(204

Term	Pointing (x, y)	Comment
09/29 18:00:00 - 09/29 18:06:54	Fixed (0.0, 0.0)	synoptic, shifted -3.0 min

PROG= 05 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 63 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 15 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 27 1-time(s) 2.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 23 1-time(s) 4.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	

Subr= 2 1-time(s) 2.0sec

Seqn= 46 1-time(s) 2.0sec													
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 93 1-time(s) 2.0sec													
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 56 1-time(s) 2.0sec													
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
09/29 18:10:00 - 09/29 21:54:54	Track (472.3, -519.1) @ 09/29 18:07:00	Track AR 13107
09/30 00:20:30 - 09/30 03:59:54	Track (472.3, -519.1) @ 09/29 18:07:00	Track AR 13107
09/30 05:54:00 - 09/30 17:42:54	Track (547.2, -512.2) @ 09/30 05:51:00	Track AR 13107
09/30 17:56:00 - 09/30 21:21:00	Track (617.2, -504.2) @ 09/30 17:53:00	Track AR 13107
10/01 06:13:00 - 10/01 12:30:00	Track (680.5, -495.1) @ 10/01 06:10:00	Track AR 13107

PROG= 06 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	

Subr= 2 4-time(s) 2.0sec

Seqn= 47 1-time(s) 2.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Seqn= 77 4-time(s) 300.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

XOB #1CC2: HOP361 - High cadence (8s thin-Be only) 384x384 at 1064 1048

Term	Pointing (x, y)	Comment
09/29 21:58:00 - 09/30 00:01:00	Track (472.3, -519.1) @ 09/29 18:07:00	Track AR 13107

PROG= 14 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 22 250-time(s) 8.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

XOB #1CCF: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 72min cad) + CME wa

Term	Pointing (x, y)	Comment
09/30 04:03:00 - 09/30 05:40:54	Fixed (0.0, 0.0)	HOP349+synoptic, shifted -19.0 min
10/01 04:03:00 - 10/01 05:59:54	Fixed (0.0, 0.0)	HOP349+synoptic

PROG= 07 Inf.-time(s)

Subr= 1 1-time(s) 300.0sec

Seqn= 55 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 15 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 79 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 30 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 15-time(s) 360.0sec

Seqn= 8 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 74 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 29 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

XOB #1BC0: Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Th

Term	Pointing (x, y)	Comment
09/30 05:44:00 - 09/30 05:50:54	Fixed (0.0, 0.0)	HOP349+synoptic, shifted -19.0 min
09/30 17:46:00 - 09/30 17:52:54	Fixed (0.0, 0.0)	synoptic, shifted -17.0 min
10/01 06:03:00 - 10/01 06:09:54	Fixed (0.0, 0.0)	HOP349+synoptic

PROG= 18 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 63 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 15 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 27 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1CC3: HOP361 - High cadence (10s thin-Be only) 256x256 at 1064 1048

Term	Pointing (x, y)	Comment
09/30 21:45:30 - 09/30 23:59:54	Track (617.2, -504.2) @ 09/30 17:53:00	Track AR 13107

PROG= 02 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 12 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 28 250-time(s) 10.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	1024x1024 (1536, 1536)	Q=95	3	0	2.0sec

XOB #1CDE: HOP393/336 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 7

Term	Pointing (x, y)	Comment
10/01 00:03:00 - 10/01 03:59:54	Track (-21.0, -156.8) @ 10/01 00:00:00	HOP393

PROG= 10 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 30 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 52 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec

Subr= 2 30-time(s) 720.0sec

Seqn= 97 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

* * * * * Flare mode * * * * *

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment
09/29 18:10:00 - 09/29 21:54:54	Track (472.3, -519.1) @ 09/29 18:07:00	Track AR 13107
09/29 21:58:00 - 09/30 00:01:00	Track (472.3, -519.1) @ 09/29 18:07:00	Track AR 13107
09/30 00:20:30 - 09/30 03:59:54	Track (472.3, -519.1) @ 09/29 18:07:00	Track AR 13107
09/30 04:03:00 - 09/30 05:40:54	Fixed (0.0, 0.0)	HOP349+synoptic, shifted -19.0 min
09/30 05:54:00 - 09/30 17:42:54	Track (547.2, -512.2) @ 09/30 05:51:00	Track AR 13107
09/30 17:56:00 - 09/30 21:21:00	Track (617.2, -504.2) @ 09/30 17:53:00	Track AR 13107
09/30 21:45:30 - 09/30 23:59:54	Track (617.2, -504.2) @ 09/30 17:53:00	Track AR 13107
10/01 00:03:00 - 10/01 03:59:54	Track (-21.0, -156.8) @ 10/01 00:00:00	HOP393
10/01 04:03:00 - 10/01 05:59:54	Fixed (0.0, 0.0)	HOP349+synoptic
10/01 06:13:00 - 10/01 12:30:00	Track (680.5, -495.1) @ 10/01 06:10:00	Track AR 13107

PROG= 04 30-time(s)

Subr= 1 20-time(s) 2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 73 1-time(s) 10.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 10 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 87 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

* * * * * Active Region Search * * * * *

NOT USED

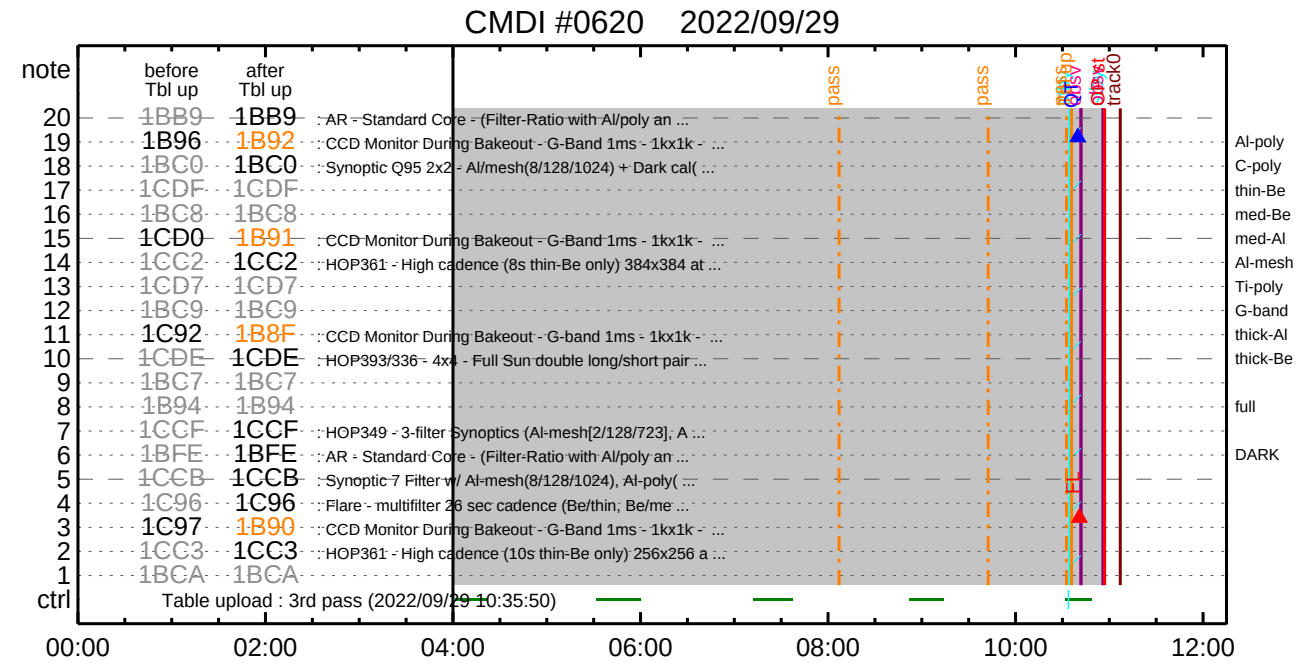
* * * * * Flare Detection * * * * *

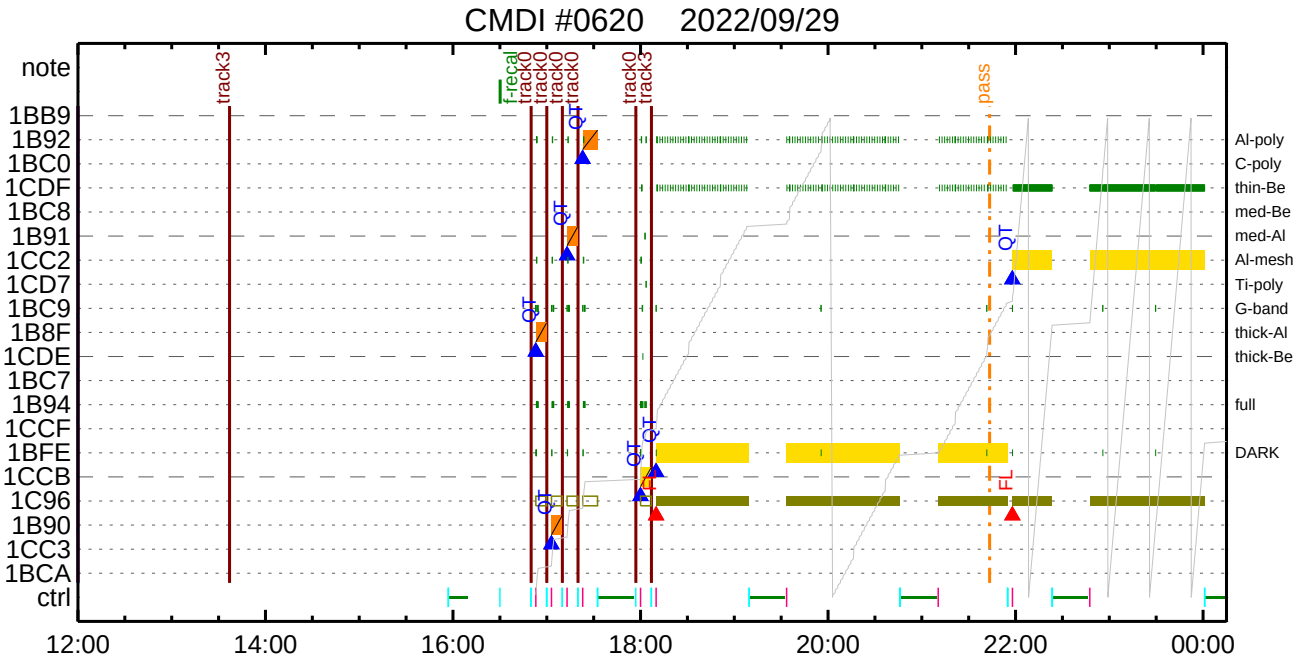
FLD Patrol

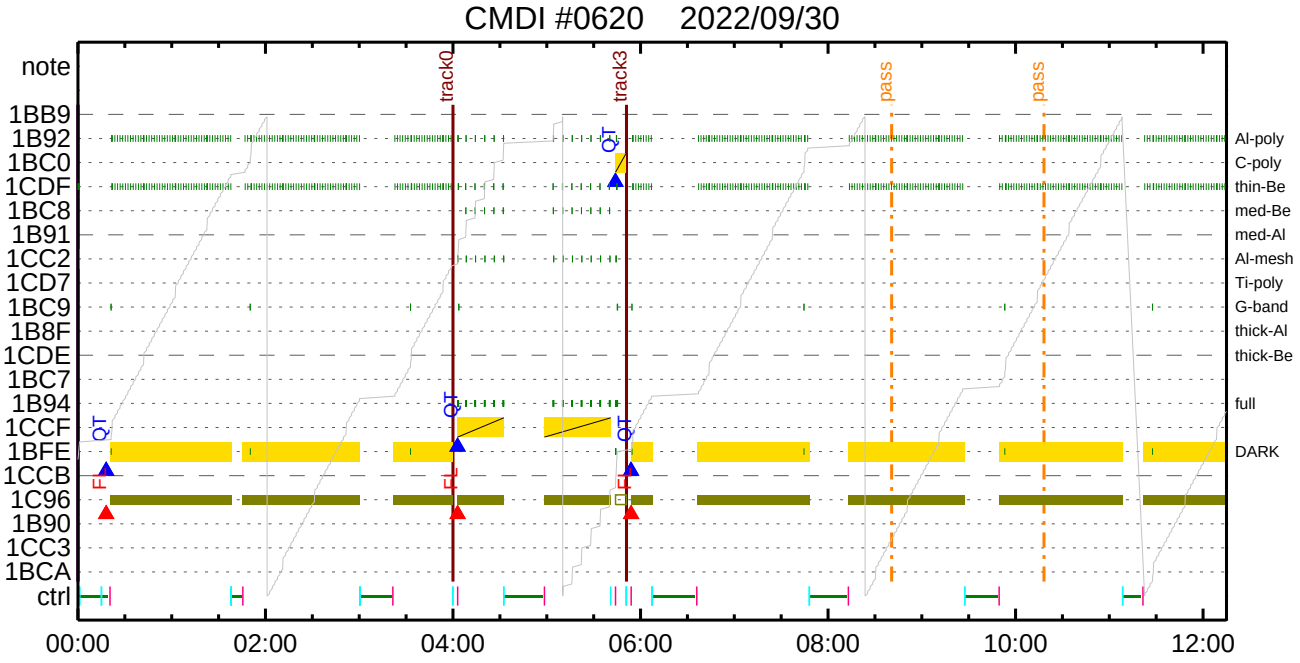
Term	Pointing (x, y)	Comment
09/29 10:36:50 - 09/29 16:52:56	cannot be identified	
09/29 18:07:18 - 09/30 05:41:16	Track (472.3, -519.1) @ 09/29 18:07:00	Track AR 13107
09/30 05:51:18 - 09/30 17:43:16	Track (547.2, -512.2) @ 09/30 05:51:00	Track AR 13107
09/30 17:53:18 - 10/01 06:00:16	Track (617.2, -504.2) @ 09/30 17:53:00	Track AR 13107

10/01 06:10:18 - 10/04 12:44:00 Track (680.5, -495.1) @ 10/01 06:10:00 Track AR 13107

Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval


CMDI #0620 2022/09/29
note
before Tbl up
after Tbl up
20 1BB9 1BB9 : AR - Standard Core - (Filter-Ratio with Al/poly an ...
19 1B96 1B92 : CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - ...
18 1BC0 1BC0 : Synoptic Q95 2x2 - Al/mesh(8/128/1024) + Dark cal(...
17 1CDF 1CDF
16 1BC8 1BC8
15 1CD0 1B91 : CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - ...
14 1CC2 1CC2 : HOP361 - High cadence (8s thin-Be only) 384x384 at ...
13 1CD7 1CD7
12 1BC9 1BC9
11 1C92 1B8F : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
10 1CDE 1CDE : HOP393/336 - 4x4 - Full Sun double long/short pair ...
9 1BC7 1BC7
8 1B94 1B94
7 1CCF 1CCF : HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], A ...
6 1BFE 1BFE : AR - Standard Core - (Filter-Ratio with Al/poly an ...
5 1CCB 1CCB : Synoptic 7 Filter w/ Al-mesh(8/128/1024), Al-poly(...
4 1C96 1C96 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
3 1C97 1B90 : CCD Monitor During Bakeout - G-Band 1ms - 1kx1k - ...
2 1CC3 1CC3 : HOP361 - High cadence (10s thin-Be only) 256x256 a ...
1 1BCA 1BCA
ctrl Table upload : 3rd pass (2022/09/29 10:35:50)
pass
pass
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0620 2022/09/29
note
1BB9
1B92
1BC0
1CDF
1BC8
1B91
1CC2
1CD7
1BC9
1B8F
1CDE
1BC7
1B94
1CCF
1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
track3
f-recal
track0
track0
track0
track0
track0
track3
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0620 2022/09/30
note
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1B92
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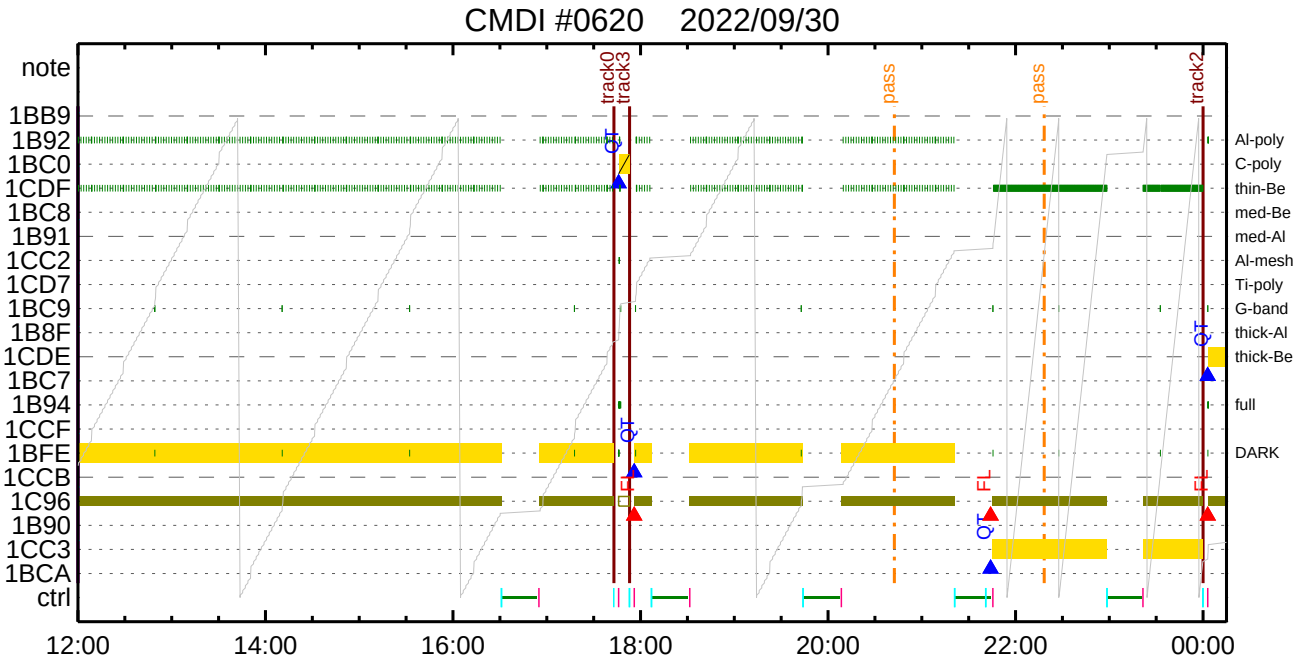
1BC8
1B91
1CC2
1CD7
1BC9
1B8F
1CDE
1BC7
1B94
1CCF
1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
track0
track3
pass
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0620 2022/09/30
note
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1CDE
1BC7
1B94
1CCF
1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
track0
track3
pass
pass
track2
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
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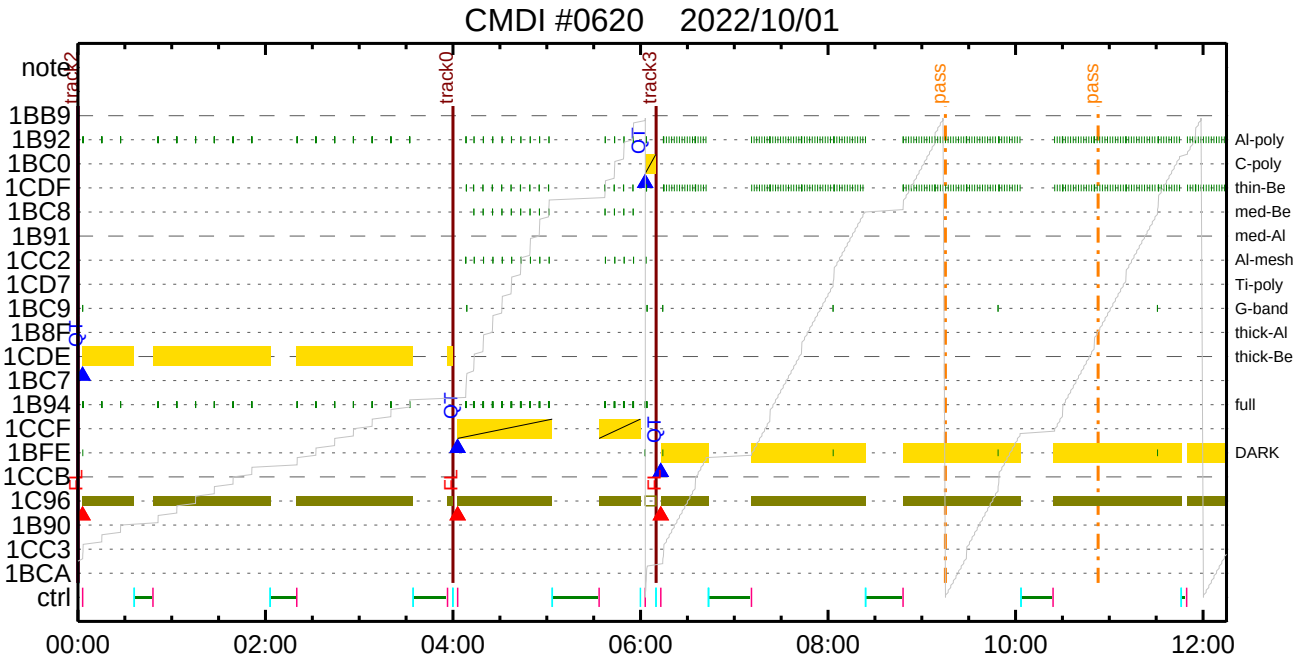
14:00
16:00
18:00
20:00
22:00
00:00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1BC9
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1CDE
1BC7
1B94
1CCF
1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
track2
track0
track3
pass
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
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thick-Be
full
DARK
00:00
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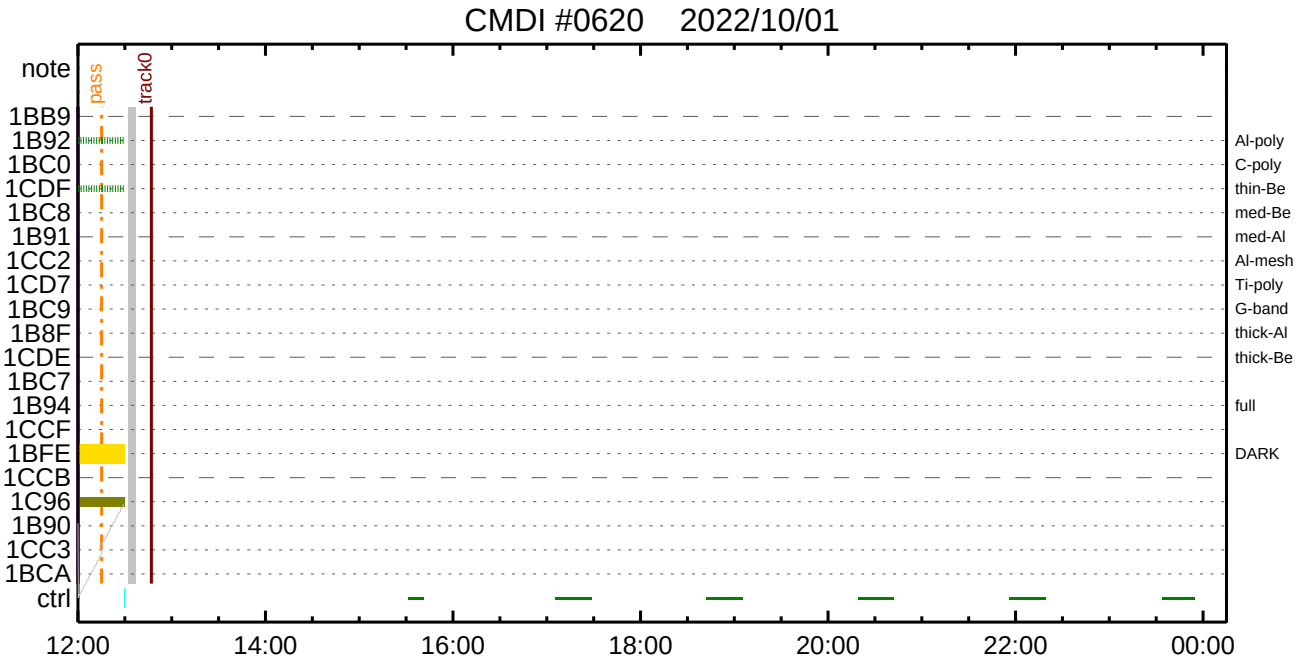
CMDI #0620 2022/10/01
note
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1CDE
1BC7
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1CCF
1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
pass
track0
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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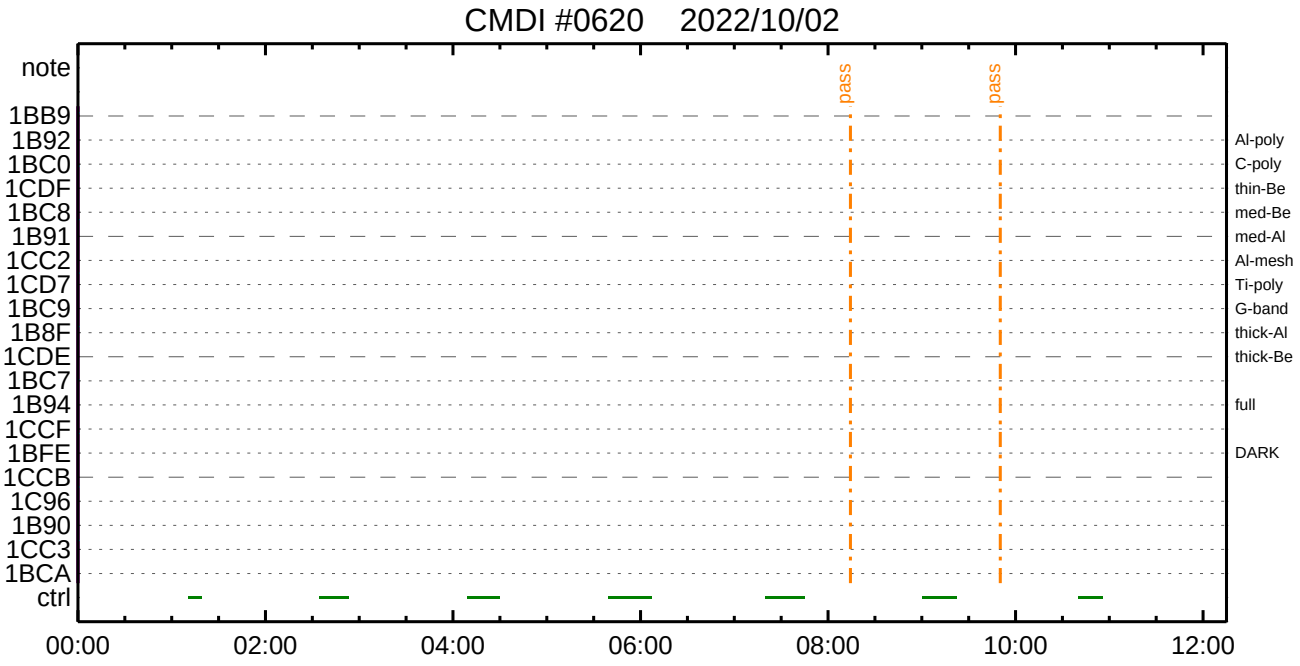
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0620 2022/10/02
note
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1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
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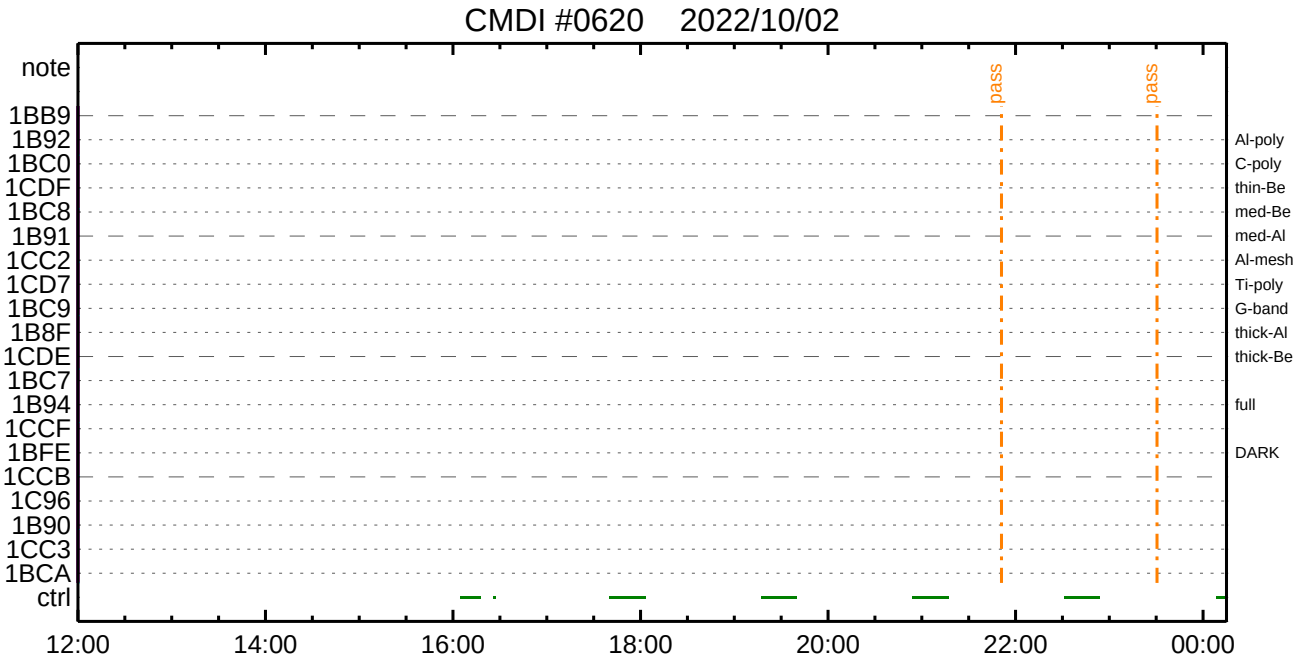
12:00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1CCF
1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
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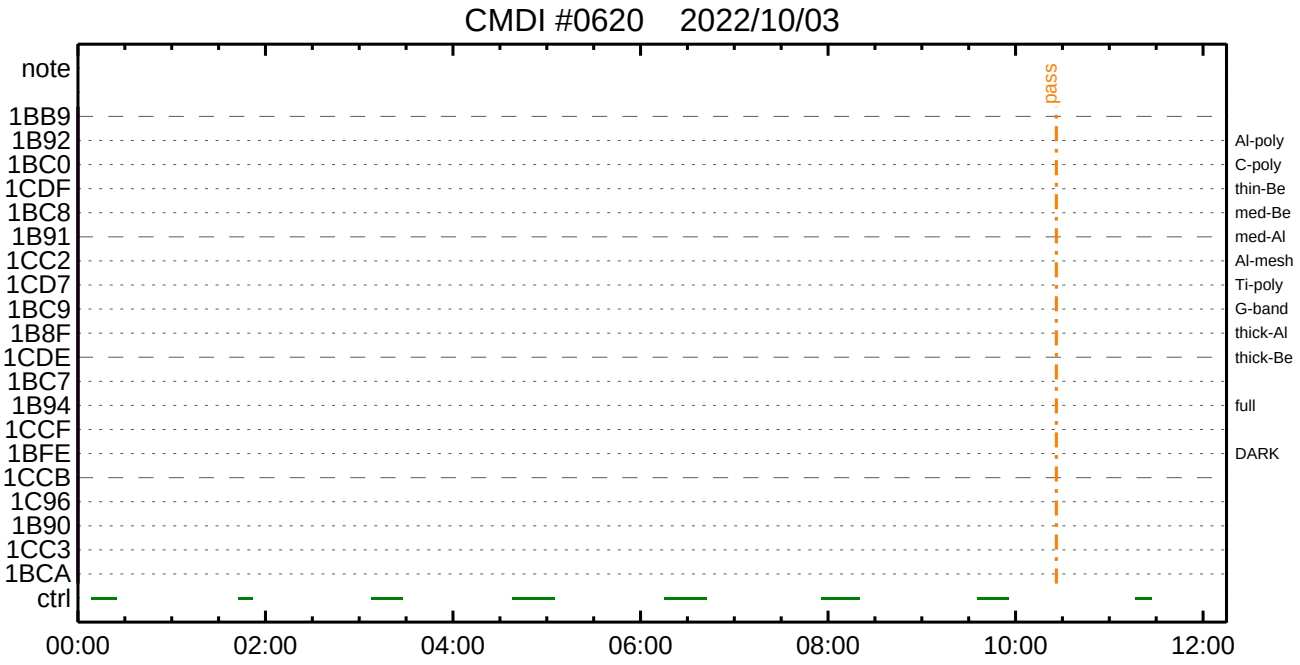
00:00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1CCF
1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
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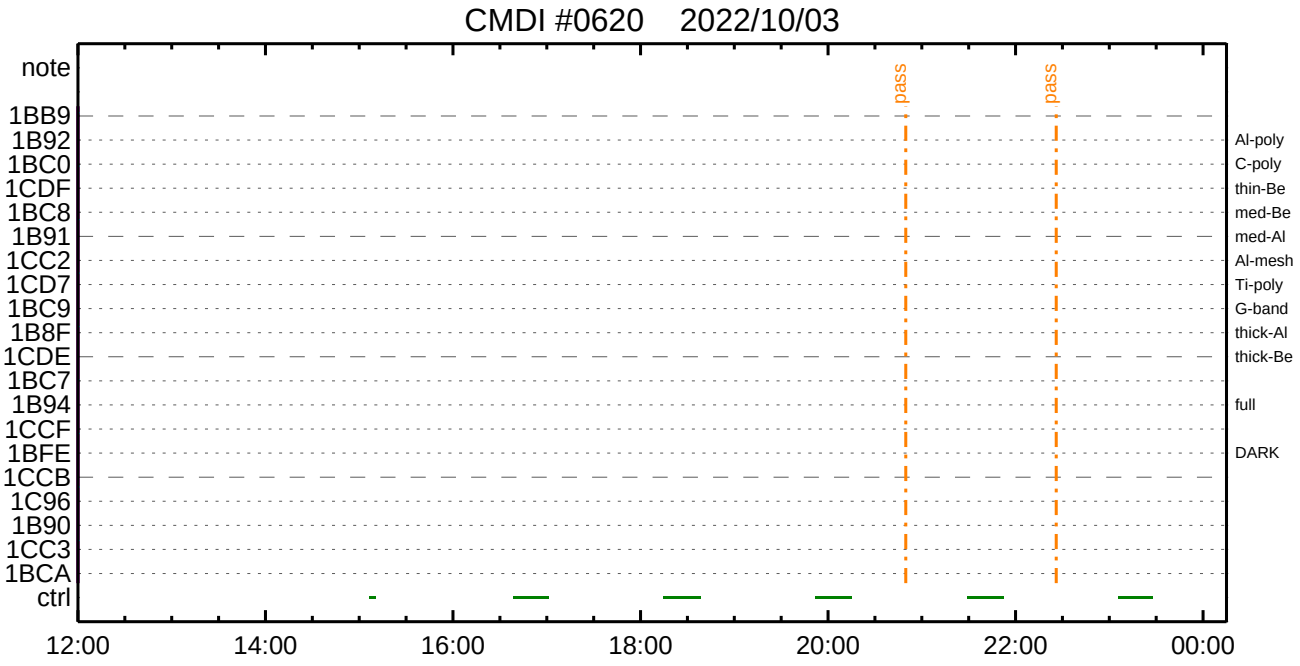
12:00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1CDE
1BC7
1B94
1CCF
1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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thick-Be
full
DARK
12:00
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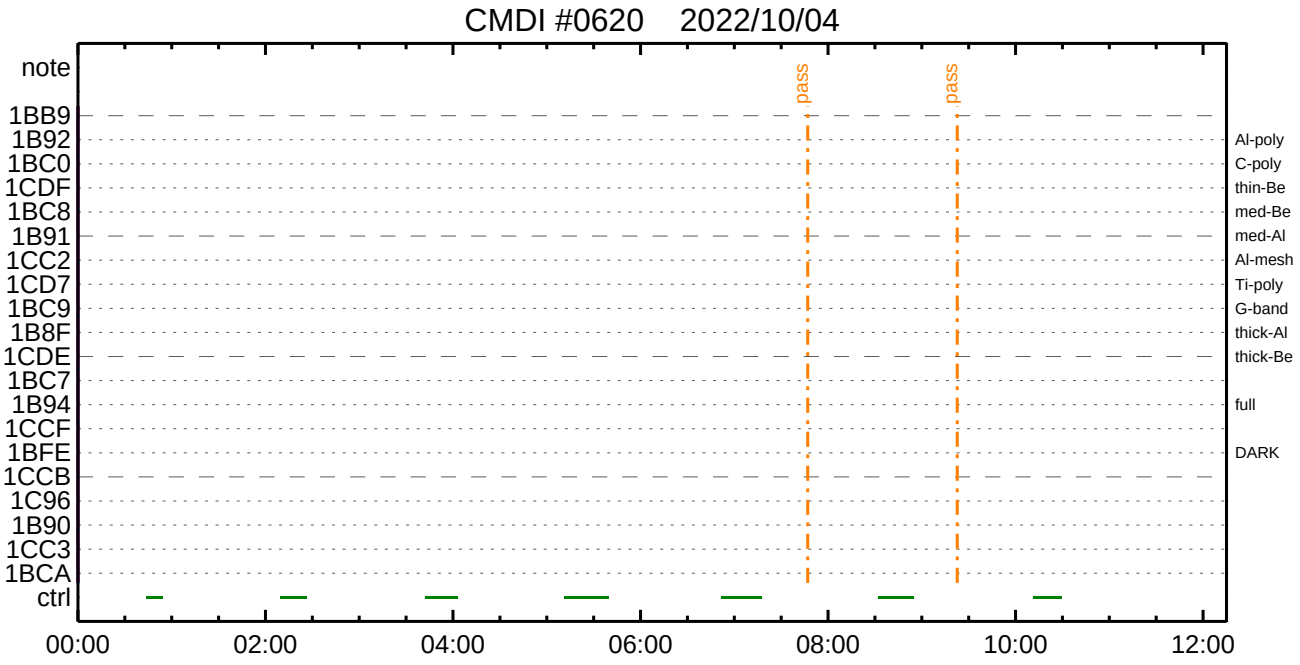
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1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
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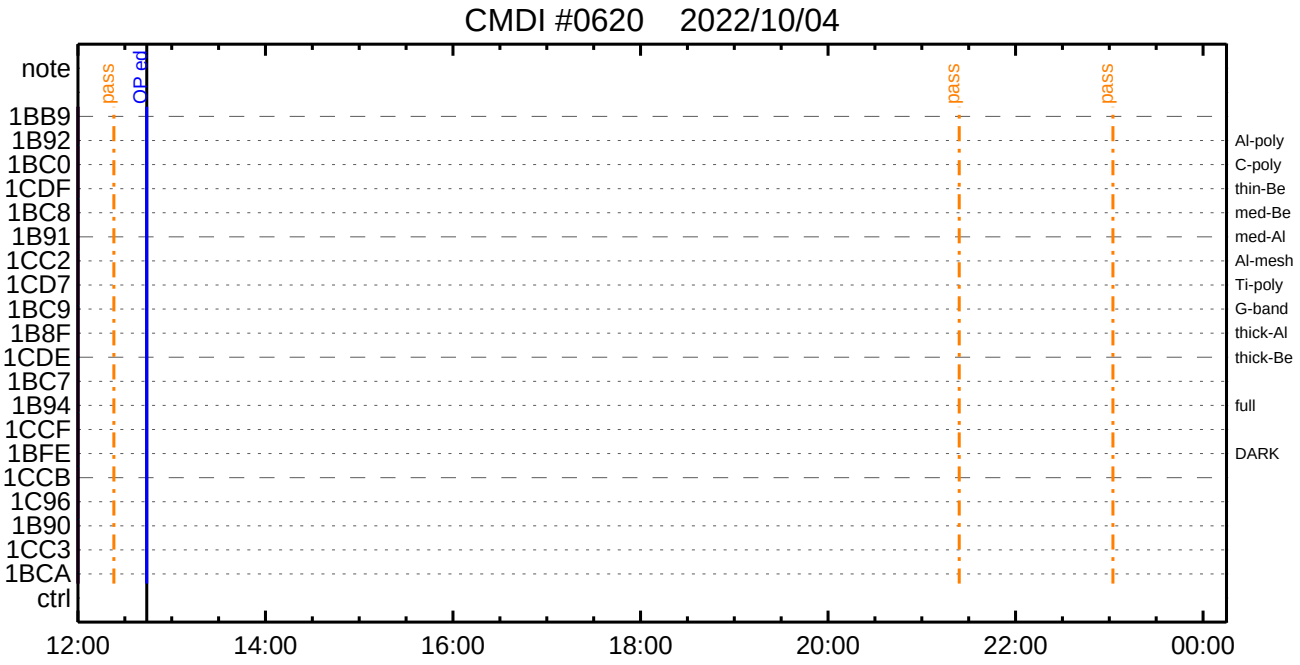
04:00
06:00
08:00
10:00
12:00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1CC2
1CD7
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1B8F
1CDE
1BC7
1B94
1CCF
1BFE
1CCB
1C96
1B90
1CC3
1BCA
ctrl
pass
OP ed
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-433 2022-09-29 11:07:23 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

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0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-433:OP
0104     ()
0105   . S. OG    og-433:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¿¼Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2022-09-29 10:52:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2022-09-29 10:52:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2022-09-29 10:52:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
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0194     C.
0195   +. TI 2022-09-29 10:56:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242   +. TI 2022-09-29 10:56:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245    . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249    . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255   +. TI 2022-09-29 10:56:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258   +. TI 2022-09-29 10:56:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268   +. TI 2022-09-29 10:56:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
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(a) Spacecraft Operation Procedure (real-commands)

main-434 2022-09-29 11:07:23 203 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007    +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011    +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019    +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021    +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023    +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029    . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031    . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036    . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037    +. DC 01-29 DHU_S/X_VC4_OFF
0038    +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040    +  DC 06-B3 DR_REP_START
0041    +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046    . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047    +. DC 06-B3 DR_REP_START
0048    +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054    . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057    . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062    . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063    +. DC 01-29 DHU_S/X_VC4_OFF
0064    +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066    +  DC 06-B3 DR_REP_START
0067    +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072    . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073    +. DC 06-B3 DR_REP_START
0074    +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079    . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083    . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084    +. DC 06-B4 DR_REP_STOP
0085    +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090    . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091    +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093    +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
    C.
    C.
  . C. ***** AOCS Commands (Tracking Curve Upload) ******
    C. Upload the Orbit Element and the Target Attitude
    C. RAM-ID:TARGET_ATT
  . S. RAM   ram-150:TARGET_ATT
    ()
    C.
    C.
    C. Set the dump memory area of TARGET_ATT
 +. DC 02-48 AOCU_DUMP_SET
    BC       (07 00 00 00 18 00)
    C.
    C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
    C.
    C.
    C. Change the TLMFormatNo for the AOCS Dump Format
 +. DC 01-22 DHU_MODE_CHNG
    BC       (04 0b f8)
    C.
    C. Wait for AOCSDUMP to end
    C.
  . C. Check the dump memory
    C.
    C.                                      Result = OK [ ]
    C.
 +. DC 01-22 DHU_MODE_CHNG
    BC       (02 0a f8)
    C.
    C.                         <A_***>[TLM STS] FMT = 2 [ ]
    C.
 +. DC 02-8E AOCU_ORB_UPD
  . C.
  . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
 +. DC 07-FC EIS_MODE_CHG_ENA
    BC       (20)
  . C. Verify EIS_MODE_CHG_FLG is ENA
 +. DC 07-FC EIS_MODE_MANU
    BC       (21 02)
  . C. Verify EIS in MANUAL mode
  . C. Estimated OBSTBL upload time is 16s
    C. ********************************
    C. EIS START OBSTBL LOAD
    C. ********************************
  . S. RAM   ram-820:EIS_OBSTBL
    ()
 +. DC 07-FC EIS_DUMP_OBSTBL
    BC       (07 07 07 00 00 70 00)
    C.
    C. Execute, after the success of OBSTBL upload.
    C. Set EIS TI-commands
 +. TI 2022-09-29 10:56:50.0
    DC 07-FC EIS_MODE_CHG_ENA
    BC       (20)
  . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
    C. ********************************
    C. EIS END OBSTBL LOAD
    C. ********************************
  . C. =====  Begin of AOCS CMD Sequence =====
  . C.
  . C. ******************************
  . C. ***** GAS¥Ç¡¼¥¿¼èÆÀ¼Â»Ü  *****
  . C. ******************************
  . C.
  . C. ***************************
  . C.  MDRV OFF
  . C. ***************************
  . C.
  . C. ***** GAS¥â¥Ë¥¿¤Î¤¿¤á MTQ¶îÆ°°ì»þÄä»ß *****
 +. DC 02-33 AOCU_MDRV-X_OFF
 +. DC 02-34 AOCU_MDRV-Y_OFF
 +. DC 02-35 AOCU_MDRV-Z_OFF
  . C.                                      [   ] <A_AOS> [COMPONENT STS] <MDRV> X = OFF ?
  . C.                                      [   ] <A_AOS> [COMPONENT STS] <MDRV> Y = OFF ?
  . C.                                      [   ] <A_AOS> [COMPONENT STS] <MDRV> Z = OFF ?
  . C.
  . C.
  . C.  ¡ú¥Ç¡¼¥¿¼èÆÀ¤Î¤¿¤á¡¢Ì¿ó1minÂÔµ¡
  . C.
  . C. ***************************
  . C.  MDRV ON
  . C. ***************************
  . C.
  . C. ***** MTQ¶îÆ°°Æ³« *****
 +. DC 02-32 AOCU_MDRV_ON
  . C.                                      [   ] <A_AOS> [COMPONENT STS] <MDRV> X = ON ?
  . C.                                      [   ] <A_AOS> [COMPONENT STS] <MDRV> Y = ON ?
  . C.                                      [   ] <A_AOS> [COMPONENT STS] <MDRV> Z = ON ?
  . C.
  . C.
  . C. =====  End of AOCS CMD Sequence =====
  . C.
    C.
  . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
    C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
  . S. DC-BC dcbc-402:DCBC
    (MDP_known_event)
    C.
```

```
0194     C.
0195   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0196   . S. DC-BC dcbc-153:DCBC
0197     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0198     C.
0199     C.
0200   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0201     C.
0202   . C. ********** LOS **********
0203     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-435 2022-09-29 11:07:23 138 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¼É¡ÊËè½µ•îÍË¡Ë¤¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017   . C. < Stop SP table >
0018   +. DC 07-F0 MDP_SP_CTRL_MANU
0019     BC       (61)
0020     C. ------------------------------------
0021     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024   . C. <Upload SP Observation Table>
0025   . S. RAM   ram-282:MDP_OBS_S
0026     ()
0027     C.
0028   . C. < Dump RAMID=MDP_OBS_S >
0029   +. DC 07-F0 MDP_DUMP_SPTBL
0030     BC       (83 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_S verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035     C. ********************************
0036     C.  SOT TI command set
0037     C. ********************************
0038     C. Execute, after the success of TBL upload.
0039   +. TI 2022-09-29 10:56:18.0
0040     DC 07-F0 MDP_SOT_MODE_OBSV
0041     BC       (40)
0042   . C. ------------------------------------
0043     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0044     C. ------------------------------------
0045     C.
0046     C.
0047     C. ************ XRT START ************
0048     C.
0049   +. DC 07-F0 MDP_XRT_CTRL_MANU
0050     BC       (c1)
0051   +  DC 07-F0 MDP_XRT_MODE_STBY
0052     BC       (c3)
0053   . C. ----------- Success Verify ?             OK / NG____
0054     C.
0055     C. XRT Obs. Table Upload
0056   . S. RAM   ram-291:MDP_OBS_X
0057     ()
0058     C.
0059   +. DC 07-F0 MDP_DUMP_XRTTBL
0060     BC       (84 07 00 00 00 3a d4)
0061   . C. ----------- Comparison Check ?                      OK / ERR ____
0062     C.
0063     C.
0064   +. DC 07-F0 MDP_XRT_ROI_SET
0065     BC       (cd 01 b1 b1 04 04)
0066   +  DC 07-F0 MDP_XRT_ROI_SET
0067     BC       (cd 02 b1 b1 08 08)
0068   +  DC 07-F0 MDP_XRT_ROI_SET
0069     BC       (cd 03 b1 b1 08 08)
0070   +  DC 07-F0 MDP_XRT_ROI_SET
0071     BC       (cd 04 b1 b1 06 06)
0072   +  DC 07-F0 MDP_XRT_ROI_SET
0073     BC       (cd 05 85 83 06 06)
0074   +  DC 07-F0 MDP_XRT_ROI_SET
0075     BC       (cd 06 85 83 06 06)
0076   +  DC 07-F0 MDP_XRT_ROI_SET
0077     BC       (cd 07 c0 c0 10 10)
0078   +  DC 07-F0 MDP_XRT_ROI_SET
0079     BC       (cd 08 80 80 20 20)
0080   +  DC 07-F0 MDP_XRT_ROI_SET
0081     BC       (cd 09 40 c0 10 10)
0082   +  DC 07-F0 MDP_XRT_ROI_SET
0083     BC       (cd 0a 40 40 10 10)
0084   +  DC 07-F0 MDP_XRT_ROI_SET
0085     BC       (cd 0b c0 40 10 10)
0086   +  DC 07-F0 MDP_XRT_ROI_SET
0087     BC       (cd 0c 80 80 20 08)
0088   +  DC 07-F0 MDP_XRT_ROI_SET
0089     BC       (cd 0d 80 80 08 20)
0090   +  DC 07-F0 MDP_XRT_ROI_SET
0091     BC       (cd 0e 80 80 08 08)
0092   +  DC 07-F0 MDP_XRT_ROI_SET
0093     BC       (cd 0f 80 80 06 06)
0094   +  DC 07-F0 MDP_XRT_ROI_SET
0095     BC       (cd 10 80 80 08 08)
```

```
0096  +  DC 07-F0 MDP_XRT_FLD_ENA
0097     BC       (d8)
0098  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0099     BC       (c8)
0100  +  DC 07-F0 MDP_XRT_ARS_DIS
0101     BC       (d5)
0102  +  DC 07-F0 MDP_XRT_AEC_RESET
0103     BC       (d0)
0104  +  DC 07-F0 MDP_XRT_FLD_RESET
0105     BC       (da)
0106  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0107     BC       (c4 14)
0108  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0109     BC       (c5 04)
0110   . C. ---------- Success Verify ?            OK / NG ____
0111     C.
0112     C.
0113   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0114     C.
0115  +. DC 07-F0 MDP_XRT_MODE_OBSV
0116     BC       (c2)
0117  +. TI 2022-09-29 10:56:02.0
0118     DC 07-F0 MDP_XRT_MODE_OBSV
0119     BC       (c2)
0120   . C. ---------- Success Verify ?            OK / NG ____
0121     C.
0122     C. ************ XRT END ***********
0123     C.
0124   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0125     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0126   . S. DC-BC dcbc-402:DCBC
0127     (MDP_known_event)
0128     C.
0129     C.
0130   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0131   . S. DC-BC dcbc-153:DCBC
0132     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133     C.
0134     C.
0135   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0136     C.
0137   . C. *********** LOS ***********
0138     C.
```

```
*** OP Sequence for XRT ***

2022/09/29  11:07:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM               5 02-76 00 00 00 00 00
2022/09/29  13:37:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM               5 02-76 03 00 00 00 00
2022/09/29  15:57:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  15:57:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  15:57:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET     1 07-F0 da
2022/09/29  15:57:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT   1 07-F0 e8
2022/09/29  16:00:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP   1 07-F0 e9
2022/09/29  16:30:00.0  XRT_CTRL_MANU_405_OG [0x195]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  16:30:10.0  XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]
                                XRT_FOCUS_RECAL       2 07-F8 78 00
2022/09/29  16:34:10.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2022/09/29  16:49:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  16:49:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  16:49:58.0  XRT_FOCUS_POSITION_443_OG [0x1bb]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2022/09/29  16:50:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM               5 02-76 00 2e f9 2e f9
2022/09/29  16:52:52.0  XRT_ARS_DIS_426_OG [0x1aa]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2022/09/29  16:52:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2022/09/29  16:52:56.0  XRT_FLD_DIS_427_OG [0x1ab]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2022/09/29  16:52:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 0b
2022/09/29  16:53:00.5  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2022/09/29  16:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  16:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  16:59:58.0  XRT_FOCUS_POSITION_443_OG [0x1bb]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2022/09/29  17:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM               5 02-76 00 2e f9 d1 07
2022/09/29  17:02:52.0  XRT_ARS_DIS_426_OG [0x1aa]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2022/09/29  17:02:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2022/09/29  17:02:56.0  XRT_FLD_DIS_427_OG [0x1ab]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2022/09/29  17:02:58.0  XRT_QT_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 03
2022/09/29  17:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2022/09/29  17:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  17:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  17:09:58.0  XRT_FOCUS_POSITION_443_OG [0x1bb]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2022/09/29  17:10:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM               5 02-76 00 d1 07 d1 07
2022/09/29  17:12:52.0  XRT_ARS_DIS_426_OG [0x1aa]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2022/09/29  17:12:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2022/09/29  17:12:56.0  XRT_FLD_DIS_427_OG [0x1ab]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2022/09/29  17:12:58.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 0f
2022/09/29  17:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2022/09/29  17:19:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  17:19:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2022/09/29  17:19:58.0  XRT_FOCUS_POSITION_443_OG [0x1bb]
                                XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2022/09/29  17:20:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM               5 02-76 00 d1 07 2e f9
2022/09/29  17:22:52.0  XRT_ARS_DIS_426_OG [0x1aa]
                                MDP_XRT_ARS_DIS       1 07-F0 d5
2022/09/29  17:22:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2022/09/29  17:22:56.0  XRT_FLD_DIS_427_OG [0x1ab]
                                MDP_XRT_FLD_DIS       1 07-F0 d9
2022/09/29  17:22:58.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                MDP_XRT_QT_PROG_SET   2 07-F0 c4 13
2022/09/29  17:23:00.0  XRT_CTRL_AUTO_408_OG [0x198]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/29  17:32:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  17:32:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  17:32:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/29  17:32:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/29  17:35:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/29  17:56:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  17:56:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2022/09/29  17:57:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2022/09/29  17:57:16.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2022/09/29  17:57:18.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2022/09/29  17:57:20.0  XRT_ARS_DIS_404_OG [0x194]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2022/09/29  17:59:58.0  XRT_QT_PROG_SET_417_OG [0x1a1]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2022/09/29  18:00:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/29  18:06:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  18:06:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  18:06:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2022/09/29  18:07:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2022/09/29  18:07:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2022/09/29  18:07:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2022/09/29  18:07:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2022/09/29  18:07:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2022/09/29  18:07:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/29  18:09:56.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2022/09/29  18:09:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2022/09/29  18:10:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/29  19:09:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  19:09:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  19:09:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/29  19:09:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/29  19:12:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/29  19:32:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/29  19:33:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/29  20:46:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  20:46:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  20:46:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/29  20:46:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/29  20:49:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/29  21:09:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/29  21:10:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/29  21:54:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  21:54:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  21:54:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2022/09/29  21:55:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2022/09/29  21:55:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2022/09/29  21:55:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2022/09/29  21:55:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2022/09/29  21:55:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
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2022/09/29  21:57:56.0  XRT_QT_PROG_SET_422_OG [0x1a6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2022/09/29  21:57:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2022/09/29  21:58:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/29  22:23:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  22:23:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/29  22:23:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/29  22:23:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/29  22:26:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/29  22:46:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/29  22:47:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  00:01:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  00:01:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  00:01:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  00:01:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  00:04:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  00:15:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  00:15:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  00:15:04.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2022/09/30  00:15:24.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2022/09/30  00:15:26.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2022/09/30  00:15:28.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2022/09/30  00:15:30.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2022/09/30  00:15:32.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  00:18:02.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2022/09/30  00:18:04.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2022/09/30  00:19:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  00:20:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  01:38:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  01:38:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  01:38:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  01:38:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  01:41:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  01:44:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  01:45:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  03:00:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  03:00:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  03:00:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  03:00:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  03:03:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  03:20:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  03:21:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  03:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  03:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2022/09/30  04:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 00 00
2022/09/30  04:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2022/09/30  04:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2022/09/30  04:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2022/09/30  04:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
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                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2022/09/30  04:00:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  04:02:56.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2022/09/30  04:02:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2022/09/30  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  04:32:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  04:32:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  04:32:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  04:32:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  04:35:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  04:57:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  04:58:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  05:40:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  05:40:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2022/09/30  05:41:16.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2022/09/30  05:41:18.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2022/09/30  05:41:20.0  XRT_ARS_DIS_404_OG [0x194]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2022/09/30  05:43:58.0  XRT_QT_PROG_SET_437_OG [0x1b5]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2022/09/30  05:44:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  05:50:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  05:50:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  05:50:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2022/09/30  05:51:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2022/09/30  05:51:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2022/09/30  05:51:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2022/09/30  05:51:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2022/09/30  05:51:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2022/09/30  05:51:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  05:53:56.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2022/09/30  05:53:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2022/09/30  05:54:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  06:07:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  06:07:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  06:07:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  06:07:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  06:10:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  06:35:00.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  06:36:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  07:48:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  07:48:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  07:48:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  07:48:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  07:51:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  08:12:00.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  08:13:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  09:27:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  09:27:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  09:27:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  09:27:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
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                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  09:30:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  09:48:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  09:49:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  11:08:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  11:08:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  11:08:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  11:08:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  11:11:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  11:20:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  11:21:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  16:31:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  16:31:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  16:31:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  16:31:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  16:34:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  16:54:00.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  16:55:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  17:42:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  17:42:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2022/09/30  17:43:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2022/09/30  17:43:16.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2022/09/30  17:43:18.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2022/09/30  17:43:20.0  XRT_ARS_DIS_404_OG [0x194]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2022/09/30  17:45:58.0  XRT_QT_PROG_SET_437_OG [0x1b5]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2022/09/30  17:46:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  17:52:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  17:52:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  17:52:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2022/09/30  17:53:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2022/09/30  17:53:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2022/09/30  17:53:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2022/09/30  17:53:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2022/09/30  17:53:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2022/09/30  17:53:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  17:55:56.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2022/09/30  17:55:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2022/09/30  17:56:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  18:07:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  18:07:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  18:07:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  18:07:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  18:10:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  18:30:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  18:31:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  19:44:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  19:44:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  19:44:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  19:44:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
```

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2022/09/30  19:47:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  20:07:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  20:08:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  21:21:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  21:21:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  21:21:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  21:21:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  21:24:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  21:40:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  21:40:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  21:40:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2022/09/30  21:41:18.0  XRT_ROI_A_433_OG [0x1b1]
                                MDP_XRT_ROI_SET      6 07-F0 cd 05 85 83 06 06
                                MDP_XRT_ROI_SET      6 07-F0 cd 06 85 83 06 06
                                MDP_XRT_ROI_SET      6 07-F0 cd 07 85 83 04 04
                                MDP_XRT_ROI_SET      6 07-F0 cd 08 80 80 20 20
                                MDP_XRT_ROI_SET      6 07-F0 cd 0c 80 80 20 08
                                MDP_XRT_ROI_SET      6 07-F0 cd 0d 80 80 08 20
                                MDP_XRT_ROI_SET      6 07-F0 cd 0e 80 80 08 08
                                MDP_XRT_ROI_SET      6 07-F0 cd 0f 80 80 06 06
2022/09/30  21:41:18.5  XRT_ROI_B_438_OG [0x1b6]
                                MDP_XRT_ROI_SET      6 07-F0 cd 0f 80 80 06 06
                                MDP_XRT_ROI_SET      6 07-F0 cd 10 80 80 08 08
2022/09/30  21:41:23.5  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2022/09/30  21:41:25.5  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2022/09/30  21:41:27.5  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2022/09/30  21:41:29.5  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2022/09/30  21:41:31.5  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  21:44:01.5  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2022/09/30  21:44:03.5  XRT_FL_PROG_SET_418_OG [0x1a2]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2022/09/30  21:44:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  21:45:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  22:58:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  22:58:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  22:58:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/09/30  22:58:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/09/30  23:01:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/09/30  23:20:30.0  XRT_Custom_430_OG [0x1ae]
2022/09/30  23:21:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/09/30  23:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  23:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/09/30  23:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2022/10/01  00:00:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 02 00 00 00 00
2022/10/01  00:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2022/10/01  00:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2022/10/01  00:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2022/10/01  00:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2022/10/01  00:00:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/10/01  00:02:56.0  XRT_QT_PROG_SET_401_OG [0x191]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2022/10/01  00:02:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2022/10/01  00:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  00:36:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  00:36:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  00:36:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
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2022/10/01  00:36:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/10/01  00:39:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/10/01  00:47:00.0  XRT_Custom_430_OG [0x1ae]
2022/10/01  00:48:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  02:03:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  02:03:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  02:03:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/10/01  02:03:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/10/01  02:06:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/10/01  02:19:00.0  XRT_Custom_430_OG [0x1ae]
2022/10/01  02:20:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  03:34:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  03:34:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  03:34:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/10/01  03:34:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/10/01  03:37:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/10/01  03:55:30.0  XRT_Custom_430_OG [0x1ae]
2022/10/01  03:56:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  03:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  03:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2022/10/01  04:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 00 00 00 00
2022/10/01  04:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2022/10/01  04:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2022/10/01  04:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2022/10/01  04:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2022/10/01  04:00:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/10/01  04:02:56.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2022/10/01  04:02:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2022/10/01  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  05:03:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  05:03:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  05:03:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2022/10/01  05:03:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/10/01  05:06:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/10/01  05:32:30.0  XRT_Custom_430_OG [0x1ae]
2022/10/01  05:33:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  05:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  05:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2022/10/01  06:00:16.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2022/10/01  06:00:18.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2022/10/01  06:00:20.0  XRT_ARS_DIS_404_OG [0x194]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2022/10/01  06:02:58.0  XRT_QT_PROG_SET_437_OG [0x1b5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2022/10/01  06:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  06:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  06:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  06:09:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2022/10/01  06:10:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 03 00 00 00 00
2022/10/01  06:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
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                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2022/10/01  06:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2022/10/01  06:10:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2022/10/01  06:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2022/10/01  06:10:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/10/01  06:12:56.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2022/10/01  06:12:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2022/10/01  06:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  06:43:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  06:43:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  06:43:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/10/01  06:43:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/10/01  06:46:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/10/01  07:10:00.0  XRT_Custom_430_OG [0x1ae]
2022/10/01  07:11:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  08:24:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  08:24:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  08:24:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/10/01  08:24:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/10/01  08:27:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/10/01  08:47:00.0  XRT_Custom_430_OG [0x1ae]
2022/10/01  08:48:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  10:03:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  10:03:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  10:03:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/10/01  10:03:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/10/01  10:06:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/10/01  10:23:00.0  XRT_Custom_430_OG [0x1ae]
2022/10/01  10:24:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  11:46:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  11:46:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  11:46:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2022/10/01  11:46:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2022/10/01  11:48:30.0  XRT_Custom_430_OG [0x1ae]
2022/10/01  11:49:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2022/10/01  11:49:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2022/10/01  12:30:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2022/10/01  12:47:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 00 00 00 00 00
```