

XRT Timeline to be uploaded on 2022/12/15

Period: 2022/12/15 11:59:00 - 2022/12/20 11:06:00

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Normal mode

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XOB #1BB9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with												
Term	Pointing (x, y)		Comment									
12/15 12:12:00 - 12/15 18:21:24	Track (744.2, -372.7) @ 12/15 12:09:00		# OP start + 10min, AR 13165									
12/15 18:34:30 - 12/16 06:09:24	Track (773.0, -373.1) @ 12/15 18:31:30		AR 13165									
12/16 06:22:30 - 12/16 18:26:54	Track (818.4, -374.1) @ 12/16 06:19:30		AR 13165									
12/16 18:40:00 - 12/17 05:23:24	Track (854.5, -375.5) @ 12/16 18:37:00		AR 13165									
12/17 05:43:30 - 12/17 10:15:00	Track (876.6, -376.9) @ 12/17 05:33:30		AR 13165									
PROG= 08 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
Subr= 2 5-time(s) 2.0sec												
Seqn= 47 1-time(s) 2.0sec												
Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec												
Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec												
thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
Seqn= 96 4-time(s) 90.0sec												
Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec												
thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec												
Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec												
thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec												
Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec												
thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1CEF: Synoptic for HOP448 w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(33/512/4096), Thick-Be(65536), Med-Al(256/8192/32768), Med-Be(12												
Term	Pointing (x, y)		Comment									
12/15 18:24:30 - 12/15 18:31:24	Fixed (0.0, 0.0)		Synoptic									
12/16 06:12:30 - 12/16 06:19:24	Fixed (0.0, 0.0)		synoptic, shifted 9.5 min									
12/16 18:30:00 - 12/16 18:36:54	Fixed (0.0, 0.0)		synoptic, shifted 27.0 min									
12/17 05:26:30 - 12/17 05:33:24	Fixed (0.0, 0.0)		synoptic, shifted -36.5 min									
PROG= 09 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
Seqn= 26 1-time(s) 2.0sec												
Open/Al-mesh Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Seqn= 15 1-time(s) 2.0sec												
Al-poly/Open Al-poly/Open close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Seqn= 83 1-time(s) 2.0sec												
thin-Be/Open thin-Be/Open close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Seqn= 23 1-time(s) 4.0sec												
Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Subr= 2 1-time(s) 2.0sec												
Seqn= 46 1-time(s) 2.0sec												
Open/thick-Be Open/thick-Be close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Seqn= 17 1-time(s) 2.0sec												
med-Al/Open med-Al/thick-Al close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
med-Al/Open med-Al/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
med-Al/Open med-Al/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Seqn= 33 1-time(s) 2.0sec												
med-Be/Open Open/thick-Al close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
med-Be/Open med-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
med-Be/Open med-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
12/15 12:12:00 - 12/15 18:21:24	Track (744.2, -372.7) @ 12/15 12:09:00	# OP start + 10min, AR 13165
12/15 18:34:30 - 12/16 06:09:24	Track (773.0, -373.1) @ 12/15 18:31:30	AR 13165
12/16 06:22:30 - 12/16 18:26:54	Track (818.4, -374.1) @ 12/16 06:19:30	AR 13165
12/16 18:40:00 - 12/17 05:23:24	Track (854.5, -375.5) @ 12/16 18:37:00	AR 13165
12/17 05:43:30 - 12/17 10:15:00	Track (876.6, -376.9) @ 12/17 05:33:30	AR 13165
PROG= 04 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 73 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 87 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

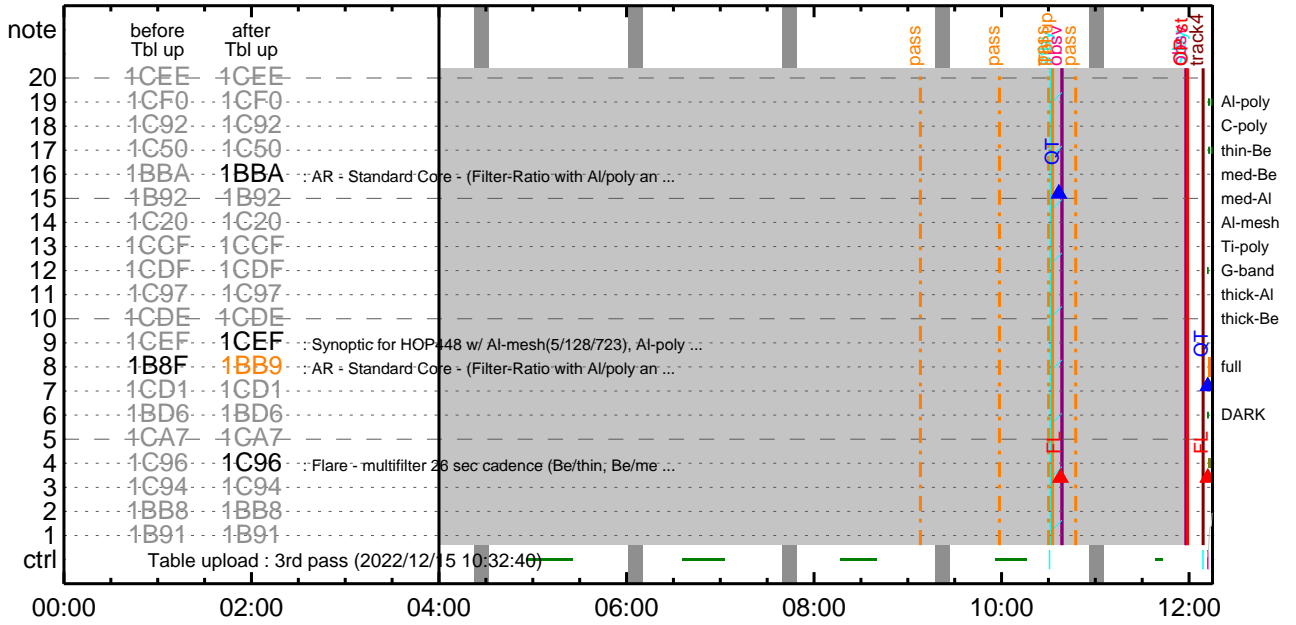
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Flare Detection

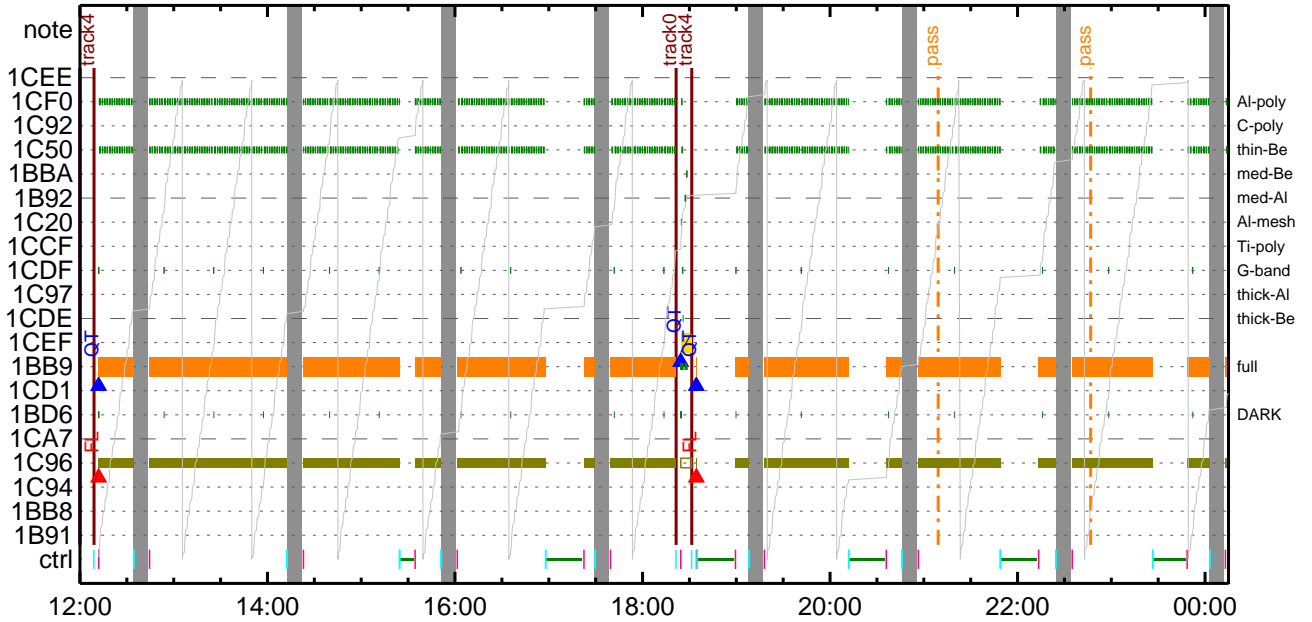
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FLD Patrol											
Term	Pointing (x, y)	Comment									
12/15 10:33:40 - 12/15 18:21:46	cannot be identified										
12/15 18:31:46 - 12/16 06:09:46	Track (773.0, -373.1) @ 12/15 18:31:30	AR 13165									
12/16 06:19:46 - 12/16 18:27:16	Track (818.4, -374.1) @ 12/16 06:19:30	AR 13165									
12/16 18:37:16 - 12/17 05:23:46	Track (854.5, -375.5) @ 12/16 18:37:00	AR 13165									
12/17 05:33:46 - 12/20 11:06:00	Track (876.6, -376.9) @ 12/17 05:33:30	AR 13165									
Al-poly/Open	Al-poly/Open close	Safe Norm 4ms Obs 8x8 Q=50 30sec									
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval									

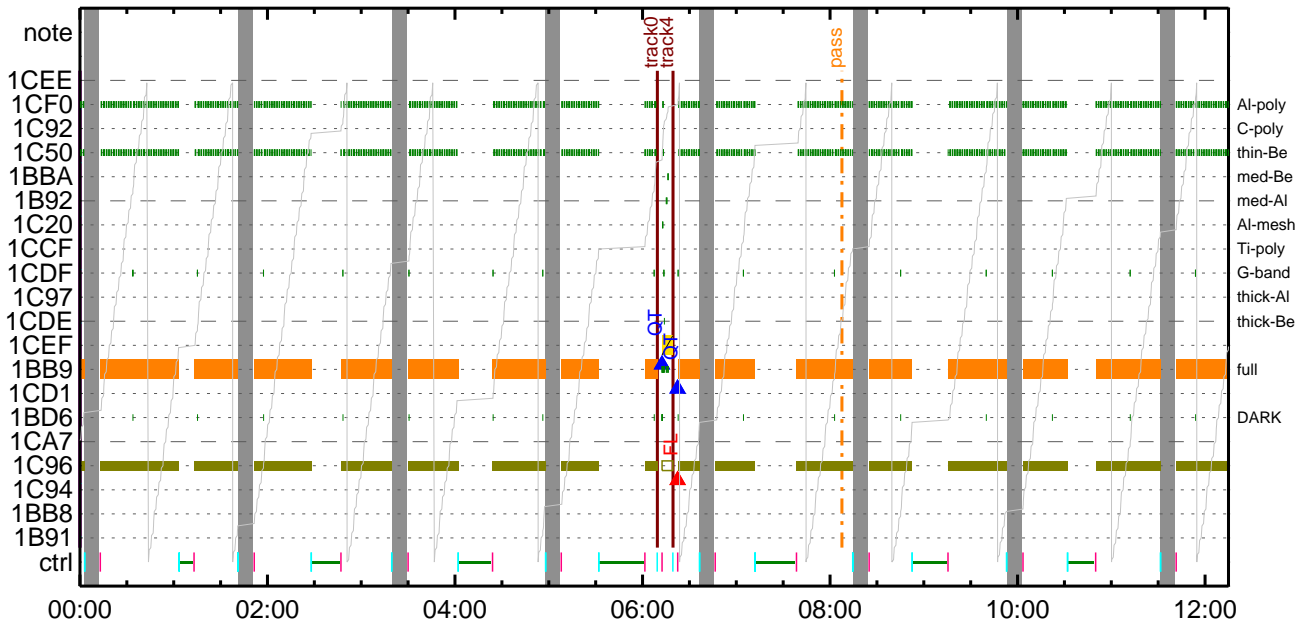
CMDI #0775 2022/12/15



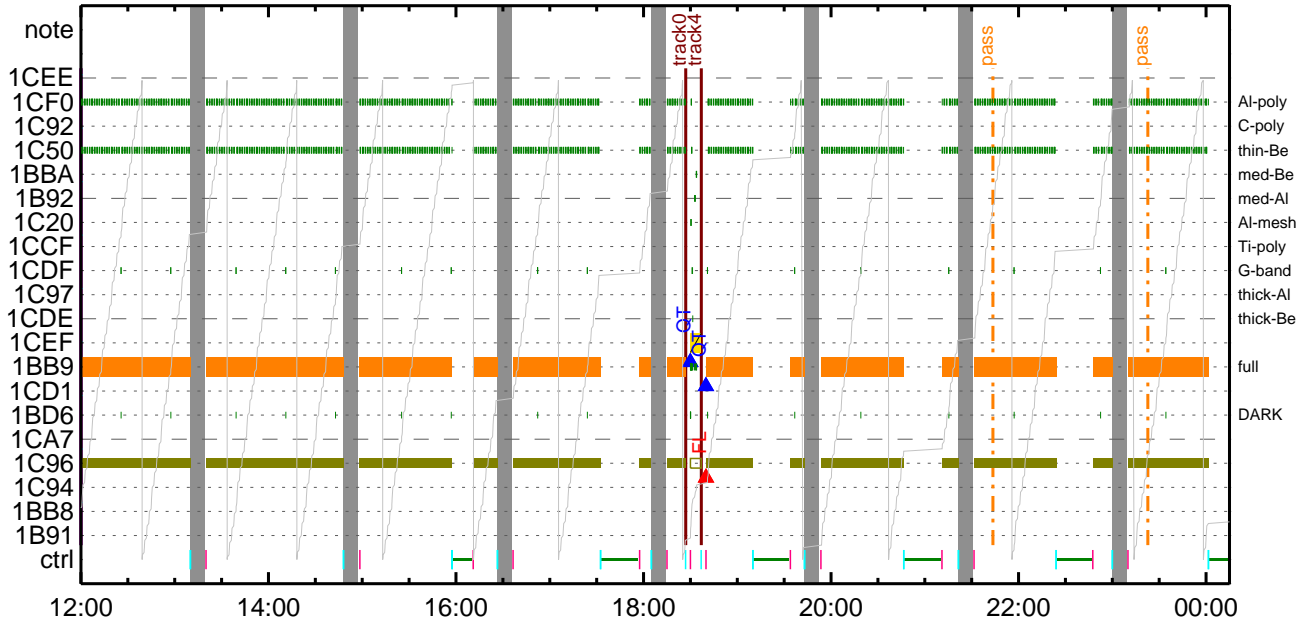
CMDI #0775 2022/12/15



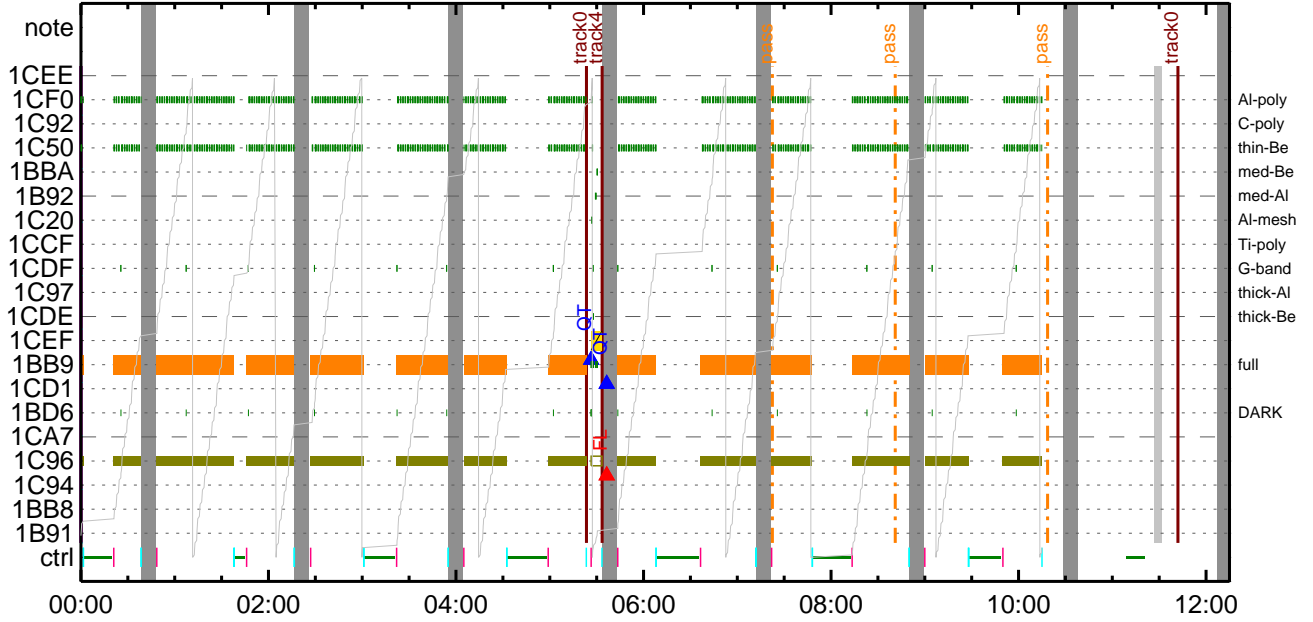
CMDI #0775 2022/12/16



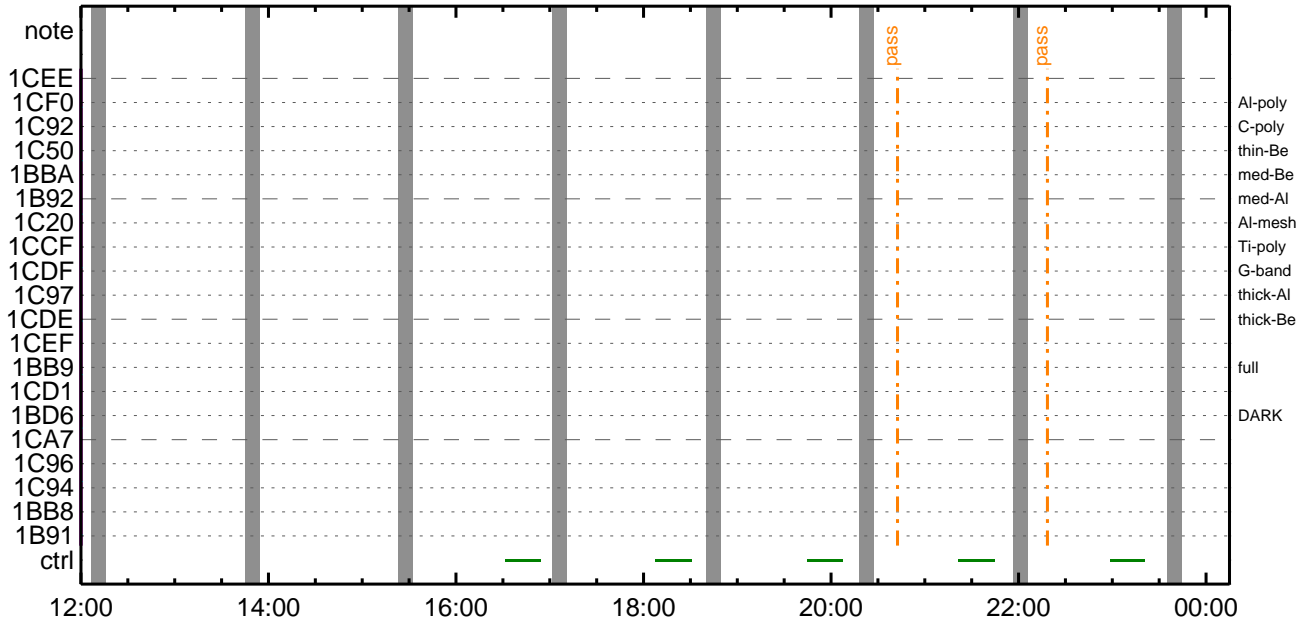
CMDI #0775 2022/12/16



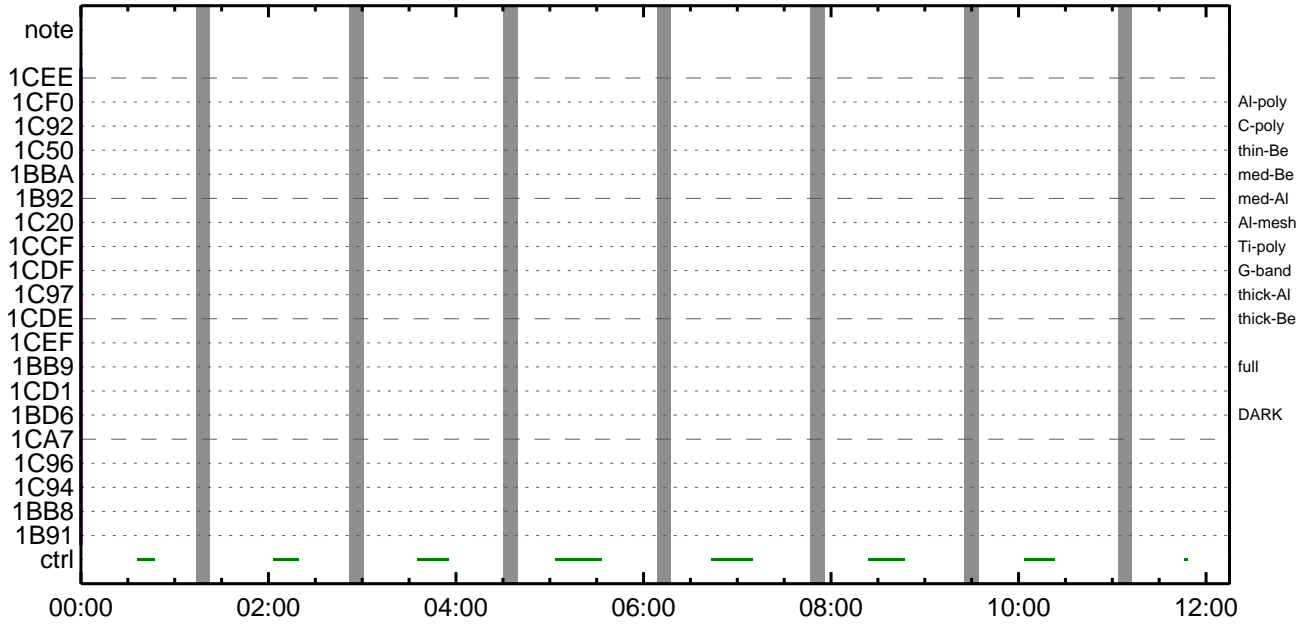
CMDI #0775 2022/12/17



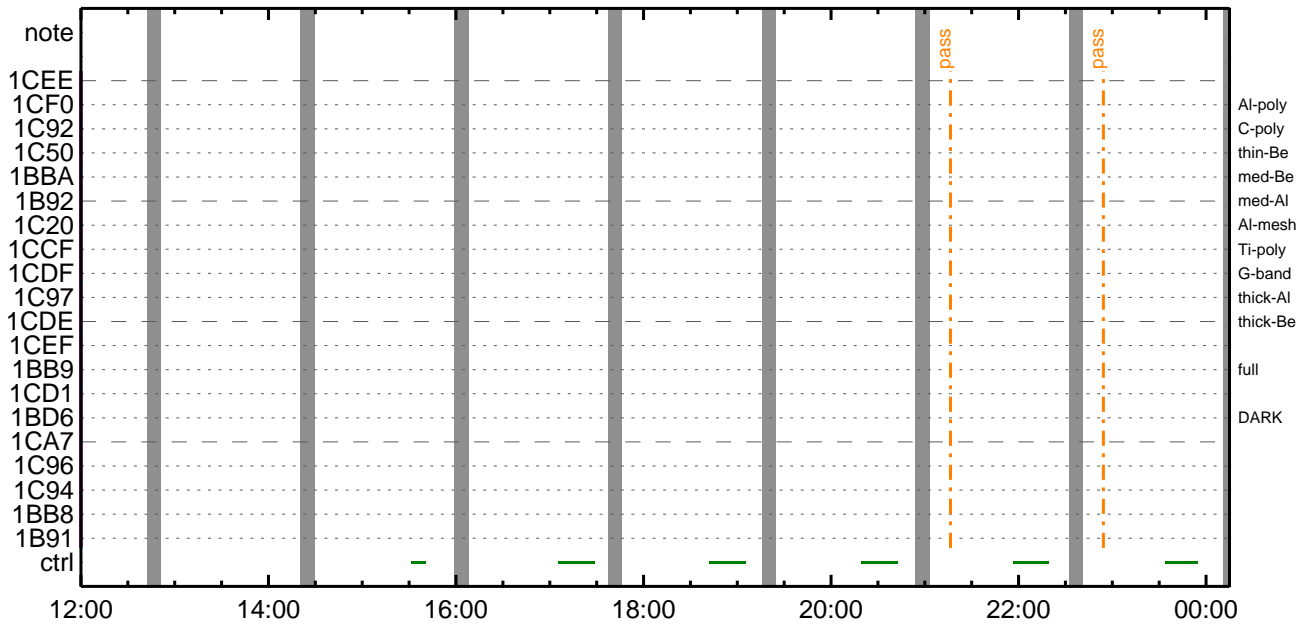
CMDI #0775 2022/12/17



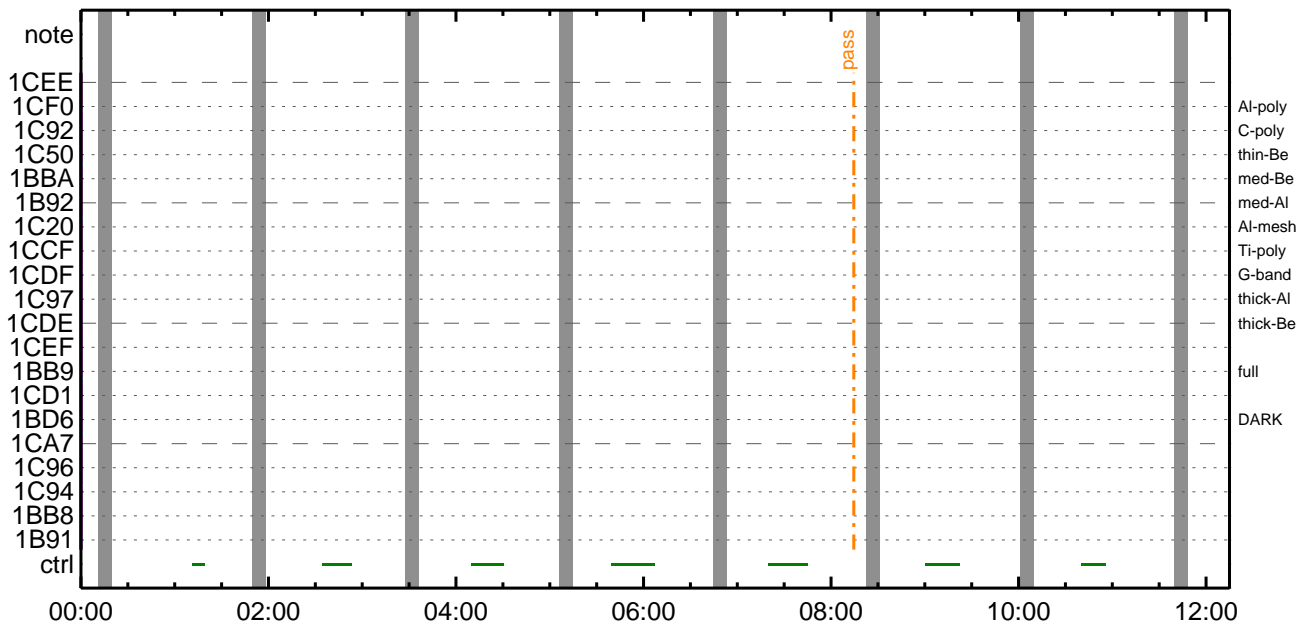
CMDI #0775 2022/12/18



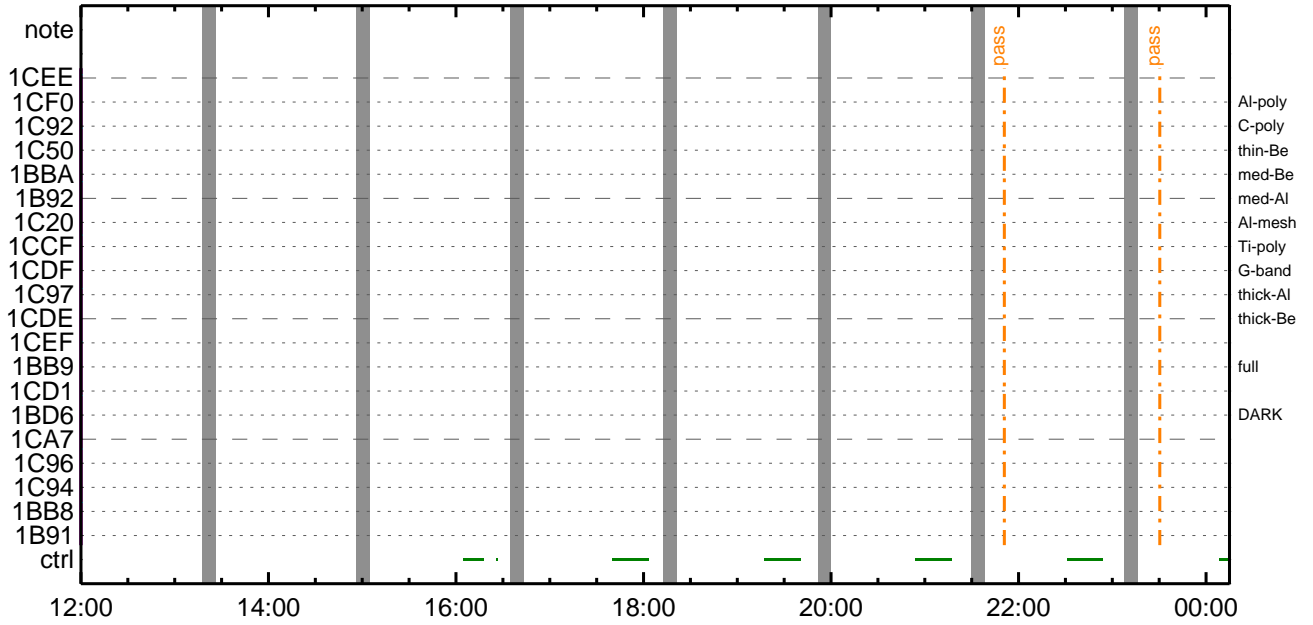
CMDI #0775 2022/12/18



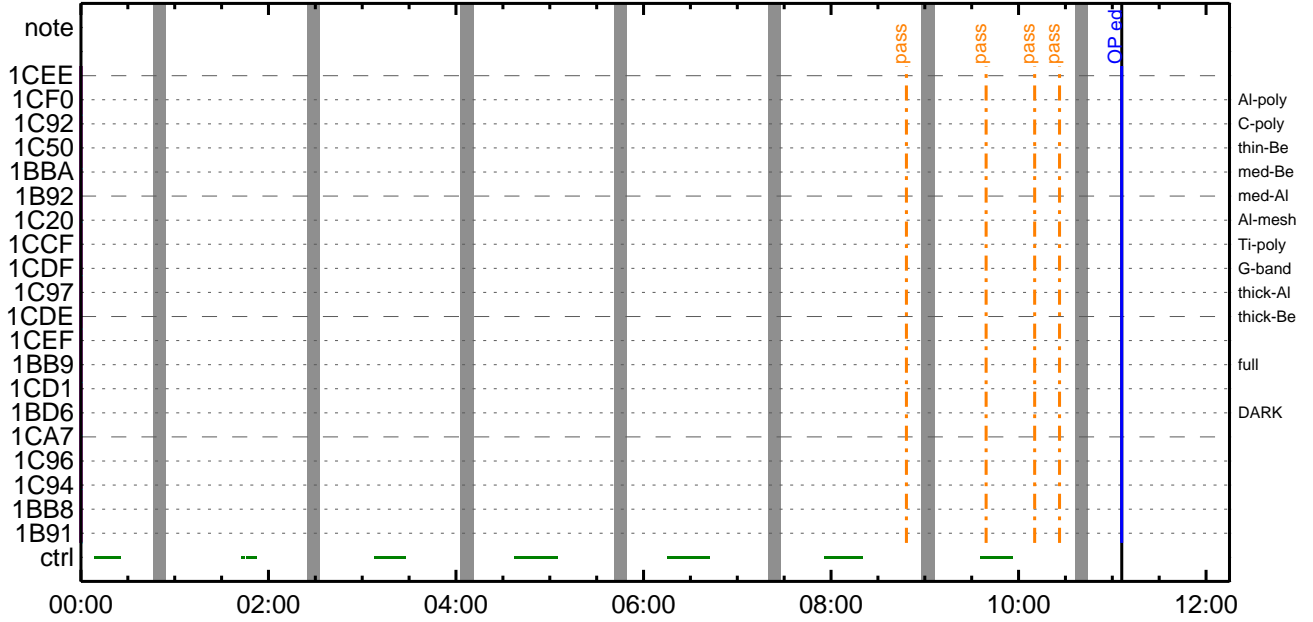
CMDI #0775 2022/12/19



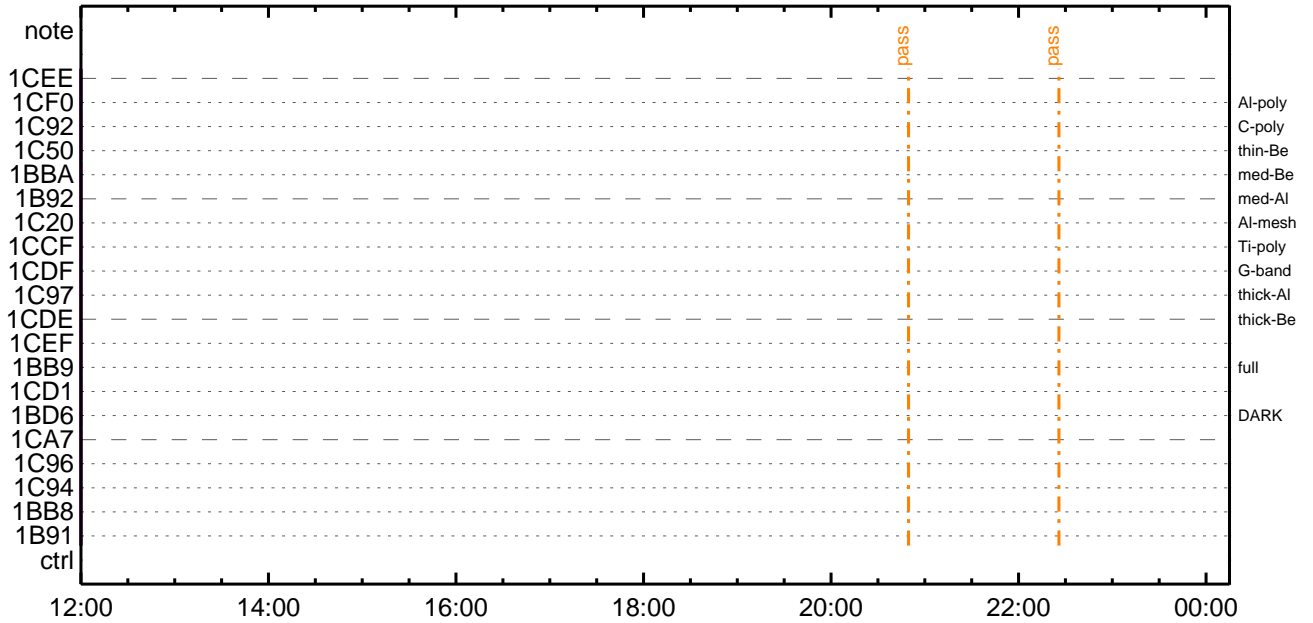
CMDI #0775 2022/12/19



CMDI #0775 2022/12/20



CMDI #0775 2022/12/20




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;YÉ;YAYÓYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;YÉ;ã
0103 S. OP op-573:OP
0104 ( )
0105 S. OG og-573:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYÓYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYÓYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGîî°è¹ç.ë²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYÓYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGîî°è¹ç.ë²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYÓYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPîî°è¹ç.ë²îOKò³îÇ§
0165 C.
0166 C. ***** °ê²¼òîî°è¹ç.ë²îOKò³îÇ§ *****
0167 C. DHUYâ;YÉ;ÊY½;Yî;YÉ;Êòîî°è¹ç
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADò-Á÷çîNGîî°è¹ç;ç°ê²¼òîî°è¹çTI-CMDÁ÷çîî°è¹ç.ë²îOKò³îÇ§;f
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³YpYóYÉòòÁîç(UT)
0183 +. TI 2022-12-15 11:54:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2022-12-15 11:54:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2022-12-15 11:54:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2022-12-15 11:58:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ïÄë%îíñòîÿÄ¥§ÿÄÿ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èÿÄÿÖÿ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                     EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                   EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]                       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                       EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]                      EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                   EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C. ¥ÄÿÖÿ×½ªî»ò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî°è¹ç•è²îOKò³îÇ§
0229 C.
0230 C. DHU¥â;¼ÿÉ;Ê¼ÿ¼. ¥î;¼ÿÉ;Ëòðîá¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                       EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]                      EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                   EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2022-12-15 11:58:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2022-12-15 11:58:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. ===== Begin of AOCs CMD Sequence =====
0255 C.
0256 C. *****
0257 C. ***** GASÿÇ;¼ÿ¼eÄÄ¼Ä»Û *****
0258 C. *****
0259 C.
0260 C. *****
0261 C. MDRV OFF
0262 C. *****
0263 C.
0264 C. ***** GASÿÄÿËÿ;òîò¿òá MTQ¶îî°è³« *****
0265 +. DC 02-33 AOCU_MDRV-X_OFF
0266 +. DC 02-34 AOCU_MDRV-Y_OFF
0267 +. DC 02-35 AOCU_MDRV-Z_OFF
0268 C.          [ ] <A_AOS> [COMPONENT STS] <MDRV> X = OFF ?
0269 C.          [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = OFF ?
0270 C.          [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = OFF ?
0271 C.
0272 C.
0273 C. îÛÿÇ;¼ÿ¼eÄÄòîò¿òá;çîîminÄÏµ;
0274 C.
0275 C. *****
0276 C. MDRV ON
0277 C. *****
0278 C.
0279 C. ***** MTQ¶îî°è³« *****
0280 +. DC 02-32 AOCU_MDRV_ON
0281 C.          [ ] <A_AOS> [COMPONENT STS] <MDRV> X = ON ?
0282 C.          [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = ON ?
0283 C.          [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = ON ?
0284 C.
0285 C.
0286 C. ===== End of AOCs CMD Sequence =====
0287 C.
0288 C.
0289 C. ***** XRT START *****
0290 C. Execute, after the success of OP upload.
0291 +. TI 2022-12-15 11:58:00.0

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```
0292 DC 07-F0 MDP_XRT_MODE_STBY
0293 BC (c3)
0294 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0295 . C.
0296 . C. ***** XRT END *****
0297 . C.
0298 . C. ***** MDP `úÃîñî»ò¼ŷñÊÃĐñ¹ñèDCBC•x²è *****
0299 . C. (¼á°îŷÓŷÃŷÊŷŷŷËŷáŷçŷèñÊ¼ñ¼Ã»Ûñ¹ñè)
0300 . S. DC-BC dcbc-402:DCBC
0301 (MDP_known_event)
0302 . C.
0303 . C.
0304 . C. ***** ŷĐŷ¹•İ Daily±¿İÑñÊ´Øñ¹ñèDCBC•x²è *****
0305 . S. DC-BC dcbc-153:DCBC
0306 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0307 . C.
0308 . C.
0309 . C. ;ãLOSŷÁŷŷŷÃŷ¬¼Ã»Ûñ¹ñè
0310 . C.
0311 . C. ***** LOS *****
0312 . C.
```



```

0096 +. DC 02-32 AOCU_MDRV_ON
0097 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> X = ON ?
0098 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Y = ON ?
0099 . C. [ ] <A_AOS> [COMPONENT STS] <MDRV> Z = ON ?
0100 . C.
0101 . C.
0102 . C. ===== End of AOCs CMD Sequence =====
0103 . C.
0104 . C.
0105 . C. ***** MDP 'ûÃîñî»ò¼ÿñÊÂÐñ¹ñèDCBC•x²è *****
0106 . C. (¼á°îÿÓÿÃÿÈÿËÿËÿáÿçÿèñÊ¼ñ¼Ã»Ûñ¹ñè)
0107 . S. DC-BC dcbc-402:DCBC
0108 (MDP_known_event)
0109 . C.
0110 . C.
0111 . C. ***** ÿÐÿ¹•Ï Daily±¿ÍññË´Øñ¹ñèDCBC•x²è *****
0112 . S. DC-BC dcbc-153:DCBC
0113 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0114 . C.
0115 . C.
0116 . C. ¡ãLOSÿÁÿSÿÃÿ¹¼Ã»Ûñ¹ñè
0117 . C.
0118 . C. ***** LOS *****
0119 . C.

```


*** OP Sequence for XRT ***

2022/12/15	12:08:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	12:08:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2022/12/15	12:09:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 03 74 01 db
2022/12/15	12:09:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2022/12/15	12:09:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2022/12/15	12:09:20.5	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2022/12/15	12:09:22.5	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2022/12/15	12:09:24.5	XRT_FLD_RESET_425_OG [0x1a9]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/15	12:11:56.5	XRT_QT_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2022/12/15	12:11:58.5	XRT_FL_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2022/12/15	12:12:00.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/15	12:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	12:34:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	12:34:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/15	12:34:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/15	12:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	12:43:30.0	XRT_Custom_430_OG [0x1ae]			
2022/12/15	12:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/15	14:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	14:12:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	14:12:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/15	14:12:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/15	14:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	14:22:01.0	XRT_Custom_430_OG [0x1ae]			
2022/12/15	14:23:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/15	15:24:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	15:24:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	15:24:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/15	15:24:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/15	15:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	15:33:30.0	XRT_Custom_430_OG [0x1ae]			
2022/12/15	15:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/15	15:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	15:51:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	15:51:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/15	15:51:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/15	15:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	16:00:30.0	XRT_Custom_430_OG [0x1ae]			
2022/12/15	16:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/15	16:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	16:58:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	16:58:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/15	16:58:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/15	17:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	17:21:30.0	XRT_Custom_430_OG [0x1ae]			
2022/12/15	17:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/15	17:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	17:29:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

2022/12/15	17:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/12/15	17:29:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/12/15	17:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/12/15	17:38:30.0	XRT_Custom_430_OG [0x1ae]						
2022/12/15	17:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/12/15	18:21:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/12/15	18:21:26.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2022/12/15	18:21:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2022/12/15	18:21:46.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2022/12/15	18:21:48.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2022/12/15	18:21:50.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2022/12/15	18:24:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2022/12/15	18:24:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/12/15	18:31:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/12/15	18:31:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2022/12/15	18:31:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 03 74 01 db		
2022/12/15	18:31:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2022/12/15	18:31:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2022/12/15	18:31:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2022/12/15	18:31:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2022/12/15	18:31:54.0	XRT_FLD_RESET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/12/15	18:34:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2022/12/15	18:34:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2022/12/15	18:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/12/15	18:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/12/15	18:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/12/15	18:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/12/15	18:35:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/12/15	18:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/12/15	18:58:30.0	XRT_Custom_430_OG [0x1ae]						
2022/12/15	18:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/12/15	19:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/12/15	19:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/12/15	19:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/12/15	19:08:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/12/15	19:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/12/15	19:17:00.0	XRT_Custom_430_OG [0x1ae]						
2022/12/15	19:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/12/15	20:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/12/15	20:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/12/15	20:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/12/15	20:12:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2022/12/15	20:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2022/12/15	20:35:01.0	XRT_Custom_430_OG [0x1ae]						
2022/12/15	20:36:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2022/12/15	20:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/12/15	20:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2022/12/15	20:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2022/12/15	20:46:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]						

2022/12/15	20:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2022/12/15	20:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	20:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/15	21:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	21:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	21:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/15	21:49:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/15	21:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	22:12:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	22:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/15	22:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	22:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	22:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	22:24:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/15	22:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/15	22:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	22:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	23:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/15	23:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	23:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/15	23:26:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/15	23:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/15	23:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/15	23:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	00:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	00:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	00:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	00:03:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	00:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	00:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	00:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	01:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	01:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	01:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	01:03:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	01:03:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	01:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	01:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	01:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	01:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	01:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	01:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	01:41:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	01:41:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	01:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	01:50:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	01:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	02:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	02:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	02:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	02:28:06.5	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	02:28:06.5	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	02:31:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

2022/12/16	02:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2022/12/16	02:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2022/12/16	03:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/12/16	03:19:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/12/16	03:19:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/12/16	03:19:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da			
2022/12/16	03:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2022/12/16	03:29:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2022/12/16	03:30:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
2022/12/16	04:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/12/16	04:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/12/16	04:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/12/16	04:02:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da			
2022/12/16	04:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2022/12/16	04:23:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2022/12/16	04:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2022/12/16	04:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/12/16	04:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/12/16	04:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/12/16	04:58:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da			
2022/12/16	04:58:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2022/12/16	05:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2022/12/16	05:07:00.0	XRT_Custom_430_OG [0x1ae]							
2022/12/16	05:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2022/12/16	05:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/12/16	05:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/12/16	05:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/12/16	05:32:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da			
2022/12/16	05:32:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2022/12/16	05:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2022/12/16	06:00:30.0	XRT_Custom_430_OG [0x1ae]							
2022/12/16	06:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2022/12/16	06:09:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/12/16	06:09:26.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/12/16	06:09:30.0	AOCS_Or-e-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2022/12/16	06:09:46.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00			
2022/12/16	06:09:48.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2022/12/16	06:09:50.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2022/12/16	06:12:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2022/12/16	06:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2022/12/16	06:19:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2022/12/16	06:19:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2022/12/16	06:19:30.0	AOCS_Or-e-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2022/12/16	06:19:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 03 74 01 db			
2022/12/16	06:19:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2022/12/16	06:19:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2022/12/16	06:19:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2022/12/16	06:19:54.0	XRT_FLD_RESET_425_OG [0x1a9]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2022/12/16	06:22:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da			
2022/12/16	06:22:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2022/12/16	06:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04			

2022/12/16	06:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	06:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	06:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	06:36:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	06:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	06:45:30.0	XRT_Custom_430_OG [0x1ae]				
2022/12/16	06:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	07:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	07:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	07:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	07:12:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	07:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	07:37:30.0	XRT_Custom_430_OG [0x1ae]				
2022/12/16	07:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	08:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	08:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	08:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	08:14:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	08:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	08:24:00.5	XRT_Custom_430_OG [0x1ae]				
2022/12/16	08:25:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	08:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	08:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	08:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	08:52:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	08:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	09:14:30.0	XRT_Custom_430_OG [0x1ae]				
2022/12/16	09:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	09:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	09:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	09:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	09:53:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	09:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	10:02:30.0	XRT_Custom_430_OG [0x1ae]				
2022/12/16	10:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	10:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	10:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	10:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	10:32:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	10:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	10:49:00.5	XRT_Custom_430_OG [0x1ae]				
2022/12/16	10:50:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	11:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	11:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	11:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	11:31:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	11:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	11:40:30.0	XRT_Custom_430_OG [0x1ae]				
2022/12/16	11:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	13:10:00.0	XRT_CTRL_MANU_400_OG [0x190]				

2022/12/16	13:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	13:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	13:10:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/12/16	13:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/12/16	13:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/12/16	13:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/12/16	14:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	14:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	14:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	14:48:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/12/16	14:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/12/16	14:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/12/16	14:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/12/16	15:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	15:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	15:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	15:57:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/12/16	16:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/12/16	16:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/12/16	16:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/12/16	16:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	16:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	16:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	16:26:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/12/16	16:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/12/16	16:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/12/16	16:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/12/16	17:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	17:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	17:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	17:32:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/12/16	17:32:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/12/16	17:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/12/16	17:56:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/12/16	17:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/12/16	18:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	18:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	18:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	18:05:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2022/12/16	18:05:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2022/12/16	18:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/12/16	18:14:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2022/12/16	18:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/12/16	18:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2022/12/16	18:26:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2022/12/16	18:27:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2022/12/16	18:27:16.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00	
2022/12/16	18:27:18.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2022/12/16	18:27:20.0	XRT_ARS_DIS_404_OG [0x194]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2022/12/16	18:29:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2022/12/16	18:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2022/12/16	18:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

2022/12/16	18:36:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	18:36:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2022/12/16	18:37:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	04 03 74 01 db
2022/12/16	18:37:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2022/12/16	18:37:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2022/12/16	18:37:20.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2022/12/16	18:37:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2022/12/16	18:37:24.0	XRT_FLD_RESET_425_OG [0x1a9] MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	18:39:56.0	XRT_QT_PROG_SET_428_OG [0x1ac] MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2022/12/16	18:39:58.0	XRT_FL_PROG_SET_418_OG [0x1a2] MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2022/12/16	18:40:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	19:10:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	19:10:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	19:10:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	19:10:06.0	XRT_PREFLR_STRT_436_OG [0x1b4] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	19:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	19:33:00.0	XRT_Custom_430_OG [0x1ae]			
2022/12/16	19:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	19:43:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	19:43:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	19:43:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	19:43:06.0	XRT_PREFLR_STRT_436_OG [0x1b4] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	19:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	19:52:30.0	XRT_Custom_430_OG [0x1ae]			
2022/12/16	19:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	20:46:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	20:46:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	20:46:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	20:46:36.0	XRT_PREFLR_STRT_436_OG [0x1b4] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	20:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	21:10:00.0	XRT_Custom_430_OG [0x1ae]			
2022/12/16	21:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	21:21:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	21:21:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	21:21:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	21:21:36.0	XRT_PREFLR_STRT_436_OG [0x1b4] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	21:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	21:30:30.0	XRT_Custom_430_OG [0x1ae]			
2022/12/16	21:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	22:24:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	22:24:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	22:24:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/16	22:24:06.0	XRT_PREFLR_STRT_436_OG [0x1b4] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	22:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	22:46:30.0	XRT_Custom_430_OG [0x1ae]			
2022/12/16	22:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/16	23:00:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	23:00:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/16	23:00:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da

2022/12/16	23:00:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/16	23:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/16	23:09:00.0	XRT_Custom_430_OG [0x1ae]			
2022/12/16	23:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/17	00:01:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	00:01:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	00:01:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/17	00:01:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/17	00:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/17	00:20:00.0	XRT_Custom_430_OG [0x1ae]			
2022/12/17	00:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/17	00:38:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	00:38:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	00:38:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/17	00:38:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/17	00:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/17	00:47:30.0	XRT_Custom_430_OG [0x1ae]			
2022/12/17	00:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/17	01:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	01:38:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	01:38:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/17	01:38:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/17	01:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/17	01:45:00.0	XRT_Custom_430_OG [0x1ae]			
2022/12/17	01:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/17	02:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	02:16:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	02:16:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/17	02:16:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/17	02:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/17	02:26:00.0	XRT_Custom_430_OG [0x1ae]			
2022/12/17	02:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/17	03:01:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	03:01:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	03:01:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/17	03:01:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/17	03:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/17	03:21:00.0	XRT_Custom_430_OG [0x1ae]			
2022/12/17	03:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/17	03:55:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	03:55:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	03:55:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/17	03:55:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/17	03:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/17	04:04:00.0	XRT_Custom_430_OG [0x1ae]			
2022/12/17	04:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/17	04:32:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	04:32:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	04:32:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/17	04:32:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

2022/12/17	04:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/12/17	04:58:00.0	XRT_Custom_430_OG [0x1ae]							
2022/12/17	04:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/12/17	05:23:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/12/17	05:23:26.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2022/12/17	05:23:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00				
2022/12/17	05:23:46.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2022/12/17	05:23:48.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2022/12/17	05:23:50.0	XRT_ARS_DIS_404_OG [0x194]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2022/12/17	05:26:28.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2022/12/17	05:26:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/12/17	05:33:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/12/17	05:33:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2022/12/17	05:33:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 03 74 01 db				
2022/12/17	05:33:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2022/12/17	05:33:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2022/12/17	05:33:50.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2022/12/17	05:33:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2022/12/17	05:33:54.0	XRT_FLD_RESET_425_OG [0x1a9]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/12/17	05:36:26.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2022/12/17	05:36:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2022/12/17	05:42:30.0	XRT_Custom_430_OG [0x1ae]							
2022/12/17	05:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/12/17	06:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/12/17	06:08:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/12/17	06:08:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/12/17	06:08:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/12/17	06:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/12/17	06:35:30.0	XRT_Custom_430_OG [0x1ae]							
2022/12/17	06:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/12/17	07:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/12/17	07:12:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/12/17	07:12:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/12/17	07:12:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/12/17	07:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/12/17	07:21:00.0	XRT_Custom_430_OG [0x1ae]							
2022/12/17	07:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/12/17	07:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/12/17	07:48:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/12/17	07:48:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/12/17	07:48:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/12/17	07:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2022/12/17	08:12:30.0	XRT_Custom_430_OG [0x1ae]							
2022/12/17	08:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2022/12/17	08:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/12/17	08:50:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2022/12/17	08:50:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2022/12/17	08:50:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2022/12/17	08:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2022/12/17	08:59:00.0	XRT_Custom_430_OG [0x1ae]			
2022/12/17	09:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/17	09:28:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	09:28:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	09:28:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2022/12/17	09:28:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2022/12/17	09:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2022/12/17	09:49:00.0	XRT_Custom_430_OG [0x1ae]			
2022/12/17	09:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2022/12/17	10:15:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2022/12/17	11:42:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00