XRT Timeline to be uploaded on 2023/04/06

Period: 2023/04/06 10:41:00 - 2023/04/11 11:19:00

* * * * * **Normal mode** * * * * *

XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/06 10:54:00 - 04/06 17:59:54	Track (642.6, -358.6) @ 04/06 10:51:00	# OP start + 10min, AR13270
04/07 09:17:00 - 04/07 17:34:00	Track (747.5, -375.1) @ 04/07 09:00:00	# AR13270
04/07 18:11:30 - 04/07 23:59:54	Track (781.1, -382.4) @ 04/07 18:08:30	# AR13270
04/08 05:55:30 - 04/08 10:53:00	Track (815.6, -392.2) @ 04/08 05:52:30	# AR13270

PROG= 19 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 47 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 96 4-time(s) 180.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

XOB #1CEF: Synoptic for HOP448 w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(33/512/4096), Thick-Be(65536), Med-Al(256/8192/32768), Med-Be(12

Term	Pointing (x, y)	Comment
04/06 18:03:00 - 04/06 18:09:54	Fixed (0.0, 0.0)	synoptic
04/07 07:03:00 - 04/07 07:13:30	Fixed (-21.0, -39.0)	synoptic, shifted 60.0 min, HOP349, SOT at [0,0]
04/07 18:01:30 - 04/07 18:08:24	Fixed (0.0, 0.0)	synoptic, shifted -1.5 min
04/08 05:45:30 - 04/08 05:52:24	Fixed (0.0, 0.0)	synoptic, shifted -17.5 min

PROG= 14 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 26 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec

Seqn= 15 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 83 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 46 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 17 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec

Seqn= 33 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	384x384 (1064, 1048)	Q=95	0	0	2.0sec

XOB #1CE7: AR - Filter-Ratio with thin-Be (long/short pairs) and Med-Be (short) with PFB, 384x384 at 1064 1048, with G-band (1ms/1ms VLS=CLS), 180 cad

Term	Pointing (x, y)	Comment
04/06 18:13:00 - 04/07 03:15:00	Track (668.9, -410.4) @ 04/06 18:10:00	# HOP458 (AR13270)

PROG= 12 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 60-time(s) 180.0sec

Seqn= 37 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 59 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1C97: AR - Filter-Ratio with thin-Be (long/short pairs) and Med-Be (short) with PFB, 384x384 at 1064 1048, with G-band (1ms/1ms VLS=CLS), 60 cad

Term	Pointing (x, y)	Comment
04/07 03:18:06 - 04/07 06:59:54	Track (668.9, -410.4) @ 04/06 18:10:00	# HOP458 (AR13270)

PROG= 17 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 120-time(s) 60.0sec

Seqn= 37 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 59 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w

Term	Pointing (x, y)	Comment
04/07 07:40:00 - 04/07 08:53:30	Fixed (-21.0, -39.0)	synoptic, shifted 60.0 min, HOP349, SOT at [0,0]
04/08 04:03:00 - 04/08 05:42:24	Fixed (0.0, 0.0)	HOP349

PROG= 06 Inf.-time(s)

Subr= 1 1-time(s) 300.0sec

Seqn= 55 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 15 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 79 1-time(s) 2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 30 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 15-time(s) 1500.0sec

Seqn= 8 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 74 1-time(s) 2.0sec

med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 29 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1CDE: HOP393/336 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 7

Term	Pointing (x, y)	Comment

04/08 00:22:01 - 04/08 03:59:54	Track (-21.0, 64.0) @ 04/08 00:00:00								HOP393					
PROG= 07 Inf.-time(s)														
Subr= 1 1-time(s) 2.0sec														
Seqn= 30 1-time(s) 2.0sec														
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 52 1-time(s) 2.0sec														
Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec		
Subr= 2 30-time(s) 720.0sec														
Seqn= 97 2-time(s) 2.0sec														
Al-poly/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec		
Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval		

Flare mode

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment
04/06 10:54:00 - 04/06 17:59:54	Track (642.6, -358.6) @ 04/06 10:51:00	# OP start + 10min, AR13270
04/06 18:13:00 - 04/07 03:15:00	Track (668.9, -410.4) @ 04/06 18:10:00	# HOP458 (AR13270)
04/07 03:18:06 - 04/07 06:59:54	Track (668.9, -410.4) @ 04/06 18:10:00	# HOP458 (AR13270)
04/07 07:40:00 - 04/07 08:53:30	Fixed (-21.0, -39.0)	synoptic, shifted 60.0 min, HOP349, SOT at [0,0]
04/07 09:17:00 - 04/07 17:34:00	Track (747.5, -375.1) @ 04/07 09:00:00	# AR13270
04/07 18:11:30 - 04/07 23:59:54	Track (781.1, -382.4) @ 04/07 18:08:30	# AR13270
04/08 00:22:01 - 04/08 03:59:54	Track (-21.0, 64.0) @ 04/08 00:00:00	HOP393
04/08 04:03:00 - 04/08 05:42:24	Fixed (0.0, 0.0)	HOP349
04/08 05:55:30 - 04/08 10:53:00	Track (815.6, -392.2) @ 04/08 05:52:30	# AR13270

PROG= 04 30-time(s)														
Subr= 1 20-time(s) 2.0sec														
Seqn= 11 1-time(s) 2.0sec														
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 73 1-time(s) 10.0sec														
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2 1-time(s) 2.0sec														
Seqn= 10 1-time(s) 2.0sec														
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11 1-time(s) 2.0sec														
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 87 1-time(s) 2.0sec														
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval		

Active Region Search

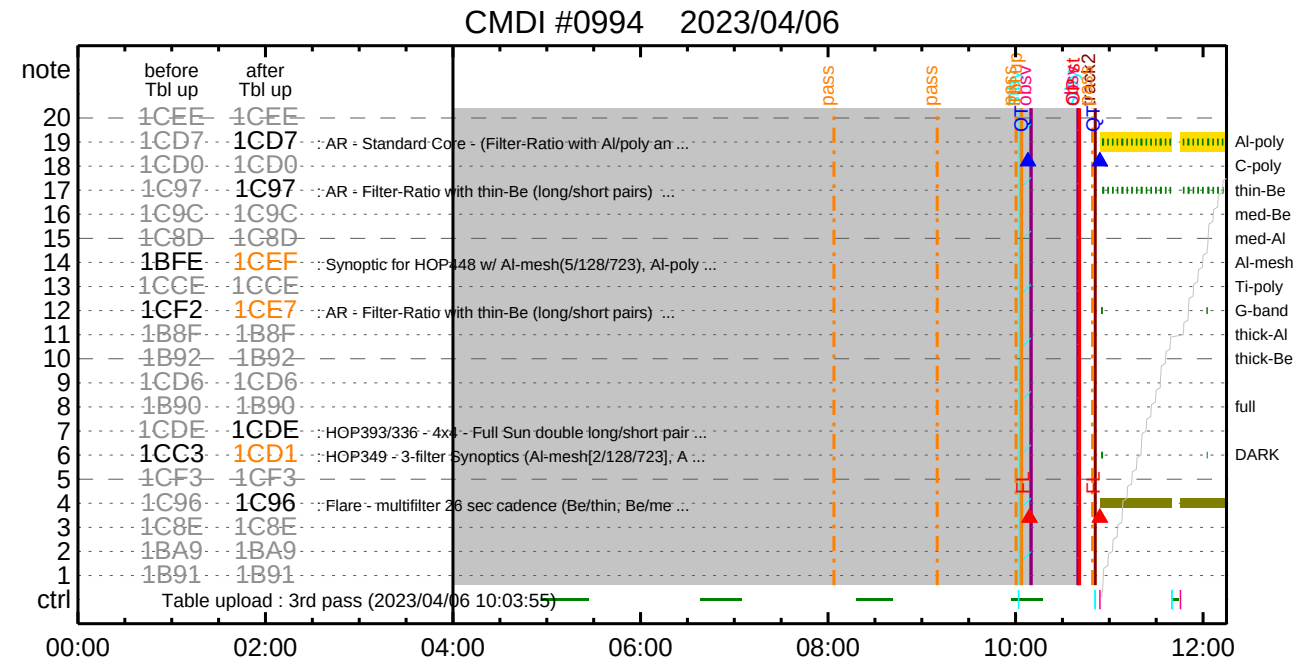
NOT USED

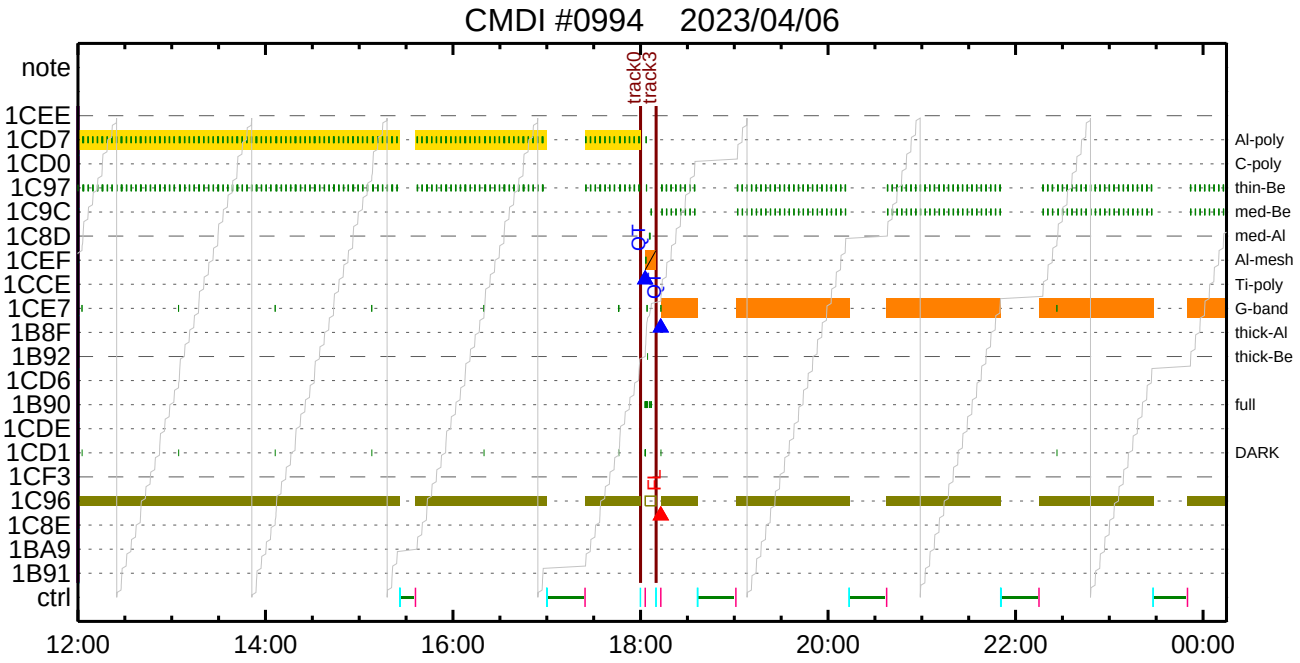
Flare Detection

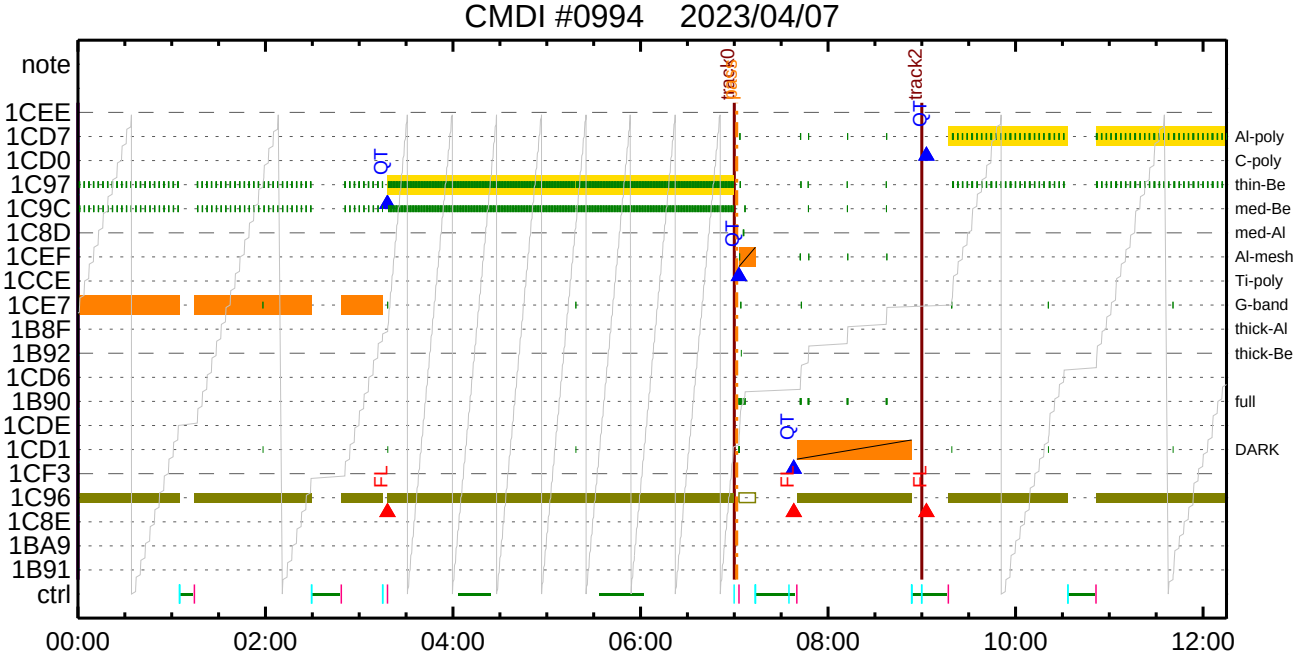
FLD Patrol

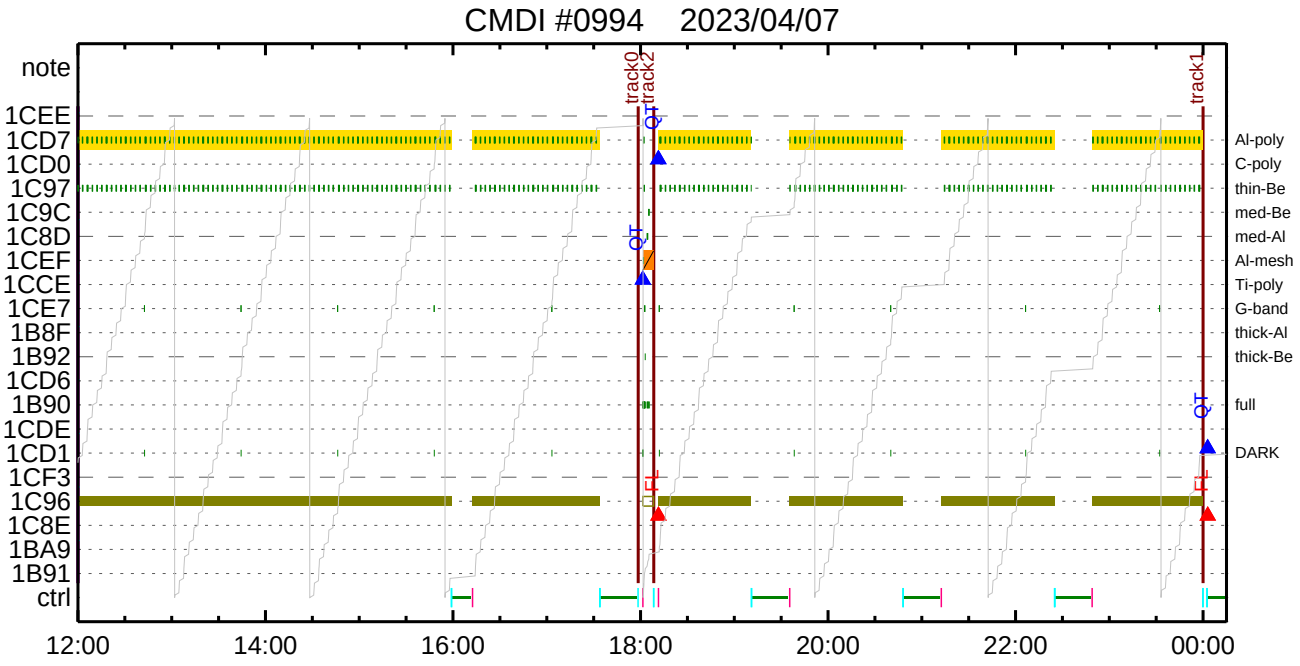
Term	Pointing (x, y)	Comment
04/06 10:04:55 - 04/06 18:00:16	cannot be identified	
04/06 18:10:18 - 04/07 07:00:16	Track (668.9, -410.4) @ 04/06 18:10:00	# HOP458 (AR13270)
04/07 07:35:22 - 04/07 17:58:46	Fixed (-21.0, -39.0)	synoptic, shifted 60.0 min, HOP349, SOT at [0,0]
04/07 18:08:48 - 04/08 05:42:46	Track (781.1, -382.4) @ 04/07 18:08:30	# AR13270
04/08 05:52:48 - 04/11 11:19:00	Track (815.6, -392.2) @ 04/08 05:52:30	# AR13270

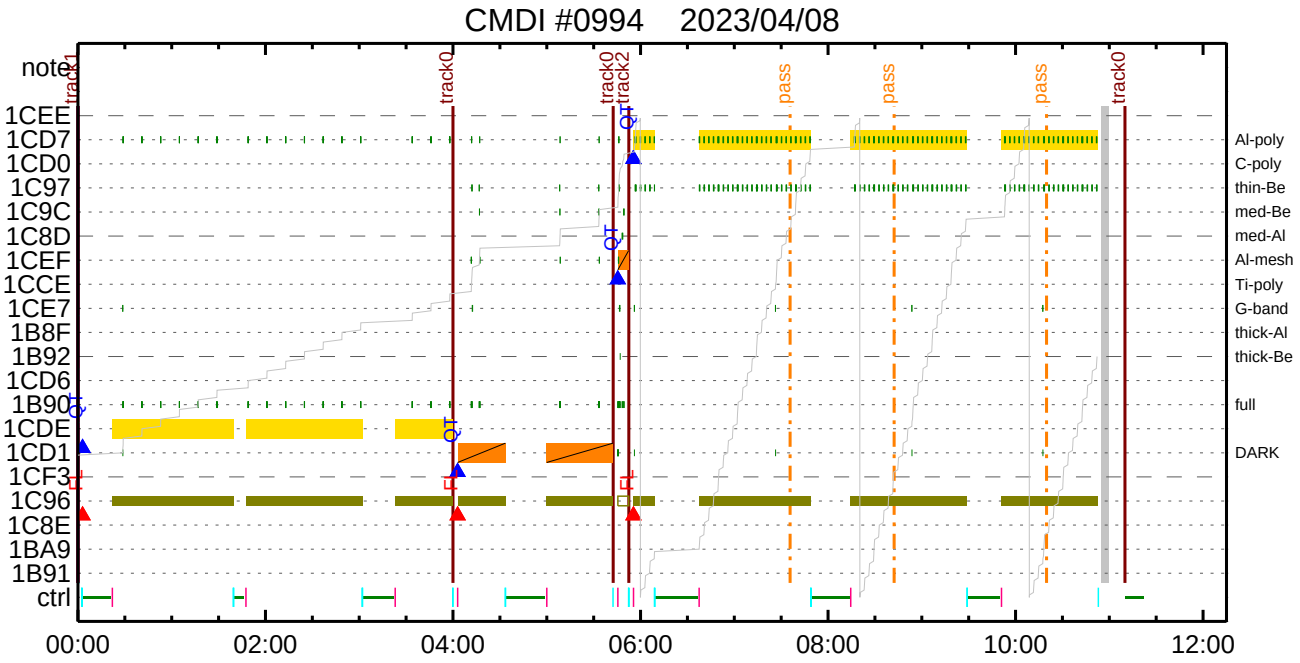
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50			30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval		

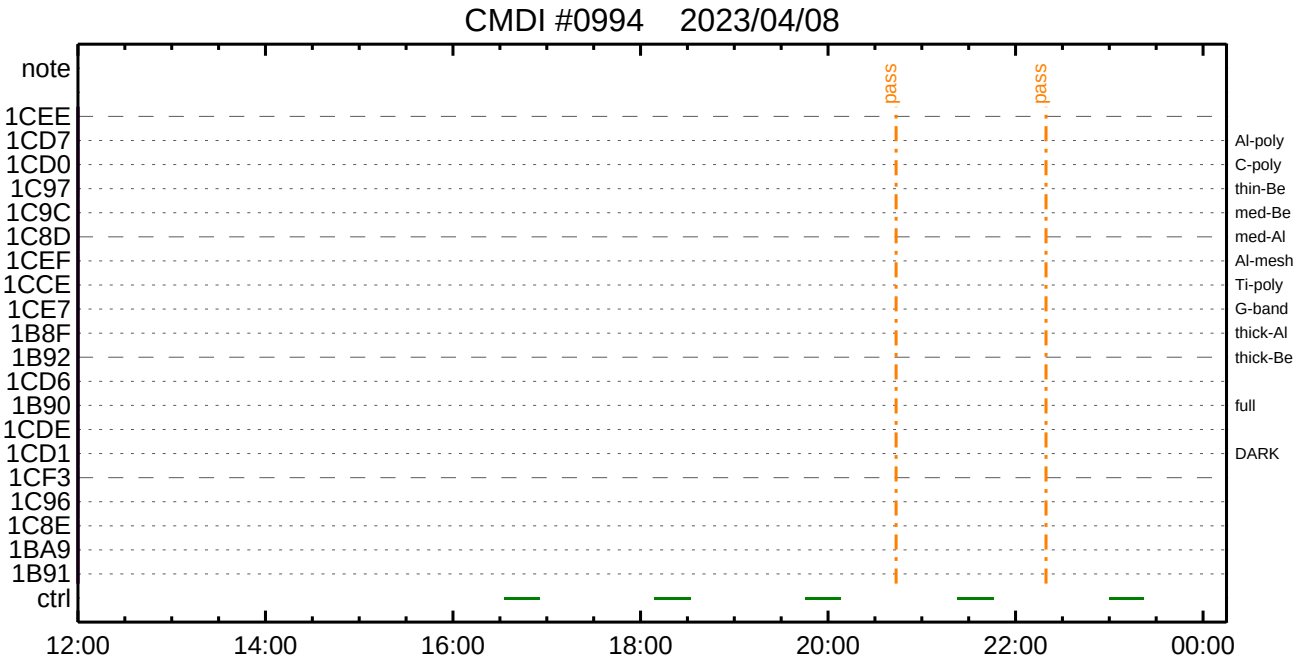

CMDI #0994 2023/04/06
note
before Tbl up
after Tbl up
20 1CEE 1CEE
19 1CD7 1CD7 : AR - Standard Core - (Filter-Ratio with Al/poly an ...
18 1CD0 1CD0
17 1C97 1C97 : AR - Filter-Ratio with thin-Be (long/short pairs) ...
16 1C9C 1C9C
15 1C8D 1C8D
14 1BFE 1CEF : Synoptic for HOP448 w/ Al-mesh(5/128/723), Al-poly ...
13 1CCE 1CCE
12 1CF2 1CE7 : AR - Filter-Ratio with thin-Be (long/short pairs) ...
11 1B8F 1B8F
10 1B92 1B92
9 1CD6 1CD6
8 1B90 1B90
7 1CDE 1CDE : HOP393/336 - 4x4 - Full Sun double long/short pair ...
6 1CC3 1CD1 : HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], A ...
5 1CF3 1CF3
4 1C96 1C96 : Flare - multifilter 26 sec cadence (Be/thin; Be/me ...
3 1C8E 1C8E
2 1BA9 1BA9
1 1B91 1B91
ctrl
Table upload : 3rd pass (2023/04/06 10:03:55)
pass
pass
QT
obsv
QT
track2
FL
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

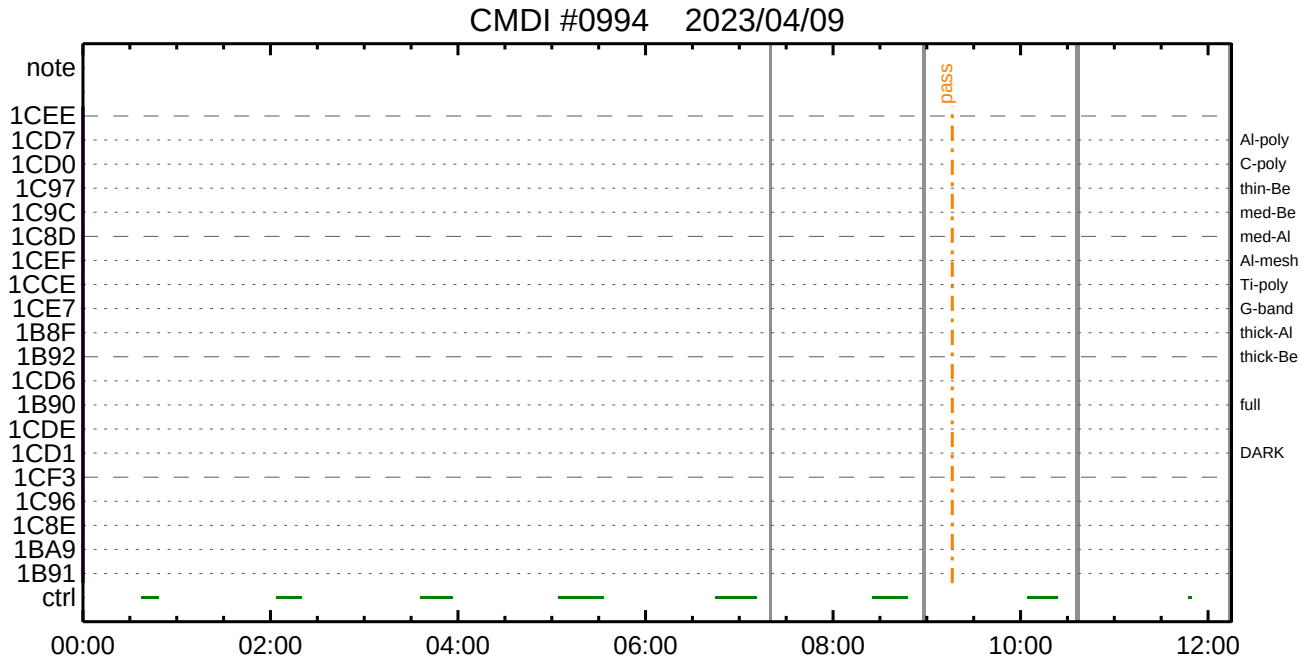

CMDI #0994 2023/04/06
note
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
track0
track3
QT
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

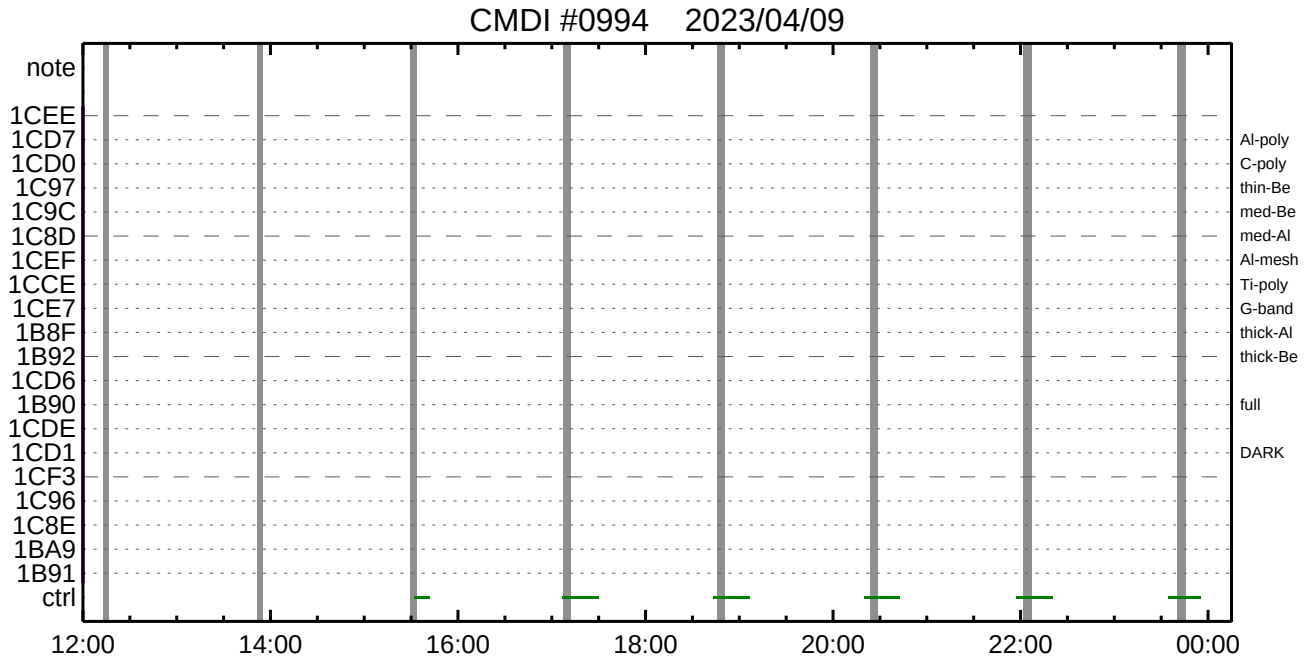

CMDI #0994 2023/04/07
note
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
track0
track2
QT
QT
QT
QT
FL
FL
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

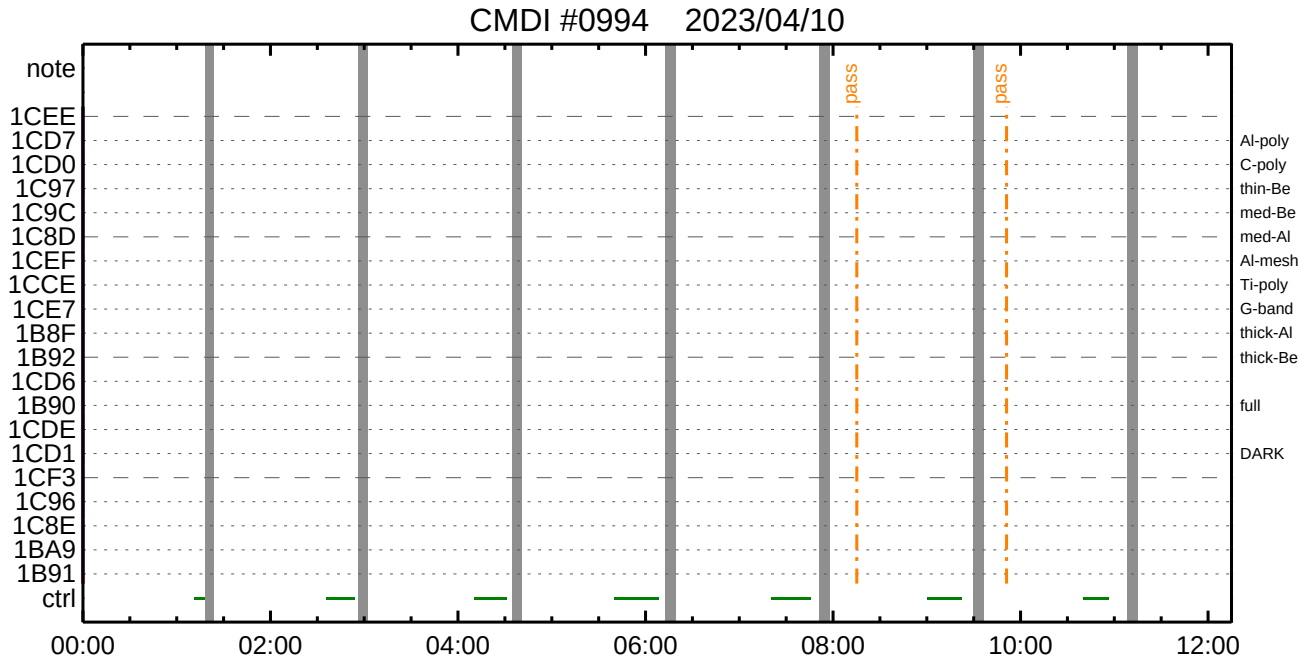

CMDI #0994 2023/04/07
note
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
track0
track2
track1
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

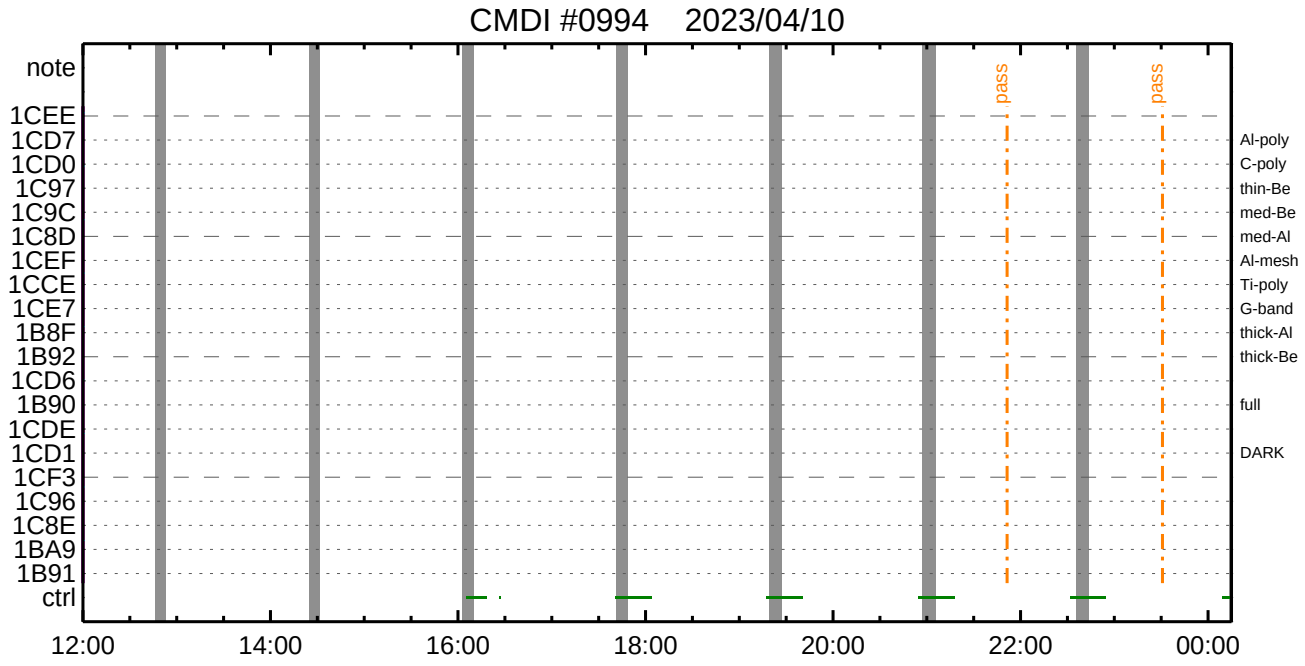

CMDI #0994 2023/04/08
note
track1
track0
track2
pass
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

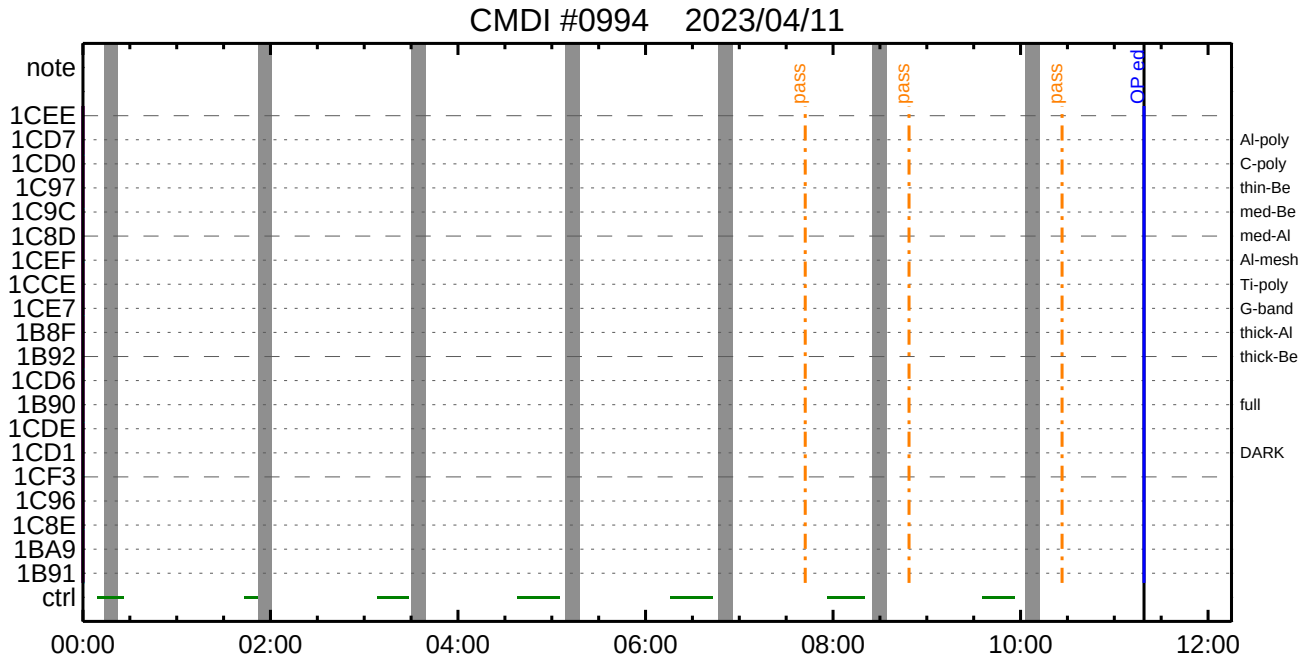

CMDI #0994 2023/04/08
note
pass
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

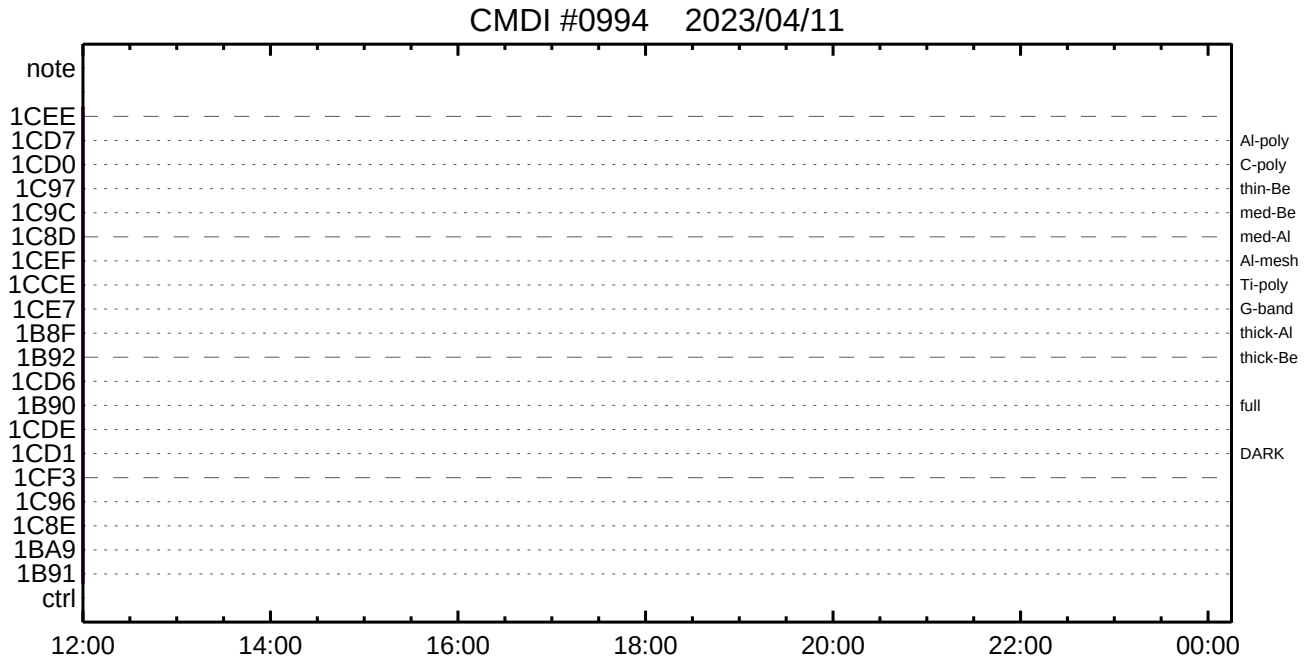

CMDI #0994 2023/04/09
note
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0994 2023/04/09
note
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0994 2023/04/10
note
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0994 2023/04/10
note
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0994 2023/04/11
note
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0994 2023/04/11
note
1CEE
1CD7
1CD0
1C97
1C9C
1C8D
1CEF
1CCE
1CE7
1B8F
1B92
1CD6
1B90
1CDE
1CD1
1CF3
1C96
1C8E
1BA9
1B91
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-773 2023-04-06 13:00:27 205 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP    op-773:OP
0020     ()
0021   . S. OG    og-773:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC       (20 00 7f 01 02)
0028     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC       (07 0b f8)
0035     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC       (20 80 7f 01 02)
0047     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC       (07 0b f8)
0054     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC       (21 00 41 01 02)
0066     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC       (07 0b f8)
0073     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC       (02 0a f8)
0086     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Î¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2023-04-06 10:36:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2023-04-06 10:36:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2023-04-06 10:36:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2023-04-06 10:40:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ìÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ****************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ****************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154     C. ******************************
0155     C.  SOT TI command set
0156     C. ******************************
0157     C. Execute, after the success of OP upload.
0158   +. TI 2023-04-06 10:40:16.0
0159     DC 07-F0 MDP_SOT_MODE_STBY
0160     BC       (41)
0161   . C. ------------------------------------
0162     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0163     C. ------------------------------------
0164     C. ************ SOT END ***********
0165   . C. Stop EIS observation and temporarily disable EIS mode changes
0166     C.
0167     C.
0168     C. ********** Start EIS operation (TI set) **********
0169     C. Execute, after the success of OP upload.
0170     C. Set EIS TI-commands
0171   +. TI 2023-04-06 10:40:30.0
0172     DC 07-FC EIS_MODE_MANU
0173     BC       (21 02)
0174   +. TI 2023-04-06 10:40:40.0
0175     DC 07-FC EIS_MODE_CHG_DIS
0176     BC       (22)
0177   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0178     C. ************ End EIS operation (TI set) ***********
0179     C.
0180     C.
0181     C.
0182     C. ************ XRT START ***********
0183     C. Execute, after the success of OP upload.
0184   +. TI 2023-04-06 10:40:00.0
0185     DC 07-F0 MDP_XRT_MODE_STBY
0186     BC       (c3)
0187   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0188     C.
0189     C. ************ XRT END ***********
0190     C.
0191   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0192     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0193   . S. DC-BC dcbc-402:DCBC
```

```
0194     (MDP_known_event)
0195     C.
0196     C.
0197   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0198   . S. DC-BC dcbc-153:DCBC
0199     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200     C.
0201     C.
0202   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0203     C.
0204   . C. ********** LOS **********
0205     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-774 2023-04-06 13:00:27 169 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ËËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇºÎ»Þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÈÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÈÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸å¤ÎºÆÀ¸ ºÆ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                       Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_CHG_ENA
0131     BC       (20)
0132    . C. Verify EIS_MODE_CHG_FLG is ENA
0133   +. DC 07-FC EIS_MODE_MANU
0134     BC       (21 02)
0135    . C. Verify EIS in MANUAL mode
0136    . C. Estimated OBSTBL upload time is 13s
0137     C. ********************************
0138     C. EIS START OBSTBL LOAD
0139     C. ********************************
0140   . S. RAM   ram-820:EIS_OBSTBL
0141     ()
0142   +. DC 07-FC EIS_DUMP_OBSTBL
0143     BC       (07 07 07 00 00 70 00)
0144     C.
0145     C. Execute, after the success of OBSTBL upload.
0146     C. Set EIS TI-commands
0147   +. TI 2023-04-06 10:40:50.0
0148     DC 07-FC EIS_MODE_CHG_ENA
0149     BC       (20)
0150   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0151     C. ********************************
0152     C. EIS END OBSTBL LOAD
0153     C. ********************************
0154     C.
0155   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0156     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0157   . S. DC-BC dcbc-402:DCBC
0158     (MDP_known_event)
0159     C.
0160     C.
0161   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0162   . S. DC-BC dcbc-153:DCBC
0163     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164     C.
0165     C.
0166   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0167     C.
0168   . C. ********** LOS **********
0169     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-775 2023-04-06 13:00:27 130 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017   . C. < Stop SP table >
0018   +. DC 07-F0 MDP_SP_CTRL_MANU
0019     BC       (61)
0020     C. ------------------------------------
0021     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024   . C. <Upload SP Observation Table>
0025   . S. RAM   ram-284:MDP_OBS_S
0026     ()
0027     C.
0028   . C. < Dump RAMID=MDP_OBS_S >
0029   +. DC 07-F0 MDP_DUMP_SPTBL
0030     BC       (83 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_S verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035     C. ********************************
0036     C.  SOT TI command set
0037     C. ********************************
0038     C. Execute, after the success of TBL upload.
0039   +. TI 2023-04-06 10:40:18.0
0040     DC 07-F0 MDP_SOT_MODE_OBSV
0041     BC       (40)
0042   . C. ------------------------------------
0043     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0044     C. ------------------------------------
0045     C.
0046     C.
0047     C. ************ XRT START ************
0048     C.
0049   +. DC 07-F0 MDP_XRT_CTRL_MANU
0050     BC       (c1)
0051   +  DC 07-F0 MDP_XRT_MODE_STBY
0052     BC       (c3)
0053   . C. ----------- Success Verify ?             OK / NG____
0054     C.
0055     C. XRT Obs. Table Upload
0056   . S. RAM   ram-291:MDP_OBS_X
0057     ()
0058     C.
0059   +. DC 07-F0 MDP_DUMP_XRTTBL
0060     BC       (84 07 00 00 00 3a d4)
0061   . C. ----------- Comparison Check ?                      OK / ERR ____
0062     C.
0063     C.
0064   +. DC 07-F0 MDP_XRT_ROI_SET
0065     BC       (cd 01 b1 b1 04 04)
0066   +  DC 07-F0 MDP_XRT_ROI_SET
0067     BC       (cd 02 b1 b1 08 08)
0068   +  DC 07-F0 MDP_XRT_ROI_SET
0069     BC       (cd 03 b1 b1 08 08)
0070   +  DC 07-F0 MDP_XRT_ROI_SET
0071     BC       (cd 04 b1 b1 06 06)
0072   +  DC 07-F0 MDP_XRT_ROI_SET
0073     BC       (cd 05 85 83 06 06)
0074   +  DC 07-F0 MDP_XRT_ROI_SET
0075     BC       (cd 06 85 83 06 06)
0076   +  DC 07-F0 MDP_XRT_ROI_SET
0077     BC       (cd 07 80 80 20 20)
0078   +  DC 07-F0 MDP_XRT_ROI_SET
0079     BC       (cd 08 80 80 20 08)
0080   +  DC 07-F0 MDP_XRT_ROI_SET
0081     BC       (cd 09 80 80 08 20)
0082   +  DC 07-F0 MDP_XRT_ROI_SET
0083     BC       (cd 0a 80 80 08 08)
0084   +  DC 07-F0 MDP_XRT_ROI_SET
0085     BC       (cd 0f 80 80 06 06)
0086   +  DC 07-F0 MDP_XRT_ROI_SET
0087     BC       (cd 10 80 80 08 08)
0088   +  DC 07-F0 MDP_XRT_FLD_ENA
0089     BC       (d8)
0090   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091     BC       (c8)
0092   +  DC 07-F0 MDP_XRT_ARS_DIS
0093     BC       (d5)
0094   +  DC 07-F0 MDP_XRT_AEC_RESET
0095     BC       (d0)
```

```
0096  +  DC 07-F0 MDP_XRT_FLD_RESET
0097     BC       (da)
0098  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099     BC       (c4 13)
0100  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101     BC       (c5 04)
0102   . C. ---------- Success Verify ?            OK / NG ____
0103     C.
0104     C.
0105   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0106     C.
0107  +. DC 07-F0 MDP_XRT_MODE_OBSV
0108     BC       (c2)
0109  +. TI 2023-04-06 10:40:02.0
0110     DC 07-F0 MDP_XRT_MODE_OBSV
0111     BC       (c2)
0112   . C. ---------- Success Verify ?            OK / NG ____
0113     C.
0114     C. ************ XRT END ***********
0115     C.
0116   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0117     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0118   . S. DC-BC dcbc-402:DCBC
0119     (MDP_known_event)
0120     C.
0121     C.
0122   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0123   . S. DC-BC dcbc-153:DCBC
0124     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125     C.
0126     C.
0127   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0128     C.
0129   . C. *********** LOS ***********
0130     C.
```

```
*** OP Sequence for XRT ***

2023/04/06  10:50:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  10:50:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  10:50:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2023/04/06  10:51:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 02 03 74 01 db
2023/04/06  10:51:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2023/04/06  10:51:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2023/04/06  10:51:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2023/04/06  10:51:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/06  10:51:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/06  10:53:56.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2023/04/06  10:53:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2023/04/06  10:54:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/06  11:40:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  11:40:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  11:40:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/06  11:40:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/06  11:43:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/06  11:44:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/06  11:45:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/06  15:26:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  15:26:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  15:26:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/06  15:26:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/06  15:29:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/06  15:35:00.0  XRT_Custom_430_OG [0x1ae]
2023/04/06  15:36:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/06  17:00:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  17:00:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  17:00:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/06  17:00:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/06  17:03:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/06  17:23:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/06  17:24:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/06  17:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  17:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2023/04/06  18:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2023/04/06  18:00:16.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2023/04/06  18:00:18.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2023/04/06  18:00:20.0  XRT_ARS_DIS_428_OG [0x1ac]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/06  18:02:58.0  XRT_QT_PROG_SET_422_OG [0x1a6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2023/04/06  18:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/06  18:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  18:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  18:09:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2023/04/06  18:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 03 03 74 01 db
2023/04/06  18:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2023/04/06  18:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
```

```
2023/04/06  18:10:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2023/04/06  18:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/06  18:10:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/06  18:12:56.0  XRT_QT_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2023/04/06  18:12:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2023/04/06  18:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/06  18:36:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  18:36:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  18:36:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/06  18:36:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/06  18:39:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/06  19:00:01.0  XRT_Custom_430_OG [0x1ae]
2023/04/06  19:01:01.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/06  20:13:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  20:13:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  20:13:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/06  20:13:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/06  20:16:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/06  20:36:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/06  20:37:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/06  21:50:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  21:50:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  21:50:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/06  21:50:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/06  21:53:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/06  22:14:00.0  XRT_Custom_430_OG [0x1ae]
2023/04/06  22:15:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/06  23:28:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  23:28:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/06  23:28:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/06  23:28:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/06  23:31:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/06  23:49:00.5  XRT_Custom_430_OG [0x1ae]
2023/04/06  23:50:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  01:05:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  01:05:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  01:05:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  01:05:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/07  01:08:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/07  01:13:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/07  01:14:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  02:29:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  02:29:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  02:29:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  02:29:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/07  02:32:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/07  02:47:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/07  02:48:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  03:15:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  03:15:02.0  XRT_CTRL_MANU_402_OG [0x192]
```

```
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  03:15:04.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2023/04/07  03:15:24.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2023/04/07  03:15:26.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2023/04/07  03:15:28.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2023/04/07  03:15:30.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/07  03:15:32.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  03:18:02.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2023/04/07  03:18:04.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2023/04/07  03:18:06.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  06:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  06:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2023/04/07  07:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 00 03 74 01 db
2023/04/07  07:00:16.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2023/04/07  07:00:18.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2023/04/07  07:00:20.5  XRT_ARS_DIS_428_OG [0x1ac]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/07  07:02:58.5  XRT_QT_PROG_SET_422_OG [0x1a6]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2023/04/07  07:03:00.5  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  07:13:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  07:13:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  07:13:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  07:13:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/07  07:16:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/07  07:35:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  07:35:02.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2023/04/07  07:35:22.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2023/04/07  07:35:24.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2023/04/07  07:35:26.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2023/04/07  07:35:28.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/07  07:35:30.0  XRT_FLD_RESET_417_OG [0x1a1]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  07:38:02.0  XRT_QT_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2023/04/07  07:38:04.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2023/04/07  07:39:00.0  XRT_Custom_430_OG [0x1ae]
2023/04/07  07:40:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  08:53:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  08:53:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  08:53:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  08:53:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/07  08:56:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/07  08:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  08:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  08:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2023/04/07  09:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 02 03 74 01 db
2023/04/07  09:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2023/04/07  09:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2023/04/07  09:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2023/04/07  09:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/07  09:00:26.0  XRT_FLD_RESET_434_OG [0x1b2]
```

```
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  09:02:56.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2023/04/07  09:02:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2023/04/07  09:16:00.0  XRT_Custom_430_OG [0x1ae]
2023/04/07  09:17:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  10:33:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  10:33:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  10:33:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  10:33:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/07  10:36:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/07  10:50:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/07  10:51:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  15:59:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  15:59:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  15:59:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  15:59:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/07  16:02:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/07  16:11:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/07  16:12:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  17:34:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  17:34:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  17:34:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  17:34:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/07  17:37:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/07  17:58:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  17:58:26.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2023/04/07  17:58:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2023/04/07  17:58:46.0  XRT_FLD_DIS_409_OG [0x199]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2023/04/07  17:58:48.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2023/04/07  17:58:50.0  XRT_ARS_DIS_428_OG [0x1ac]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/07  18:01:28.0  XRT_QT_PROG_SET_422_OG [0x1a6]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2023/04/07  18:01:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  18:08:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  18:08:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  18:08:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2023/04/07  18:08:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 02 03 74 01 db
2023/04/07  18:08:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2023/04/07  18:08:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2023/04/07  18:08:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2023/04/07  18:08:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/07  18:08:56.0  XRT_FLD_RESET_434_OG [0x1b2]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  18:11:26.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2023/04/07  18:11:28.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2023/04/07  18:11:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  19:11:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  19:11:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  19:11:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  19:11:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/07  19:14:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
```

```
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/07  19:34:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/07  19:35:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  20:48:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  20:48:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  20:48:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  20:48:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/07  20:51:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/07  21:11:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/07  21:12:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  22:25:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  22:25:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  22:25:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/07  22:25:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/07  22:28:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/07  22:48:00.0  XRT_Custom_430_OG [0x1ae]
2023/04/07  22:49:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/07  23:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  23:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/07  23:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2023/04/08  00:00:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                        AOCU_NM              5 02-76 01 03 74 01 db
2023/04/08  00:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2023/04/08  00:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2023/04/08  00:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2023/04/08  00:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/08  00:00:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/08  00:02:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  00:02:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  00:02:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/08  00:02:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/08  00:02:56.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2023/04/08  00:02:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2023/04/08  00:05:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/08  00:21:01.0  XRT_Custom_430_OG [0x1ae]
2023/04/08  00:22:01.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/08  01:39:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  01:39:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  01:39:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/08  01:39:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/08  01:42:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/08  01:46:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/08  01:47:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/08  03:02:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  03:02:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  03:02:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/08  03:02:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/08  03:05:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/08  03:22:00.0  XRT_Custom_430_OG [0x1ae]
2023/04/08  03:23:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/08  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
```

```
2023/04/08  03:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  03:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2023/04/08  04:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2023/04/08  04:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2023/04/08  04:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2023/04/08  04:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2023/04/08  04:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/08  04:00:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/08  04:02:56.0  XRT_QT_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2023/04/08  04:02:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2023/04/08  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/08  04:33:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  04:33:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  04:33:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/08  04:33:36.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/08  04:36:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/08  04:59:00.0  XRT_Custom_430_OG [0x1ae]
2023/04/08  05:00:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/08  05:42:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  05:42:26.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2023/04/08  05:42:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2023/04/08  05:42:46.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2023/04/08  05:42:48.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2023/04/08  05:42:50.0  XRT_ARS_DIS_428_OG [0x1ac]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/08  05:45:28.0  XRT_QT_PROG_SET_422_OG [0x1a6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0e
2023/04/08  05:45:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/08  05:52:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  05:52:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  05:52:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2023/04/08  05:52:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 02 03 74 01 db
2023/04/08  05:52:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2023/04/08  05:52:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2023/04/08  05:52:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2023/04/08  05:52:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2023/04/08  05:52:56.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/08  05:55:26.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2023/04/08  05:55:28.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 04
2023/04/08  05:55:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/08  06:09:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  06:09:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  06:09:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/08  06:09:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/08  06:12:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/08  06:36:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/08  06:37:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/08  07:49:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  07:49:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  07:49:04.0  XRT_FLD_RESET_415_OG [0x19f]
```

```
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/08  07:49:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/08  07:52:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/08  08:13:30.0  XRT_Custom_430_OG [0x1ae]
2023/04/08  08:14:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/08  09:29:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  09:29:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  09:29:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2023/04/08  09:29:06.0  XRT_PREFLR_STRT_436_OG [0x1b4]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2023/04/08  09:32:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2023/04/08  09:50:00.5  XRT_Custom_430_OG [0x1ae]
2023/04/08  09:51:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2023/04/08  10:53:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2023/04/08  11:10:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
```