

XRT Timeline to be uploaded on 2023/05/13

Period: 2023/05/13 10:37:00 - 2023/05/18 11:19:00

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Normal mode

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XOB #1BA9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/13 10:55:30 - 05/13 18:24:54	Fixed (887.0, 202.0)	# OP start + 10min, Track AR13296
05/13 19:14:30 - 05/14 03:59:54	Fixed (887.0, 202.0)	Track AR13296
PROG= 15 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	
Subr= 2 5-time(s) 2.0sec		
Seqn= 47 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
Seqn= 96 8-time(s) 30.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1CEE: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(512/8192/32768), Med-Be(128/576)

Term	Pointing (x, y)	Comment
05/13 18:28:00 - 05/13 18:35:30	Fixed (0.0, 0.0)	Synoptic, shifted 25 min
05/14 05:24:36 - 05/14 05:47:30	Fixed (0.0, 0.0)	synoptic & HOP349 & 448
PROG= 03 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec	
Seqn= 26 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Al-mesh	Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Al-mesh	Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 15 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Al-poly/Open	Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Al-poly/Open	Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 83 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
thin-Be/Open	thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
thin-Be/Open	thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 23 1-time(s) 4.0sec		
Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Subr= 2 1-time(s) 2.0sec		
Seqn= 41 1-time(s) 2.0sec		
Open/thick-Be	Open/thick-Be close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Seqn= 17 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
med-Al/Open	med-Al/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
med-Al/Open	med-Al/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 33 1-time(s) 2.0sec		
med-Be/Open	Open/thick-Al close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
med-Be/Open	med-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
med-Be/Open	med-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 56 1-time(s) 2.0sec		
Al-poly/Ti-poly	Al-poly/thick-Al close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Al-poly/Ti-poly	Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1CF8: Stray light study 2023-01; Al-mesh and Al-poly, 2x2 full FOV(1min-cad) and 1x1 384 on AR(10sec-cad)

Term	Pointing (x, y)	Comment
05/13 18:38:00 - 05/13 18:47:54	Fixed (-500.0, 0.0)	Straylight measurements
PROG= 01 1-time(s)		

Subr= 2 16-time(s) 2.0sec													
Seqn= 43 1-time(s) 10.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.83s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 8-time(s) 2.0sec													
Seqn= 64 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 43 3-time(s) 10.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.83s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CCF: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 72min cad) + CME wa

Term	Pointing (x, y)	Comment
05/14 04:03:00 - 05/14 05:21:30	Fixed (0.0, 0.0)	synoptic & HOP349 & 448

PROG= 17 Inf.-time(s)													
Subr= 1 1-time(s) 300.0sec													
Seqn= 55 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 15-time(s) 360.0sec													
Seqn= 8 1-time(s) 2.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74 1-time(s) 2.0sec													
	med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
05/13 10:55:30 - 05/13 18:24:54	Fixed (887.0, 202.0)	# OP start + 10min, Track AR13296
05/13 19:14:30 - 05/14 03:59:54	Fixed (887.0, 202.0)	Track AR13296
05/14 04:03:00 - 05/14 05:21:30	Fixed (0.0, 0.0)	synoptic & HOP349 & 448

PROG= 04 30-time(s)													
Subr= 1 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73 1-time(s) 10.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

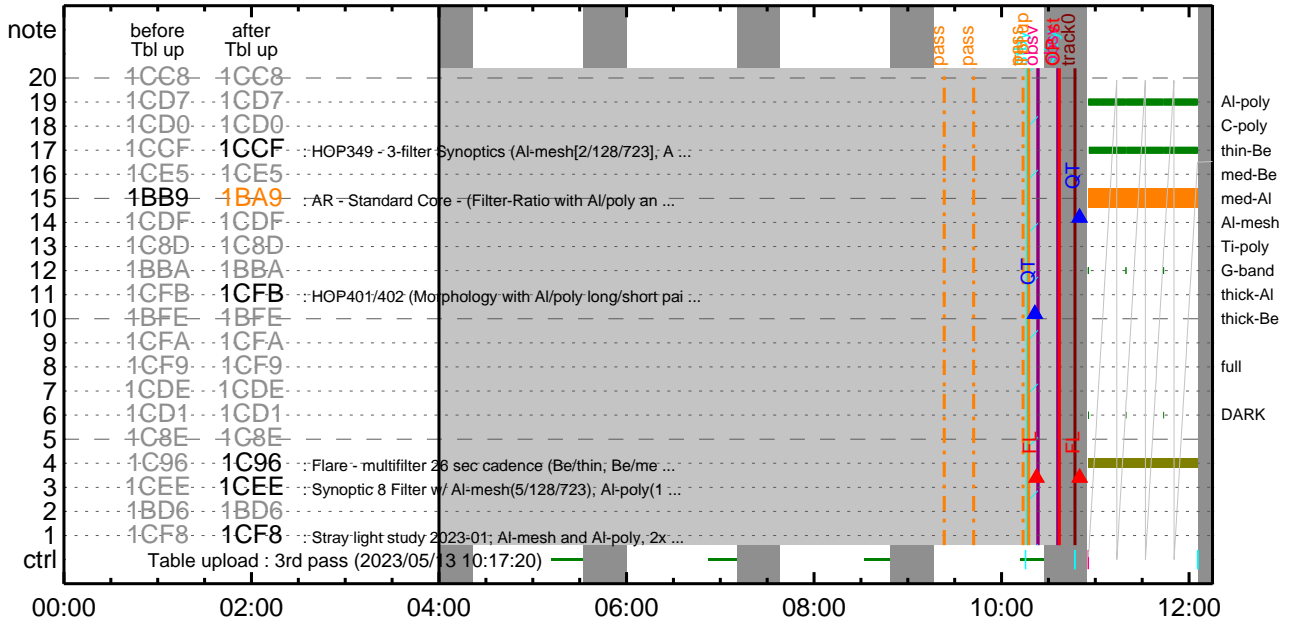
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Flare Detection

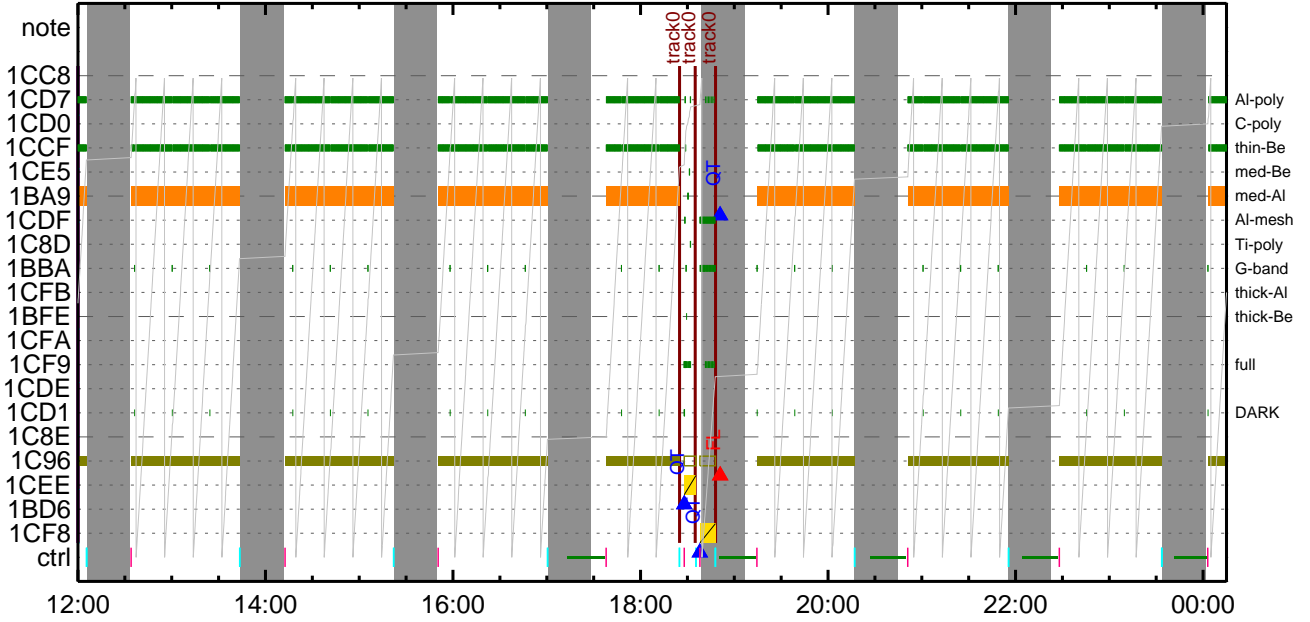
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FLD Patrol											
Term		Pointing (x, y)						Comment			
05/13 10:18:20 - 05/13 18:25:18		cannot be identified									
05/13 18:48:18 - 05/14 05:21:54		Fixed (887.0, 202.0) Track AR13296									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

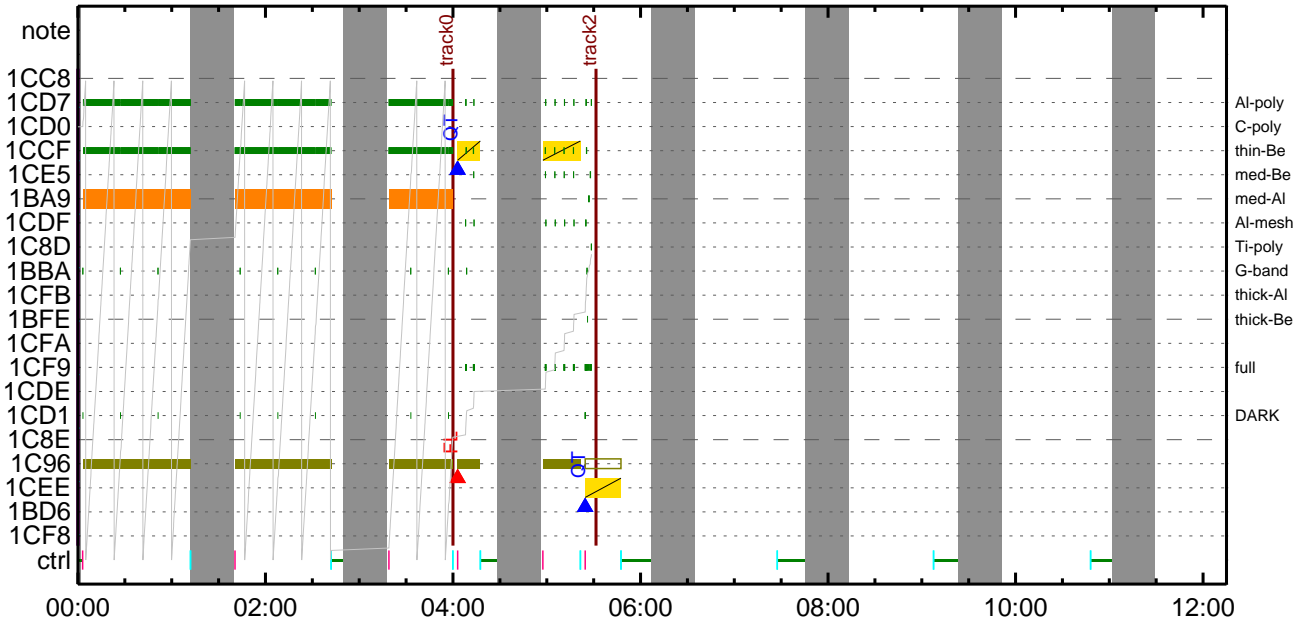
CMDI #0072 2023/05/13



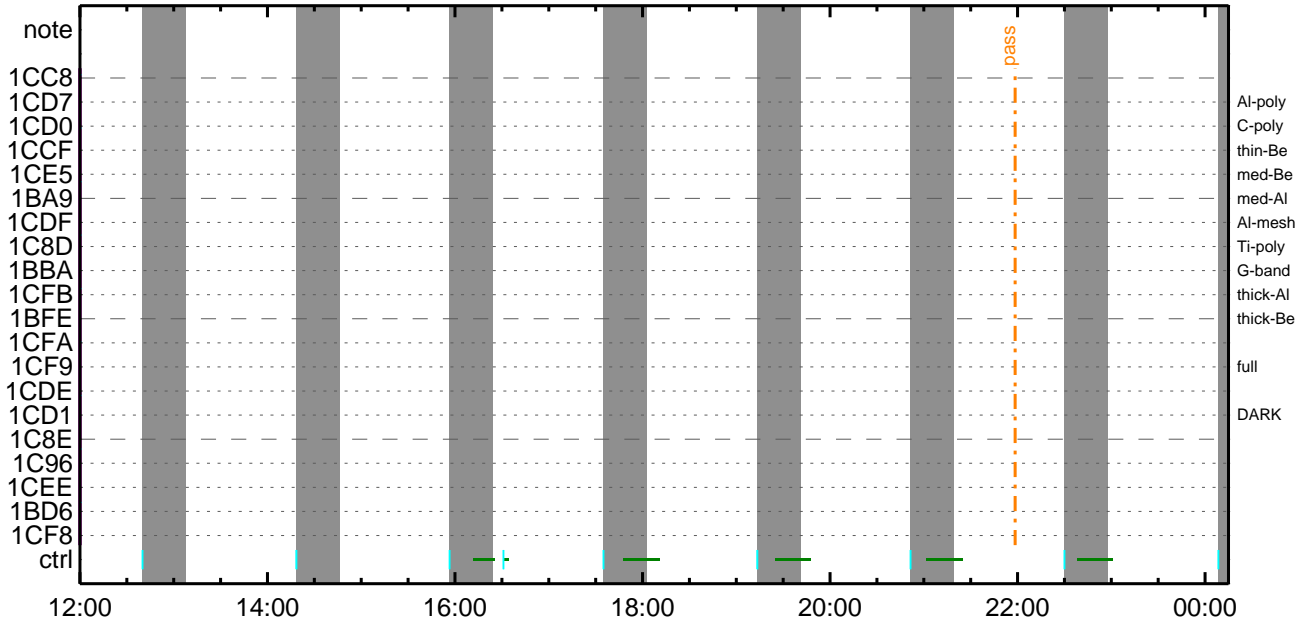
CMDI #0072 2023/05/13



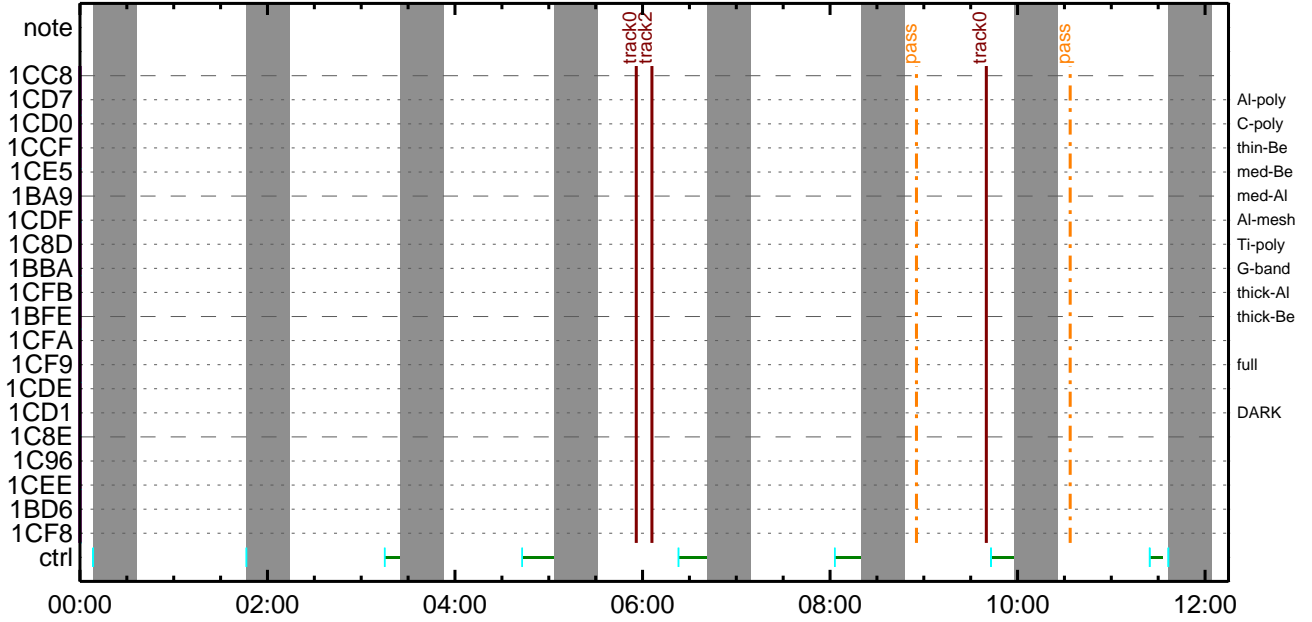
CMDI #0072 2023/05/14



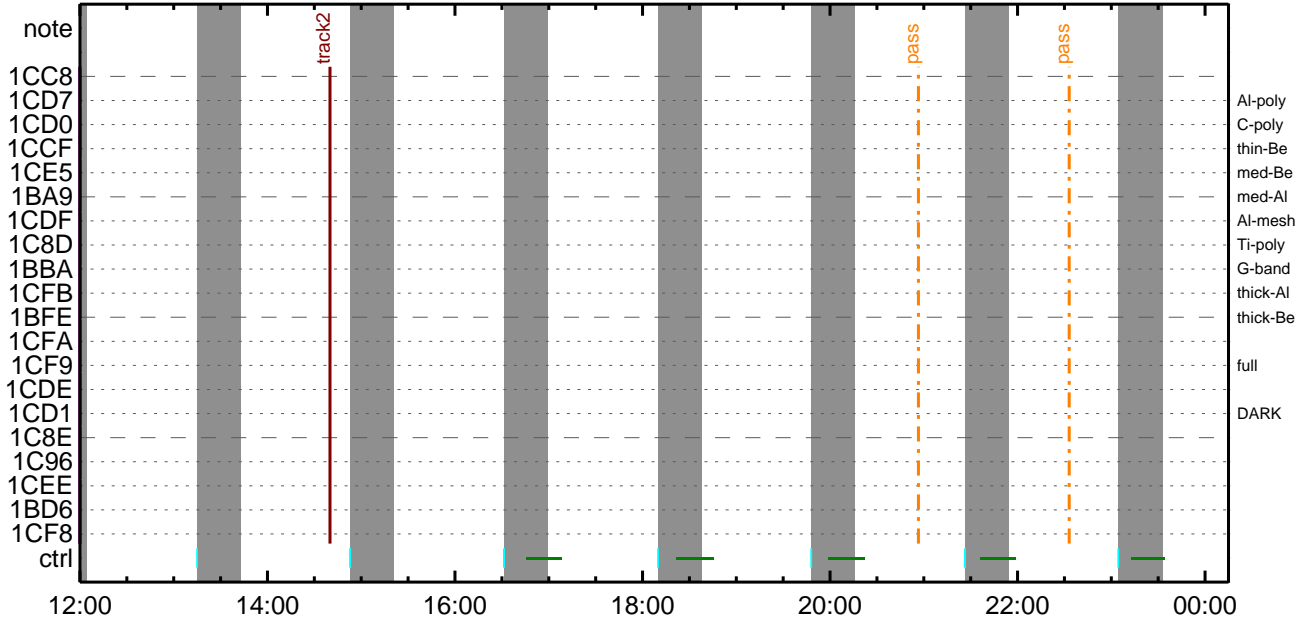
CMDI #0072 2023/05/14



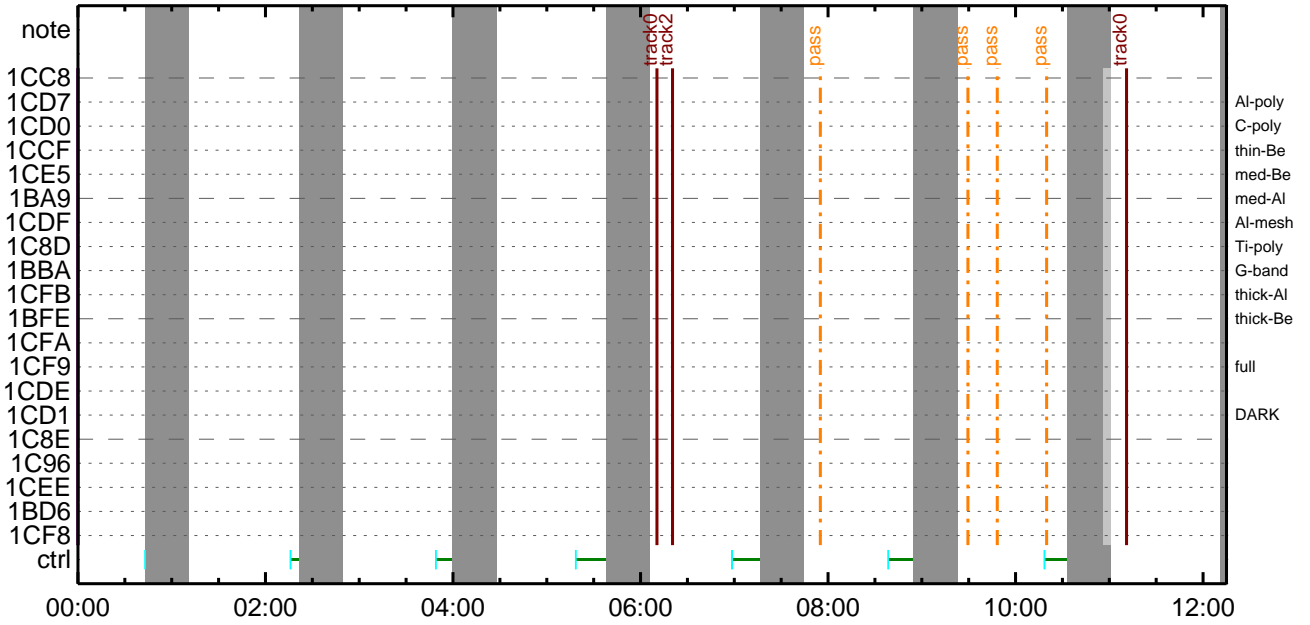
CMDI #0072 2023/05/15



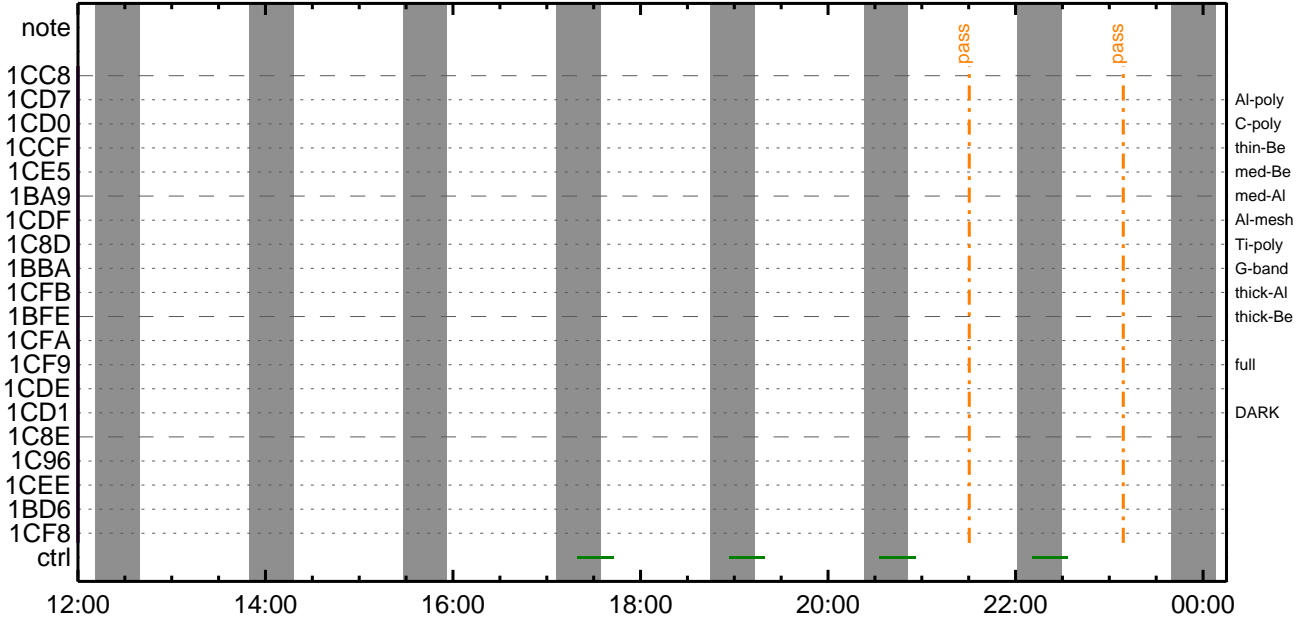
CMDI #0072 2023/05/15



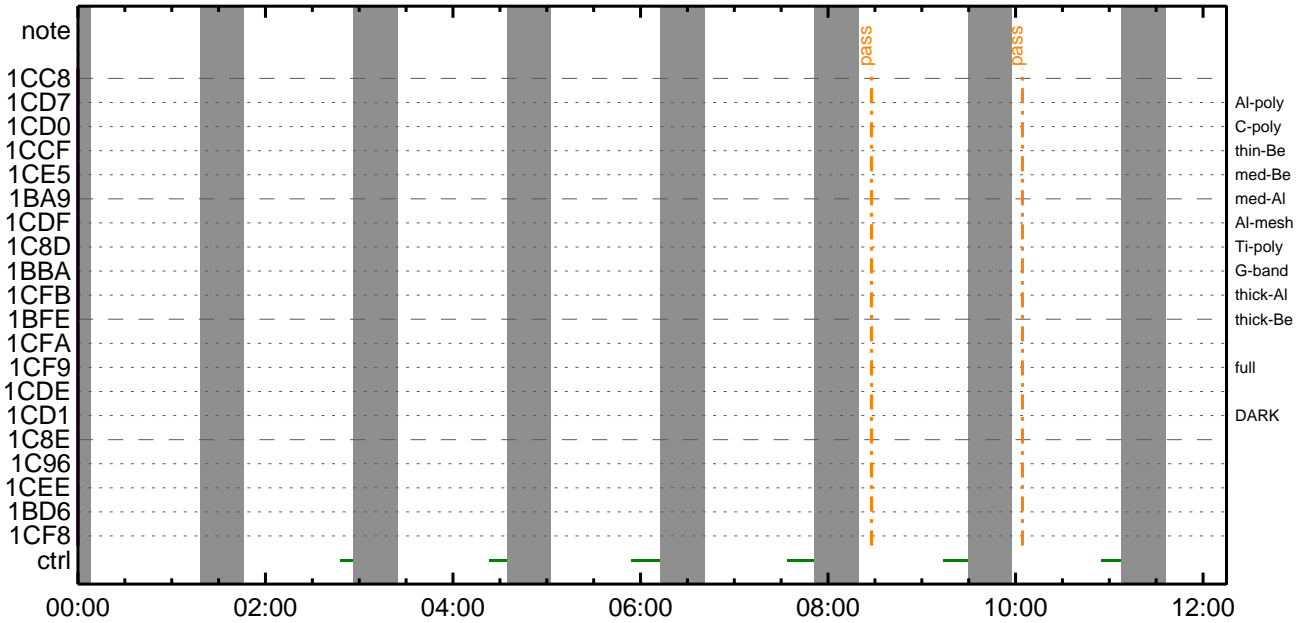
CMDI #0072 2023/05/16



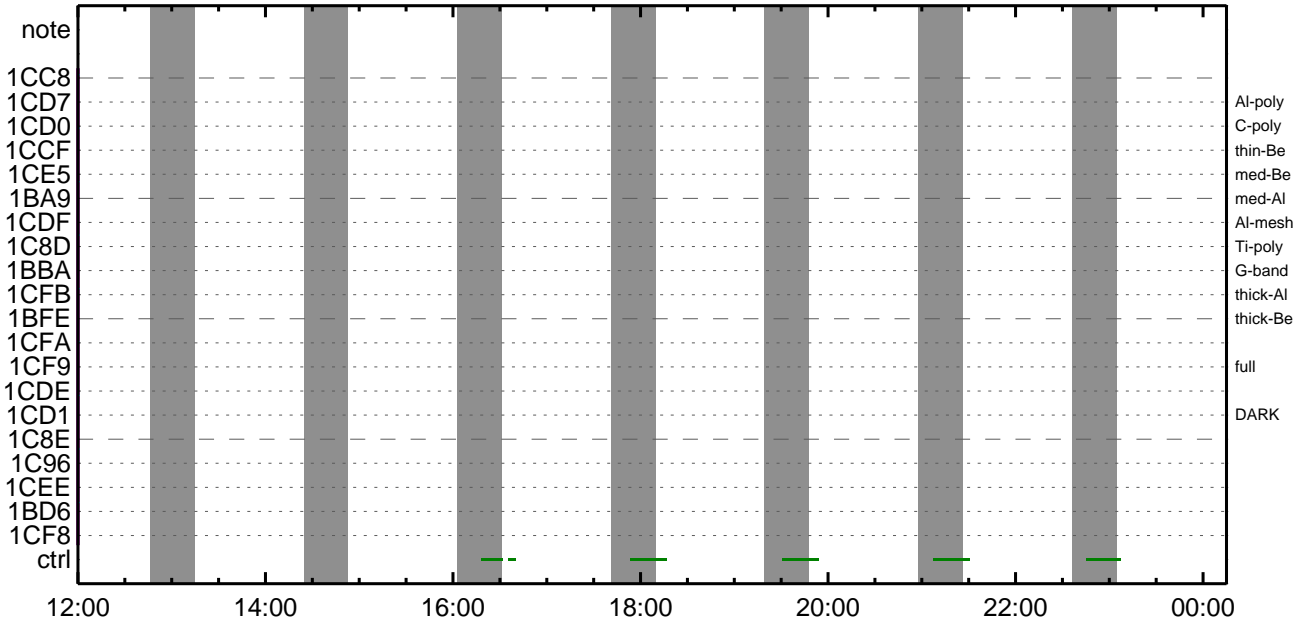
CMDI #0072 2023/05/16



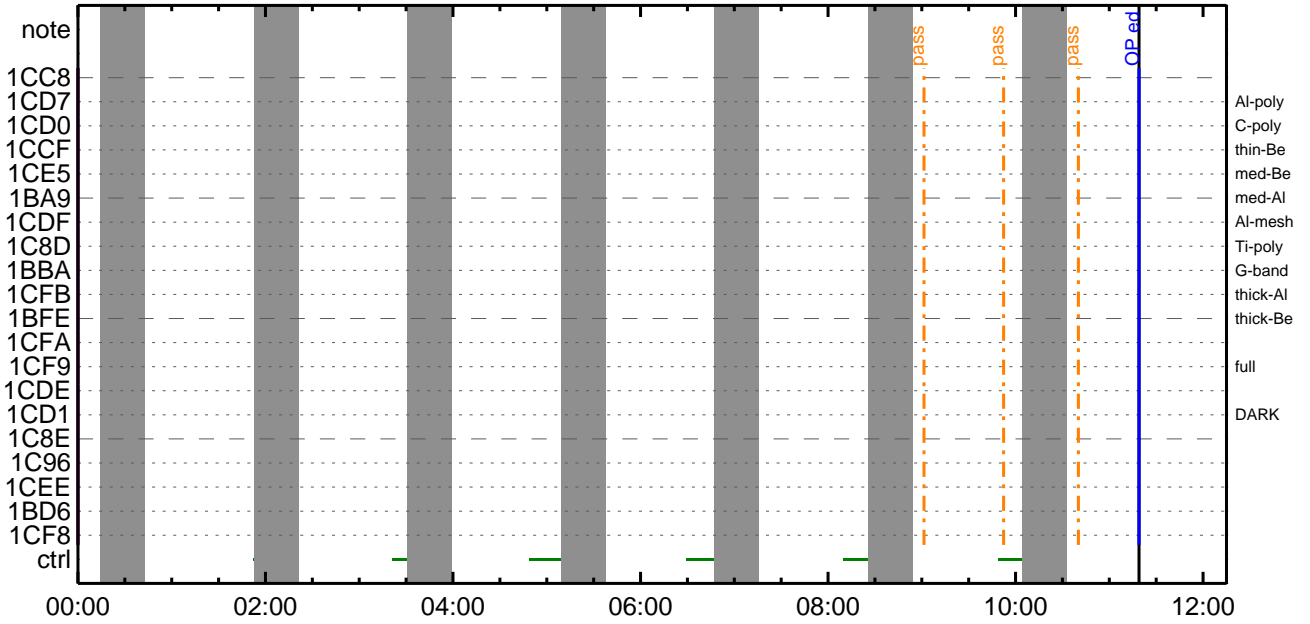
CMDI #0072 2023/05/17



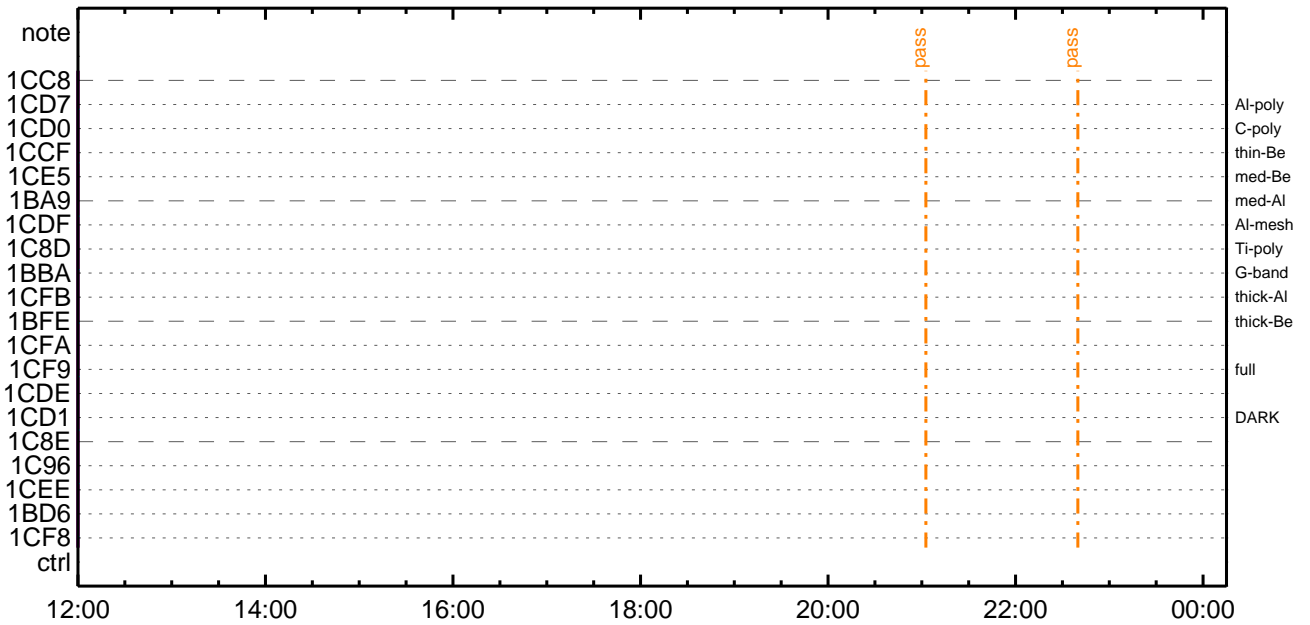
CMDI #0072 2023/05/17



CMDI #0072 2023/05/18



CMDI #0072 2023/05/18




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;Y6;Y6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;Y6;ã
0103 S. OP op-850:OP
0104 ( )
0105 S. OG og-850:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPf°eY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. Y6Yx½^aî»ò³îçš
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGqî%è¹ç.ë²îOKò³îçš
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. Y6Yx½^aî»ò³îçš
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGqî%è¹ç.ë²îOKò³îçš
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. Y6Yx½^aî»ò³îçš
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPqî%è¹ç.ë²îOKò³îçš
0165 C.
0166 C. ***** òê²¼òî%Ã`¶Á°òèè-ò°Á÷¿@ (%ãµ-Y6Yx½^e½çòðÁÔæòç½^a°òè%î¹çòçòâ) *****
0167 C. DHUYâ;Y6;êY½.Yî;Y6;èòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |s OPOG UPLOADò-Á÷¿@NGuî%î¹ç;ç°ê²¼òîTI-CMDÁ÷¿@î%Á¹Òò.òèòòò³òè;f
0180 C. òèò¿;çSETòèDUMPòîÆ±òîYÑY¹òç¹Òò|ò³òè;f
0181 C.
0182 C. TIY³Y6Y6Y6èòðÁî¿(UT)
0183 +. TI 2023-05-13 10:32:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2023-05-13 10:32:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2023-05-13 10:32:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

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0194 C.
0195 +. TI 2023-05-13 10:36:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °È²¼∞îÄë%îíñ∞îŷÄŷ$ŷÄŷ-¹àîŮ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»∞ð³îÇ$
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL∞îŷÈ¹ç•è²îOK∞ð³îÇ$
0229 C.
0230 C. DHUŷâ;¼ŷÈ;Èŷ¼ŷ.ŷî;¼ŷÈ;È∞ðîâ∞¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2023-05-13 10:36:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2023-05-13 10:36:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]       EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2023-05-13 10:36:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]       EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄî∞î»ò¼ŷ∞ÈÄ∞¹∞èDCBC•×²è *****
0265 C. (¼ª∞îŷÖŷÄŷÈŷŷŷÈŷâŷçŷè∞È¼∞∞¼Ä»Ů∞¹∞è)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îñ∞È¹∞èDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;ãLOSŷÄŷ$ŷÄŷ-¼Ä»Ů;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-852 2023-05-13 11:13:04 102 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÀø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈøÈ¼°ÇÇø•ø¿¼í¹çøÍ;çÁ®, ùø¹øèøÈøÇÁ+¿®ø•øÈøøøøÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 08 08)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 + DC 07-F0 MDP_XRT_FLD_ENA
0061 BC (d8)
0062 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063 BC (c8)
0064 + DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 + DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 + DC 07-F0 MDP_XRT_FLD_RESET
0069 BC (da)
0070 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0071 BC (c4 0b)
0072 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0073 BC (c5 04)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C.
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078 C.
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV
0080 BC (c2)
0081 +. TI 2023-05-13 10:36:02.0
0082 DC 07-F0 MDP_XRT_MODE_OBSV
0083 BC (c2)
0084 . C. ----- Success Verify ? OK / NG ____
0085 C.
0086 C. ***** XRT END *****
0087 C.
0088 . C. ***** MDP 'úÁíøí»ø¼YøÈÁøø¹øÈDCBC•x²è *****
0089 C. (%á°íYÖYÁYÈYB¥YÉYçYèøÈ¼øø¼Á»Ûø¹øè)
0090 . S. DC-BC dcbc-402:DCBC
0091 (MDP_known_event)
0092 C.
0093 C.
0094 . C. ***** YD¥¹.í Daily+¿íÑøÈ´Øø¹øÈDCBC•x²è *****
0095 . S. DC-BC dcbc-153:DCBC
```

```
0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. ;äLOSŸÁŸSŸÄŸ-¼Ä»Û;ä
0100 C.
0101 . C. ***** LOS *****
0102 C.
```

*** OP Sequence for XRT ***

2023/05/13	10:46:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	10:46:56.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	10:46:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2023/05/13	10:47:00.0	AOCS_Ore-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	00 ee 0d b1 27	
2023/05/13	10:47:18.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2023/05/13	10:47:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2023/05/13	10:47:22.0	XRT_AEC_RESET_448_OG [0x1c0]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2023/05/13	10:47:24.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2023/05/13	10:47:26.0	XRT_FLD_RESET_434_OG [0x1b2]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2023/05/13	10:49:56.0	XRT_QT_PROG_SET_446_OG [0x1be]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2023/05/13	10:49:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2023/05/13	10:54:30.0	XRT_Custom_430_OG [0x1ae]				
2023/05/13	10:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/05/13	12:05:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	12:05:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	12:05:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2023/05/13	12:05:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/05/13	12:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/05/13	12:33:00.0	XRT_Custom_430_OG [0x1ae]				
2023/05/13	12:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/05/13	13:43:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	13:43:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	13:43:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2023/05/13	13:43:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/05/13	13:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/05/13	14:11:30.0	XRT_Custom_430_OG [0x1ae]				
2023/05/13	14:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/05/13	15:22:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	15:22:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	15:22:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2023/05/13	15:22:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/05/13	15:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/05/13	15:49:30.0	XRT_Custom_430_OG [0x1ae]				
2023/05/13	15:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/05/13	17:00:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	17:00:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	17:00:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2023/05/13	17:00:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/05/13	17:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/05/13	17:37:00.0	XRT_Custom_430_OG [0x1ae]				
2023/05/13	17:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/05/13	18:24:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	18:24:56.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/05/13	18:24:58.0	XRT_FOCUS_POSITION_406_OG [0x196]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2023/05/13	18:25:00.0	AOCS_Ore-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2023/05/13	18:25:18.0	XRT_FLD_DIS_409_OG [0x199]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2023/05/13	18:25:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2023/05/13	18:25:22.0	XRT_ARS_DIS_401_OG [0x191]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	

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XRT_OGLIST_0072.chk

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2023/05/13	18:27:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2023/05/13	18:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/13	18:35:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00	2c 72	
2023/05/13	18:35:30.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/13	18:35:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/13	18:35:34.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00	
2023/05/13	18:35:54.0	XRT_FLD_DIS_435_OG [0x1b3]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2023/05/13	18:37:54.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2023/05/13	18:37:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2023/05/13	18:37:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01	
2023/05/13	18:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/13	18:47:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/13	18:47:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/13	18:47:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00	
2023/05/13	18:48:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 ee 0d b1	27	
2023/05/13	18:48:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2023/05/13	18:48:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2023/05/13	18:48:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2023/05/13	18:48:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2023/05/13	18:48:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2023/05/13	18:50:56.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f	
2023/05/13	18:50:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04	
2023/05/13	19:13:30.0	XRT_Custom_430_OG [0x1ae]					
2023/05/13	19:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/13	20:17:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/13	20:17:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/13	20:17:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2023/05/13	20:17:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2023/05/13	20:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/05/13	20:50:00.0	XRT_Custom_430_OG [0x1ae]					
2023/05/13	20:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/13	21:55:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/13	21:55:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/13	21:55:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2023/05/13	21:55:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2023/05/13	21:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/05/13	22:27:00.0	XRT_Custom_430_OG [0x1ae]					
2023/05/13	22:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/13	23:33:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/13	23:33:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/13	23:33:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2023/05/13	23:33:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2023/05/13	23:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/05/14	00:02:00.0	XRT_Custom_430_OG [0x1ae]					
2023/05/14	00:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/14	01:12:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	01:12:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	01:12:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2023/05/14	01:12:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]					

2023/05/14	01:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/05/14	01:39:30.0	XRT_Custom_430_OG [0x1ae]						
2023/05/14	01:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/14	02:42:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	02:42:02.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	02:42:04.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2023/05/14	02:42:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2023/05/14	02:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/05/14	03:18:00.0	XRT_Custom_430_OG [0x1ae]						
2023/05/14	03:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/14	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2023/05/14	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2023/05/14	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2023/05/14	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2023/05/14	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2023/05/14	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2023/05/14	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2023/05/14	04:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2023/05/14	04:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2023/05/14	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/14	04:17:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	04:17:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	04:17:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2023/05/14	04:17:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2023/05/14	04:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/05/14	04:56:30.0	XRT_Custom_430_OG [0x1ae]						
2023/05/14	04:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/14	05:21:30.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	05:21:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	05:21:34.0	XRT_FOCUS_POSITION_406_OG [0x196]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2023/05/14	05:21:54.0	XRT_FLD_DIS_409_OG [0x199]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2023/05/14	05:21:56.0	XRT_FLRCTRL_DIS_413_OG [0x19d]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2023/05/14	05:21:58.0	XRT_ARS_DIS_401_OG [0x191]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2023/05/14	05:24:34.0	XRT_QT_PROG_SET_444_OG [0x1bc]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2023/05/14	05:24:36.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/05/14	05:30:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_440_OG [0x1b8]						
			TCIB_XRT_S_HTR_A_ENA	0	04-BC			
2023/05/14	05:31:30.0	AOCS_ORe-point_Start_4_OG [0x09a]						
			AOCU_NM	5	02-76	02 05 0e 01 99		
2023/05/14	05:47:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	05:47:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	05:47:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2023/05/14	05:47:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2023/05/14	05:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/05/14	07:27:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	07:27:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/05/14	07:27:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2023/05/14	07:27:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]						

2023/05/14	07:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/05/14	09:07:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	09:07:33.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	09:07:35.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/05/14	09:07:37.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2023/05/14	09:10:45.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/05/14	10:48:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	10:48:03.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	10:48:05.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/05/14	10:48:07.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2023/05/14	10:51:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/05/14	12:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	12:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	12:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/05/14	12:40:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2023/05/14	12:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/05/14	14:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	14:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	14:18:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/05/14	14:18:36.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2023/05/14	14:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/05/14	15:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	15:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	15:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/05/14	15:56:36.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2023/05/14	15:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/05/14	16:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	16:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	16:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/05/14	16:31:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2023/05/14	16:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/05/14	17:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	17:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	17:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/05/14	17:35:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2023/05/14	17:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/05/14	19:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	19:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	19:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/05/14	19:13:36.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2023/05/14	19:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/05/14	20:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	20:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/05/14	20:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/05/14	20:51:36.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
2023/05/14	20:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9

2023/05/14	22:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/14	22:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/14	22:30:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/14	22:30:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/14	22:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	00:08:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	00:08:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	00:08:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	00:08:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/15	00:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	01:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	01:46:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	01:46:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	01:46:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/15	01:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	03:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	03:15:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	03:15:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	03:15:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/15	03:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	04:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	04:43:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	04:43:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	04:43:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/15	04:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	05:56:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2023/05/15	06:06:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02 05 0e 01 99				
2023/05/15	06:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	06:23:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	06:23:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	06:23:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/15	06:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	08:03:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	08:03:03.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	08:03:05.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	08:03:07.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/15	08:06:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	09:40:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 56 d9 01 99				
2023/05/15	09:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	09:43:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	09:43:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	09:43:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/15	09:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	11:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	11:24:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	11:24:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	11:24:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							

2023/05/15	11:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2023/05/15	11:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	11:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	11:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	11:36:36.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	11:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2023/05/15	13:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	13:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	13:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	13:15:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	13:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	da				
2023/05/15	14:40:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2023/05/15	14:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	02 05 0e 01 99				
2023/05/15	14:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	14:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	14:53:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	14:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	da				
2023/05/15	16:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2023/05/15	16:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	16:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	16:31:36.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	16:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	da				
2023/05/15	18:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2023/05/15	18:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	18:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	18:10:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	18:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	da				
2023/05/15	19:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2023/05/15	19:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	19:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	19:48:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	19:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	da				
2023/05/15	21:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2023/05/15	21:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	21:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	21:26:36.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	21:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	da				
2023/05/15	23:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2023/05/15	23:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/15	23:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/15	23:04:36.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/15	23:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	da				
2023/05/16	00:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2023/05/16	00:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/16	00:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	00:43:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/16			MDP_XRT_PREFLR_STOP	1	07-F0	e8				

2023/05/16	00:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/16	02:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	02:16:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	02:16:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/16	02:16:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/16	02:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/16	03:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	03:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	03:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/16	03:49:06.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/16	03:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/16	05:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	05:18:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	05:18:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/16	05:18:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/16	05:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/16	06:10:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2023/05/16	06:20:30.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02 05 0e 01 99				
2023/05/16	06:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	06:58:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	06:58:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/16	06:58:36.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/16	07:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/16	08:38:30.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	08:38:33.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	08:38:35.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/16	08:38:37.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/16	08:41:45.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/16	10:18:30.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	10:18:33.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/05/16	10:18:35.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/05/16	10:18:37.0	XRT_PREFLR_STRT_436_OG [0x1b4]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/05/16	10:21:45.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/05/16	11:11:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				