

# XRT Timeline to be uploaded on 2023/10/05

Period: 2023/10/05 10:25:00 - 2023/10/10 10:02:00

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## Normal mode

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XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms												
Term	Pointing (x, y)						Comment					
10/05 10:51:30 - 10/05 17:33:30	Fixed ( -22.0, -963.0)						# OP start + 10min +HOP81(S-pole)					
<b>PROG= 12 Inf.-time(s)</b>												
├─ Subr= 1 1-time(s) 2.0sec												
├─ Seqn= 16 1-time(s) 2.0sec												
├─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
├─ Subr= 2 1-time(s) 2.0sec												
├─ Seqn= 90 1-time(s) 30.0sec												
├─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
├─ Subr= 3 60-time(s) 60.0sec												
├─ Seqn= 24 1-time(s) 30.0sec												
├─ Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> <span>Default Filter</span> <span>Thicker Filter</span> <span>VLS</span> <span>mode</span> <span>image</span> <span>Exp.</span> <span>CCD</span> <span>Bin</span> <span>ROI: size (center)</span> <span>Comp.</span> <span>AEC Buffer</span> <span>Interval</span> </div>												

XOB #1CEE: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(512/8192/32768), Med-Be(128/512/4096)												
Term	Pointing (x, y)						Comment					
10/05 18:01:00 - 10/05 18:07:54	Fixed ( 0.0, 0.0)						synoptic, shifted -2.0 min					
10/06 05:45:00 - 10/06 05:51:54	Fixed ( 0.0, 0.0)						HOP349 + synoptic, shifted -18.0 min					
10/06 17:46:30 - 10/06 17:53:24	Fixed ( 0.0, 0.0)						synoptic, shifted -16.5 min					
10/07 06:03:00 - 10/07 06:09:54	Fixed ( 0.0, 0.0)						HOP349 + synoptic					
<b>PROG= 17 1-time(s)</b>												
├─ Subr= 1 1-time(s) 2.0sec												
├─ Seqn= 5 1-time(s) 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
├─ Seqn= 26 1-time(s) 2.0sec												
├─ Open/Al-mesh Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Seqn= 15 1-time(s) 2.0sec												
├─ Al-poly/Open Al-poly/Open close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Seqn= 83 1-time(s) 2.0sec												
├─ thin-Be/Open thin-Be/Open close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Seqn= 23 1-time(s) 4.0sec												
├─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
├─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Subr= 2 1-time(s) 2.0sec												
├─ Seqn= 41 1-time(s) 2.0sec												
├─ Open/thick-Be Open/thick-Be close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Seqn= 17 1-time(s) 2.0sec												
├─ med-Al/Open med-Al/thick-Al close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ med-Al/Open med-Al/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ med-Al/Open med-Al/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Seqn= 33 1-time(s) 2.0sec												
├─ med-Be/Open Open/thick-Al close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ med-Be/Open med-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ med-Be/Open med-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
├─ Seqn= 56 1-time(s) 2.0sec												
├─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
├─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> <span>Default Filter</span> <span>Thicker Filter</span> <span>VLS</span> <span>mode</span> <span>image</span> <span>Exp.</span> <span>CCD</span> <span>Bin</span> <span>ROI: size (center)</span> <span>Comp.</span> <span>AEC Buffer</span> <span>Interval</span> </div>												

XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with PFB												
Term	Pointing (x, y)						Comment					
10/05 18:11:00 - 10/06 03:59:54	Track ( 460.5, -435.8) @ 10/05 18:08:00						AR13450					
<b>PROG= 10 Inf.-time(s)</b>												
├─ Subr= 1 1-time(s) 2.0sec												
├─ Seqn= 92 1-time(s) 2.0sec												
├─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
├─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
├─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
├─ Subr= 2 5-time(s) 2.0sec												
├─ Seqn= 47 1-time(s) 2.0sec												
├─ Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec												

	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	<b>Seqn= 96 4-time(s) 120.0sec</b>												
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1CD0: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 300min cad) + CME w</b>													
Term	Pointing (x, y)							Comment					
10/06 04:03:00 - 10/06 05:41:54	Fixed ( 0.0, 0.0)							HOP349 + synoptic, shifted -18.0 min					
10/07 04:03:00 - 10/07 05:59:54	Fixed ( 0.0, 0.0)							HOP349 + synoptic					

<b>PROG= 05 Inf.-time(s)</b>													
	<b>Subr= 1 1-time(s) 300.0sec</b>												
	<b>Seqn= 55 1-time(s) 2.0sec</b>												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 15 1-time(s) 2.0sec</b>												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 79 1-time(s) 2.0sec</b>												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 30 1-time(s) 2.0sec</b>												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Subr= 2 20-time(s) 900.0sec</b>												
	<b>Seqn= 8 1-time(s) 2.0sec</b>												
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
	<b>Seqn= 74 1-time(s) 2.0sec</b>												
	med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	<b>Seqn= 6 1-time(s) 2.0sec</b>												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
	<b>Seqn= 29 1-time(s) 2.0sec</b>												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with</b>													
Term	Pointing (x, y)							Comment					
10/06 05:55:00 - 10/06 09:59:54	Track ( 543.8, -428.8) @ 10/06 05:52:00							AR13450					
10/06 17:56:30 - 10/06 23:59:54	Track ( 621.8, -420.6) @ 10/06 17:53:30							AR13450					
10/07 06:13:00 - 10/07 09:49:00	Track ( 377.6, 25.0) @ 10/07 06:10:00							AR13452					

<b>PROG= 06 Inf.-time(s)</b>													
	<b>Subr= 1 1-time(s) 2.0sec</b>												
	<b>Seqn= 92 1-time(s) 2.0sec</b>												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	<b>Subr= 2 5-time(s) 2.0sec</b>												
	<b>Seqn= 47 1-time(s) 2.0sec</b>												
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	<b>Seqn= 96 4-time(s) 180.0sec</b>												
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1BD7: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 900s cad (G-band/Leak last)</b>													
Term	Pointing (x, y)							Comment					
10/06 10:03:00 - 10/06 17:43:24	Track ( -331.6, -453.2) @ 10/06 10:00:00							ECP1(QS and CH)					

**PROG= 16 Inf.-time(s)**

<b>Subr= 1 8-time(s) 900.0sec</b>													
<b>Seqn= 8 1-time(s) 2.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>													
<b>Seqn= 30 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1CDE: HOP393/336 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 7**

Term	Pointing (x, y)	Comment
10/07 00:03:00 - 10/07 03:59:54	Track ( -40.7, -151.0) @ 10/07 00:00:00	HOP393

**PROG= 08 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>													
<b>Seqn= 30 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 52 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Subr= 2 30-time(s) 720.0sec</b>													
<b>Seqn= 97 2-time(s) 2.0sec</b>													
	Al-poly/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \* **Flare mode** \* \* \* \* \*

**XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GB**

Term	Pointing (x, y)	Comment
10/05 10:51:30 - 10/05 17:33:30	Fixed ( -22.0, -963.0)	# OP start + 10min +HOP81(S-pole)
10/05 18:11:00 - 10/06 03:59:54	Track ( 460.5, -435.8) @ 10/05 18:08:00	AR13450
10/06 04:03:00 - 10/06 05:41:54	Fixed ( 0.0, 0.0)	HOP349 + synoptic, shifted -18.0 min
10/06 05:55:00 - 10/06 09:59:54	Track ( 543.8, -428.8) @ 10/06 05:52:00	AR13450
10/06 10:03:00 - 10/06 17:43:24	Track ( -331.6, -453.2) @ 10/06 10:00:00	ECP1(QS and CH)
10/06 17:56:30 - 10/06 23:59:54	Track ( 621.8, -420.6) @ 10/06 17:53:30	AR13450
10/07 00:03:00 - 10/07 03:59:54	Track ( -40.7, -151.0) @ 10/07 00:00:00	HOP393
10/07 04:03:00 - 10/07 05:59:54	Fixed ( 0.0, 0.0)	HOP349 + synoptic
10/07 06:13:00 - 10/07 09:49:00	Track ( 377.6, 25.0) @ 10/07 06:10:00	AR13452

**PROG= 04 30-time(s)**

<b>Subr= 1 20-time(s) 2.0sec</b>													
<b>Seqn= 11 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 73 1-time(s) 10.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>													
<b>Seqn= 10 1-time(s) 2.0sec</b>													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 87 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \* **Active Region Search** \* \* \* \* \*

NOT USED

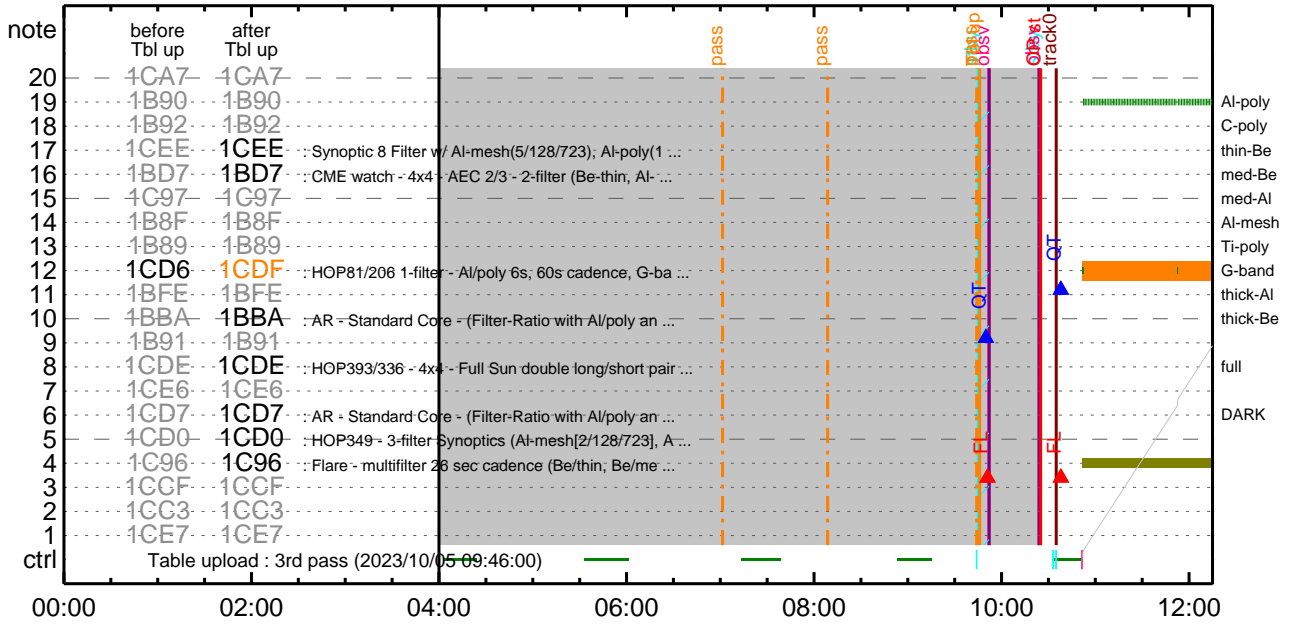
\* \* \* \* \* **Flare Detection** \* \* \* \* \*

**FLD Patrol**

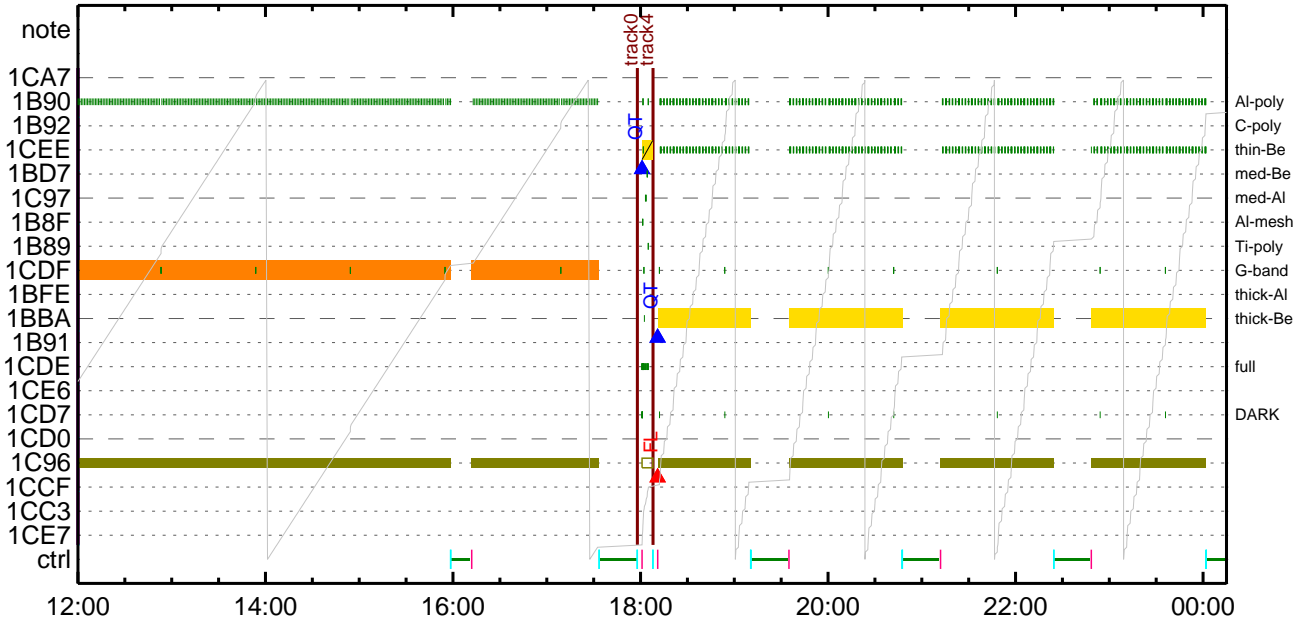
Term	Pointing (x, y)	Comment
10/05 09:47:00 - 10/05 17:58:18	cannot be identified	
10/05 18:08:18 - 10/06 05:42:18	Track ( 460.5, -435.8) @ 10/05 18:08:00	AR13450
10/06 05:52:18 - 10/06 17:43:48	Track ( 543.8, -428.8) @ 10/06 05:52:00	AR13450
10/06 17:53:48 - 10/07 06:00:18	Track ( 621.8, -420.6) @ 10/06 17:53:30	AR13450
10/07 06:10:18 - 10/10 10:02:00	Track ( 377.6, 25.0) @ 10/07 06:10:00	AR13452

Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

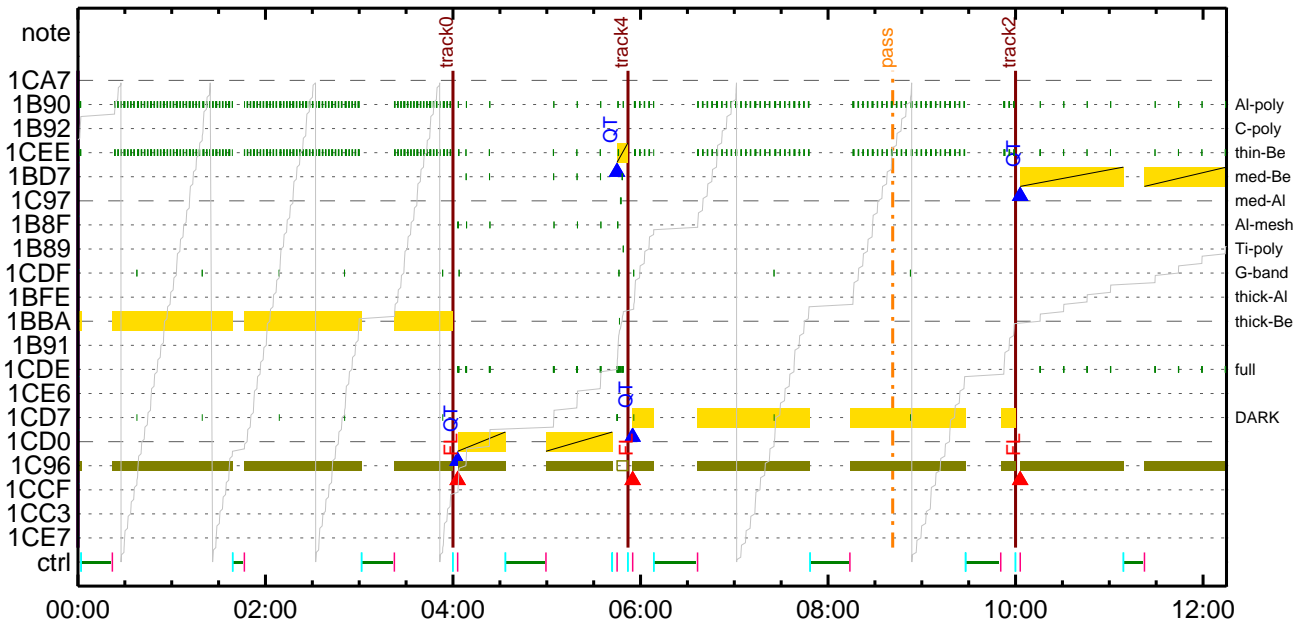
CMDI #0366 2023/10/05



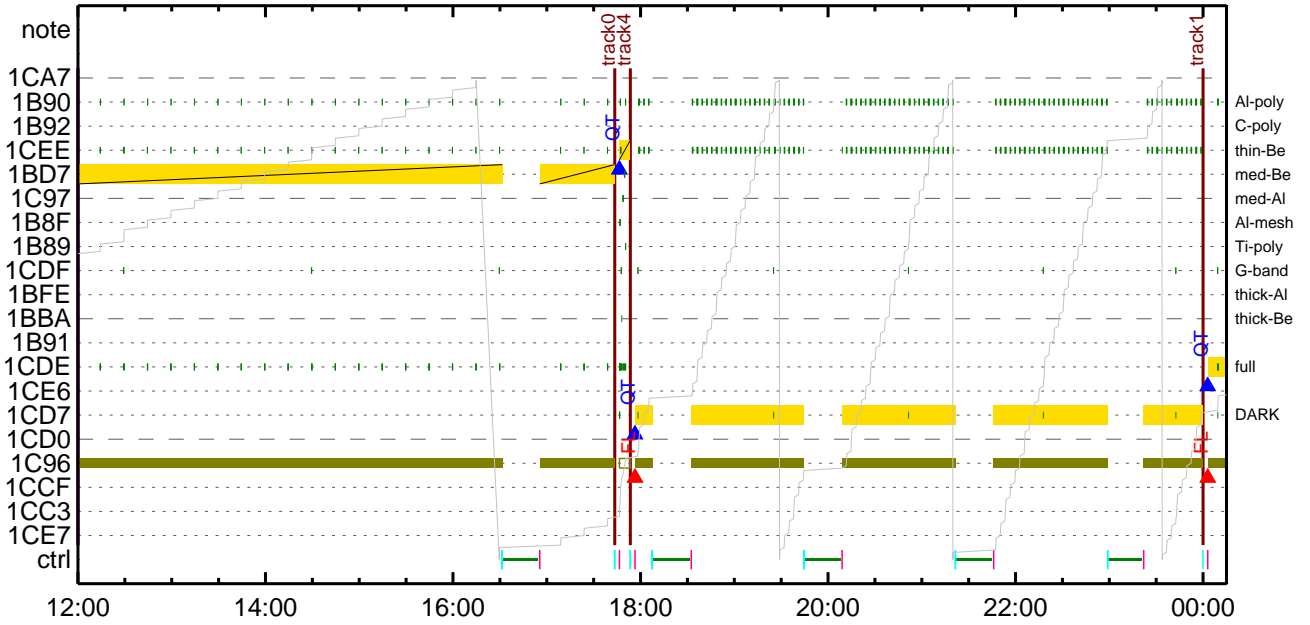
CMDI #0366 2023/10/05



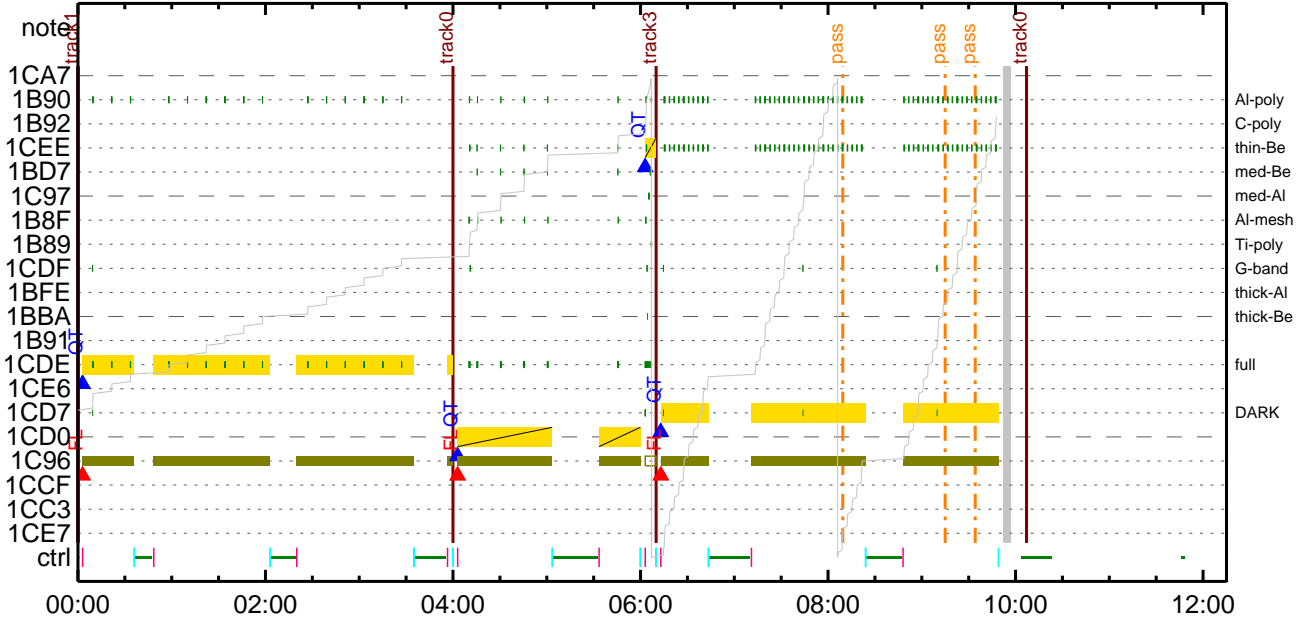
CMDI #0366 2023/10/06



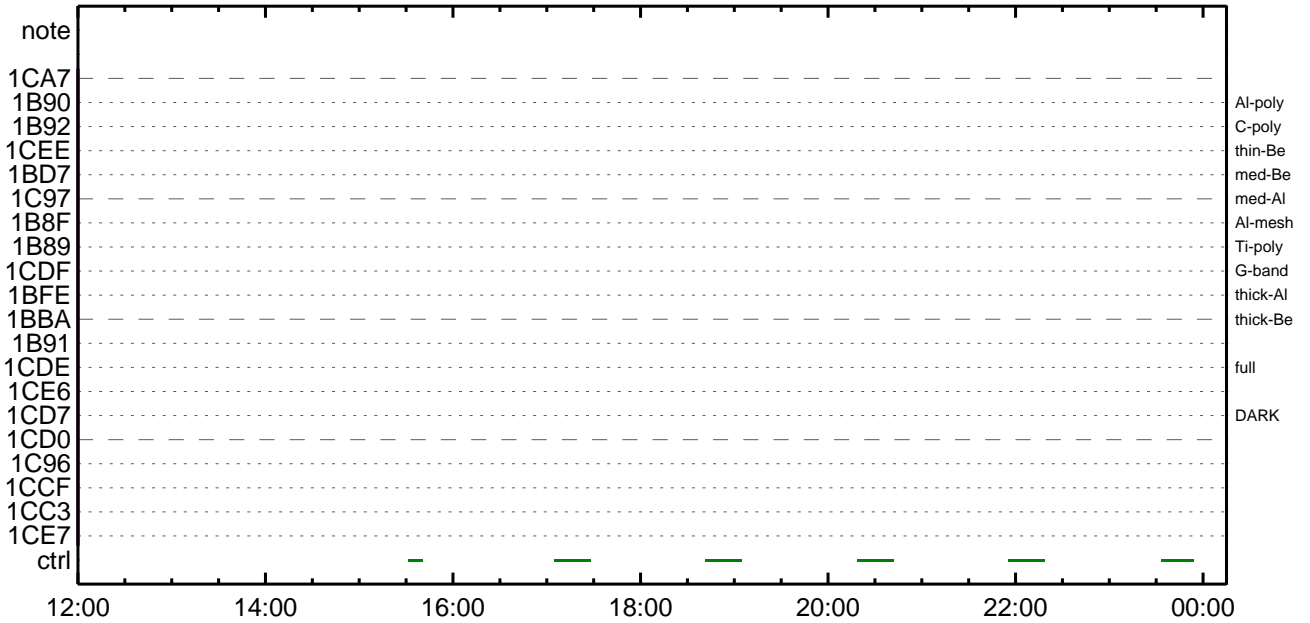
CMDI #0366 2023/10/06



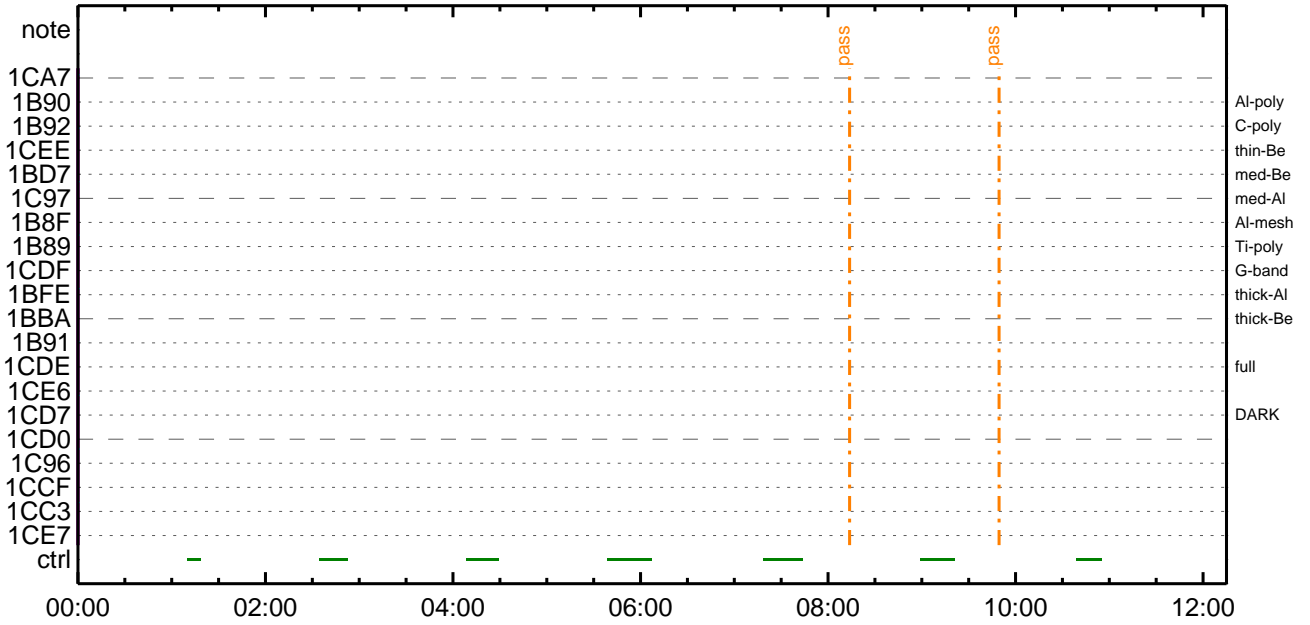
CMDI #0366 2023/10/07



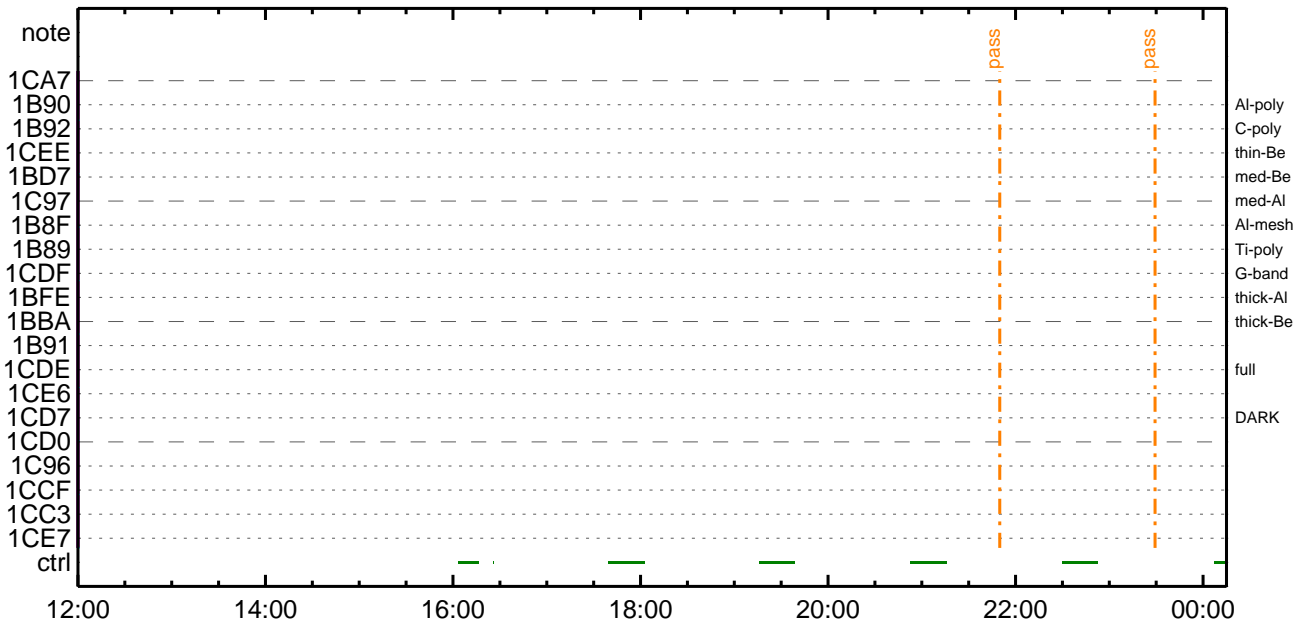
CMDI #0366 2023/10/07



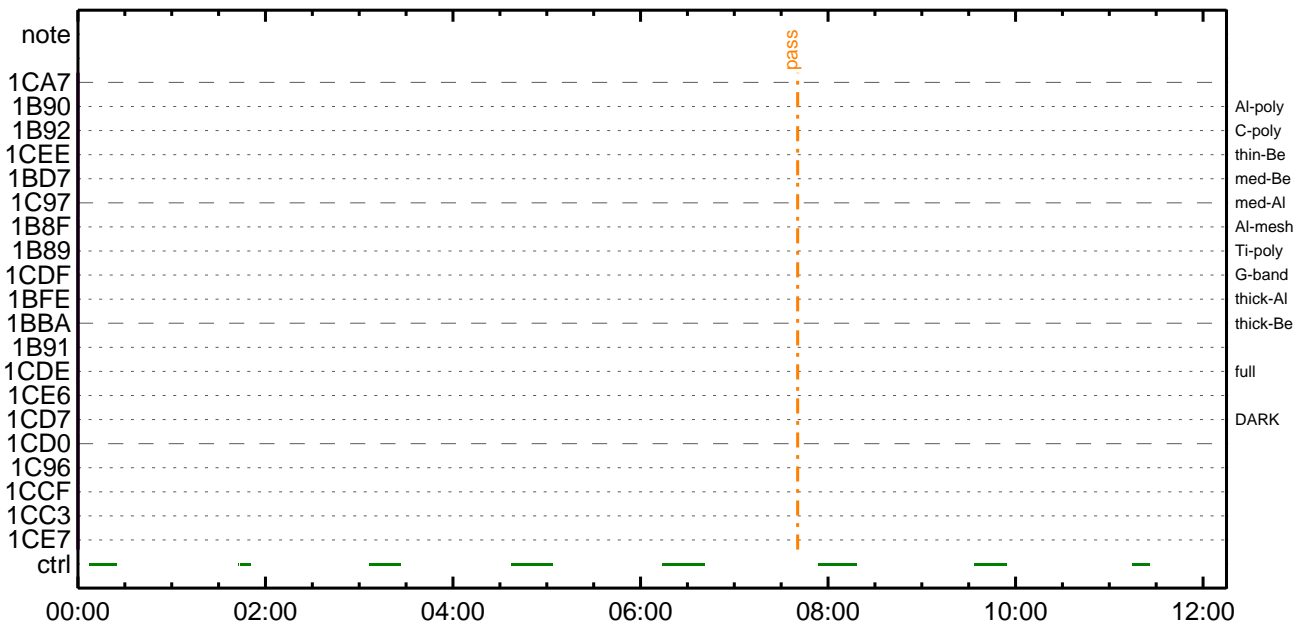
CMDI #0366 2023/10/08



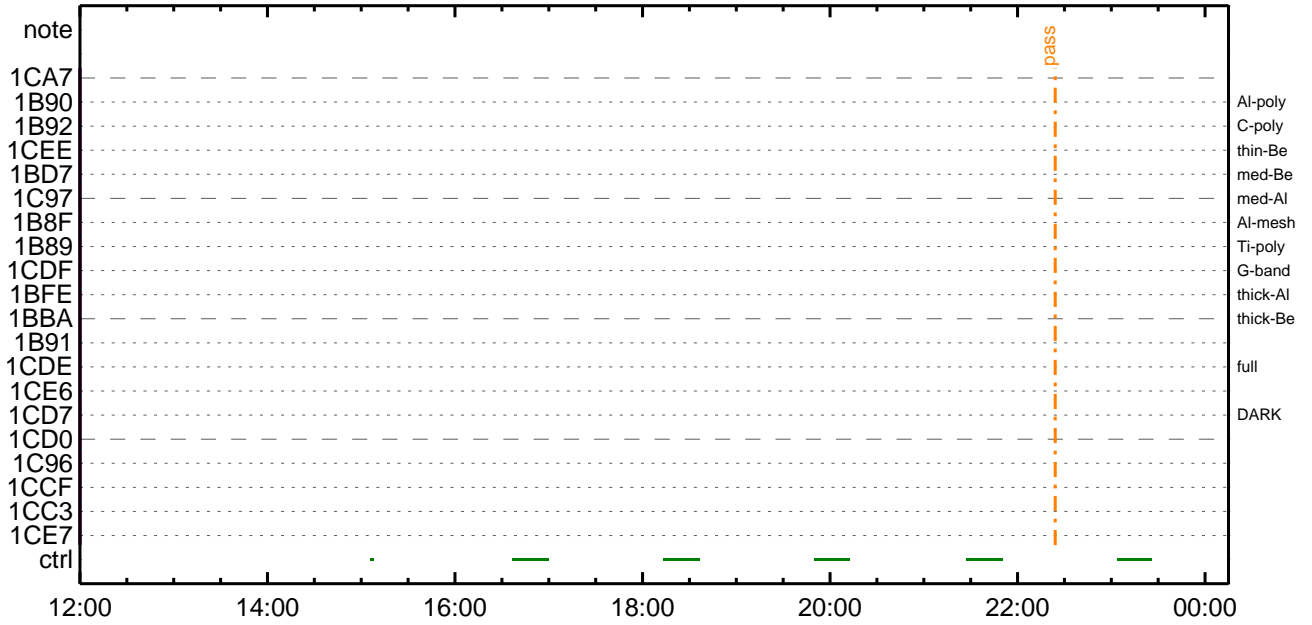
CMDI #0366 2023/10/08



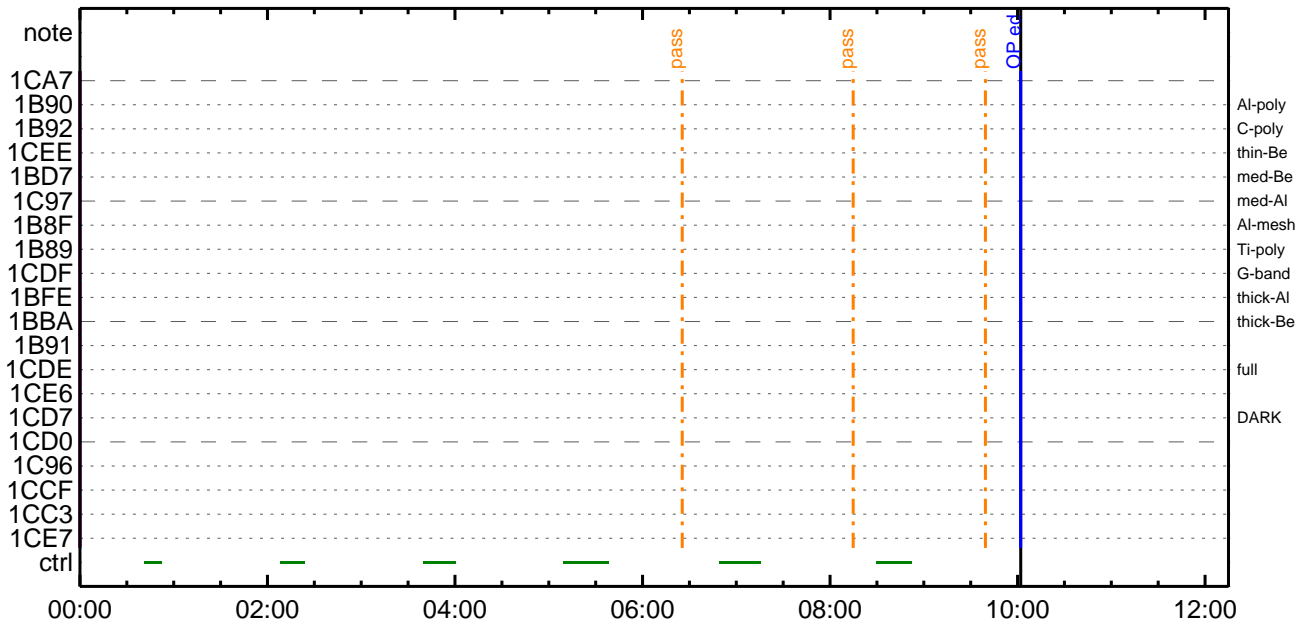
CMDI #0366 2023/10/09



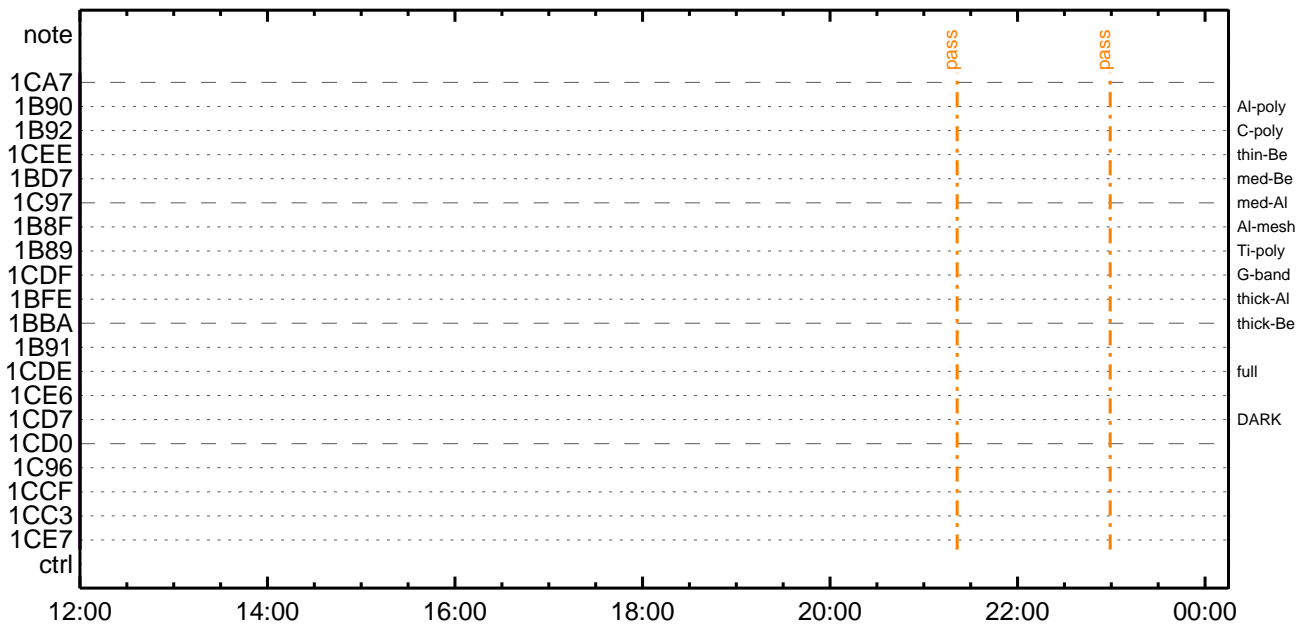
CMDI #0366 2023/10/09



CMDI #0366 2023/10/10



CMDI #0366 2023/10/10







```

0096 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0097 C.
0098 C. TI 2023-10-05 10:20:00.0
0099 +. TI 2023-10-05 10:20:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0102 C.
0103 +. TI 2023-10-05 10:20:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0106 C.
0107 +. TI 2023-10-05 10:20:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0110 C.
0111 +. TI 2023-10-05 10:24:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0114 C.
0115 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0116 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0117 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0118 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0119 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0120 C.
0121 C. *****
0122 C. TI 2023-10-05 10:24:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0129 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0130 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0131 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0132 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0136 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0137 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0138 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0139 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0140 C.
0141 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0142 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0143 C.
0144 C. RAM ID=TI_TBL 01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0145 C.
0146 C. DHU 01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0150 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0151 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0152 C.                01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2023-10-05 10:24:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2023-10-05 10:24:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 C.                [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2023-10-05 10:24:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 C.                [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP 01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E *****
0181 C. (% 01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E)
0182 S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** 01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E *****
0187 S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;ãLOS 01-03; SET 01-03 DUMP 01-03 01-03 01-03 01-03; E
0192 C.
0193 C. ***** LOS *****

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 9s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2023-10-05 10:24:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÃîâî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0156 C. (¼ã°îÿÓÿÄÿËÿPÿËÿâÿçÿèñË¼ã¼Ä»Û¹ñè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ÿDÿ¹•Ï Daily±¿îÑñË'Ø¹ñèDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOSÿÁÿSÿÿÄÿ¹¼Ä»Û;ã
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

main-145 2023-10-05 11:50:10 182 33 SOLAR-B MAIN //
0001 C.
0002 . C. \*\*\*\*\* AOS \*\*\*\*\*
0003 C.
0004 . C. ;ãAOSYÁYŒYËYˆ¼Á»Û;ã
0005 C.
0006 C. YÀYŒ;¼Y³YŒYÓYÉÁ+z®
0007 +. DC 00-00 NULL\_DUMMY\_CMD
0008 C.
0009 . C. \*\*\*\*\* AOCs : Reload orbital element (send every contact) \*\*\*\*\*
0010 C. Áí;ËzãÁð•µ°Æ»Í×ÁÇøÍYÇYÁY×YÍ;¼YŒË;ËËÈ¼µ•ííË;ËðÈ¼°ÇÓð•αz¼í¹Çðí;çÁ®. ùα¹ðÈððçÁ+z®ð•ðÈððððÈ;f
0011 +. DC 02-8E AOCU\_ORB\_UPD
0012 C.
0013 C.
0014 . C. \*\*\*\*\*
0015 C. XÁ+z®µ;ON
0016 C. \*\*\*\*\*
0017 C. ç“ °ÆÀ. Í×ËYðáLÒSðBçðí»p´ Öðð¹íí. ð. ; çÉÖÍ×ðÈXÁÓONðí¹ÖðÈðíðÈððð³ðÈ;f
0018 C.
0019 +. DC 03-B4 TCIA\_XPA\_ON/HI
0020 M. WAIT\_SEC 1
0021 + DC 03-84 TCIA\_XMOD\_ON
0022 M. WAIT\_SEC 1
0023 + DC 03-95 TCIA\_XMOD\_QPSK
0024 C. çç[HK1\_XPA\_ON/OFF] EQ ON
0025 C. çç[HK1\_XPA\_PWR\_HI/LO] EQ HI
0026 C. çç[HK1\_XMOD\_ON/OFF] EQ ON
0027 C. çç[HK1\_XMOD\_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŸDŸÓYÉYÍYÁYˆ¼ÖÁÖð-°ÁÁÈð•αzðÉ; ç°È²¼ðí°ÆÀ. ¼É¼Çðð¼Á¹Öð¹ðÉ;f
0030 C.
0031 . C. \*\*\*\*\*
0032 C. DR PT1 Áí¼í°ÆÀ.
0033 C. \*\*\*\*\*
0034 C. ç“ RESTART;ÈPT1;Ëð•αzα¼í¹Çðí; ç°È²¼ðí¼Á¹Öð»ð° ; çDCBC-150ðØzÈðà;f
0035 C.
0036 . C. ;ãPT1°ÆÀ. ³«»Û;ã
0037 +. DC 01-29 DHU\_S/X\_VC4\_OFF
0038 + DC 06-C8 DR\_PT1\_REP\_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR\_REP\_START
0041 + DC 01-32 DHU\_X\_VC4\_ON
0042 C. çç[HK1\_REP\_PT\_1/2] EQ PT1 (¼Á¹Ö. ;¼Ú)
0043 C. çç[HK1\_REP\_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0044 C. çç[HK1\_X\_VC4\_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYŒYŒYÈÁÚÁØ;ÈÁ•Á°²óÈð;Ë. áðí°ÆÀ. °Æ³«;ã
0047 +. DC 06-B3 DR\_REP\_START
0048 + DC 01-32 DHU\_X\_VC4\_ON
0049 C. çç[HK1\_REP\_PT\_1/2] EQ PT1 (¼Á¹Ö. ;¼Ú)
0050 C. çç[HK1\_REP\_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0051 C. çç[HK1\_X\_VC4\_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ. ð-¼«Æ°Áá»Œð•αz. á; ç°È²¼ðð¼Á¹Öð¹ðÉ;f
0055 C. YÇYÓYŒYŒYÈÁÚÁØðÁÁ•Á°²óÈðð-¼áð¼í¹Çðí°í»ð¹ðÈððçÁÓðÁ;f
0056 C.
0057 . C. \*\*\*\*\*
0058 C. DR PT2 Áí¼í°ÆÀ.
0059 C. \*\*\*\*\*
0060 C. ç“ RESTART;ÈPT2;Ëð•αzα¼í¹Çðí; ç°È²¼ðí¼Á¹Öð»ð° ; çDCBC-151ðØzÈðà;f
0061 C.
0062 . C. ;ãPT2°ÆÀ. ³«»Û;ã
0063 +. DC 01-29 DHU\_S/X\_VC4\_OFF
0064 + DC 06-C8 DR\_PT2\_REP\_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR\_REP\_START
0067 + DC 01-32 DHU\_X\_VC4\_ON
0068 C. çç[HK1\_REP\_PT\_1/2] EQ PT2 (¼Á¹Ö. ;¼Ú)
0069 C. çç[HK1\_REP\_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0070 C. çç[HK1\_X\_VC4\_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYŒYŒYÈÁÚÁØ;ÈÁ•Á°²óÈð;Ë. áðí°ÆÀ. °Æ³«;ã
0073 +. DC 06-B3 DR\_REP\_START
0074 + DC 01-32 DHU\_X\_VC4\_ON
0075 C. çç[HK1\_REP\_PT\_1/2] EQ PT2 (¼Á¹Ö. ;¼Ú)
0076 C. çç[HK1\_REP\_STA/STP] EQ START (¼Á¹Ö. ;¼Ú)
0077 C. çç[HK1\_X\_VC4\_ON/OFF] EQ ON (¼Á¹Ö. ;¼Ú)
0078 C.
0079 . C. \*\*\*\*\*
0080 C. DR°ÆÀ. Áá»Œ;çXÁ+z®µ;OFF
0081 C. \*\*\*\*\*
0082 C.
0083 . C. ;ãDR°ÆÀ. Áá»Œ;ã
0084 +. DC 06-B4 DR\_REP\_STOP
0085 + DC 01-29 DHU\_S/X\_VC4\_OFF
0086 C. çç[HK1\_REP\_STA/STP] EQ STOP
0087 C. çç[HK1\_S\_VC4\_ON/OFF] EQ OFF
0088 C. çç[HK1\_X\_VC4\_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+z®µ;OFF;ã
0091 +. DC 03-85 TCIA\_XMOD\_OFF
0092 M. WAIT\_SEC 1
0093 + DC 03-B5 TCIA\_XPA\_OFF
0094 C. çç[HK1\_XMOD\_ON/OFF] EQ OFF
0095 C. çç[HK1\_XPA\_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 0a)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 04)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2023-10-05 10:24:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP `úÃîï»ö%ÝðËÄð¹ñèDCBC•x²è *****
0169 C. (%á°îÿÓYÁYÈYÏYÉYáYçYèè%¼¼¼¼»Û¹ñè)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥D¥¹•İ Daily±¿İÑñÈ´Ø¹ñèDCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Å»Û;ã
0180 C.
0181 . C. ***** LOS *****
0182 C.

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\*\*\* OP Sequence for XRT \*\*\*

```

2023/10/05 10:33:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 10:33:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 10:33:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2023/10/05 10:33:06.0 XRT_PREFLR_STRT_431_OG [0x1af]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2023/10/05 10:34:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 10:34:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 10:34:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2023/10/05 10:35:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 55 99 01 f3
2023/10/05 10:35:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2023/10/05 10:35:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2023/10/05 10:35:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2023/10/05 10:35:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2023/10/05 10:35:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2023/10/05 10:36:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2023/10/05 10:37:56.0 XRT_QT_PROG_SET_404_OG [0x194]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0c
2023/10/05 10:37:58.0 XRT_FL_PROG_SET_418_OG [0x1a2]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 04
2023/10/05 10:50:30.0 XRT_Custom_430_OG [0x1ae]
2023/10/05 10:51:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2023/10/05 15:58:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 15:58:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 15:58:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2023/10/05 15:58:36.0 XRT_PREFLR_STRT_431_OG [0x1af]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2023/10/05 16:01:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2023/10/05 16:11:00.0 XRT_Custom_430_OG [0x1ae]
2023/10/05 16:12:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2023/10/05 17:33:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 17:33:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 17:33:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2023/10/05 17:33:36.0 XRT_PREFLR_STRT_431_OG [0x1af]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2023/10/05 17:36:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2023/10/05 17:57:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 17:57:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 17:57:58.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2023/10/05 17:58:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2023/10/05 17:58:18.0 XRT_FLD_DIS_409_OG [0x199]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2023/10/05 17:58:20.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2023/10/05 17:58:22.0 XRT_ARS_DIS_435_OG [0x1b3]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2023/10/05 18:00:58.0 XRT_QT_PROG_SET_416_OG [0x1a0]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 11
2023/10/05 18:01:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2023/10/05 18:07:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 18:07:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2023/10/05 18:07:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2023/10/05 18:08:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 04 03 ce 01 f3
2023/10/05 18:08:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2023/10/05 18:08:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2023/10/05 18:08:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2023/10/05 18:08:24.0 XRT_ARS_DIS_423_OG [0x1a7]

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2023/10/05	18:08:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2023/10/05	18:10:56.0	XRT_OT_PROG_SET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/05	18:10:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_OT_PROG_SET	2	07-F0	c4 0a
2023/10/05	18:11:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2023/10/05	19:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/05	19:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/05	19:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/05	19:10:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/05	19:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/05	19:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/05	19:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2023/10/05	20:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/05	20:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/05	20:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/05	20:47:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/05	20:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/05	21:11:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/05	21:12:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2023/10/05	22:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/05	22:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/05	22:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/05	22:24:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/05	22:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/05	22:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/05	22:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2023/10/06	00:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/06	00:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	00:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	00:02:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/06	00:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/06	00:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/06	00:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2023/10/06	01:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/06	01:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	01:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	01:39:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/06	01:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/06	01:45:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/06	01:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2023/10/06	03:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/06	03:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	03:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	03:01:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/06	03:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/06	03:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/06	03:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	c0
2023/10/06	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/06	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

2023/10/06	04:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2023/10/06	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2023/10/06	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2023/10/06	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2023/10/06	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2023/10/06	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/10/06	04:02:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2023/10/06	04:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2023/10/06	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2023/10/06	04:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/10/06	04:33:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/10/06	04:33:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/10/06	04:33:36.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/10/06	04:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/10/06	04:58:30.0	XRT_Custom_430_OG [0x1ae]							
2023/10/06	04:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2023/10/06	05:41:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/10/06	05:41:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/10/06	05:41:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2023/10/06	05:42:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2023/10/06	05:42:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2023/10/06	05:42:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2023/10/06	05:44:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2023/10/06	05:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2023/10/06	05:51:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/10/06	05:51:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/10/06	05:51:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2023/10/06	05:52:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04	03	ce	01	f3
2023/10/06	05:52:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2023/10/06	05:52:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2023/10/06	05:52:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2023/10/06	05:52:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2023/10/06	05:52:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/10/06	05:54:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2023/10/06	05:54:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	04			
2023/10/06	05:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2023/10/06	06:08:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/10/06	06:08:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/10/06	06:08:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/10/06	06:08:36.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/10/06	06:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/10/06	06:35:30.5	XRT_Custom_430_OG [0x1ae]							
2023/10/06	06:36:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2023/10/06	07:48:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/10/06	07:48:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/10/06	07:48:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/10/06	07:48:36.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/10/06	07:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2023/10/06	08:13:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/10/06	08:14:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]						
2023/10/06	09:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/10/06	09:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	09:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	09:28:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2023/10/06	09:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2023/10/06	09:49:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/10/06	09:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2023/10/06	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/10/06	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	09:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	10:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2023/10/06	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00		
2023/10/06	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2023/10/06	10:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2023/10/06	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2023/10/06	10:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2023/10/06	10:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da		
2023/10/06	10:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2023/10/06	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04		
2023/10/06	11:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/10/06	11:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	11:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	11:09:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2023/10/06	11:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2023/10/06	11:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/10/06	11:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2023/10/06	16:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/10/06	16:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	16:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	16:31:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2023/10/06	16:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2023/10/06	16:54:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2023/10/06	16:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2023/10/06	17:43:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/10/06	17:43:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	17:43:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	17:43:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2023/10/06	17:43:48.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00		
2023/10/06	17:43:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2023/10/06	17:43:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2023/10/06	17:46:28.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2023/10/06	17:46:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2023/10/06	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2023/10/06	17:53:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	17:53:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2023/10/06	17:53:30.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
			AOCU_NM	5	02-76	04 03 ce 01 f3		

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2023/10/06	17:53:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2023/10/06	17:53:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2023/10/06	17:53:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2023/10/06	17:53:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2023/10/06	17:53:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/06	17:56:26.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2023/10/06	17:56:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2023/10/06	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/06	18:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	18:07:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	18:07:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/06	18:07:36.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/06	18:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/06	18:31:30.5	XRT_Custom_430_OG [0x1ae]			
2023/10/06	18:32:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/06	19:44:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	19:44:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	19:44:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/06	19:44:36.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/06	19:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/06	20:08:00.0	XRT_Custom_430_OG [0x1ae]			
2023/10/06	20:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/06	21:21:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	21:21:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	21:21:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/06	21:21:36.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/06	21:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/06	21:45:00.0	XRT_Custom_430_OG [0x1ae]			
2023/10/06	21:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/06	22:59:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	22:59:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	22:59:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/06	22:59:06.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/06	23:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/06	23:21:00.0	XRT_Custom_430_OG [0x1ae]			
2023/10/06	23:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/06	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/06	23:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2023/10/07	00:00:00.0	AOCS_Or-e-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	01 03 ce 01 f3
2023/10/07	00:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2023/10/07	00:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2023/10/07	00:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2023/10/07	00:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2023/10/07	00:00:24.0	XRT_FLD_RESET_439_OG [0x1b7]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/07	00:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2023/10/07	00:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2023/10/07	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/07	00:36:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/07	00:36:02.0	XRT_CTRL_MANU_402_OG [0x192]			

2023/10/07	00:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	00:36:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/10/07	00:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/10/07	00:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/10/07	00:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/10/07	02:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	02:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	02:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	02:03:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/10/07	02:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/10/07	02:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/10/07	02:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/10/07	03:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	03:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	03:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	03:35:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/10/07	03:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/10/07	03:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/10/07	03:56:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/10/07	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2023/10/07	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2023/10/07	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2023/10/07	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2023/10/07	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2023/10/07	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2023/10/07	04:02:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/10/07	04:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2023/10/07	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2023/10/07	05:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/10/07	05:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	05:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	05:03:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/10/07	05:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/10/07	05:32:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/10/07	05:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/10/07	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2023/10/07	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2023/10/07	06:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2023/10/07	06:02:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2023/10/07	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2023/10/07	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/10/07	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/07	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2023/10/07	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2023/10/07	06:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c] AOCU_NM	5	02-76	03 03 ce 01 f3
2023/10/07	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2023/10/07	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2023/10/07	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2023/10/07	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2023/10/07	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/07	06:12:56.0	XRT_QT_PROG_SET_438_OG [0x1b6] MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2023/10/07	06:12:58.0	XRT_FL_PROG_SET_418_OG [0x1a2] MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2023/10/07	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/07	06:43:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/07	06:43:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/07	06:43:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/07	06:43:36.0	XRT_PREFLR_STRT_431_OG [0x1af] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/07	06:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/07	07:10:00.5	XRT_Custom_430_OG [0x1ae]			
2023/10/07	07:11:00.5	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/07	08:24:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/07	08:24:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/07	08:24:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/07	08:24:06.0	XRT_PREFLR_STRT_431_OG [0x1af] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/07	08:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/07	08:47:00.0	XRT_Custom_430_OG [0x1ae]			
2023/10/07	08:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/07	09:49:00.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/07	10:07:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00