

# XRT Timeline to be uploaded on 2023/10/31

Period: 2023/10/31 10:38:00 - 2023/11/04 11:50:00

\* \* \* \* \*

## Normal mode

\* \* \* \* \*

XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms												
Term	Pointing (x, y)							Comment				
10/31 11:03:00 - 10/31 17:46:30	Fixed ( -23.9, 910.4)							# OP start + 10min + HOP470				
<b>PROG= 14 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 16 2-time(s) 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 30.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
└─ Subr= 3 60-time(s) 60.0sec												
└─ Seqn= 24 1-time(s) 30.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> <span>Default Filter</span> <span>Thicker Filter</span> <span>VLS</span> <span>mode</span> <span>image</span> <span>Exp.</span> <span>CCD</span> <span>Bin</span> <span>ROI: size (center)</span> <span>Comp.</span> <span>AEC Buffer</span> <span>Interval</span> </div>												

XOB #1CEE: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(512/8192/32768), Med-Be(128/576)												
Term	Pointing (x, y)							Comment				
10/31 18:13:00 - 10/31 18:19:54	Fixed ( 0.0, 0.0)							synoptic, shifted 10.0 min				
11/01 05:58:00 - 11/01 06:04:54	Fixed ( 0.0, 0.0)							HOP349 + synoptic, shifted -5.0 min				
11/01 17:59:30 - 11/01 18:06:24	Fixed ( 0.0, 0.0)							synoptic, shifted -3.5 min				
11/02 06:03:00 - 11/02 06:09:54	Fixed ( 0.0, 0.0)							HOP349 + synoptic				

PROG= 17 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 26 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 15 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 83 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 23 1-time(s) 4.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 41 1-time(s) 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 17 1-time(s) 2.0sec												
└─ med-Al/Open med-Al/thick-Al close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ med-Al/Open med-Al/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ med-Al/Open med-Al/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 33 1-time(s) 2.0sec												
└─ med-Be/Open Open/thick-Al close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 56 1-time(s) 2.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> <span>Default Filter</span> <span>Thicker Filter</span> <span>VLS</span> <span>mode</span> <span>image</span> <span>Exp.</span> <span>CCD</span> <span>Bin</span> <span>ROI: size (center)</span> <span>Comp.</span> <span>AEC Buffer</span> <span>Interval</span> </div>												

XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with												
Term	Pointing (x, y)							Comment				
10/31 18:23:00 - 11/01 03:59:54	Track ( 156.0, -404.7) @ 10/31 18:20:00							AR13474				
11/01 06:08:00 - 11/01 17:56:24	Track ( 256.6, -402.0) @ 11/01 06:05:00							AR13474				

PROG= 09 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Subr= 2 5-time(s) 2.0sec												
└─ Seqn= 47 1-time(s) 2.0sec												

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 96 4-time(s) 120.0sec</b>												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1CD0: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 300min cad) + CME w</b>												
Term			Pointing (x, y)				Comment					
11/01 04:03:00 - 11/01 05:54:54			Fixed ( 0.0, 0.0)				HOP349 + synoptic, shifted -5.0 min					
11/02 04:09:00 - 11/02 05:59:54			Fixed ( 0.0, 0.0)				HOP349 + synoptic					

<b>PROG= 05 Inf.-time(s)</b>												
<b>Subr= 1 1-time(s) 300.0sec</b>												
<b>Seqn= 55 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 15 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 79 1-time(s) 2.0sec</b>												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 30 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 20-time(s) 900.0sec</b>												
<b>Seqn= 8 1-time(s) 2.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 74 1-time(s) 2.0sec</b>												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
<b>Seqn= 29 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with</b>												
Term			Pointing (x, y)				Comment					
11/01 18:09:30 - 11/02 03:47:30			Track ( 355.8, -398.4) @ 11/01 18:06:30				AR13474					
11/02 06:13:00 - 11/02 10:16:30			Track ( 450.3, -393.9) @ 11/02 06:10:00				AR13474					

<b>PROG= 02 Inf.-time(s)</b>												
<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 92 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Subr= 2 5-time(s) 2.0sec</b>												
<b>Seqn= 47 1-time(s) 2.0sec</b>												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 96 4-time(s) 180.0sec</b>												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

Flare mode

\* \* \* \* \*

<b>XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G</b>												
---	--	--	--	--	--	--	--	--	--	--	--	--

Term	Pointing (x, y)	Comment
10/31 11:03:00 - 10/31 17:46:30	Fixed ( -23.9, 910.4)	# OP start + 10min + HOP470
10/31 18:23:00 - 11/01 03:59:54	Track ( 156.0, -404.7) @ 10/31 18:20:00	AR13474
11/01 04:03:00 - 11/01 05:54:54	Fixed ( 0.0, 0.0)	HOP349 + synoptic, shifted -5.0 min
11/01 06:08:00 - 11/01 17:56:24	Track ( 256.6, -402.0) @ 11/01 06:05:00	AR13474
11/01 18:09:30 - 11/02 03:47:30	Track ( 355.8, -398.4) @ 11/01 18:06:30	AR13474
11/02 04:09:00 - 11/02 05:59:54	Fixed ( 0.0, 0.0)	HOP349 + synoptic
11/02 06:13:00 - 11/02 10:16:30	Track ( 450.3, -393.9) @ 11/02 06:10:00	AR13474
<b>PROG= 04 30-time(s)</b>		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 73 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 87 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

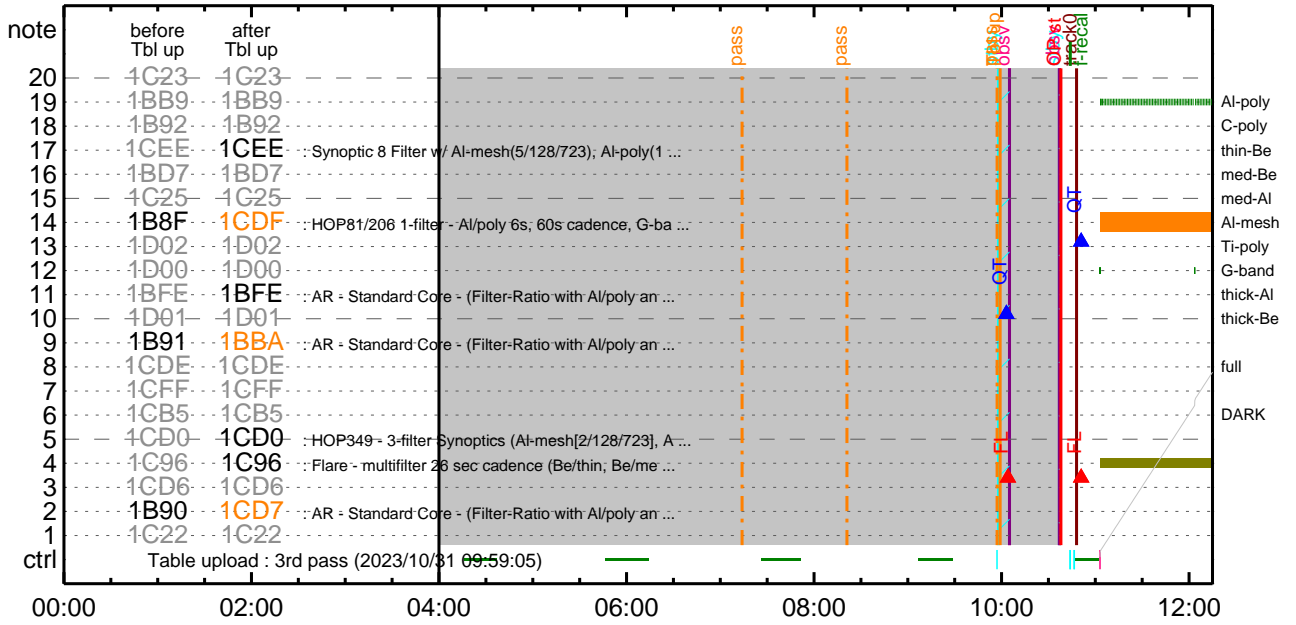
\* \* \* \* \*

### Flare Detection

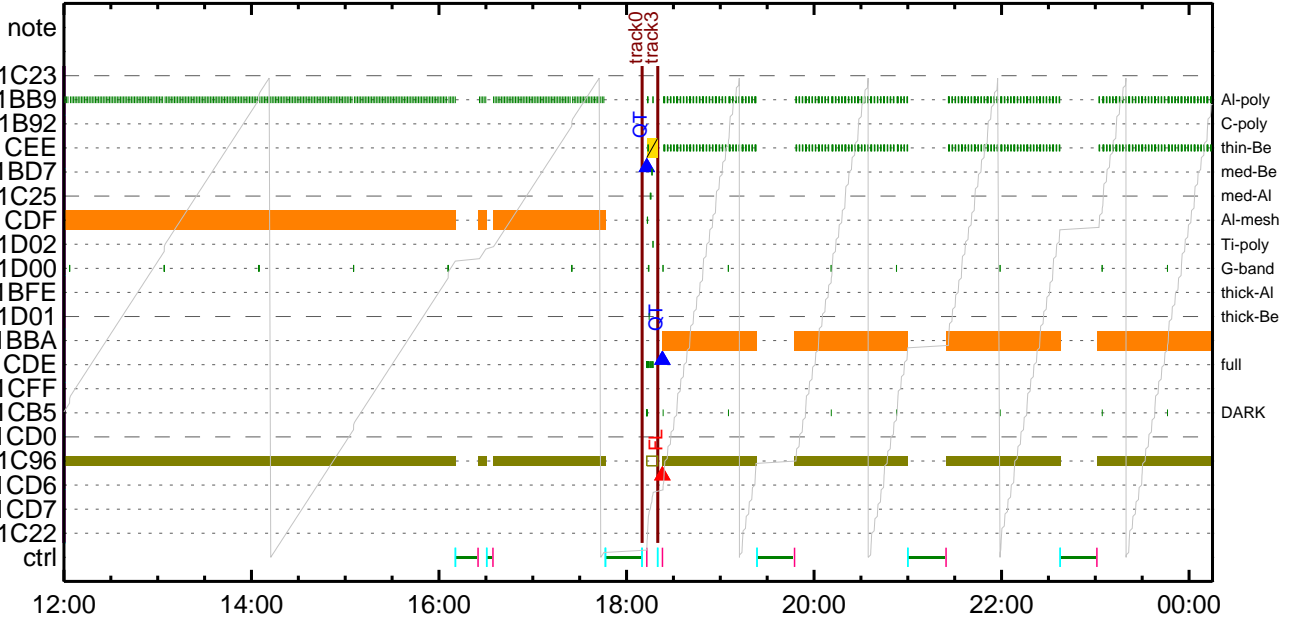
\* \* \* \* \*

<b>FLD Patrol</b>											
Term	Pointing (x, y)	Comment									
10/31 10:00:05 - 10/31 18:10:18	cannot be identified										
10/31 18:20:18 - 11/01 05:55:18	Track ( 156.0, -404.7) @ 10/31 18:20:00	AR13474									
11/01 06:05:18 - 11/01 17:56:48	Track ( 256.6, -402.0) @ 11/01 06:05:00	AR13474									
11/01 18:06:48 - 11/02 06:00:18	Track ( 355.8, -398.4) @ 11/01 18:06:30	AR13474									
11/02 06:10:18 - 11/04 11:50:00	Track ( 450.3, -393.9) @ 11/02 06:10:00	AR13474									
Al-poly/Open	Al-poly/Open close	Safe Norm 4ms Obs 8x8 Q=50 30sec									
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval									

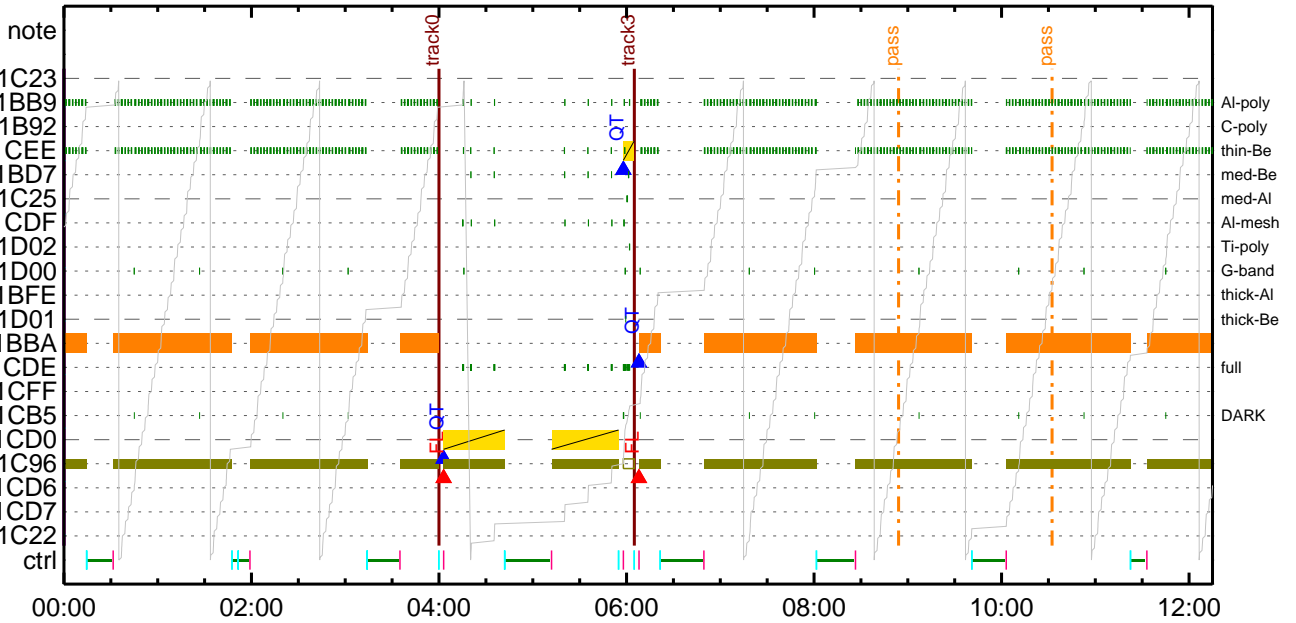
### CMDI #0423 2023/10/31



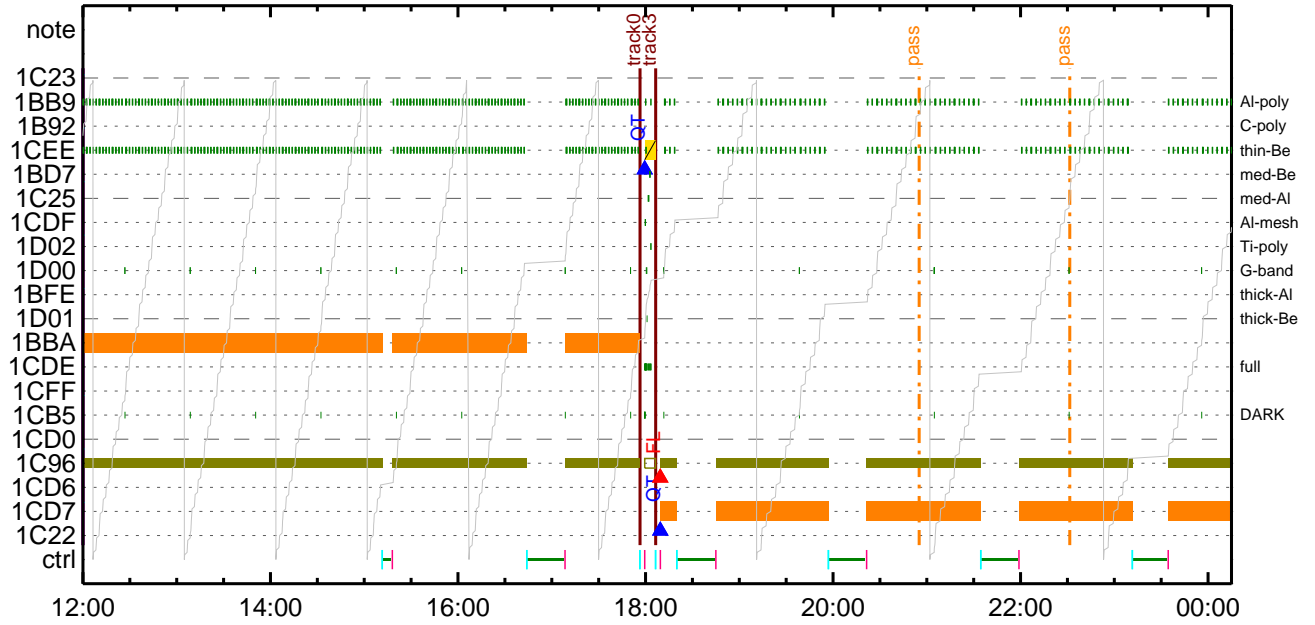
### CMDI #0423 2023/10/31



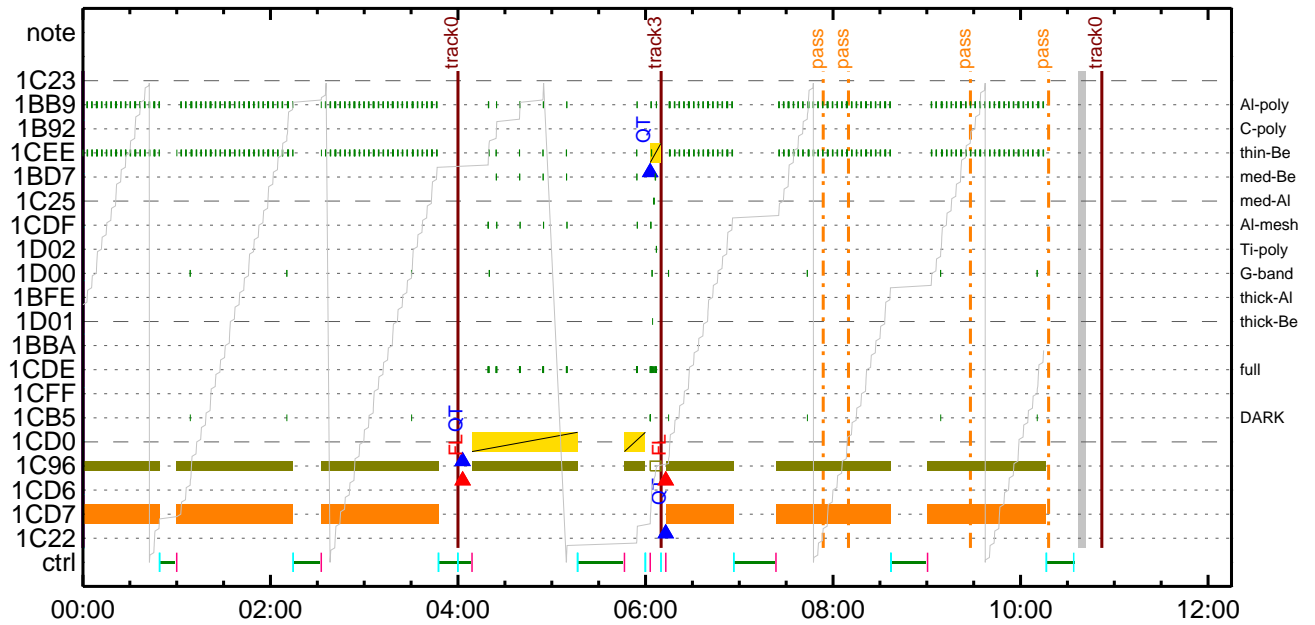
### CMDI #0423 2023/11/01



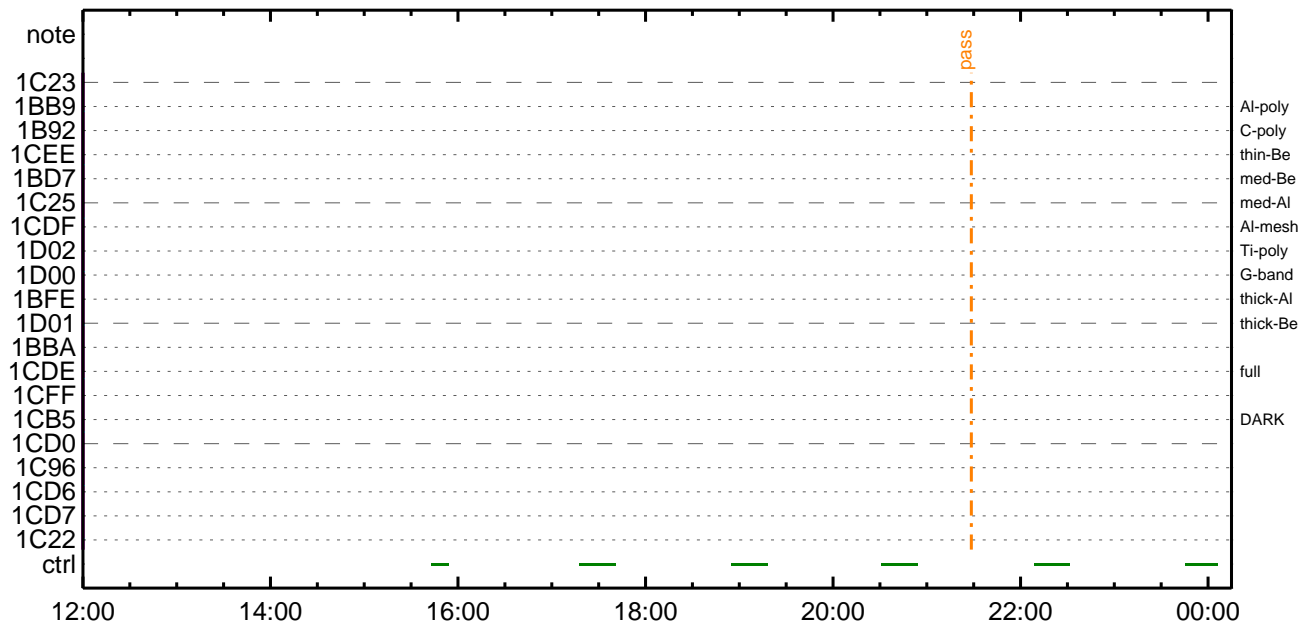
CMDI #0423 2023/11/01



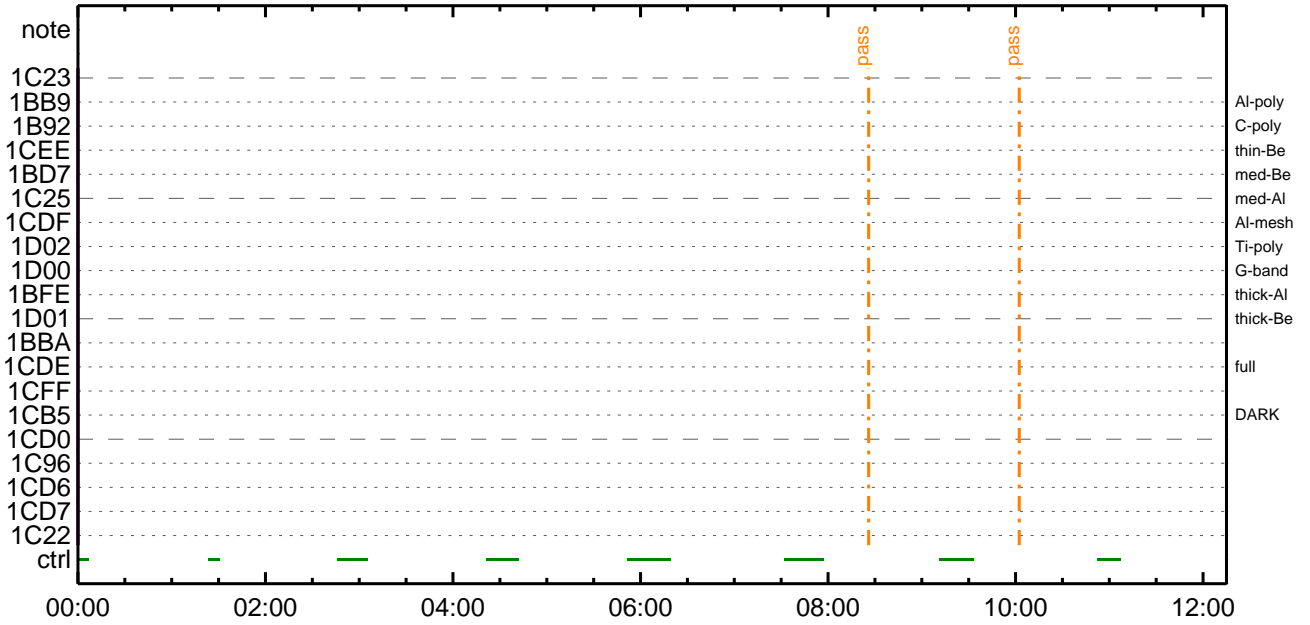
CMDI #0423 2023/11/02



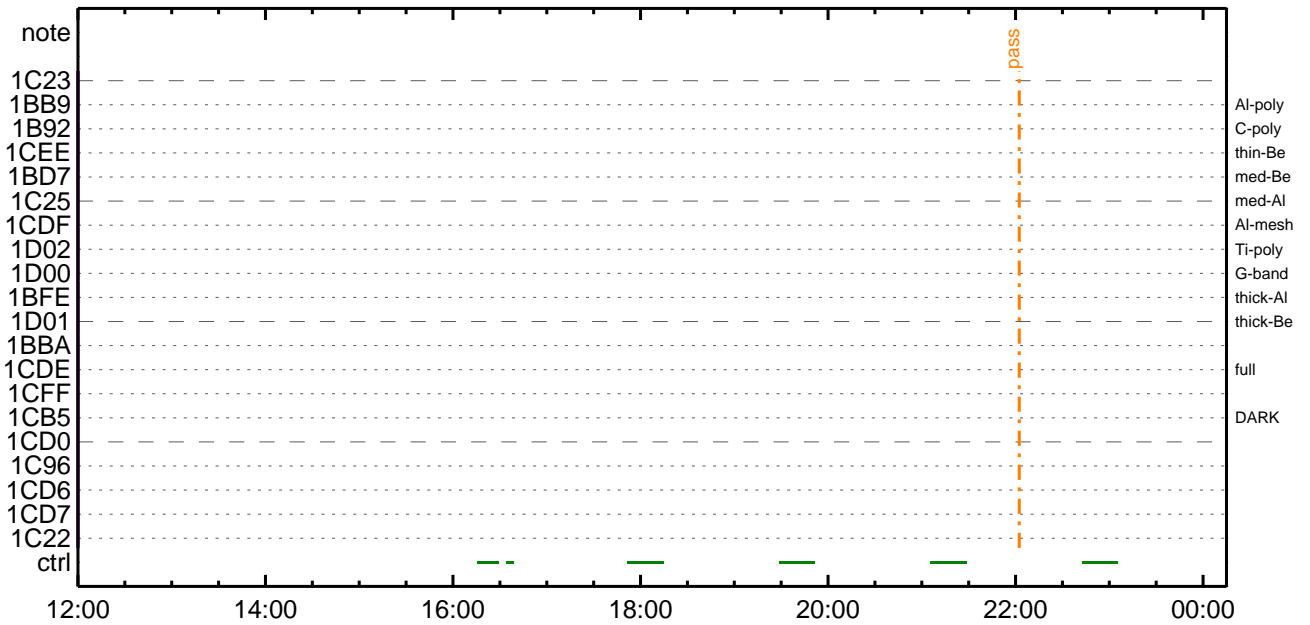
CMDI #0423 2023/11/02



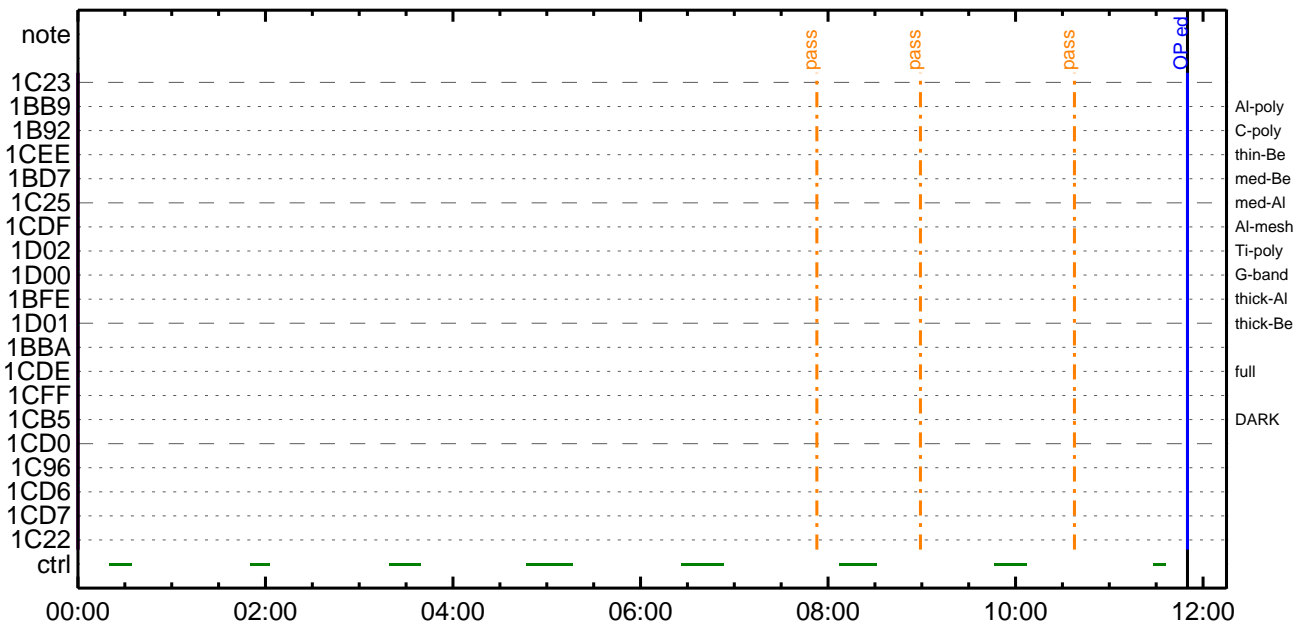
CMDI #0423 2023/11/03



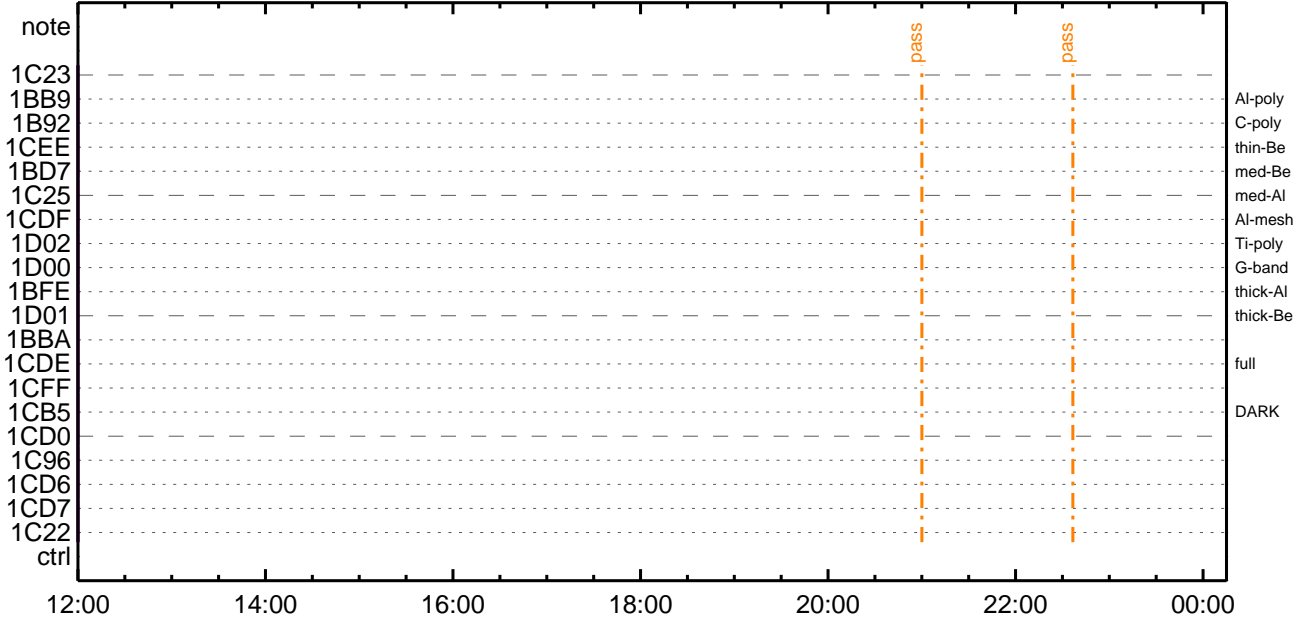
CMDI #0423 2023/11/03



CMDI #0423 2023/11/04



CMDI #0423 2023/11/04



(a) Spacecraft Operation Procedure (real-commands)

```
main-190 2023-10-31 11:50:37 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOS¥Ã¥S¥Ã¥~¼Ä»Û;ä
0005 C.
0006 C. ¥Ã¥B;¼¥³¥P¥ó¥ÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;ÈççðÄð•μ°Æ»Í×ÄÇçðÍ¥ç¥Ã¥×¥Í;¼¥É;ÈÈè%µ•ííÉ;ÈðÈ¼°ÇÓç•çç¼l¹ççÍ;çÄ®, ùç¹çèçðçÄ+ç®ç•çÈççççè;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OG¥í;¼¥É;|¥Ã¥ó¥×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OG¥í;¼¥É;ä
0019 . S. OP op-190:OP
0020 ()
0021 . S. OG og-190:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°è¥Ã¥ó¥×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ¥Ã¥ó¥×½ªî»ðð³îç§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGçî½É¹ç•è²îOKçð³îç§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ¥Ã¥ó¥×½ªî»ðð³îç§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGçî½É¹ç•è²îOKçð³îç§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ¥Ã¥ó¥×½ªî»ðð³îç§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPçî½É¹ç•è²îOKçð³îç§
0081 C.
0082 . C. ***** çÈ²¼çðî½Ä´¶¹çðÈÈ-ççÄ+ç® (¼åµ-¥Ã¥ó¥×½çððÄóÄççªççµçç¼¹¹ççççâ) *****
0083 C. DHU¥â;¼¥É;È%ÿ%¥í;¼¥É;ÈçðÍáç¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADçç-Ä+ç®NGççî½¹¹çç;ç°È²¼çðî½TI-CMDÄ+ç®ççî½Ä¹Öçç•çÈççççè;ç
```



```
0096 C.          0p0z;çSET0EDUMP0İÆ±°iYÑY¹0Ç¹00|0³0E;f
0097 C.
0098 . C. TIY³YFÿÖYÉ00ðÄDİç(UT)
0099 +. TI 2023-10-31 10:33:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2023-10-31 10:33:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2023-10-31 10:33:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2023-10-31 10:37:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C.  °Ê²¼0İÄê%îíÑ0İYÁY$YÄY-¹àİÜ
0116 C.          çç[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0118 C.          çç[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          çç[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          çç[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          çç[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          çç[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          çç[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          çç[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          çç[HK1_PKT_FORM_NO]          EQ          7
0136 C.          çç[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          çç[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          çç[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          çç[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 . C. YÄYÖY×¼ª¹İ»0ð³İÇ$
0142 C.          çç[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0ð³İÇ$
0145 C.
0146 . C. DHUYâ;¼YÉ;Ê¼Y¼, Yİ;¼YÈ;Ë0ðİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          çç[HK1_PKT_FORM_NO]          EQ          2
0150 C.          çç[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          çç[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          çç[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C. Execute, after the success of OP upload.
0157 +. TI 2023-10-31 10:37:00.0
0158 DC 07-F0 MDP_XRT_MODE_STBY
0159 BC          (c3)
0160 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0161 C.
0162 C. ***** XRT END *****
0163 . C. Stop EIS observation and temporarily disable EIS mode changes
0164 C.
0165 C.
0166 C. ***** Start EIS operation (TI set) *****
0167 C. Execute, after the success of OP upload.
0168 C. Set EIS TI-commands
0169 +. TI 2023-10-31 10:37:30.0
0170 DC 07-FC EIS_MODE_MANU
0171 BC          (21 02)
0172 +. TI 2023-10-31 10:37:40.0
0173 DC 07-FC EIS_MODE_CHG_DIS
0174 BC          (22)
0175 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0176 C. ***** End EIS operation (TI set) *****
0177 C.
0178 C.
0179 C.
0180 . C. ***** MDP ´úÄî0İ»0¼Y0ÈÄ00¹0ÈDCBC•x²è *****
0181 C. (%á°İYÖYÄYÉYFÿYÄYÇYÈ0È%¼00¼Ä»Ü0¹0è)
0182 . S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 . C. ***** YDÿ¹.İ Daily±çİÑ0È'00¹0èDCBC•x²è *****
0187 . S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 . C. ;ãLOSÿÄY$YÄY-¼Ä»Ü;ä
0192 C.
0193 . C. ***** LOS *****
```









```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 0b)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 04)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2023-10-31 10:37:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP `úÃîï»ö%ÝðÊÄð¹ñèDCBC•x²è *****
0169 C. (%ã°îÿÓYÁYÈYÏYÉYáYçYèèE%¼ø¼Á»Û¹ñè)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥D¥¹•İ Daily±¿İÑñÈ´Øñ¹ñèDCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Á»Û;ã
0180 C.
0181 . C. ***** LOS *****
0182 C.

```

\*\*\* OP Sequence for XRT \*\*\*

2023/10/31	10:43:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	10:43:56.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	10:43:58.0	XRT_FOCUS_RECALIBRATE_405_OG [0x195]				
		XRT_FOCUS_RECAL	2	07-F8	78 00	
2023/10/31	10:46:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	10:46:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	10:46:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2023/10/31	10:46:36.0	XRT_PREFLR_STRT_431_OG [0x1af]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/10/31	10:47:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2023/10/31	10:48:00.0	AOCS_Ore-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	00 af 13 02 1c	
2023/10/31	10:48:18.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2023/10/31	10:48:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2023/10/31	10:48:22.0	XRT_AEC_RESET_448_OG [0x1c0]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2023/10/31	10:48:24.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2023/10/31	10:48:26.0	XRT_FLD_RESET_434_OG [0x1b2]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2023/10/31	10:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/10/31	10:50:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2023/10/31	10:50:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2023/10/31	11:02:00.0	XRT_Custom_430_OG [0x1ae]				
2023/10/31	11:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/10/31	16:10:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	16:10:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	16:10:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2023/10/31	16:10:36.0	XRT_PREFLR_STRT_431_OG [0x1af]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/10/31	16:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/10/31	16:24:00.0	XRT_Custom_430_OG [0x1ae]				
2023/10/31	16:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/10/31	16:30:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	16:30:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	16:30:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2023/10/31	16:30:36.0	XRT_PREFLR_STRT_431_OG [0x1af]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/10/31	16:33:30.0	XRT_Custom_430_OG [0x1ae]				
2023/10/31	16:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/10/31	16:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/10/31	17:46:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	17:46:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	17:46:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2023/10/31	17:46:36.0	XRT_PREFLR_STRT_431_OG [0x1af]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/10/31	17:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/10/31	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/10/31	18:09:58.0	XRT_FOCUS_POSITION_406_OG [0x196]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2023/10/31	18:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2023/10/31	18:10:18.0	XRT_FLD_DIS_409_OG [0x199]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2023/10/31	18:10:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2023/10/31	18:10:22.0	XRT_ARS_DIS_435_OG [0x1b3]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2023/10/31	18:12:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2023/10/31	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	

2023/10/31	18:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/31	18:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/31	18:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2023/10/31	18:20:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 03 ce 01 f3
2023/10/31	18:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2023/10/31	18:20:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2023/10/31	18:20:22.5	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2023/10/31	18:20:24.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2023/10/31	18:20:26.5	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/31	18:22:56.5	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2023/10/31	18:22:58.5	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2023/10/31	18:23:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/31	19:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/31	19:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/31	19:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/31	19:23:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/31	19:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/31	19:46:30.0	XRT_Custom_430_OG [0x1ae]				
2023/10/31	19:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/31	21:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/31	21:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/31	21:00:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/31	21:00:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/31	21:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/31	21:23:30.0	XRT_Custom_430_OG [0x1ae]				
2023/10/31	21:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/10/31	22:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/31	22:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/10/31	22:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/10/31	22:37:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/10/31	22:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/10/31	23:00:00.0	XRT_Custom_430_OG [0x1ae]				
2023/10/31	23:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/01	00:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	00:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	00:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/01	00:14:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/11/01	00:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/11/01	00:30:30.0	XRT_Custom_430_OG [0x1ae]				
2023/11/01	00:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/01	01:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	01:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	01:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/01	01:47:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/11/01	01:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/11/01	01:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	01:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	01:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/01	01:51:36.0	XRT_PREFLR_STRT_431_OG [0x1af]				



2023/11/01	01:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/11/01	01:58:00.0	XRT_Custom_430_OG [0x1ae]					
2023/11/01	01:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	03:14:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	03:14:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	03:14:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2023/11/01	03:14:06.0	XRT_PREFLR_STRT_431_OG [0x1af]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/11/01	03:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/11/01	03:34:00.0	XRT_Custom_430_OG [0x1ae]					
2023/11/01	03:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2023/11/01	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2023/11/01	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2023/11/01	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2023/11/01	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2023/11/01	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2023/11/01	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2023/11/01	04:02:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2023/11/01	04:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 04	
2023/11/01	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	04:42:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	04:42:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	04:42:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2023/11/01	04:42:06.0	XRT_PREFLR_STRT_431_OG [0x1af]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/11/01	04:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/11/01	05:11:00.0	XRT_Custom_430_OG [0x1ae]					
2023/11/01	05:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	05:54:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	05:54:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	05:54:58.0	XRT_FOCUS_POSITION_406_OG [0x196]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2023/11/01	05:55:18.0	XRT_FLD_DIS_409_OG [0x199]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2023/11/01	05:55:20.5	XRT_FLRCTRL_DIS_413_OG [0x19d]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2023/11/01	05:55:22.5	XRT_ARS_DIS_435_OG [0x1b3]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2023/11/01	05:57:58.5	XRT_QT_PROG_SET_416_OG [0x1a0]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2023/11/01	05:58:00.5	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	06:04:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	06:04:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2023/11/01	06:05:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	03 03 ce 01 f3	
2023/11/01	06:05:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2023/11/01	06:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2023/11/01	06:05:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2023/11/01	06:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2023/11/01	06:05:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2023/11/01	06:07:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2023/11/01	06:07:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]					

2023/11/01	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	04
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	06:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	06:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	06:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/11/01	06:21:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/11/01	06:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/11/01	06:48:30.0	XRT_Custom_430_OG [0x1ae]					
2023/11/01	06:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	08:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	08:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	08:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/11/01	08:01:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/11/01	08:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/11/01	08:25:30.0	XRT_Custom_430_OG [0x1ae]					
2023/11/01	08:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	09:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	09:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	09:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/11/01	09:41:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/11/01	09:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/11/01	10:02:00.5	XRT_Custom_430_OG [0x1ae]					
2023/11/01	10:03:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	11:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	11:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	11:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/11/01	11:22:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/11/01	11:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/11/01	11:32:00.0	XRT_Custom_430_OG [0x1ae]					
2023/11/01	11:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	15:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	15:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	15:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/11/01	15:11:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/11/01	15:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/11/01	15:17:00.0	XRT_Custom_430_OG [0x1ae]					
2023/11/01	15:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	16:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	16:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	16:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2023/11/01	16:44:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2023/11/01	16:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2023/11/01	17:07:30.0	XRT_Custom_430_OG [0x1ae]					
2023/11/01	17:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2023/11/01	17:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	17:56:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2023/11/01	17:56:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2023/11/01	17:56:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2023/11/01	17:56:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2023/11/01	17:56:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	

2023/11/01	17:56:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2023/11/01	17:59:28.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2023/11/01	17:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/01	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	18:06:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	18:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2023/11/01	18:06:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	03 03 ce 01 f3
2023/11/01	18:06:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2023/11/01	18:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2023/11/01	18:06:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2023/11/01	18:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2023/11/01	18:06:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/01	18:09:26.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2023/11/01	18:09:28.0	XRT_FL_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2023/11/01	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/01	18:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	18:20:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	18:20:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/01	18:20:06.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/11/01	18:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/11/01	18:44:00.0	XRT_Custom_430_OG [0x1ae]			
2023/11/01	18:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/01	19:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	19:57:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	19:57:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/01	19:57:06.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/11/01	20:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/11/01	20:20:30.0	XRT_Custom_430_OG [0x1ae]			
2023/11/01	20:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/01	21:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	21:34:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	21:34:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/01	21:34:36.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/11/01	21:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/11/01	21:58:00.0	XRT_Custom_430_OG [0x1ae]			
2023/11/01	21:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/01	23:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	23:11:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/01	23:11:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/01	23:11:36.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/11/01	23:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/11/01	23:33:30.0	XRT_Custom_430_OG [0x1ae]			
2023/11/01	23:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/02	00:49:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/02	00:49:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/02	00:49:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/02	00:49:06.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/11/02	00:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/11/02	00:59:00.0	XRT_Custom_430_OG [0x1ae]			

2023/11/02	01:00:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2023/11/02	02:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	02:14:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	02:14:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/11/02	02:14:36.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/11/02	02:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/11/02	02:31:30.0	XRT_Custom_430_OG [0x1ae]							
2023/11/02	02:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2023/11/02	03:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	03:47:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	03:47:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/11/02	03:47:36.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/11/02	03:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/11/02	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2023/11/02	04:00:00.0	AOCS_Or-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2023/11/02	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2023/11/02	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2023/11/02	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2023/11/02	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2023/11/02	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/11/02	04:02:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2023/11/02	04:02:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04				
2023/11/02	04:08:00.5	XRT_Custom_430_OG [0x1ae]							
2023/11/02	04:09:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2023/11/02	05:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	05:16:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	05:16:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2023/11/02	05:16:36.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2023/11/02	05:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2023/11/02	05:45:30.0	XRT_Custom_430_OG [0x1ae]							
2023/11/02	05:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2023/11/02	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2023/11/02	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2023/11/02	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2023/11/02	06:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2023/11/02	06:02:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2023/11/02	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2023/11/02	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2023/11/02	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2023/11/02	06:10:00.0	AOCS_Or-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03 03 ce 01 f3				
2023/11/02	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2023/11/02	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2023/11/02	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

2023/11/02	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2023/11/02	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/02	06:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2023/11/02	06:12:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 04
2023/11/02	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/02	06:56:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/02	06:56:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/02	06:56:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/02	06:56:36.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/11/02	06:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/11/02	07:22:30.0	XRT_Custom_430_OG [0x1ae]			
2023/11/02	07:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/02	08:37:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/02	08:37:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/02	08:37:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/02	08:37:06.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/11/02	08:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/11/02	08:59:30.0	XRT_Custom_430_OG [0x1ae]			
2023/11/02	09:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2023/11/02	10:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/02	10:16:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/02	10:16:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2023/11/02	10:16:36.0	XRT_PREFLR_STRT_431_OG [0x1af]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2023/11/02	10:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2023/11/02	10:34:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2023/11/02	10:52:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00