

XRT Timeline to be uploaded on 2024/02/27

Period: 2024/02/27 11:02:00 - 2024/03/02 11:08:00

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Normal mode

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XOB #1CE6: HOP81/206 1-filter - Al/poly 6s, 120s cadence, G-band - 384x384 1ms												
Term	Pointing (x, y)							Comment				
02/27 11:15:00 - 02/27 18:42:30	Fixed (-21.0, -959.0)	# OP start + 10min + HOP206(S-Pole)										
PROG= 20 Inf.-time(s)												
├ Subr= 1 1-time(s) 2.0sec												
├└ Seqn= 16 2-time(s) 2.0sec												
├└└ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
├ Subr= 2 1-time(s) 2.0sec												
├└ Seqn= 90 1-time(s) 30.0sec												
├└└ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
├ Subr= 3 60-time(s) 60.0sec												
├└ Seqn= 24 1-time(s) 120.0sec												
├└└ Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval </div>												

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5792)												
Term	Pointing (x, y)							Comment				
02/27 19:18:00 - 02/27 19:24:54	Fixed (0.0, 0.0)	synoptic shifted manually										
02/28 06:09:30 - 02/28 06:26:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 16.5 min										
02/28 18:06:00 - 02/28 18:12:54	Fixed (0.0, 0.0)	synoptic, shifted 3.0 min										
02/29 05:50:30 - 02/29 05:57:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -12.5 min										

XOB #1D0A: HOP 480 Rocket - Be-thin/Be-med 8ms, 256ms, AEC1, 60s-cad												
Term	Pointing (x, y)							Comment				
02/27 19:28:00 - 02/28 03:59:54	Track (504.1, 366.2) ^{© 02/27 19:25:00}	AR13590 obs.										
PROG= 07 Inf.-time(s)												
├ Subr= 1 1-time(s) 2.0sec												
├└ Seqn= 92 1-time(s) 2.0sec												
├└└ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
├└└ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
├└└ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
├ Subr= 2 120-time(s) 60.0sec												
├└ Seqn= 44 1-time(s) 2.0sec												
├└└ thin-Be/Open med-Be/Open close Safe Norm 8ms Obs 1x1 384x384 (1024, 1024) DPCM 1 0 2.0sec												
<div style="display: flex; justify-content: space-between; font-size: small;"> Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval </div>												

med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	1	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w

Term	Pointing (x, y)	Comment
02/28 04:31:00 - 02/28 05:39:00	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 16.5 min
02/29 04:03:00 - 02/29 05:47:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -12.5 min

PROG= 10 Inf.-time(s)

Subr= 1	1-time(s)	300.0sec										
Seqn= 55	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	15-time(s)	1500.0sec										
Seqn= 8	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74	1-time(s)	2.0sec										
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D0E: HOP 476 SOOP-Thin-Be(Med-Be), Med-Be(Al-Thick) AEC3, PFB-(Med-Be, Thick- Be 384x384 AEC3)

Term	Pointing (x, y)	Comment
02/28 06:29:30 - 02/28 07:19:30	Track (580.1, 358.7) @ 02/28 06:26:30	AR13590 obs.

PROG= 06 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 92	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	180-time(s)	2.0sec										
Seqn= 20	1-time(s)	2.0sec										
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	2	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	2	2.0sec
Seqn= 60	1-time(s)	2.0sec										
Open/thick-Be	Open/thick-Be	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	1	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	1	2.0sec
Seqn= 68	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
02/28 07:47:30 - 02/28 17:39:00	Track (580.1, 358.7) @ 02/28 06:26:30	AR13590 obs.
02/28 18:16:00 - 02/29 03:59:54	Track (653.9, 349.6) @ 02/28 18:13:00	AR13590 obs.

PROG= 03 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 92	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	4-time(s)	2.0sec										
Seqn= 47	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 77	4-time(s)	300.0sec										

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C94: AR (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 300s

Term	Pointing (x, y)	Comment
02/29 06:00:30 - 02/29 10:52:00	Track (719.0, 339.6) @ 02/29 05:57:30	AR13590 obs.
PROG= 04 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 81 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1064, 1048) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1064, 1048) Q=95 0 0 2.0sec
Seqn= 82 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Dark 1.00s Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec
Seqn= 71 3-time(s) 2.0sec		
Open/thick-Al	Open/thick-Be close	Safe Norm 16.0s Obs 1x1 512x512 (1064, 1048) Q=98 3 0 2.0sec
Subr= 2 20-time(s) 300.0sec		
Seqn= 94 1-time(s) 100.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 58 1-time(s) 100.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Seqn= 48 1-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1D0D: HOP 476 SOOP Flare- Thick-Be 2s cadence, Med-Be context (384x384)

Term	Pointing (x, y)	Comment
02/27 11:15:00 - 02/27 18:42:30	Fixed (-21.0, -959.0)	# OP start + 10min + HOP206(S-Pole)
02/27 19:28:00 - 02/28 03:59:54	Track (504.1, 366.2) @ 02/27 19:25:00	AR13590 obs.
02/28 04:31:00 - 02/28 05:39:00	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 16.5 min
02/28 06:29:30 - 02/28 07:19:30	Track (580.1, 358.7) @ 02/28 06:26:30	AR13590 obs.
02/28 07:47:30 - 02/28 17:39:00	Track (580.1, 358.7) @ 02/28 06:26:30	AR13590 obs.
02/28 18:16:00 - 02/29 03:59:54	Track (653.9, 349.6) @ 02/28 18:13:00	AR13590 obs.
02/29 04:03:00 - 02/29 05:47:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -12.5 min
02/29 06:00:30 - 02/29 10:52:00	Track (719.0, 339.6) @ 02/29 05:57:30	AR13590 obs.
PROG= 17 30-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 87 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Subr= 2 120-time(s) 2.0sec		
Seqn= 7 1-time(s) 2.0sec		
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) DPCM 3 0 2.0sec
Seqn= 2 10-time(s) 2.0sec		
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) DPCM 3 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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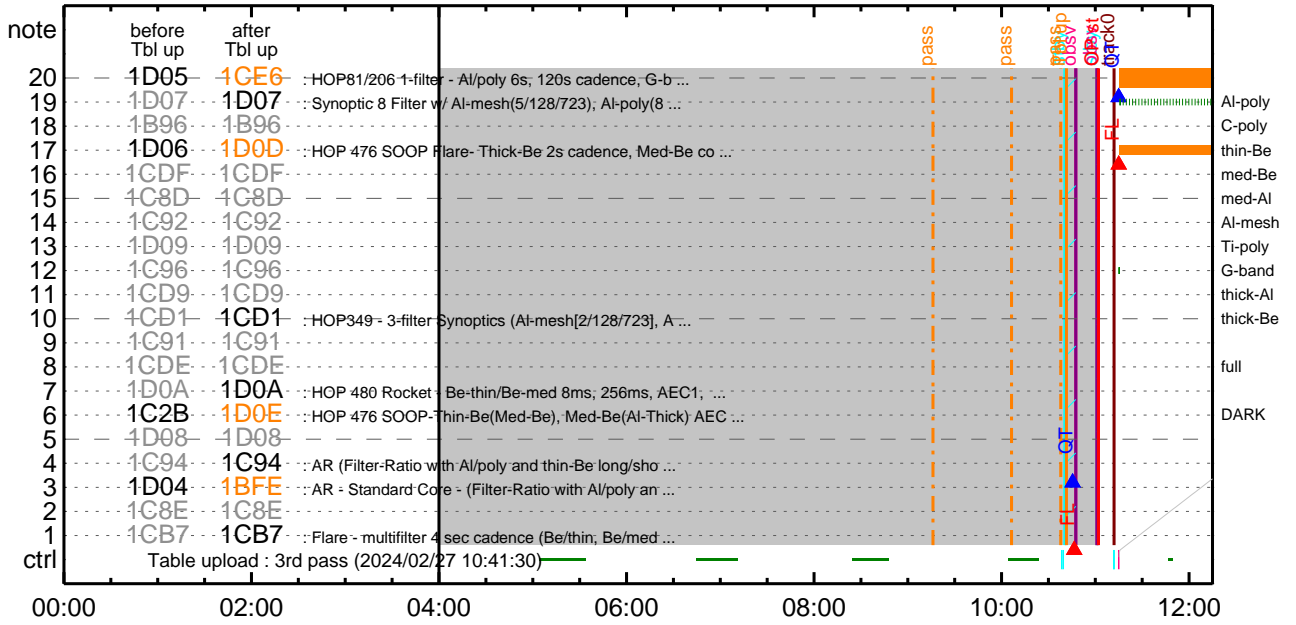
Flare Detection

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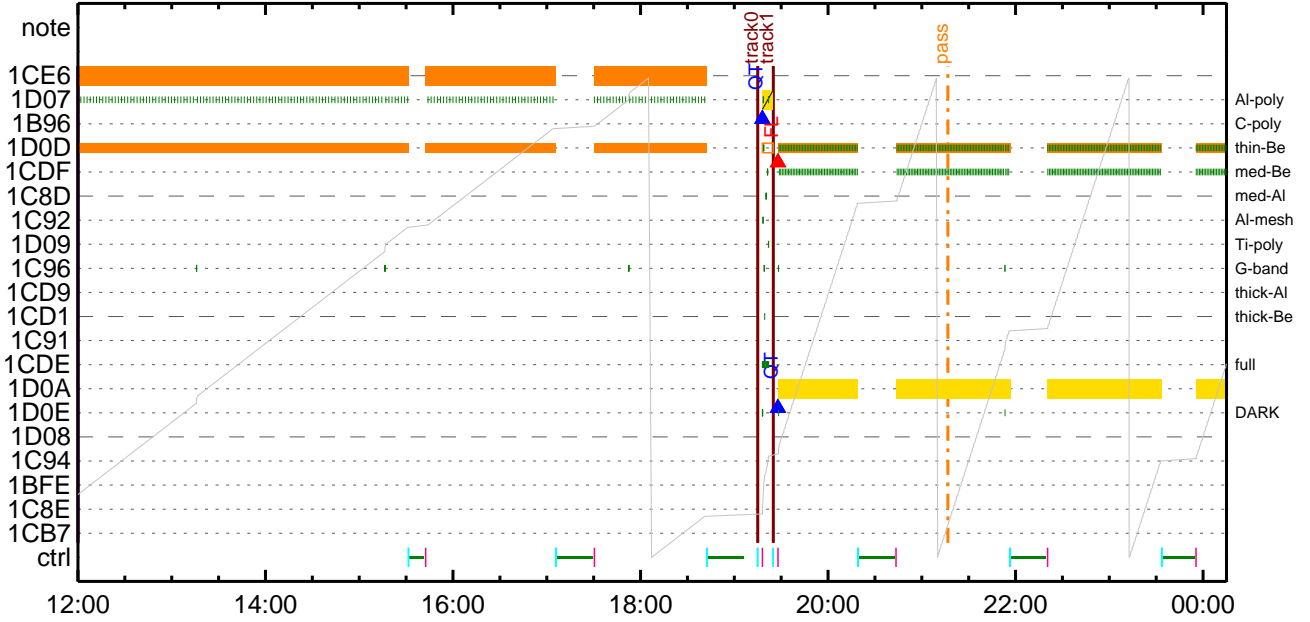
FLD Patrol

Term	Pointing (x, y)	Comment
02/27 10:42:30 - 02/27 19:15:18	cannot be identified	
02/27 19:25:18 - 02/28 06:06:48	Track (504.1, 366.2) @ 02/27 19:25:00	AR13590 obs.
02/28 06:26:48 - 02/28 18:03:18	Track (580.1, 358.7) @ 02/28 06:26:30	AR13590 obs.
02/28 18:13:18 - 02/29 05:47:48	Track (653.9, 349.6) @ 02/28 18:13:00	AR13590 obs.
02/29 05:57:48 - 03/02 11:08:00	Track (719.0, 339.6) @ 02/29 05:57:30	AR13590 obs.
Al-poly/Open	Al-poly/Open close	Safe Norm 4ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

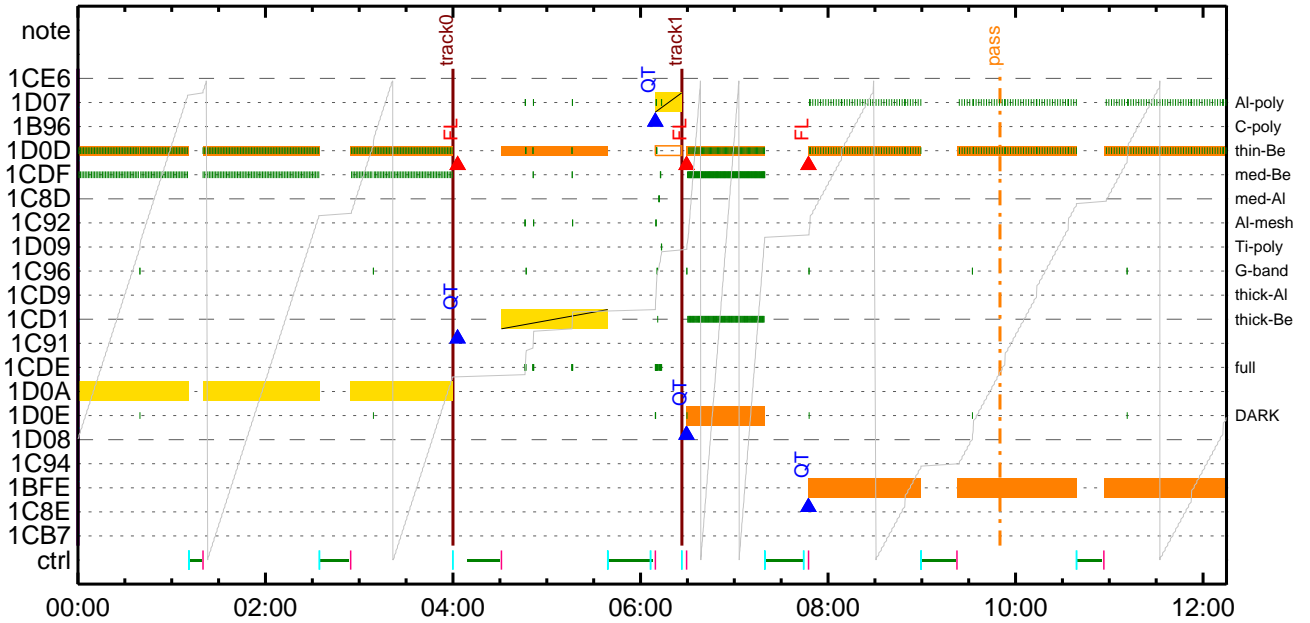
CMDI #0645 2024/02/27



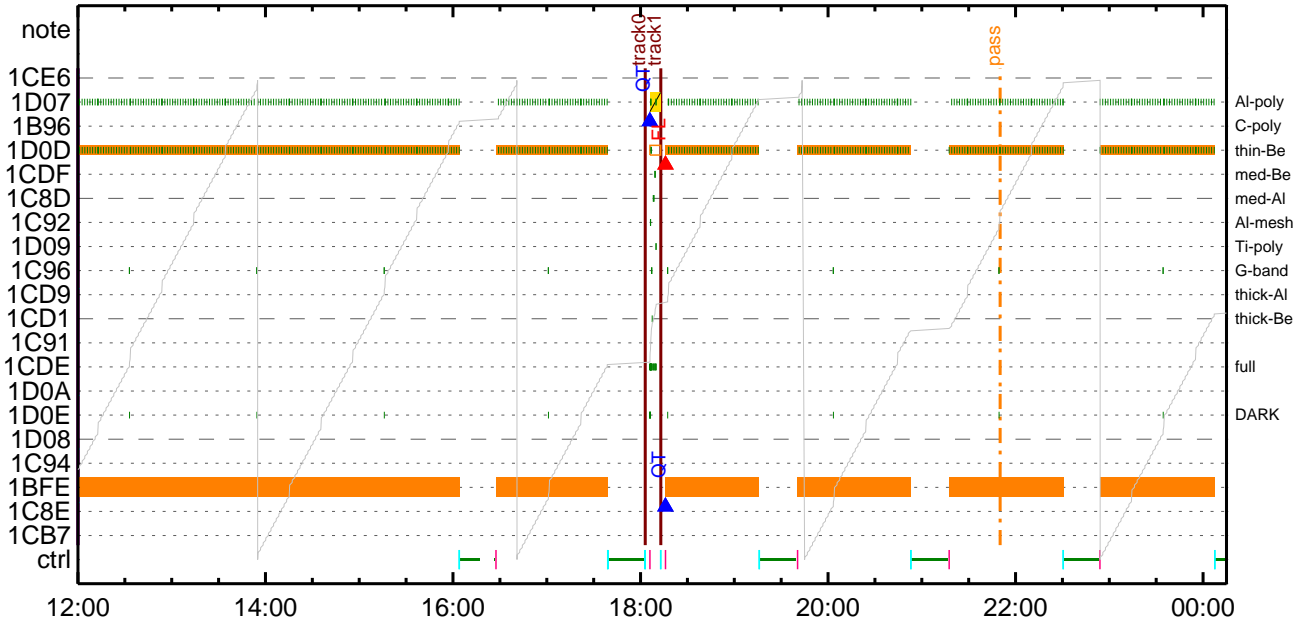
CMDI #0645 2024/02/27



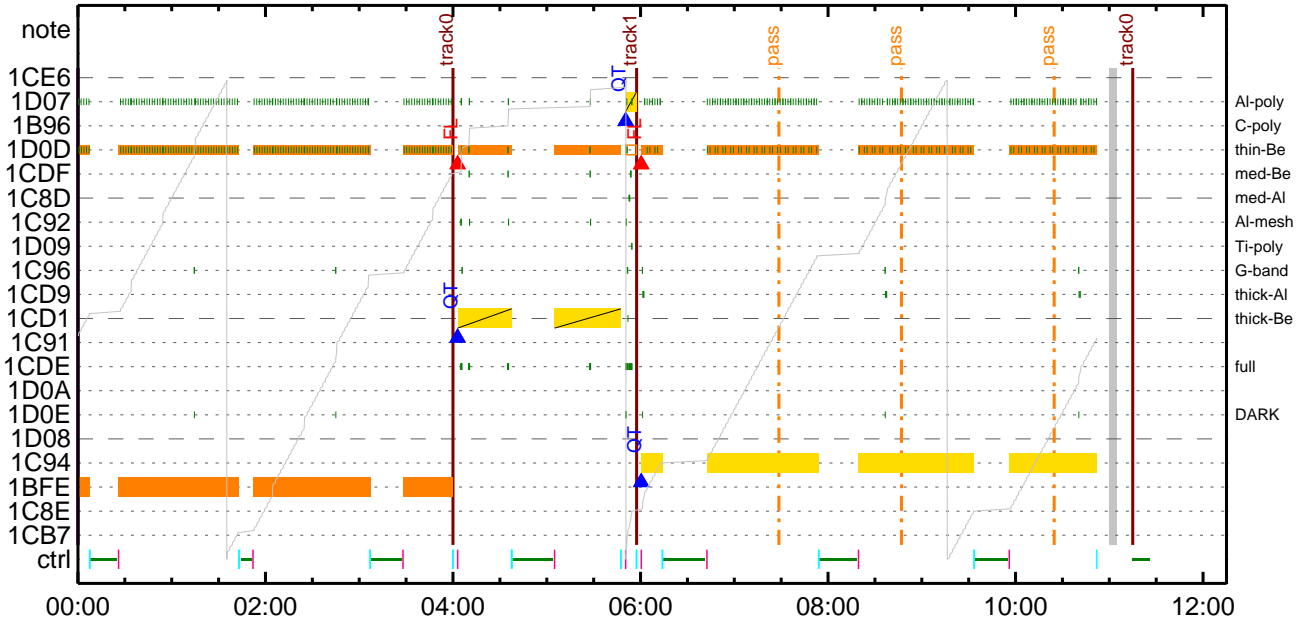
CMDI #0645 2024/02/28



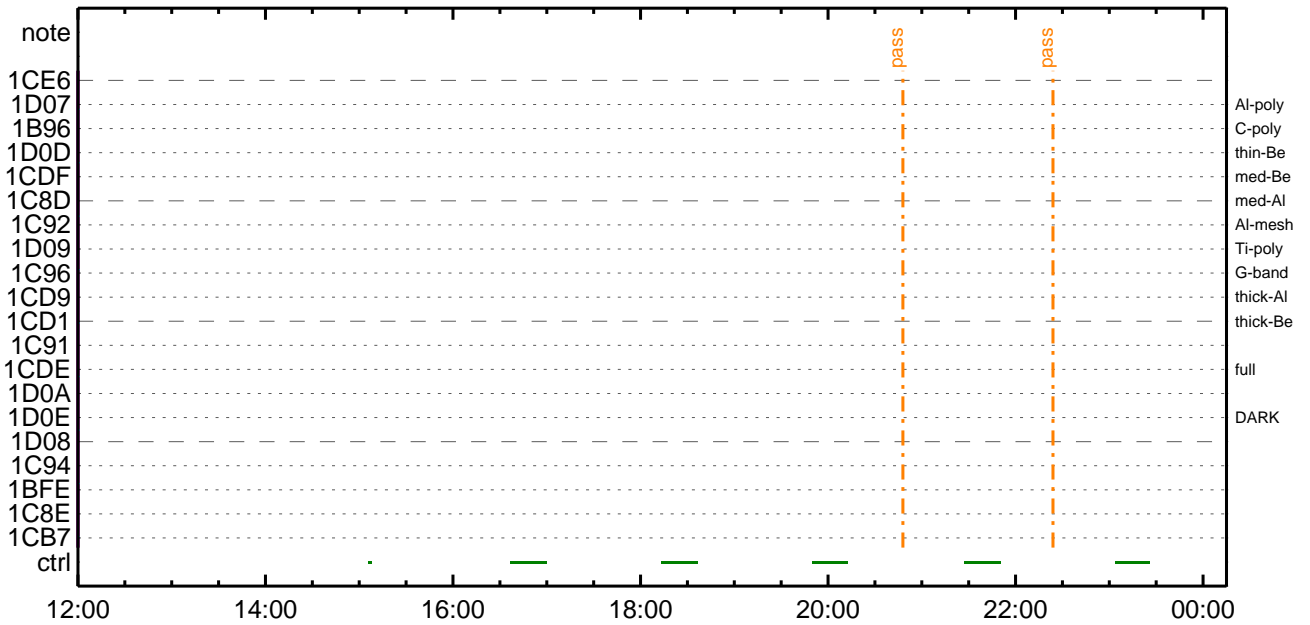
CMDI #0645 2024/02/28



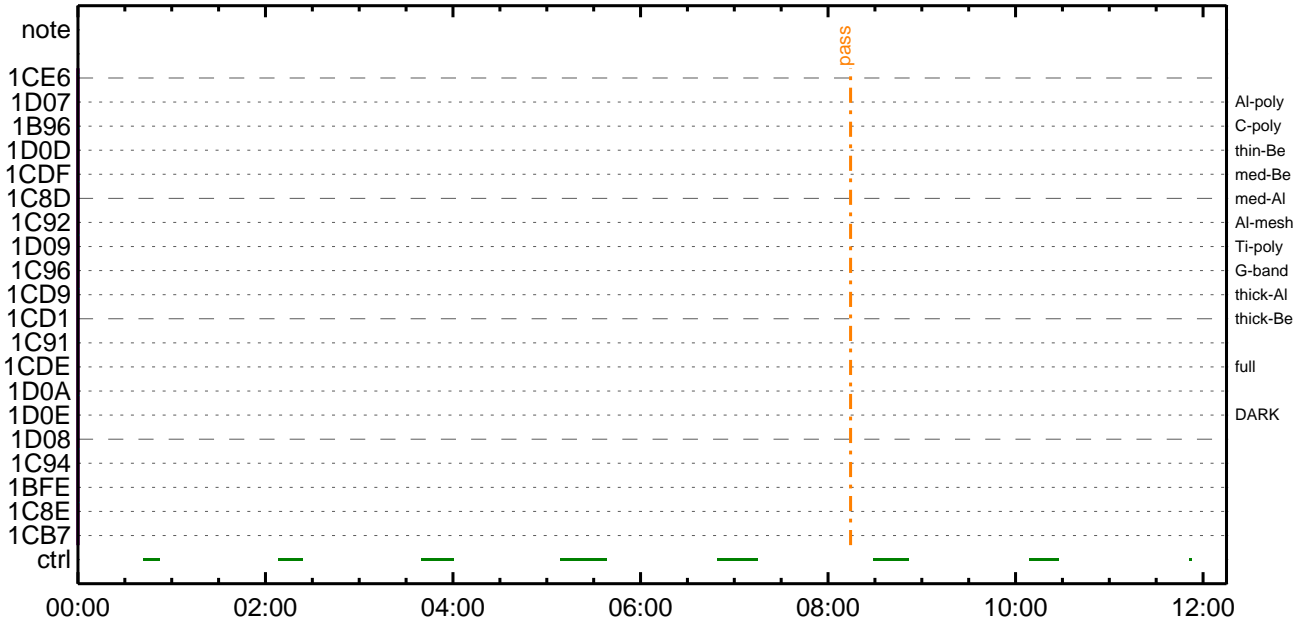
CMDI #0645 2024/02/29



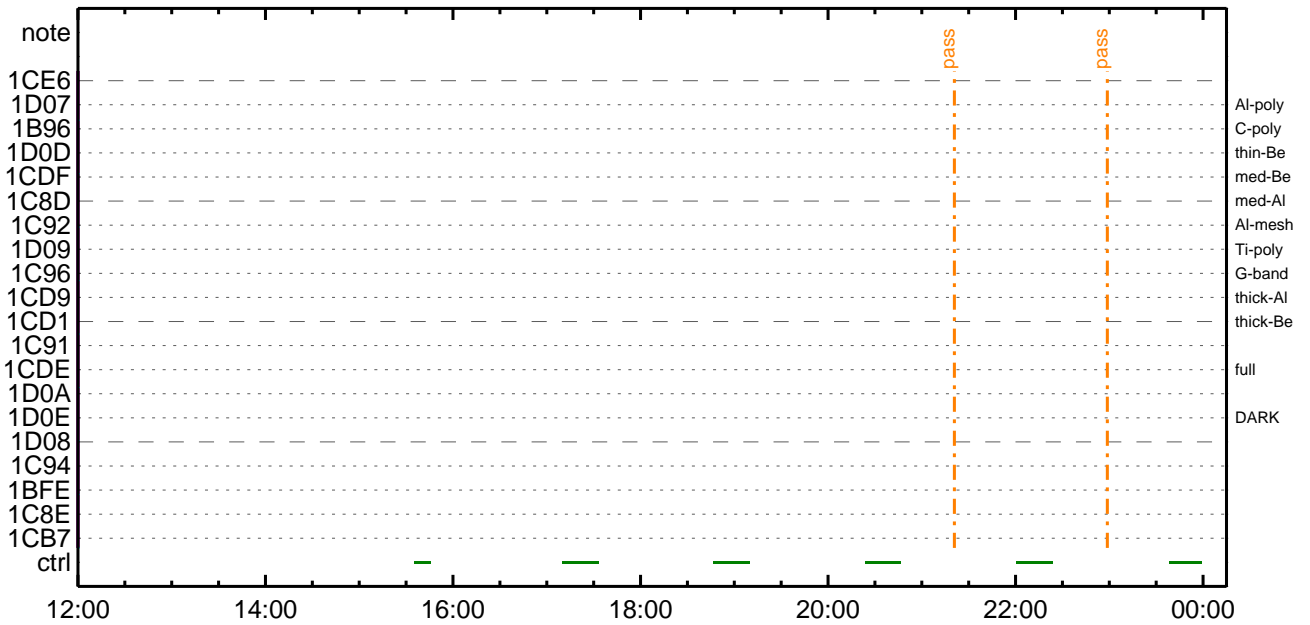
CMDI #0645 2024/02/29



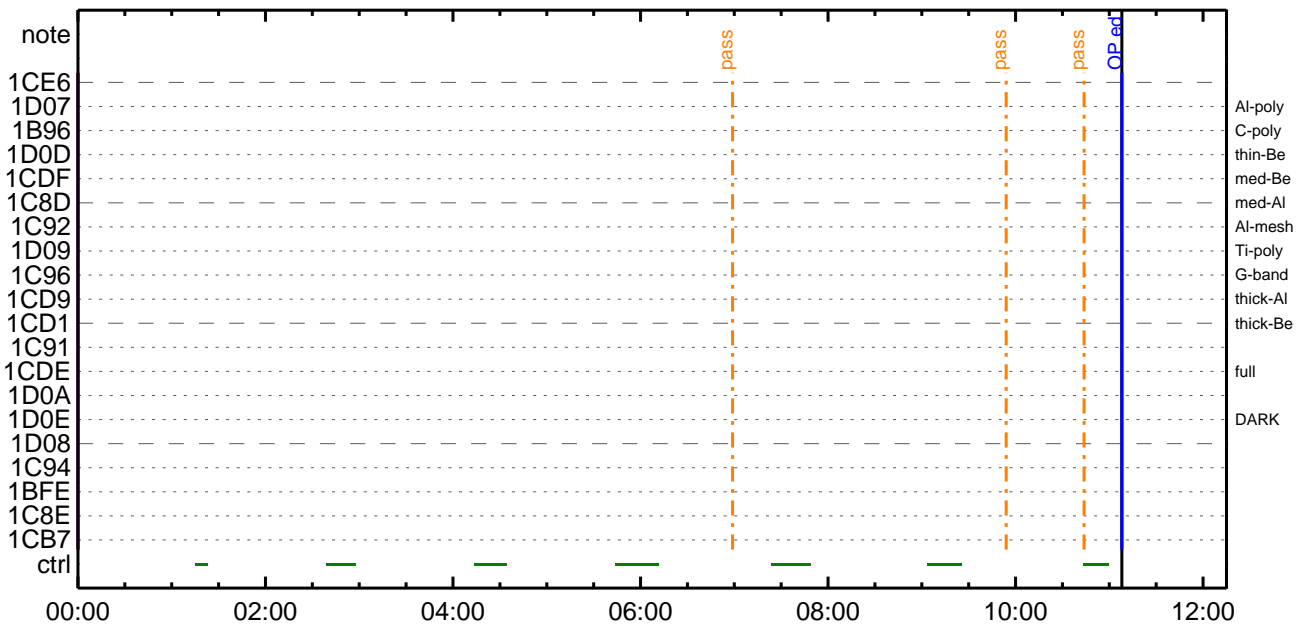
CMDI #0645 2024/03/01

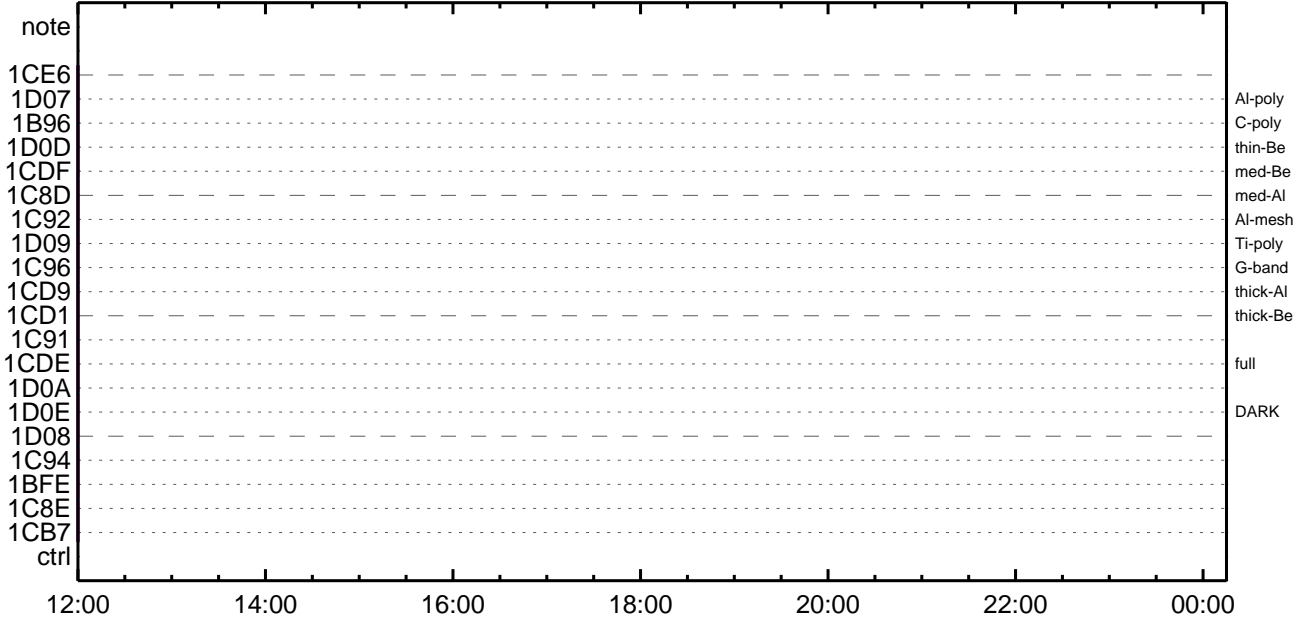


CMDI #0645 2024/03/01



CMDI #0645 2024/03/02





0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-406:OP
0104 ()
0105 . S. OG og-406:OG
0106 ()
0107 C.
0108 . C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²î½E¹ç.ë²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²î½E¹ç.ë²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG,RAM ID=OP²î½E¹ç.ë²îOKò³îÇ§
0165 C.
0166 . C. ***** °E²¼òî½Ã´¶Á°òEÉ-ò°Á÷¿@ (¼âµ-YAYôYx½ê½çòðÁÓÆòÇ¼ª°²òE¼i¹çòÇòâ) *****
0167 C. DHUYâ;½YE;E½Y½, Yî;½YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½i¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ô².²E²ò²³²E;f
0180 C. ²²²;çSET²E²DUMP²î½±²î½Y¹²ç¹Ô²|²³²E;f
0181 C.
0182 . C. TIY³Y²YôYÉ²òðÁDî¿(UT)
0183 +. TI 2024-02-27 10:57:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2024-02-27 10:57:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2024-02-27 10:57:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2024-02-27 11:01:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îíñαîŷÄŷ§ŷÄŷ-¹àîŰ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]       EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]         EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷÈ¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼.ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]         EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2024-02-27 11:01:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2024-02-27 11:01:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]       EQ          2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2024-02-27 11:01:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]       EQ          1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîαî»ò¼ŷαÈÄα¹αèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷÈŷâŷçŷèè²¼αα¼Ä»Űα¹αè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îñαÈ´Øα¹αèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. îãLOSŷÄŷŷÄŷŷÄŷ-¼Ä»Ű;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-408 2024-02-27 14:27:59 104 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ÁÍ;È□¿□Á□•µ°È×Í×ÁÇ□ÍYÇYÁY×YÍ;¼YÉ;ÈÈèµ•íÉ;È□È¼°ÇÖ□•□¿¼í¹Ç□Í;ÇÁ®, ù□¹□è□□□ÇÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 + DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ( )
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 85 83 06 06)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 80 80 20 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 80 80 08 20)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0b 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0c 80 80 08 08)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0f 80 80 06 06)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 10 80 80 08 08)
0062 + DC 07-F0 MDP_XRT_FLD_ENA
0063 BC (d8)
0064 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0065 BC (c8)
0066 + DC 07-F0 MDP_XRT_ARS_DIS
0067 BC (d5)
0068 + DC 07-F0 MDP_XRT_AEC_RESET
0069 BC (d0)
0070 + DC 07-F0 MDP_XRT_FLD_RESET
0071 BC (da)
0072 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0073 BC (c4 04)
0074 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0075 BC (c5 01)
0076 . C. ----- Success Verify ? OK / NG ____
0077 C.
0078 C.
0079 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0080 C.
0081 +. DC 07-F0 MDP_XRT_MODE_OBSV
0082 BC (c2)
0083 +. TI 2024-02-27 11:01:02.0
0084 DC 07-F0 MDP_XRT_MODE_OBSV
0085 BC (c2)
0086 . C. ----- Success Verify ? OK / NG ____
0087 C.
0088 C. ***** XRT END *****
0089 C.
0090 . C. ***** MDP `úÁÍ□Í»ö¼Y□ÈÁ□¹□èDCBC•x²è *****
0091 C. (¼á°íYÓYÁYÈY¥YÉYáYÇYèE¼□□¼Á»Û¹□è)
0092 . S. DC-BC dcbc-402:DCBC
0093 (MDP_known_event)
0094 C.
0095 C.
```

```
0096 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øσ¹σēDCBC•x²è *****
0097 . S. DC-BC dcbc-153:DCBC
0098 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0099 C.
0100 C.
0101 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0102 C.
0103 . C. ***** LOS *****
0104 C.
```

*** OP Sequence for XRT ***

2024/02/27	11:11:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	11:11:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	11:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2024/02/27	11:12:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	55	3f	01	db
2024/02/27	11:12:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/02/27	11:12:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/02/27	11:12:22.5	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/02/27	11:12:24.5	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/02/27	11:12:26.5	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/02/27	11:14:56.5	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2024/02/27	11:14:58.5	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	11			
2024/02/27	11:15:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/02/27	15:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	15:31:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	15:31:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/02/27	15:31:36.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/02/27	15:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/02/27	15:41:30.0	XRT_Custom_430_OG [0x1ae]							
2024/02/27	15:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/02/27	17:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	17:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	17:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/02/27	17:06:06.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/02/27	17:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/02/27	17:29:30.0	XRT_Custom_430_OG [0x1ae]							
2024/02/27	17:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/02/27	18:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	18:42:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	18:42:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/02/27	18:42:36.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/02/27	18:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/02/27	19:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	19:14:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	19:14:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2024/02/27	19:15:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2024/02/27	19:15:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/02/27	19:15:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/02/27	19:15:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/02/27	19:17:58.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2024/02/27	19:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/02/27	19:24:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	19:24:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/27	19:24:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2024/02/27	19:25:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	03	74	01	db
2024/02/27	19:25:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/02/27	19:25:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/02/27	19:25:22.0	XRT_AEC_RESET_448_OG [0x1c0]							

2024/02/27	19:25:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2024/02/27	19:25:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/02/27	19:27:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2024/02/27	19:27:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 11
2024/02/27	19:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/02/27	20:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/27	20:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/27	20:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/02/27	20:19:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/02/27	20:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/02/27	20:42:30.0	XRT_Custom_430_OG [0x1ae]				
2024/02/27	20:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/02/27	21:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/27	21:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/27	21:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/02/27	21:56:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/02/27	21:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/02/27	22:19:30.0	XRT_Custom_430_OG [0x1ae]				
2024/02/27	22:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/02/27	23:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/27	23:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/27	23:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/02/27	23:33:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/02/27	23:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/02/27	23:54:30.0	XRT_Custom_430_OG [0x1ae]				
2024/02/27	23:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/02/28	01:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/28	01:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/28	01:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/02/28	01:11:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/02/28	01:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/02/28	01:19:00.0	XRT_Custom_430_OG [0x1ae]				
2024/02/28	01:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/02/28	02:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/28	02:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/28	02:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/02/28	02:34:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/02/28	02:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/02/28	02:53:30.0	XRT_Custom_430_OG [0x1ae]				
2024/02/28	02:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/02/28	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/28	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/02/28	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/02/28	04:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2024/02/28	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2024/02/28	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/02/28	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2024/02/28	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2024/02/28	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]				

2024/02/28	04:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/02/28	04:02:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
2024/02/28	04:30:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	11	
2024/02/28	04:31:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/02/28	05:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	05:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	05:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/02/28	05:39:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/02/28	05:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/02/28	06:06:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	06:06:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	06:06:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2024/02/28	06:06:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2024/02/28	06:06:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2024/02/28	06:06:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/02/28	06:09:28.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13	
2024/02/28	06:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/02/28	06:26:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	06:26:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	06:26:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2024/02/28	06:26:30.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 03 74 01	db	
2024/02/28	06:26:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/02/28	06:26:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/02/28	06:26:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/02/28	06:26:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/02/28	06:26:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/02/28	06:29:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06	
2024/02/28	06:29:28.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	11	
2024/02/28	06:29:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/02/28	07:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	07:19:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	07:19:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/02/28	07:19:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/02/28	07:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/02/28	07:44:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	07:44:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	07:44:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2024/02/28	07:44:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/02/28	07:44:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/02/28	07:44:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/02/28	07:44:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/02/28	07:44:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/02/28	07:47:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2024/02/28	07:47:28.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	11	
2024/02/28	07:47:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/02/28	08:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	08:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

2024/02/28	08:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/02/28	08:59:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/02/28	09:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/02/28	09:21:30.0	XRT_Custom_430_OG [0x1ae]						
2024/02/28	09:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	10:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	10:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	10:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/02/28	10:39:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/02/28	10:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/02/28	10:55:30.0	XRT_Custom_430_OG [0x1ae]						
2024/02/28	10:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	16:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	16:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	16:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/02/28	16:04:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/02/28	16:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/02/28	16:26:30.0	XRT_Custom_430_OG [0x1ae]						
2024/02/28	16:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	17:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	17:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	17:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/02/28	17:39:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/02/28	17:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/02/28	18:02:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	18:02:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	18:02:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/02/28	18:03:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2024/02/28	18:03:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2024/02/28	18:03:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2024/02/28	18:03:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/02/28	18:05:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2024/02/28	18:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/02/28	18:12:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	18:12:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	18:12:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2024/02/28	18:13:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 03 74 01 db		
2024/02/28	18:13:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/02/28	18:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/02/28	18:13:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/02/28	18:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/02/28	18:13:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/02/28	18:15:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2024/02/28	18:15:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 11		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/02/28	18:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	19:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	19:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/02/28	19:16:04.0	XRT_FLD_RESET_415_OG [0x19f]						

2024/02/28	19:16:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/02/28	19:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/02/28	19:39:30.0	XRT_Custom_430_OG [0x1ae]					
2024/02/28	19:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/02/28	20:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/28	20:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/28	20:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/02/28	20:53:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/02/28	20:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/02/28	21:16:30.0	XRT_Custom_430_OG [0x1ae]					
2024/02/28	21:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/02/28	22:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/28	22:30:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/28	22:30:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/02/28	22:30:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/02/28	22:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/02/28	22:53:00.0	XRT_Custom_430_OG [0x1ae]					
2024/02/28	22:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/02/29	00:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/29	00:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/29	00:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/02/29	00:07:36.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/02/29	00:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/02/29	00:25:00.0	XRT_Custom_430_OG [0x1ae]					
2024/02/29	00:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/02/29	01:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/29	01:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/29	01:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/02/29	01:43:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/02/29	01:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/02/29	01:51:00.0	XRT_Custom_430_OG [0x1ae]					
2024/02/29	01:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/02/29	03:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/29	03:07:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/29	03:07:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/02/29	03:07:06.0	XRT_PREFLR_STRT_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/02/29	03:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/02/29	03:27:00.0	XRT_Custom_430_OG [0x1ae]					
2024/02/29	03:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/02/29	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/29	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/02/29	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2024/02/29	04:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2024/02/29	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/02/29	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/02/29	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/02/29	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/02/29	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/02/29	04:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	

2024/02/29	04:02:58.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	11			
2024/02/29	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/02/29	04:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	04:37:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	04:37:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/02/29	04:37:36.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/02/29	04:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/02/29	05:04:00.0	XRT_Custom_430_OG [0x1ae]							
2024/02/29	05:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/02/29	05:47:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	05:47:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	05:47:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2024/02/29	05:47:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/02/29	05:47:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/02/29	05:47:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/02/29	05:50:28.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2024/02/29	05:50:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/02/29	05:57:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	05:57:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	05:57:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2024/02/29	05:57:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	03	74	01	db
2024/02/29	05:57:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/02/29	05:57:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/02/29	05:57:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/02/29	05:57:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/02/29	05:57:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/02/29	06:00:26.0	XRT_QT_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2024/02/29	06:00:28.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	11			
2024/02/29	06:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/02/29	06:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	06:14:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	06:14:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/02/29	06:14:06.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/02/29	06:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/02/29	06:41:30.0	XRT_Custom_430_OG [0x1ae]							
2024/02/29	06:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/02/29	07:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	07:54:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	07:54:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/02/29	07:54:06.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/02/29	07:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/02/29	08:18:30.0	XRT_Custom_430_OG [0x1ae]							
2024/02/29	08:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/02/29	09:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	09:33:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/02/29	09:33:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/02/29	09:33:36.0	XRT_PREFLR_STRT_431_OG [0x1af]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/02/29	09:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2024/02/29	09:55:00.0	XRT_Custom_430_OG [0x1ae]		
2024/02/29	09:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2024/02/29	10:52:00.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2024/02/29	11:15:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00