

XRT Timeline to be uploaded on 2024/03/26

Period: 2024/03/26 11:09:00 - 2024/03/30 11:14:00

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Normal mode

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XOB #1D03: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant -AI/mesh(2048ms) - 1x1, AI/Poly(1443ms) - 2x2 - w leak image-1msC													
Term		Pointing (x, y)					Comment						
03/27 12:03:00 - 03/27 12:09:54		Fixed (-528.4, -528.4)					Post bakeout Q1						
PROG= 20 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 51 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 120.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/AI-mesh	Open/thick-AI	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	AI-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 34 1-time(s) 60.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D04: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 2nd Quadrant -AI/mesh(2048ms) - 1x1, AI/Poly(1443ms) - 2x2 - w leak image-1msC													
Term		Pointing (x, y)					Comment						
03/27 12:13:00 - 03/27 12:19:54		Fixed (528.4, -528.4)					Post bakeout Q2						
PROG= 12 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 38 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 120.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/AI-mesh	Open/thick-AI	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	AI-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 34 1-time(s) 60.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D05: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 3rd Quadrant -AI/mesh(2048ms) - 1x1, AI/Poly(1443ms) - 2x2 - w leak image-1msC													
Term		Pointing (x, y)					Comment						
03/27 12:23:00 - 03/27 12:29:54		Fixed (528.4, 528.4)					Post bakeout Q3						
PROG= 17 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 21 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 120.0sec													
└─ Seqn= 93 2-time(s) 2.0sec													
	Open/AI-mesh	Open/thick-AI	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	AI-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 34 1-time(s) 60.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D06: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 4th Quadrant -AI/mesh(2048ms) - 1x1, AI/Poly(1443ms) - 2x2 - w leak image-1msC													
Term		Pointing (x, y)					Comment						
03/27 12:33:00 - 03/27 12:39:54		Fixed (-528.4, 528.4)					Post bakeout Q4						
PROG= 03 1-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 14 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 120.0sec													

XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w

Term	Pointing (x, y)	Comment
03/28 04:03:00 - 03/28 06:24:00	Fixed (0.0, 0.0)	synoptic, shifted 24.5 min + HOP 349
PROG= 07 Inf-time(s)		
Subr= 1 1-time(s) 300.0sec		
Seqn= 55 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 15 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 79 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=95 0 0 2.0sec
Subr= 2 15-time(s) 1500.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 74 1-time(s) 2.0sec		
med-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 2.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 29 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
03/27 12:53:00 - 03/27 17:52:54	Track (664.4, -166.8) ^{Ⓜ 03/27 12:50:00}	AR 13615
03/27 18:06:05 - 03/28 03:59:54	Track (695.8, -171.0) ^{Ⓜ 03/27 18:03:00}	AR 13615
03/28 04:03:00 - 03/28 06:24:00	Fixed (0.0, 0.0)	synoptic, shifted 24.5 min + HOP 349
03/28 06:37:30 - 03/28 11:21:00	Track (763.6, -181.8) ^{Ⓜ 03/28 06:34:30}	AR 13615
PROG= 14 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 73 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 87 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

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Flare Detection

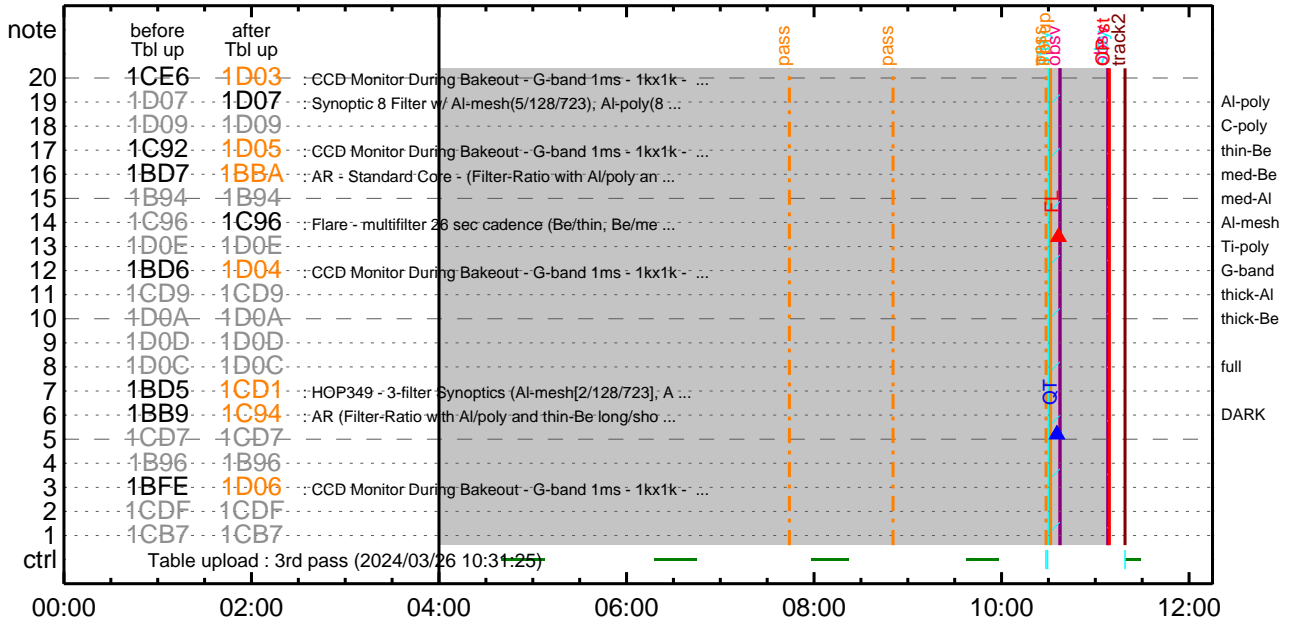
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FLD Patrol

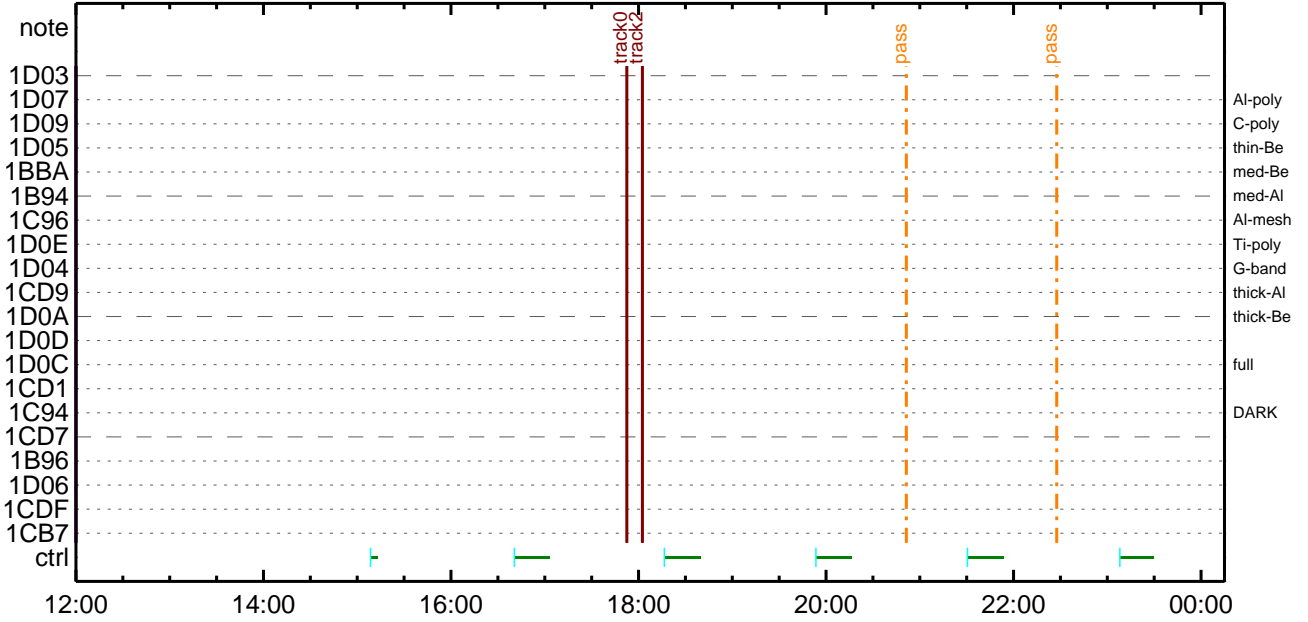
Term	Pointing (x, y)	Comment
03/26 10:32:25 - 03/27 12:02:56	cannot be identified	
03/27 12:50:18 - 03/27 17:53:18	Track (664.4, -166.8) ^{Ⓜ 03/27 12:50:00}	AR 13615
03/27 18:03:18 - 03/28 06:24:24	Track (695.8, -171.0) ^{Ⓜ 03/27 18:03:00}	AR 13615
03/28 06:34:48 - 03/30 11:14:00	Track (763.6, -181.8) ^{Ⓜ 03/28 06:34:30}	AR 13615

Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

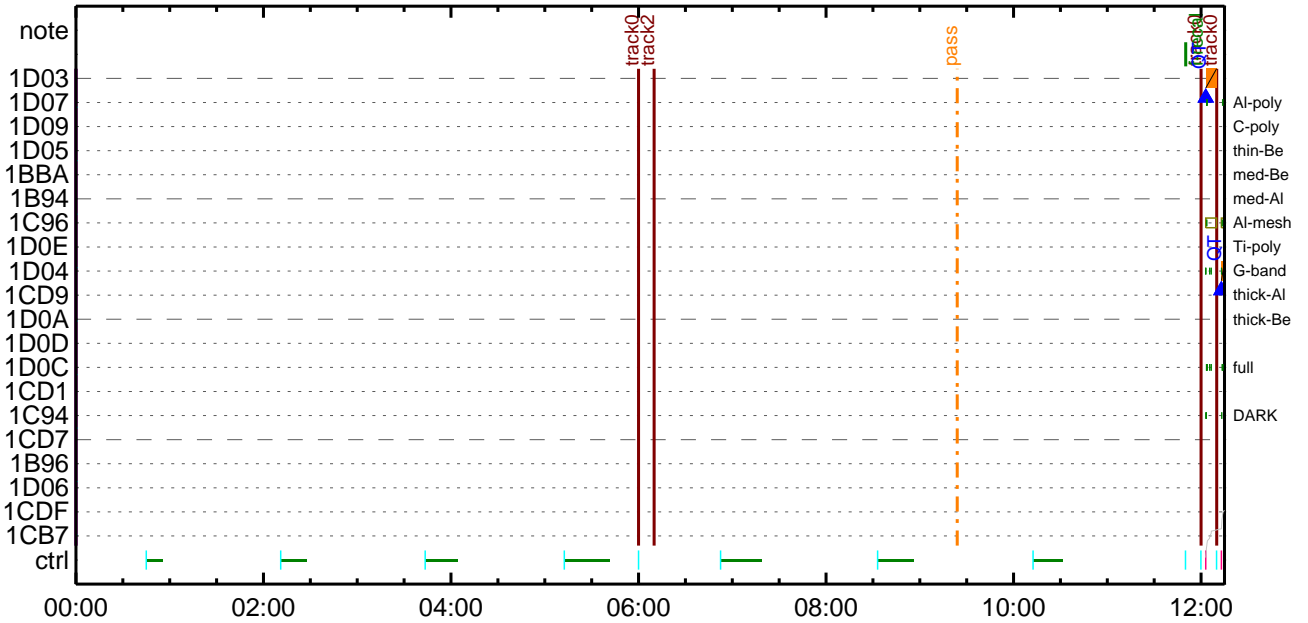
CMDI #0707 2024/03/26



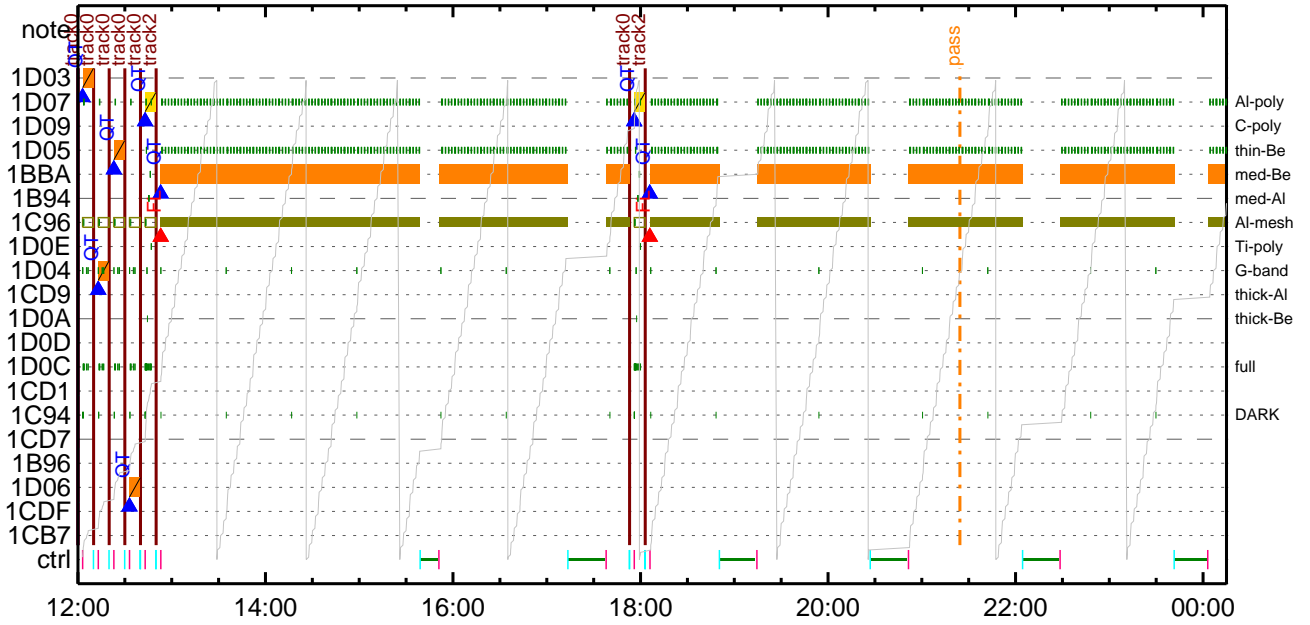
CMDI #0707 2024/03/26



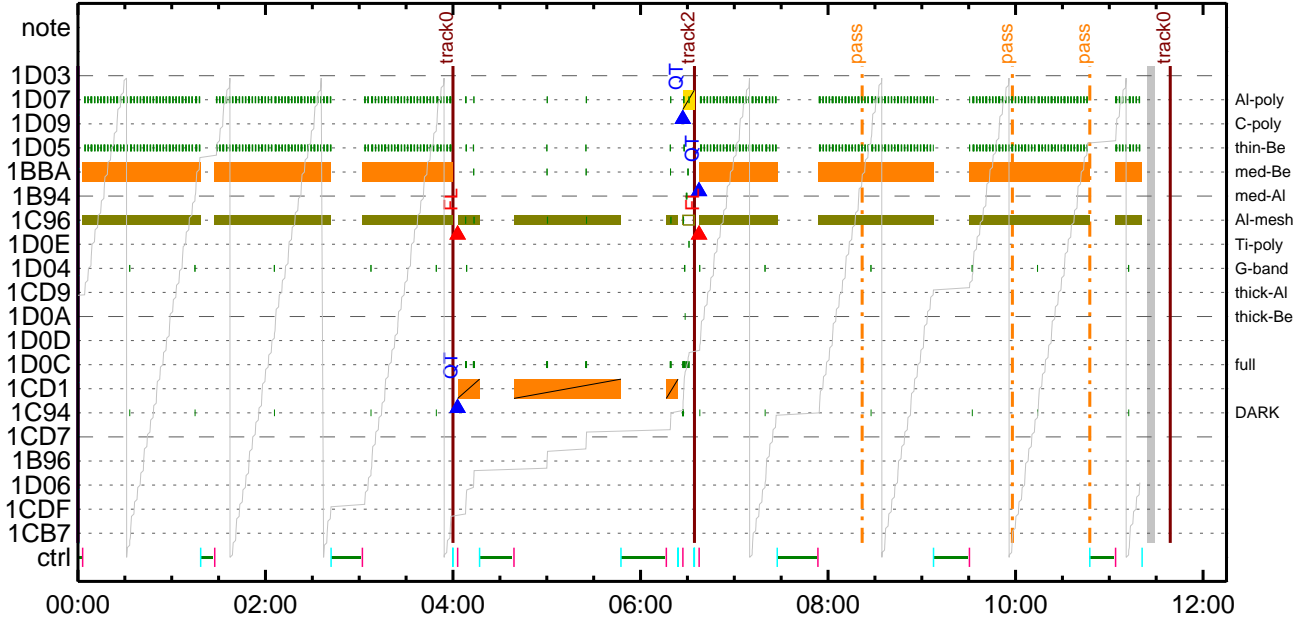
CMDI #0707 2024/03/27



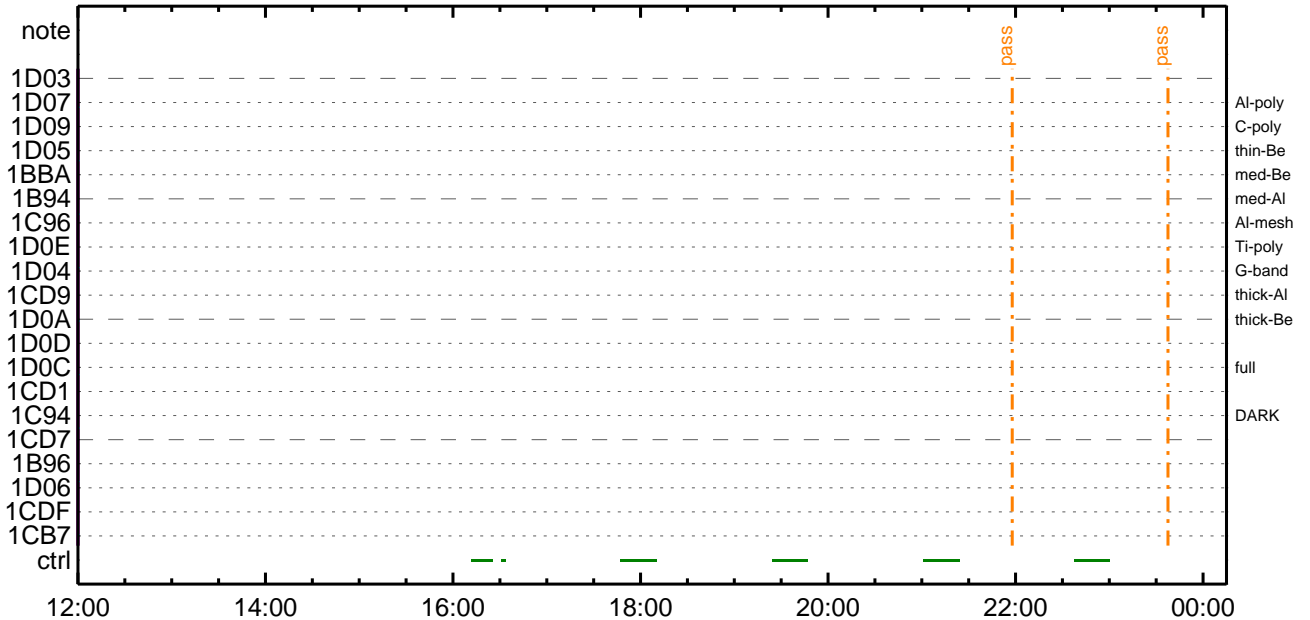
CMDI #0707 2024/03/27



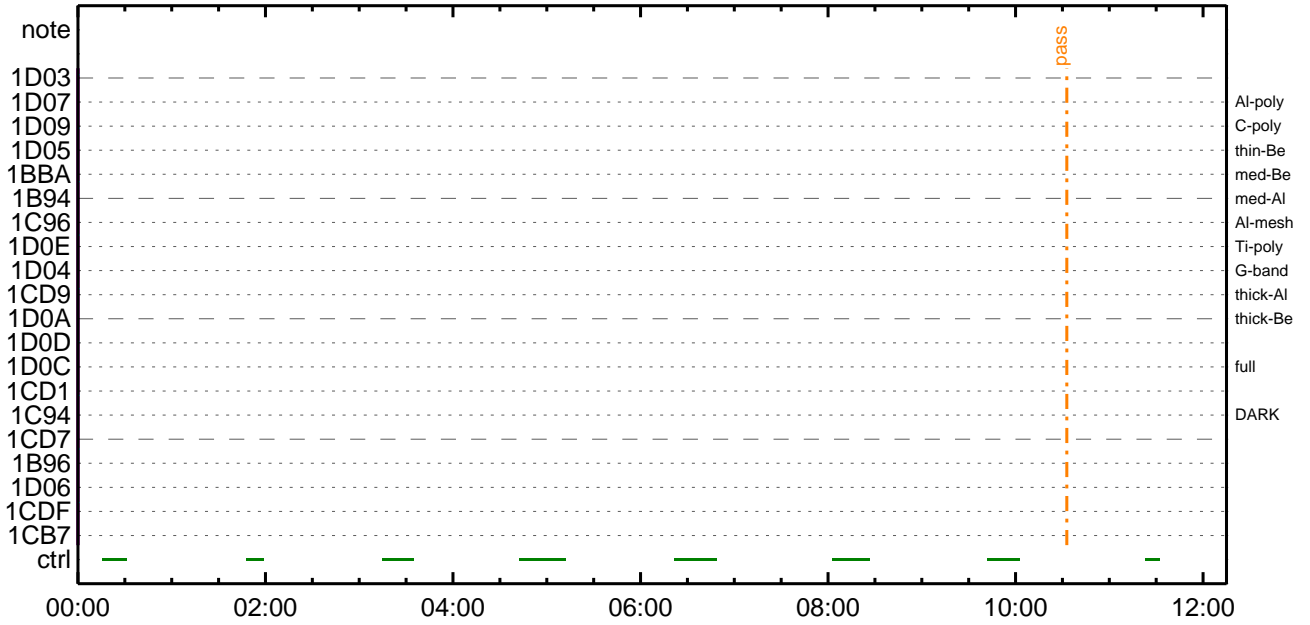
CMDI #0707 2024/03/28



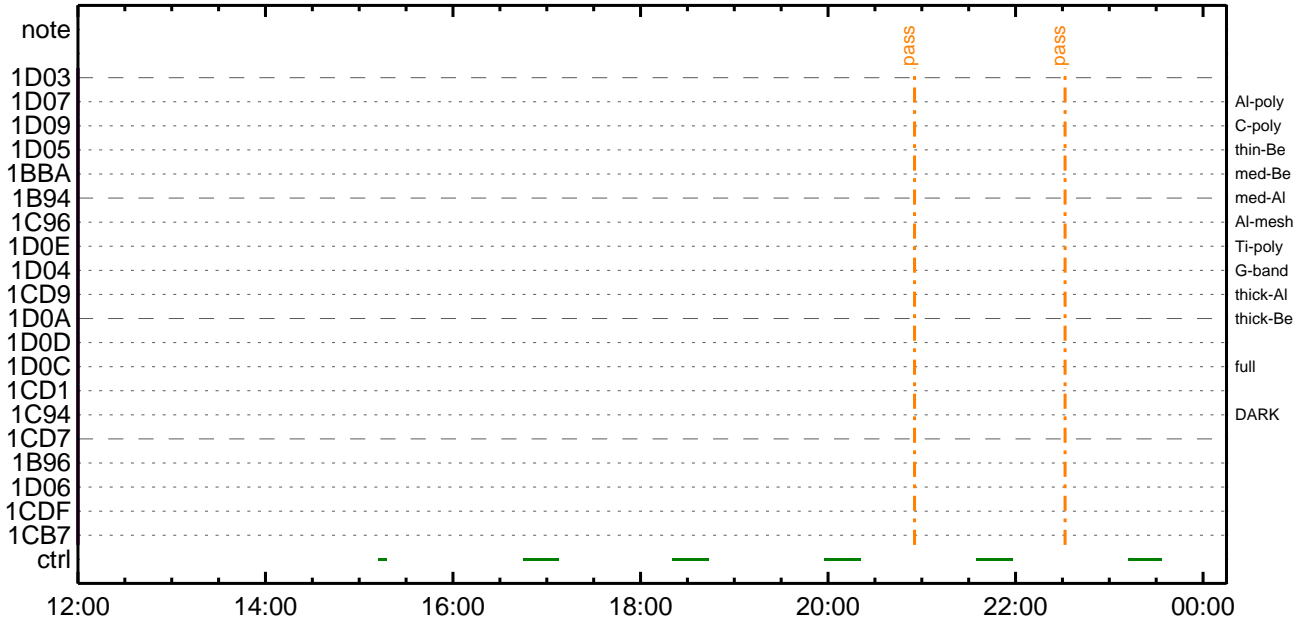
CMDI #0707 2024/03/28



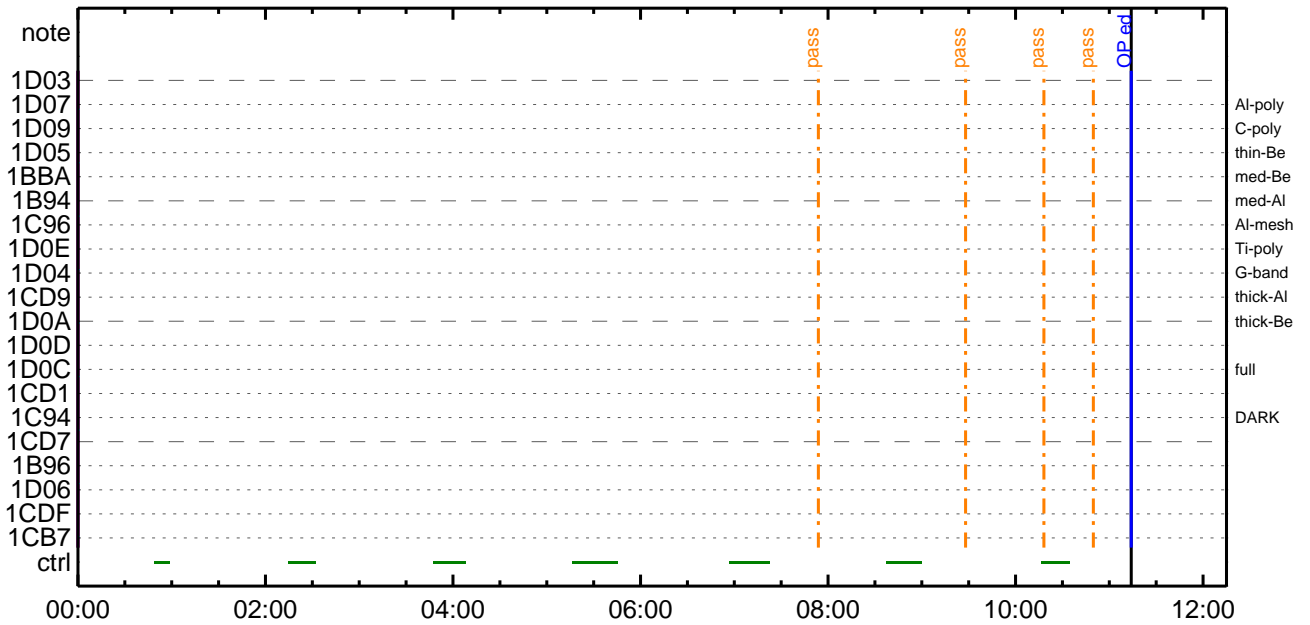
CMDI #0707 2024/03/29



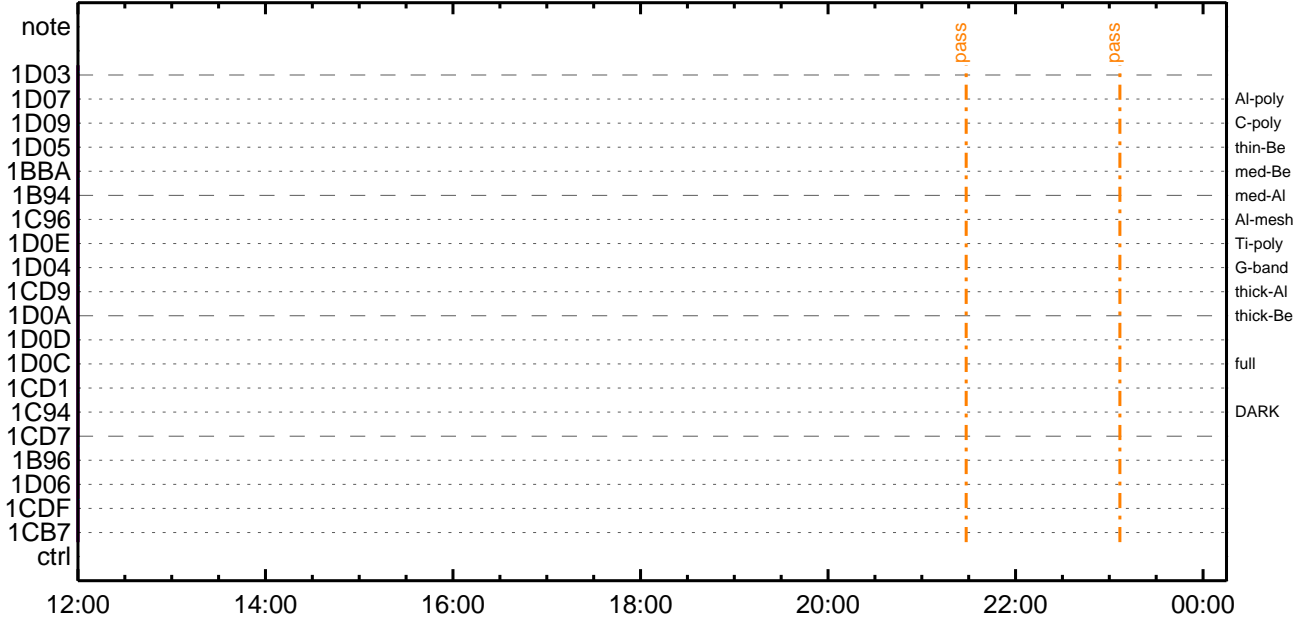
CMDI #0707 2024/03/29



CMDI #0707 2024/03/30



CMDI #0707 2024/03/30



(a) Spacecraft Operation Procedure (real-commands)

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main-462 2024-03-26 12:07:30 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿Áâ•µ°Æ»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈèÈ¼°ÇÓâ•µ¿¼l¹ç¿í;çÀ®, ù¹âèâ¿çÁ+¿®â•µÈâââ³âÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-462:OP
0020 ( )
0021 . S. OG og-462:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPÍ°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYÖY×¼ªª î»ðð³ îÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGâ¼È¹ç•è² ÌOKðð³ îÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYÖY×¼ªª î»ðð³ îÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGâ¼È¹ç•è² ÌOKðð³ îÇ§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYÖY×¼ªª î»ðð³ îÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPâ¼È¹ç•è² ÌOKðð³ îÇ§
0081 C.
0082 . C. ***** øÈ²¼ðí¼Á´ ¶¼°èÈÈ-ð°Á+¿® (¼áµ-YÁYÖY×¼è¼çððÁÓÃæç¼ªª ð°¼¼ì¹çççâ) *****
0083 C. DHUYâ;¼YÉ;ÈY¼; Yí;¼YÉ;Èâðíá¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOADâ-Á+¿®NGâ¼È¹ç; ç°È²¼ðí¼TI-CMDÁ+¿®ðí¼Á¹Ôâ•µÈâââ³âÈ;f
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0096 C.                0p0z;çSET0EDUMP0İÆ±°iŸÑŸ¹0Ç¹00|0³0E;E
0097 C.
0098 . C. TIŸ³ŸPŸ6ŸE00d0Dİç(UT)
0099 +. TI 2024-03-26 11:04:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2024-03-26 11:04:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2024-03-26 11:04:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2024-03-26 11:08:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                çç[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. °E²¼0İÄè%îÍÑ0İŸÁŸŸŸÄŸ-¹àİÜ
0116 C.                çç[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                çç[HK1_TI_CMD_NUM]                EQ        4
0118 C.                çç[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                çç[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èŸÄŸ6Ÿx
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                çç[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                çç[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                çç[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                çç[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                çç[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                çç[HK1_PKT_FORM_NO]                EQ        7
0136 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                çç[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 . C. ŸÄŸ6Ÿx½ªİ»0d³İÇŸ
0142 C.                çç[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İŸE¹ç•è²İOK0d³İÇŸ
0145 C.
0146 . C. DHUŸâ;¼ŸÉ;È¼Ÿ¼,Ÿi;¼ŸÈ;È0dİã0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                çç[HK1_PKT_FORM_NO]                EQ        2
0150 C.                çç[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                çç[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                çç[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2024-03-26 11:08:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2024-03-26 11:08:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2024-03-26 11:08:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 . C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´úÄî0İ»ö¼Ÿ0ÈÄ00¹0èDCBC•x²è *****
0181 C. (¼á°İŸÖŸÄŸEŸPŸEŸŸŸÄŸçŸè0È¼00¼Ä»Ü0¹0è)
0182 . S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 . C. ***** ŸDŸ¹•İ Daily±çİÑ0È'00¹0èDCBC•x²è *****
0187 . S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 . C. ;ãLOSŸÄŸŸŸÄŸ-¼Ä»Ü;ä
0192 C.
0193 . C. ***** LOS *****

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ( )
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 c0 c0 10 10)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 80 20 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 40 c0 10 10)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 40 40 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0b c0 40 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0c 80 80 20 08)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0d 80 80 08 20)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0e 85 83 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 0f 80 80 06 06)
0148 + DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 10 80 80 08 08)
0150 + DC 07-F0 MDP_XRT_FLD_ENA
0151 BC (d8)
0152 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0153 BC (c8)
0154 + DC 07-F0 MDP_XRT_ARS_DIS
0155 BC (d5)
0156 + DC 07-F0 MDP_XRT_AEC_RESET
0157 BC (d0)
0158 + DC 07-F0 MDP_XRT_FLD_RESET
0159 BC (da)
0160 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0161 BC (c4 06)
0162 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0163 BC (c5 0e)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C.
0167 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0168 C.
0169 +. DC 07-F0 MDP_XRT_MODE_OBSV
0170 BC (c2)
0171 +. TI 2024-03-26 11:08:02.0
0172 DC 07-F0 MDP_XRT_MODE_OBSV
0173 BC (c2)
0174 . C. ----- Success Verify ? OK / NG ____
0175 C.
0176 C. ***** XRT END *****
0177 C.
0178 . C. ***** MDP `úÃîñ»ò%ŸñÊÄð¹ñèDCBC•x²è *****
0179 C. (%ã°îŸÓŸÃŸÈŸËŸËŸáŸçŸèñÊ%¼ññ%Ã»Ûñ¹ñè)
0180 . S. DC-BC dcbc-402:DCBC
0181 (MDP_known_event)
0182 C.
0183 C.
0184 . C. ***** ŸĐŸ!•İ Daily±;îññÊ`Øñ¹ñèDCBC•x²è *****
0185 . S. DC-BC dcbc-153:DCBC
0186 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0187 C.
0188 C.
0189 . C. ;ãLOSŸÃŸŸŸÃŸŸ-¼Ã»Û;ã
0190 C.
0191 . C. ***** LOS *****
0192 C.

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*** OP Sequence for XRT ***

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2024/03/26 11:19:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                    AOCU_NM                    5 02-76 02 03 74 01 db
2024/03/26 11:19:00.5 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/26 11:19:02.5 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/26 11:19:04.5 XRT_PREFLR_STRT_433_OG [0x1b1]
                    MDP_XRT_PREFLR_STRT        1 07-F0 e8
2024/03/26 11:22:14.5 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP        1 07-F0 e9
2024/03/26 15:08:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/26 15:08:32.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/26 15:08:34.0 XRT_PREFLR_STRT_433_OG [0x1b1]
                    MDP_XRT_PREFLR_STRT        1 07-F0 e8
2024/03/26 15:11:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP        1 07-F0 e9
2024/03/26 16:40:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/26 16:40:32.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/26 16:40:34.0 XRT_PREFLR_STRT_433_OG [0x1b1]
                    MDP_XRT_PREFLR_STRT        1 07-F0 e8
2024/03/26 16:43:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP        1 07-F0 e9
2024/03/26 17:52:30.0 AOCs_OrE-point_Start_2_OG [0x098]
                    AOCU_NM                    5 02-76 00 00 00 00 00
2024/03/26 18:02:30.0 AOCs_OrE-point_Start_1_OG [0x097]
                    AOCU_NM                    5 02-76 02 03 74 01 db
2024/03/26 18:16:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/26 18:16:32.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/26 18:16:34.0 XRT_PREFLR_STRT_433_OG [0x1b1]
                    MDP_XRT_PREFLR_STRT        1 07-F0 e8
2024/03/26 18:19:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP        1 07-F0 e9
2024/03/26 19:53:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/26 19:53:32.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/26 19:53:34.0 XRT_PREFLR_STRT_433_OG [0x1b1]
                    MDP_XRT_PREFLR_STRT        1 07-F0 e8
2024/03/26 19:56:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP        1 07-F0 e9
2024/03/26 21:30:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/26 21:30:32.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/26 21:30:34.0 XRT_PREFLR_STRT_433_OG [0x1b1]
                    MDP_XRT_PREFLR_STRT        1 07-F0 e8
2024/03/26 21:33:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP        1 07-F0 e9
2024/03/26 23:08:00.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/26 23:08:02.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/26 23:08:04.0 XRT_PREFLR_STRT_433_OG [0x1b1]
                    MDP_XRT_PREFLR_STRT        1 07-F0 e8
2024/03/26 23:11:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP        1 07-F0 e9
2024/03/27 00:45:00.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/27 00:45:02.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/27 00:45:04.0 XRT_PREFLR_STRT_433_OG [0x1b1]
                    MDP_XRT_PREFLR_STRT        1 07-F0 e8
2024/03/27 00:48:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP        1 07-F0 e9
2024/03/27 02:11:00.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/27 02:11:02.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/27 02:11:04.0 XRT_PREFLR_STRT_433_OG [0x1b1]
                    MDP_XRT_PREFLR_STRT        1 07-F0 e8
2024/03/27 02:14:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP        1 07-F0 e9
2024/03/27 03:43:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/27 03:43:32.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/27 03:43:34.0 XRT_PREFLR_STRT_433_OG [0x1b1]
                    MDP_XRT_PREFLR_STRT        1 07-F0 e8
2024/03/27 03:46:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                    MDP_XRT_PREFLR_STOP        1 07-F0 e9
2024/03/27 05:12:30.0 XRT_CTRL_MANU_400_OG [0x190]
                    MDP_XRT_CTRL_MANU          1 07-F0 c1
2024/03/27 05:12:32.0 XRT_FLD_RESET_415_OG [0x19f]
                    MDP_XRT_FLD_RESET           1 07-F0 da
2024/03/27 05:12:34.0 XRT_PREFLR_STRT_433_OG [0x1b1]

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2024/03/27	05:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/03/27	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00		
2024/03/27	06:00:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/03/27	06:00:02.5	XRT_TCIB_XRT_S_HTR_A_DIS_438_OG [0x1b6]	TCIB_XRT_S_HTR_A_DIS	0	04-C0			
2024/03/27	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 03 74 01		db
2024/03/27	06:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/03/27	06:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/03/27	06:52:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/03/27	06:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/03/27	08:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/03/27	08:33:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/03/27	08:33:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/03/27	08:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/03/27	10:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/03/27	10:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/03/27	10:12:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/03/27	10:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/03/27	11:50:00.0	XRT_CTRL_MANU_407_OG [0x197]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/03/27	11:50:10.0	XRT_FOCUS_RECALIBRATE_405_OG [0x195]	XRT_FOCUS_RECAL	2	07-F8	78 00		
2024/03/27	11:54:10.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/03/27	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/03/27	11:59:56.0	XRT_FOCUS_POSITION_443_OG [0x1bb]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/03/27	12:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 2e f9 2e f9		
2024/03/27	12:02:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/03/27	12:02:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2024/03/27	12:02:56.0	XRT_FLD_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2024/03/27	12:02:58.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2024/03/27	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/03/27	12:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/03/27	12:09:56.0	XRT_FOCUS_POSITION_443_OG [0x1bb]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/03/27	12:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 2e f9 d1 07		
2024/03/27	12:12:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/03/27	12:12:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2024/03/27	12:12:56.0	XRT_FLD_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2024/03/27	12:12:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2024/03/27	12:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/03/27	12:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/03/27	12:19:56.0	XRT_FOCUS_POSITION_443_OG [0x1bb]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/03/27	12:20:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 d1 07 d1 07		
2024/03/27	12:22:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/03/27	12:22:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2024/03/27	12:22:56.0	XRT_FLD_DIS_447_OG [0x1bf]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2024/03/27	12:22:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2024/03/27	12:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/03/27	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/03/27	12:29:56.0	XRT_FOCUS_POSITION_443_OG [0x1bb]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		

2024/03/27	12:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 d1	07 2e	f9		
2024/03/27	12:32:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/03/27	12:32:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/03/27	12:32:56.0	XRT_FLD_DIS_447_OG [0x1bf]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/03/27	12:32:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2024/03/27	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/03/27	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/03/27	12:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/03/27	12:39:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00			
2024/03/27	12:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00	00 00	00 00		
2024/03/27	12:40:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/03/27	12:40:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/03/27	12:40:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/03/27	12:42:58.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2024/03/27	12:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/03/27	12:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/03/27	12:49:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/03/27	12:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00			
2024/03/27	12:50:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 03	74 01	db		
2024/03/27	12:50:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/03/27	12:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/03/27	12:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/03/27	12:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/03/27	12:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/03/27	12:52:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2024/03/27	12:52:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/03/27	12:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/03/27	15:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/03/27	15:39:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/03/27	15:39:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/03/27	15:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/03/27	15:50:00.0	XRT_Custom_430_OG [0x1ae]							
2024/03/27	15:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/03/27	17:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/03/27	17:13:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/03/27	17:13:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/03/27	17:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/03/27	17:37:00.0	XRT_Custom_430_OG [0x1ae]							
2024/03/27	17:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/03/27	17:52:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/03/27	17:52:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/03/27	17:52:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00			
2024/03/27	17:53:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00	00 00	00 00		
2024/03/27	17:53:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/03/27	17:53:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/03/27	17:53:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/03/27	17:55:58.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2024/03/27	17:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]							

2024/03/27	18:02:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/03/27	18:02:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/03/27	18:02:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2024/03/27	18:03:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 03 74 01 db			
2024/03/27	18:03:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/03/27	18:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/03/27	18:03:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/03/27	18:03:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/03/27	18:03:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/03/27	18:05:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2024/03/27	18:05:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2024/03/27	18:06:00.0	XRT_ROI_A_422_OG [0x1a6]	MDP_XRT_ROI_SET	6	07-F0	cd 05 85 83 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 06 85 83 08 08			
			MDP_XRT_ROI_SET	6	07-F0	cd 07 80 80 08 08			
			MDP_XRT_ROI_SET	6	07-F0	cd 08 80 80 20 20			
			MDP_XRT_ROI_SET	6	07-F0	cd 0c 80 80 20 08			
			MDP_XRT_ROI_SET	6	07-F0	cd 0d 80 80 08 20			
			MDP_XRT_ROI_SET	6	07-F0	cd 0e 85 83 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06			
2024/03/27	18:06:00.5	XRT_ROI_B_440_OG [0x1b8]	MDP_XRT_ROI_SET	6	07-F0	cd 0f 80 80 06 06			
			MDP_XRT_ROI_SET	6	07-F0	cd 10 80 80 08 08			
2024/03/27	18:06:05.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/03/27	18:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/03/27	18:50:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/03/27	18:50:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/03/27	18:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/03/27	19:13:30.0	XRT_Custom_430_OG [0x1ae]							
2024/03/27	19:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/03/27	20:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/03/27	20:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/03/27	20:27:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/03/27	20:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/03/27	20:50:30.0	XRT_Custom_430_OG [0x1ae]							
2024/03/27	20:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/03/27	22:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/03/27	22:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/03/27	22:04:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/03/27	22:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/03/27	22:27:30.0	XRT_Custom_430_OG [0x1ae]							
2024/03/27	22:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/03/27	23:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/03/27	23:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/03/27	23:41:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/03/27	23:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/03/28	00:02:00.0	XRT_Custom_430_OG [0x1ae]							
2024/03/28	00:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/03/28	01:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/03/28	01:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/03/28	01:18:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/03/28	01:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/03/28	01:26:30.0	XRT_Custom_430_OG [0x1ae]							
2024/03/28	01:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/03/28	02:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							

2024/03/28	02:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	02:42:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/03/28	02:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/03/28	03:01:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/03/28	03:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/03/28	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	04:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2024/03/28	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2024/03/28	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/03/28	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/03/28	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/03/28	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/03/28	04:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/03/28	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2024/03/28	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2024/03/28	04:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/03/28	04:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	04:17:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/03/28	04:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/03/28	04:38:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/03/28	04:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/03/28	05:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	05:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	05:47:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/03/28	05:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/03/28	06:15:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/03/28	06:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/03/28	06:24:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/03/28	06:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	06:24:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	06:24:24.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2024/03/28	06:24:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2024/03/28	06:24:28.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2024/03/28	06:27:04.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/03/28	06:27:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2024/03/28	06:34:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/03/28	06:34:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	06:34:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	06:34:30.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2024/03/28	06:34:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 03 74 01 db	
2024/03/28	06:34:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/03/28	06:34:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/03/28	06:34:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/03/28	06:34:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/03/28	06:37:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/03/28	06:37:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	

2024/03/28	06:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/03/28	07:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	07:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/03/28	07:27:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/03/28	07:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/03/28	07:52:30.0	XRT_Custom_430_OG [0x1ae]					
2024/03/28	07:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/03/28	09:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	09:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/03/28	09:07:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/03/28	09:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/03/28	09:29:30.0	XRT_Custom_430_OG [0x1ae]					
2024/03/28	09:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/03/28	10:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	10:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/03/28	10:47:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/03/28	10:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/03/28	11:03:00.0	XRT_Custom_430_OG [0x1ae]					
2024/03/28	11:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/03/28	11:21:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/03/28	11:39:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00 00