

XRT Timeline to be uploaded on 2024/04/04

Period: 2024/04/04 11:19:00 - 2024/04/09 11:59:00

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Normal mode

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XOB #1B96: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
04/04 11:40:00 - 04/04 16:03:54	Fixed (897.3, -56.9)	# OP start + 10min HOP 457
PROG= 04 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 30 1-time(s) 2.0sec		
Open/G-band Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024)	Q=90 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024)	Q=95 0 0 2.0sec
Subr= 2 10-time(s) 360.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024)	Q=98 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024)	DPCM 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 3 0 2.0sec
Al-poly/Open Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024)	DPCM 2 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/04 16:07:00 - 04/04 17:59:54	Track (-870.4, 147.8) ^{04/04 16:04:00}	AR Track
04/06 02:23:00 - 04/06 03:32:54	Track (-686.2, 174.9) ^{04/06 02:20:00}	AR Track
PROG= 05 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 92 1-time(s) 2.0sec		
Open/G-band Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
Subr= 2 5-time(s) 2.0sec		
Seqn= 47 1-time(s) 2.0sec		
Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
Seqn= 96 4-time(s) 180.0sec		
Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5792)

Term	Pointing (x, y)	Comment
04/04 18:03:00 - 04/04 18:09:54	Fixed (0.0, 0.0)	synoptic
04/05 06:05:30 - 04/05 06:12:24	Fixed (0.0, 0.0)	synoptic, shifted 2.5 min
04/05 16:53:00 - 04/05 16:59:54	Fixed (0.0, 0.0)	Synoptic
04/06 05:36:00 - 04/06 05:42:54	Fixed (0.0, 0.0)	synoptic, shifted -27.0 min
PROG= 19 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024)	DPCM 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024)	DPCM 0 0 2.0sec
Seqn= 26 1-time(s) 2.0sec		
Open/Al-mesh Open/Al-mesh close	Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 99 1-time(s) 2.0sec		
Al-poly/Open Al-poly/Open close	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Al-poly/Open Al-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Al-poly/Open Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 83 1-time(s) 2.0sec		
thin-Be/Open thin-Be/Open close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 23 1-time(s) 4.0sec		
Open/G-band Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		

Seqn= 41 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec												
med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/04 18:13:00 - 04/05 03:55:30	Track (-861.6, 149.7) ^{04/04 18:10:00}	AR Track
04/05 06:15:30 - 04/05 16:49:54	Track (-804.7, 159.9) ^{04/05 06:12:30}	AR Track
04/06 05:46:00 - 04/06 11:33:54	Track (-663.7, 177.1) ^{04/06 05:43:00}	AR Track

PROG= 08 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 4-time(s) 2.0sec												
Seqn= 47 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 77 4-time(s) 300.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D09: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 90min cad) + CME wat

Term	Pointing (x, y)	Comment
04/05 04:17:00 - 04/05 06:02:24	Fixed (0.0, 0.0)	HOP 349
04/06 03:36:00 - 04/06 05:32:54	Fixed (0.0, 0.0)	HOP 349

PROG= 18 Inf.-time(s)

Subr= 1 1-time(s) 600.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 98 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 7-time(s) 600.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74 1-time(s) 2.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C97: AR - Filter-Ratio with thin-Be (long/short pairs) and Med-Be (short) with PFB, 384x384 at 1064 1048, with G-band (1ms/1ms VLS=CLS), 60 cad

Term	Pointing (x, y)	Comment
04/05 17:03:00 - 04/05 22:59:54	Track (-744.6, 168.3) ^{Ⓢ 04/05 17:00:00}	HOP 480 (Test)
PROG= 02 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 120-time(s) 60.0sec		
Seqn= 37 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 59 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1CD9: HOP393 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s

Term	Pointing (x, y)	Comment
04/05 23:03:00 - 04/06 02:19:54	Fixed (-21.0, 65.0)	HOP 393
PROG= 11 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 52 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Subr= 2 30-time(s) 360.0sec		
Seqn= 97 2-time(s) 2.0sec		
Al-poly/Open	med-Be/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	med-Be/Open close	Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * * **Flare mode** * * * * *

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GB

Term	Pointing (x, y)	Comment
04/04 11:40:00 - 04/04 16:03:54	Fixed (897.3, -56.9)	# OP start + 10min HOP 457
04/04 16:07:00 - 04/04 17:59:54	Track (-870.4, 147.8) ^{Ⓢ 04/04 16:04:00}	AR Track
04/04 18:13:00 - 04/05 03:55:30	Track (-861.6, 149.7) ^{Ⓢ 04/04 18:10:00}	AR Track
04/05 04:17:00 - 04/05 06:02:24	Fixed (0.0, 0.0)	HOP 349
04/05 06:15:30 - 04/05 16:49:54	Track (-804.7, 159.9) ^{Ⓢ 04/05 06:12:30}	AR Track
04/05 23:03:00 - 04/06 02:19:54	Fixed (-21.0, 65.0)	HOP 393
04/06 02:23:00 - 04/06 03:32:54	Track (-686.2, 174.9) ^{Ⓢ 04/06 02:20:00}	AR Track
04/06 03:36:00 - 04/06 05:32:54	Fixed (0.0, 0.0)	HOP 349
04/06 05:46:00 - 04/06 11:33:54	Track (-663.7, 177.1) ^{Ⓢ 04/06 05:43:00}	AR Track
PROG= 14 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 73 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 87 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D10: Flare - multifilter 5 sec cadence (Be/thin, Be/med), AEC 3, 384x384

Term	Pointing (x, y)	Comment
04/05 17:03:00 - 04/05 22:59:54	Track (-744.6, 168.3) ^{Ⓢ 04/05 17:00:00}	HOP 480 (Test)
PROG= 15 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 9 1-time(s) 2.0sec		

thin-Be/Open	med-Be/Open	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 49 255-time(s) 5.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

* * * * *

NOT USED

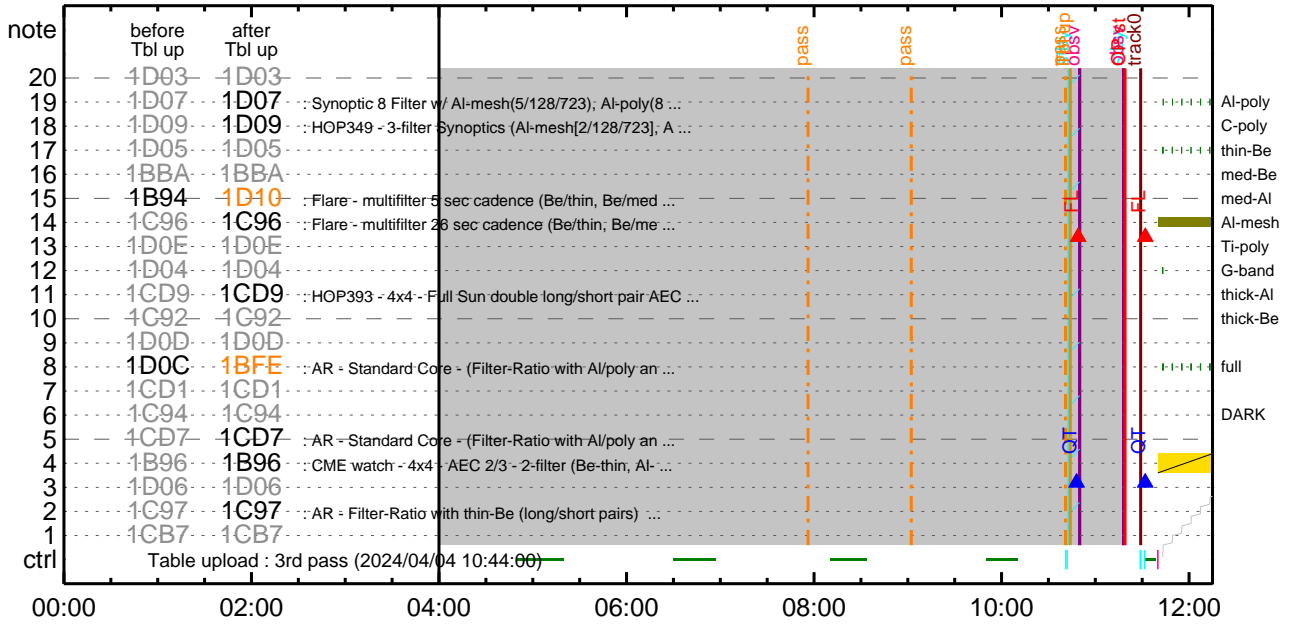
* * * * *

Flare Detection

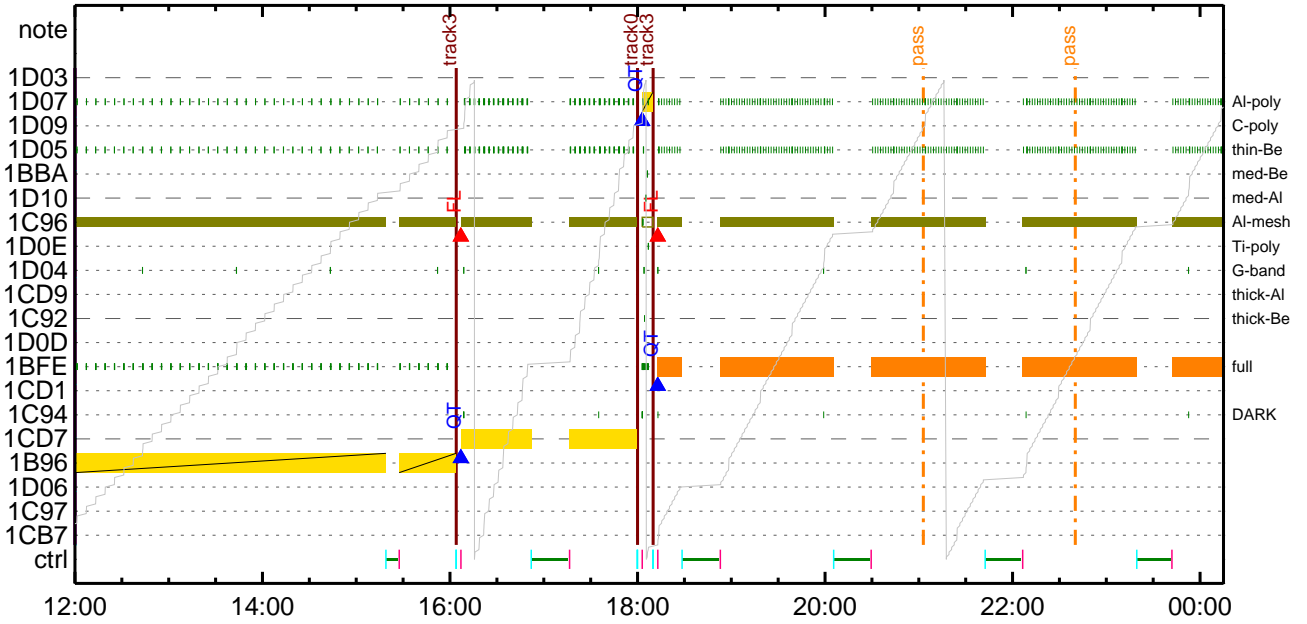
* * * * *

FLD Patrol												
Term		Pointing (x, y)						Comment				
04/04 10:45:00 - 04/04 18:00:18		cannot be identified										
04/04 18:10:18 - 04/05 06:02:48		Track (-861.6, 149.7)		④ 04/04 18:10:00		AR Track						
04/05 06:12:48 - 04/05 16:50:18		Track (-804.7, 159.9)		④ 04/05 06:12:30		AR Track						
04/05 17:00:18 - 04/06 05:33:18		Track (-744.6, 168.3)		④ 04/05 17:00:00		HOP 480 (Test)						
04/06 05:43:18 - 04/09 11:59:00		Track (-663.7, 177.1)		④ 04/06 05:43:00		AR Track						
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

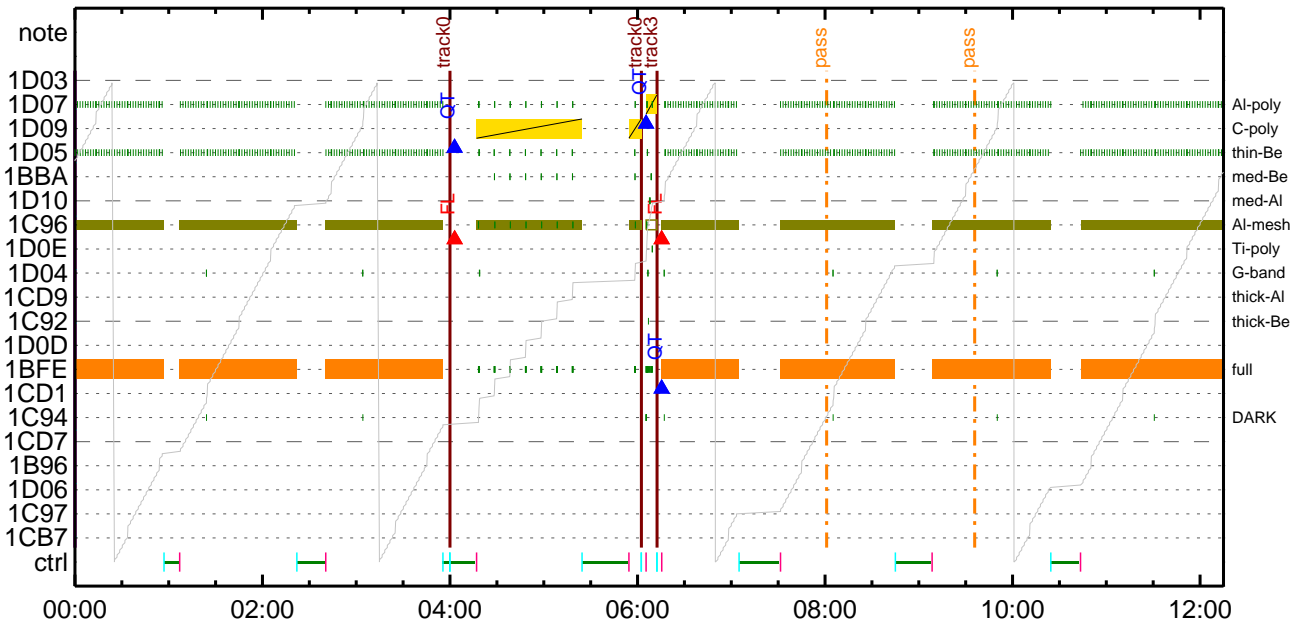
CMDI #0725 2024/04/04



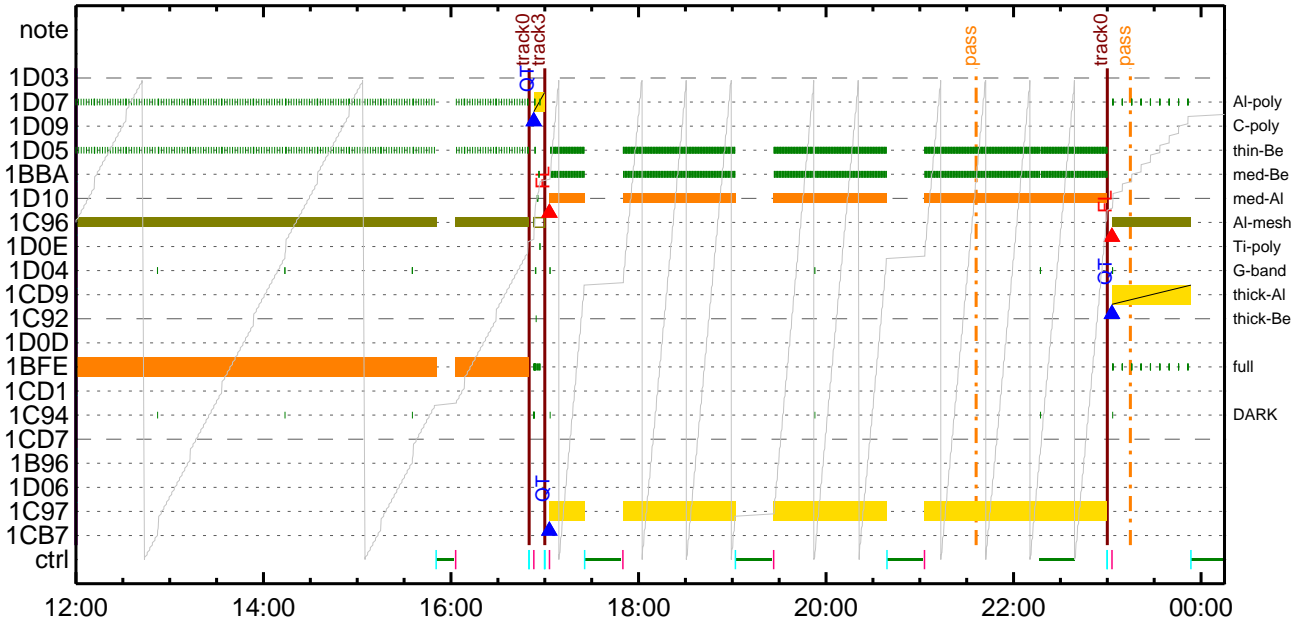
CMDI #0725 2024/04/04



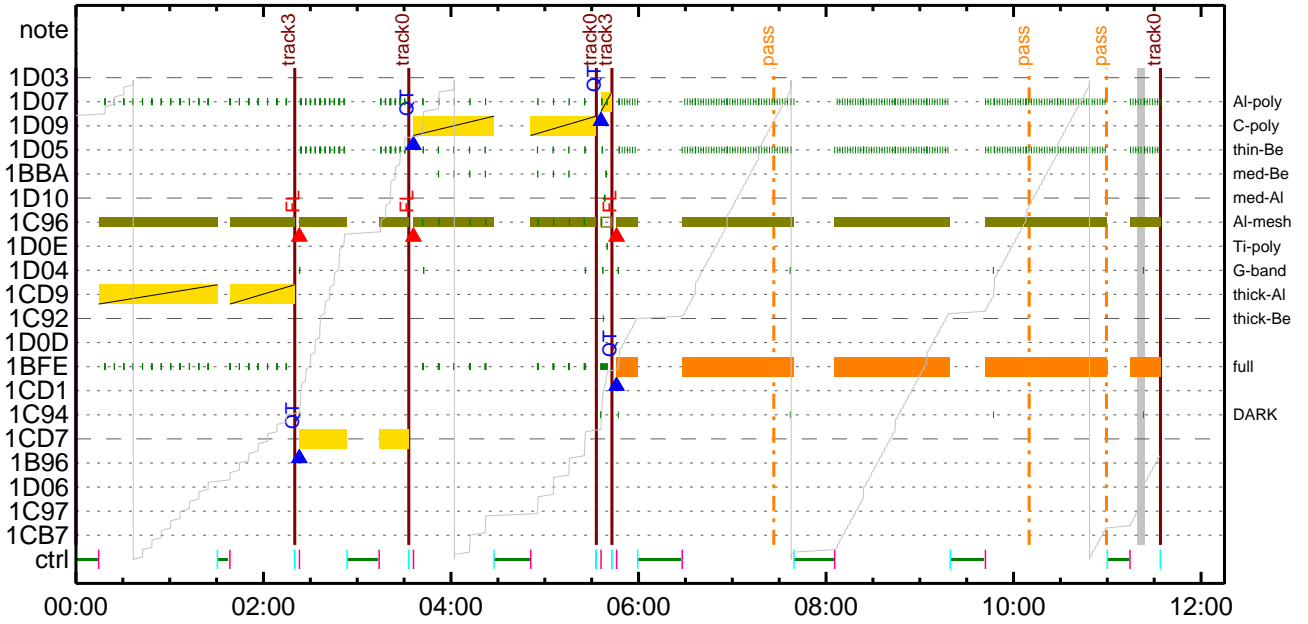
CMDI #0725 2024/04/05



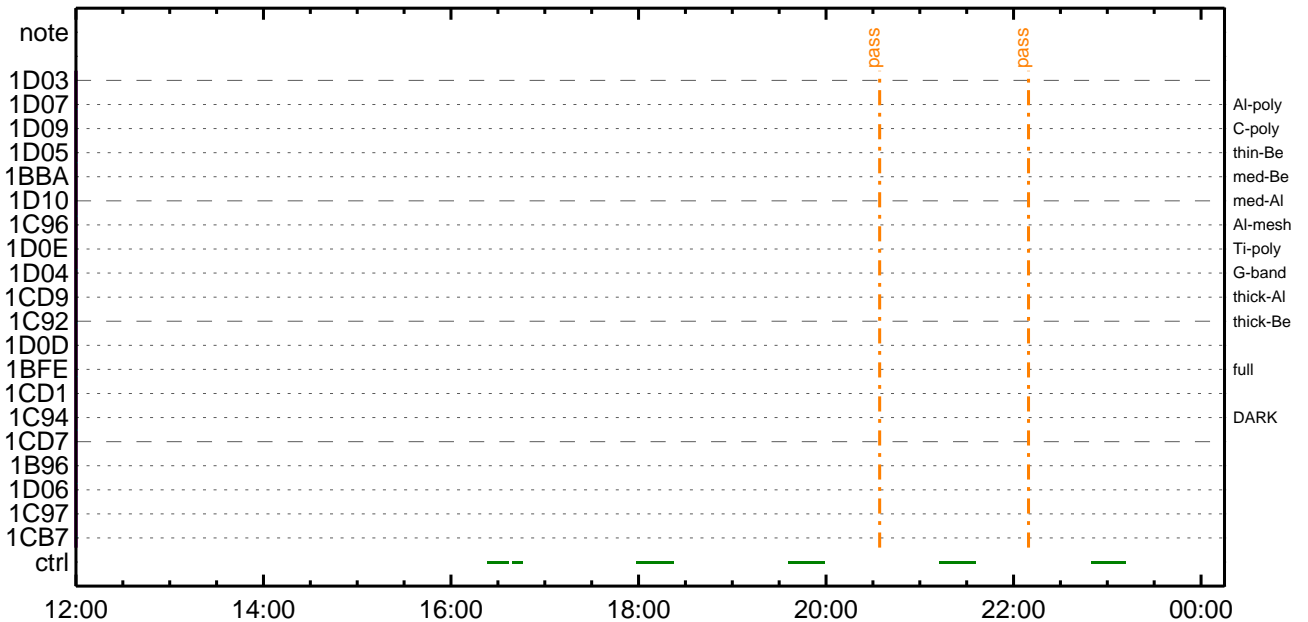
CMDI #0725 2024/04/05



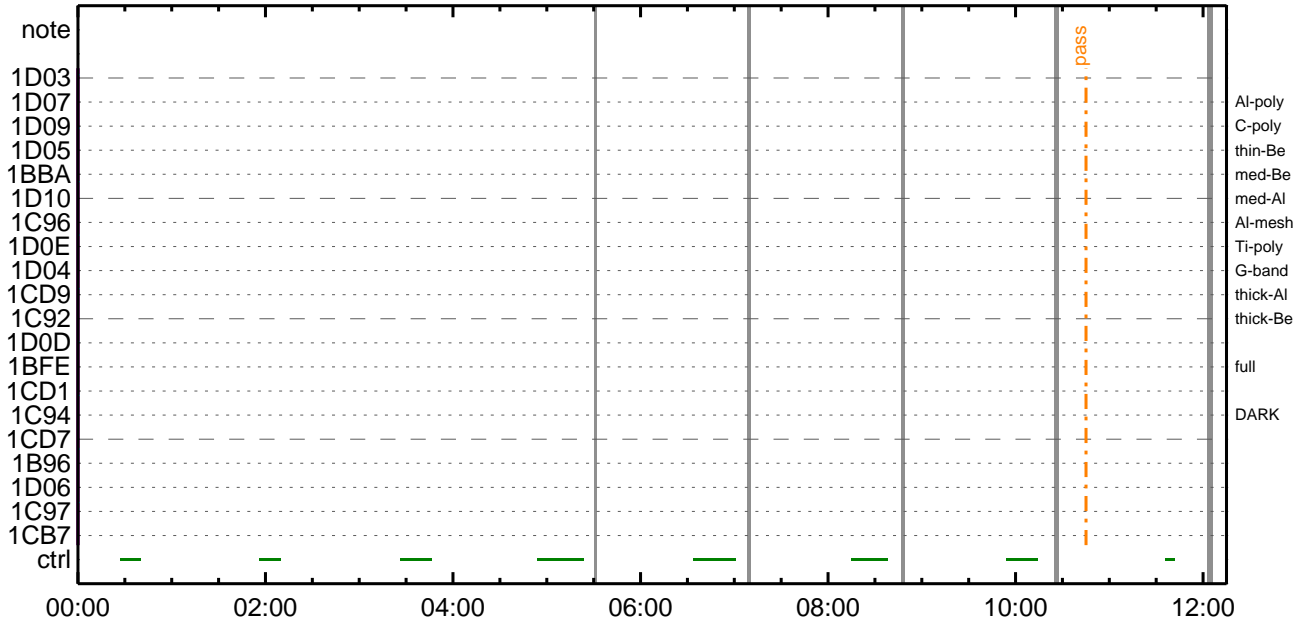
CMDI #0725 2024/04/06



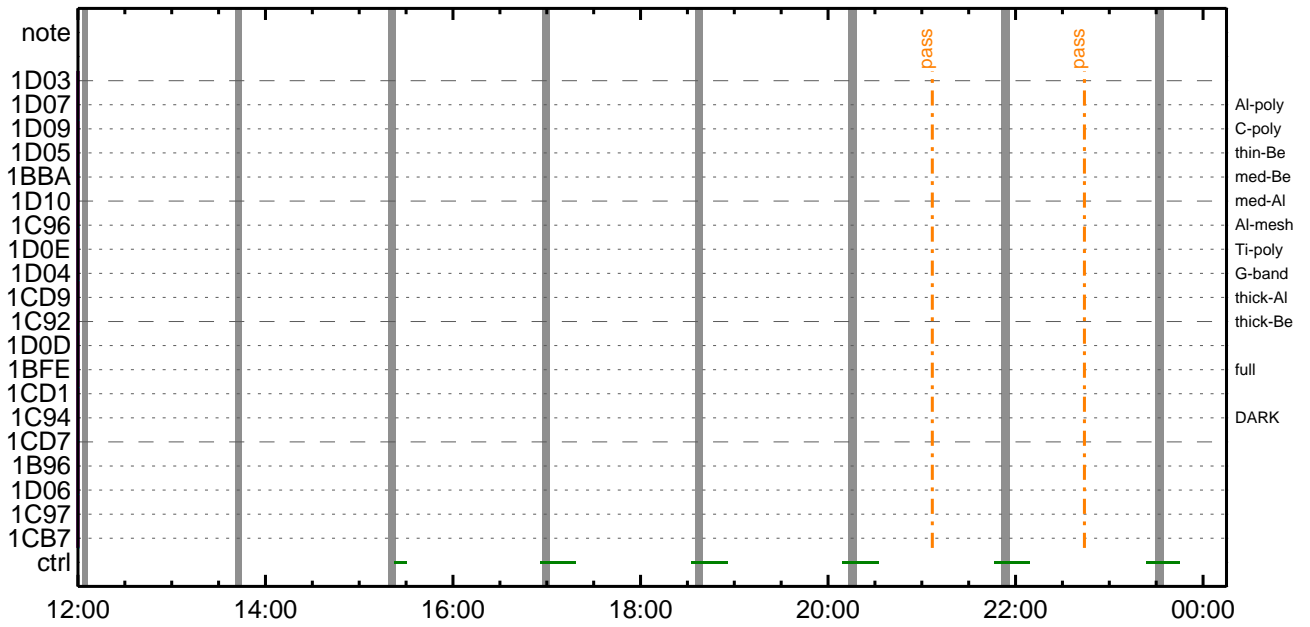
CMDI #0725 2024/04/06



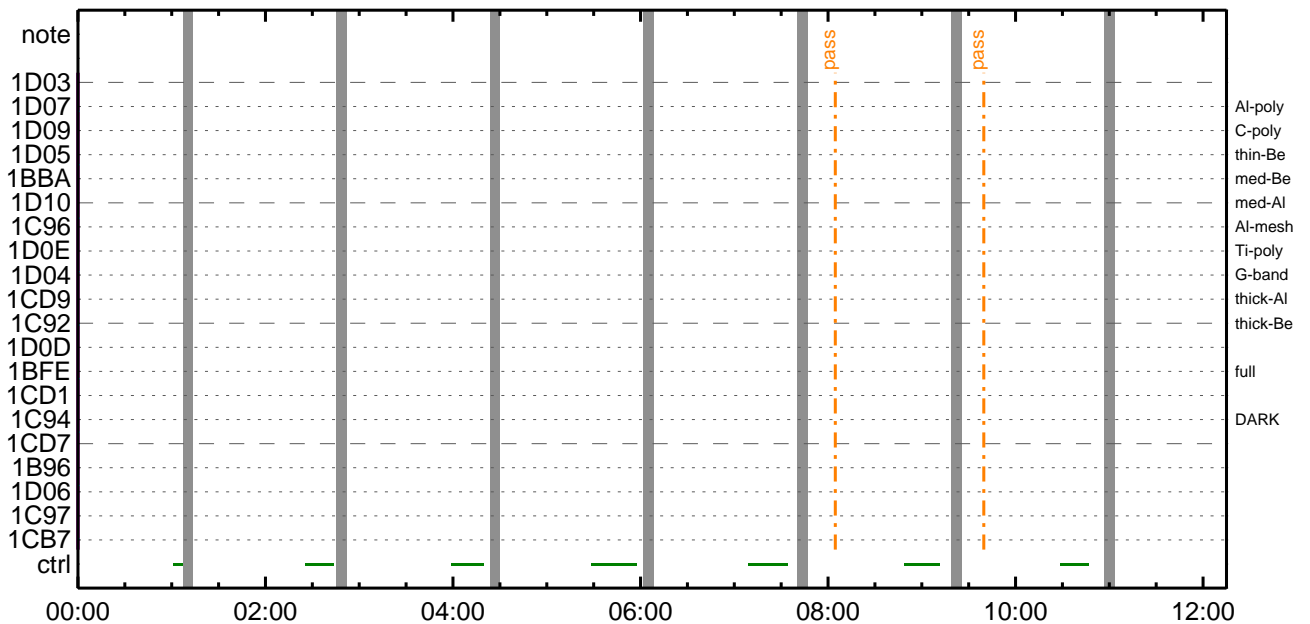
CMDI #0725 2024/04/07



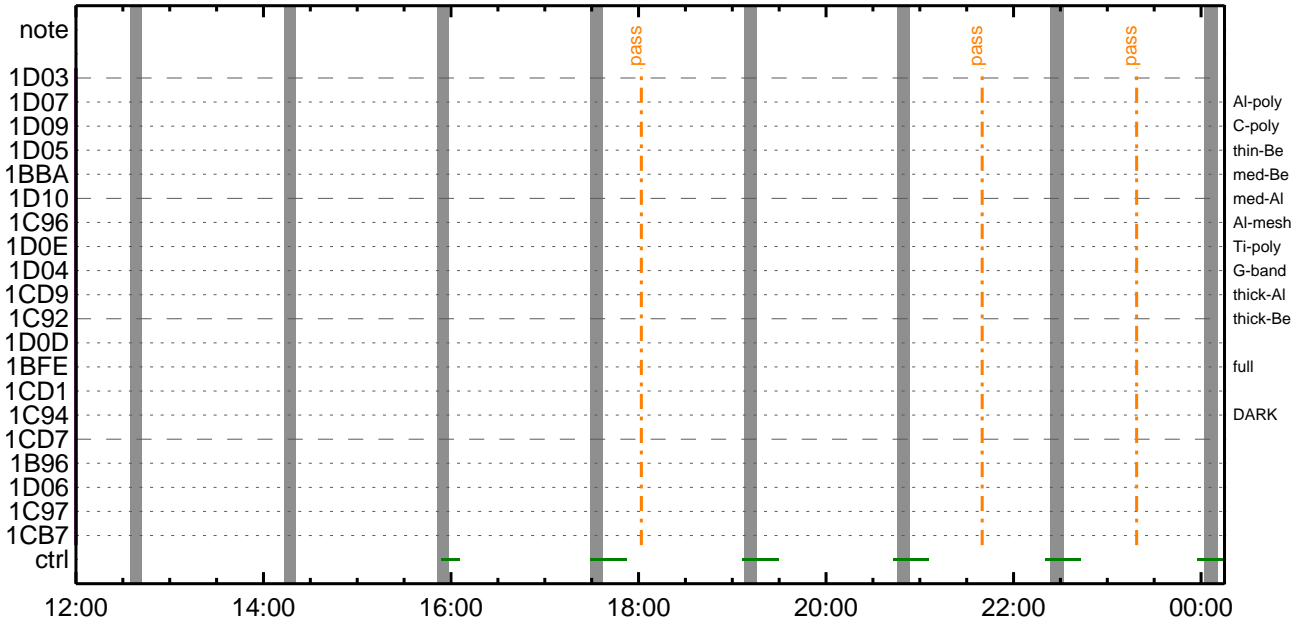
CMDI #0725 2024/04/07



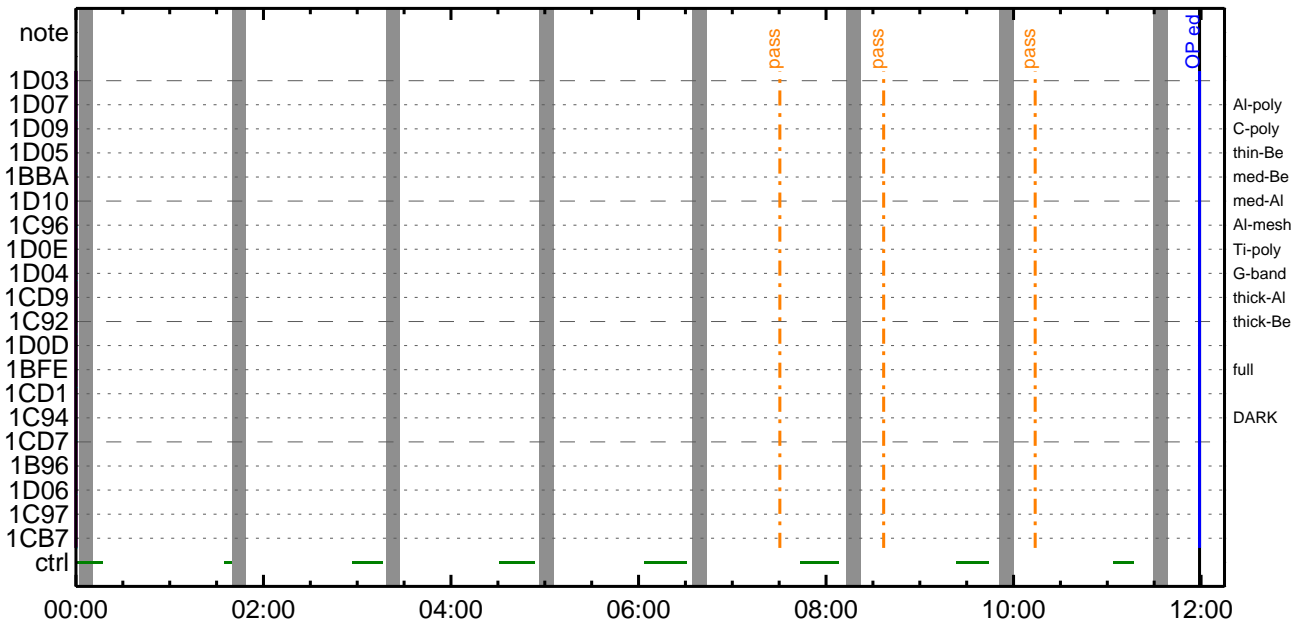
CMDI #0725 2024/04/08



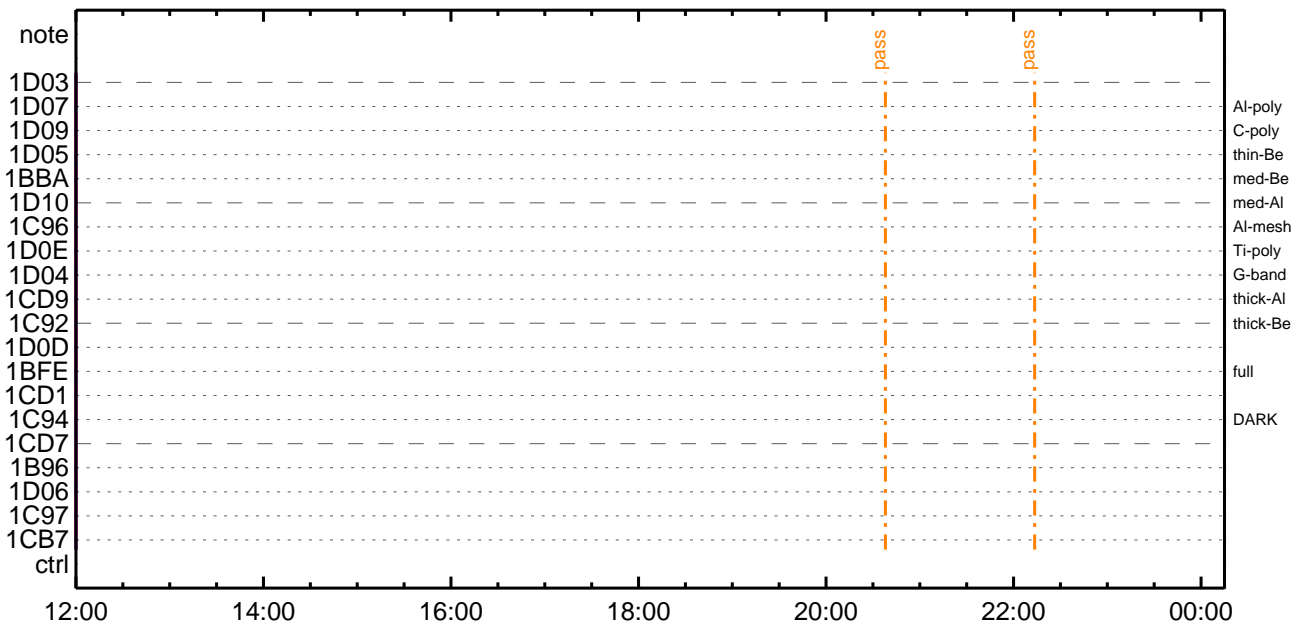
CMDI #0725 2024/04/08



CMDI #0725 2024/04/09



CMDI #0725 2024/04/09




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0096 C.          ɁɁSETɁEDUMPɁĪÆ±°iŷŃŷ¹ɁÇ¹ÔɁ|Ɂ³ɁĒ;Ē
0097 C.
0098 . C. TIŷ³ŷĐŷÓŷĒɁðĀĐĪĵ (UT)
0099 +. TI 2024-04-04 11:14:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          ɁɁ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2024-04-04 11:14:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          ɁɁ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2024-04-04 11:14:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          ɁɁ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2024-04-04 11:18:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          ɁɁ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. °Ē²¼ɁĪĀē³iĪŃɁĪŷĀŷŷĀŷ⁻¹àĪŪ
0116 C.          ɁɁ[HK1_TI_CMD_ENA/DIS]          EQ          ENA
0117 C.          ɁɁ[HK1_TI_CMD_NUM]          EQ          4
0118 C.          ɁɁ[HK1_NEXT_EXEC_PIM]          EQ          DHU
0119 C.          ɁɁ[HK1_NEXT_EXEC_DC]          EQ          0xB3
0120 C.
0121 . C. *****
0122 C. TIĪĪ°ēŷĀŷÓŷ×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          ɁɁ[HK1_DMP_TOP_ADRS_1]          EQ          07
0129 C.          ɁɁ[HK1_DMP_TOP_ADRS_0]          EQ          2B
0130 C.          ɁɁ[HK1_DMP_BLOCK_NUM]          EQ          3
0131 C.          ɁɁ[HK1_DMP_REPEAT_NUM]          EQ          0
0132 C.          ɁɁ[HK1_DMA_DMP_PIM]          EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          ɁɁ[HK1_PKT_FORM_NO]          EQ          7
0136 C.          ɁɁ[HK1_PKT_GEN_TIME]          EQ          0.25 s
0137 C.          ɁɁ[HK1_S_TLM_BIT_RATE]          EQ          32k
0138 C.          ɁɁ[HK1_X_TLM_BIT_RATE]          EQ          4M
0139 C.          ɁɁ[HK1_DMP_CHK_FLG]          EQ          EXEC
0140 C.
0141 . C. ŷĀŷÓŷ×½ªĪ»Ɂð³ĪÇŞ
0142 C.          ɁɁ[HK1_DMP_CHK_FLG]          EQ          NON
0143 C.
0144 . C. RAM ID=TI_TBLɁĪ¼Ē¹Ç•ē²ĪOKɁð³ĪÇŞ
0145 C.
0146 . C. DHUŷĀ;¼ŷĒ;Ē¼ŷ½,ŷi;¼ŷĒ;ĒɁðĪáɁ¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          ɁɁ[HK1_PKT_FORM_NO]          EQ          2
0150 C.          ɁɁ[HK1_PKT_GEN_TIME]          EQ          0.5S
0151 C.          ɁɁ[HK1_S_TLM_BIT_RATE]          EQ          32K
0152 C.          ɁɁ[HK1_X_TLM_BIT_RATE]          EQ          4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2024-04-04 11:18:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 . C. -----
0162 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C.
0166 C. ***** XRT START *****
0167 C. Execute, after the success of OP upload.
0168 +. TI 2024-04-04 11:18:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC          (c3)
0171 . C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0172 C.
0173 C. ***** XRT END *****
0174 . C. Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C. ***** Start EIS operation (TI set) *****
0178 C. Execute, after the success of OP upload.
0179 C. Set EIS TI-commands
0180 +. TI 2024-04-04 11:18:30.0
0181 DC 07-FC EIS_MODE_MANU
0182 BC          (21 02)
0183 +. TI 2024-04-04 11:18:40.0
0184 DC 07-FC EIS_MODE_CHG_DIS
0185 BC          (22)
0186 . C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0187 C. ***** End EIS operation (TI set) *****
0188 C.
0189 C.
0190 C.
0191 . C. ***** MDP 'ũĀĪɁĪ»ð¾ŷɁĒĀĐɁ¹ɁĒDCBC•×²ē *****
0192 C. (¾ā°iŷŃŷ¹ŷĐŷÓŷĒŷĀŷŷĀŷ⁻¹ēɁĒ¼Ɂ¼Ā»ŪɁ¹Ɂē)
0193 . S. DC-BC dcbc-402:DCBC

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```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ĐŸ!•İ Daily±;İÑ¤Ē'Ø¤¹¤ēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-481 2024-04-04 11:37:58 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Ü;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝÓYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¼¿µÄµ•µ°E»Í×ÁÇµÍYÇYÁY×Yí;¼YÉ;ËËÈ¼µ•íÍË;ËËÈ¼°ÇÖµ•µ¿¼í¹ÇµÍ; ÇÁ®, ùµ¹µÈµDµÇÁ÷¿®µ•µËµµµ³µÈ; £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. °EÀ, Í×ÈYµäLOSµDµÇµÍ»p´Öµò¹íÍ, µ•; ÇÉÖÍ×µÈXÁÓONµÍ¹ÖµÈµÍµËµµµ³µÈ; £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C.          ÇÇ [HK1_XPA_ON/OFF]                EQ      ON
0025 C.          ÇÇ [HK1_XPA_PWR_HI/LO]            EQ      HI
0026 C.          ÇÇ [HK1_XMOD_ON/OFF]              EQ      ON
0027 C.          ÇÇ [HK1_XMOD_QPSK/PM]             EQ      QPSK
0028 C.
0029 . C. XYDÝÓYÉYíYÁY~¾ÖÁÖµ¬°ÄÄÈµ•µ¿µé; Ç°È²¼µÍ°EÀ, ¼È½Çµò¼Á¹Öµ¹µÈ; £
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°EÀ,
0033 C. *****
0034 C. °° RESTART;ËPT1;Ëµ•µ¿µµ¼í¹ÇµÍ; Ç°È²¼µÍ¼Á¹Öµµ»°; ÇDCBC-150µØ¿Èµà; £
0035 C.
0036 . C. ;ãPT1°EÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC          (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C.          ÇÇ [HK1_REP_PT_1/2]                EQ      PT1    (¼Á¹Ö, ;¼Ú)
0043 C.          ÇÇ [HK1_REP_STA/STP]              EQ      START  (¼Á¹Ö, ;¼Ú)
0044 C.          ÇÇ [HK1_X_VC4_ON/OFF]             EQ      ON     (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYÉÄÜÁØ;ËÁ•Ä°²óÈò;Ë, áµÍ°EÀ, °E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C.          ÇÇ [HK1_REP_PT_1/2]                EQ      PT1    (¼Á¹Ö, ;¼Ú)
0050 C.          ÇÇ [HK1_REP_STA/STP]              EQ      START  (¼Á¹Ö, ;¼Ú)
0051 C.          ÇÇ [HK1_X_VC4_ON/OFF]             EQ      ON     (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EÀ, µ¬¼«°E°Äá»ßµ•µ¿, á; Ç°È²¼µò¼Á¹Öµ¹µÈ; £
0055 C. YÇYÓYÉYÉÄÜÁØµàÄ•Ä°²óÈòµ¬¼áµµ¼í¹ÇµÍ´°Í°Í»µ¹µÈµDµÇÁÖµÄ; £
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°EÀ,
0059 C. *****
0060 C. °° RESTART;ËPT2;Ëµ•µ¿µµ¼í¹ÇµÍ; Ç°È²¼µÍ¼Á¹Öµµ»°; ÇDCBC-151µØ¿Èµà; £
0061 C.
0062 . C. ;ãPT2°EÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC          (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C.          ÇÇ [HK1_REP_PT_1/2]                EQ      PT2    (¼Á¹Ö, ;¼Ú)
0069 C.          ÇÇ [HK1_REP_STA/STP]              EQ      START  (¼Á¹Ö, ;¼Ú)
0070 C.          ÇÇ [HK1_X_VC4_ON/OFF]             EQ      ON     (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYÉÄÜÁØ;ËÁ•Ä°²óÈò;Ë, áµÍ°EÀ, °E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C.          ÇÇ [HK1_REP_PT_1/2]                EQ      PT2    (¼Á¹Ö, ;¼Ú)
0076 C.          ÇÇ [HK1_REP_STA/STP]              EQ      START  (¼Á¹Ö, ;¼Ú)
0077 C.          ÇÇ [HK1_X_VC4_ON/OFF]             EQ      ON     (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÀ, Äá»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÀ, Äá»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C.          ÇÇ [HK1_REP_STA/STP]                EQ      STOP
0087 C.          ÇÇ [HK1_S_VC4_ON/OFF]              EQ      OFF
0088 C.          ÇÇ [HK1_X_VC4_ON/OFF]              EQ      OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C.          ÇÇ [HK1_XMOD_ON/OFF]                EQ      OFF
0095 C.          ÇÇ [HK1_XPA_ON/OFF]                EQ      OFF
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 17s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2024-04-04 11:18:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'uAÎI»ö¼YnEÄDn¹nëDCBC•x²è *****
0156 C. (%ã°I¥ÓYÄYÉYÐYËYÄYÇYÈnÈ¼n¼Ä»Ûn¹në)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•Ï Daily±çÍÑnÈ'Øn¹nëDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥Ä¥~¼Ä»Û;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-482 2024-04-04 11:37:58 216 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÁÝSÝÄÝ~¼Ä»Û;ä
0005 C.
0006 C. ÝÀÝß;¼Ý³ÝÐÝóÝÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËçµÄµ°µ»Í×ÁÇµÍÝçÝÄÝ×Ýí;¼ÝÉ;ËËÈµ•ííË;ËÈ¼°ÇÖµ•µç¼í¹ÇµÍ;çÀ®, ùµ¹µÈµÐµÇÄ÷ç®µ•µËµµµ³µÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷ç®µ;ON
0016 C. *****
0017 C. ç¨ °ÈÀ, í×ÈÝµÄLOSµÐµÇµÍ»p´Öµò¹íí, µ•; çÉÖÍ×µÈXÁÓONµÍ¹ÖµÈµíµËµµµ³µÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç [HK1_XPA_ON/OFF] EQ ON
0025 C. çç [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç [HK1_XMOD_ON/OFF] EQ ON
0027 C. çç [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÝÐÝóÝÉÝíÝÄÝ~¼°Öµ°-ÄÄÈµ•µçµé; ç°È²¼µí°ÈÀ, ¼È½Çµò¼Ä¹Öµ¹µé;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÀ,
0033 C. *****
0034 C. ç¨ RESTART;ËPT1;Ëµ•µçµ¼¼í¹ÇµÍ; ç°È²¼µí°ÈÀ¹Öµ»µ°; çDCBC-150µØçÈµà;£
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãÝçÝóÝÉÝÈÄÜÁØ;ËÄ•Ä°²óÈò;Ë, áµí°ÈÀ, °È³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, µ¼«°ÈÄ»ßµ•µç, á; ç°È²¼µò¼Ä¹Öµ¹µé;£
0055 C. ÝçÝóÝÉÝÈÄÜÁØµÄ•Ä°²óÈòµ°µ¼µ¼¼í¹ÇµÍ´°í°íµ¹µÈµÐµÇÄÖµÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÀ,
0059 C. *****
0060 C. ç¨ RESTART;ËPT2;Ëµ•µçµ¼¼í¹ÇµÍ; ç°È²¼µí°ÈÀ¹Öµ»µ°; çDCBC-151µØçÈµà;£
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãÝçÝóÝÉÝÈÄÜÁØ;ËÄ•Ä°²óÈò;Ë, áµí°ÈÀ, °È³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÄ, Ää»ß; çXÁ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÄ, Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç [HK1_REP_STA/STP] EQ STOP
0087 C. çç [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç [HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç [HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-286:MDP_OBS_S
0110 ()
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2024-04-04 11:18:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 +. DC 07-F0 MDP_XRT_CTRL_MANU
0136 BC (c1)
0137 +. DC 07-F0 MDP_XRT_MODE_STBY
0138 BC (c3)
0139 . C. ----- Success Verify ? OK / NG____
0140 C.
0141 C. XRT Obs. Table Upload
0142 . S. RAM ram-291:MDP_OBS_X
0143 ()
0144 C.
0145 +. DC 07-F0 MDP_DUMP_XRTTBL
0146 BC (84 07 00 00 00 3a d4)
0147 . C. ----- Comparison Check ? OK / ERR ____
0148 C.
0149 C.
0150 +. DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 01 b1 b1 04 04)
0152 +. DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 02 b1 b1 08 08)
0154 +. DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 03 b1 b1 08 08)
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 04 b1 b1 06 06)
0158 +. DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 05 85 83 06 06)
0160 +. DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 06 80 80 08 08)
0162 +. DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 07 80 80 20 20)
0164 +. DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 08 85 83 06 06)
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 09 80 80 20 08)
0168 +. DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0a 80 80 08 20)
0170 +. DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 0f 80 80 06 06)
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 10 80 80 08 08)
0174 +. DC 07-F0 MDP_XRT_FLD_ENA
0175 BC (d8)
0176 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0177 BC (c8)
0178 +. DC 07-F0 MDP_XRT_ARS_DIS
0179 BC (d5)
0180 +. DC 07-F0 MDP_XRT_AEC_RESET
0181 BC (d0)
0182 +. DC 07-F0 MDP_XRT_FLD_RESET
0183 BC (da)
0184 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0185 BC (c4 04)
0186 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0187 BC (c5 0e)
0188 . C. ----- Success Verify ? OK / NG ____
0189 C.
0190 C.
0191 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0192 C.
0193 +. DC 07-F0 MDP_XRT_MODE_OBSV

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0194 BC (c2)
0195 +. TI 2024-04-04 11:18:02.0
0196 DC 07-F0 MDP_XRT_MODE_OBSV
0197 BC (c2)
0198 . C. ----- Success Verify ? OK / NG ____
0199 C.
0200 C. ***** XRT END *****
0201 C.
0202 . C. ***** MDP 'úÃîñî»ö¼ýñëâðñ¹ñëDCBC•x²è *****
0203 C. (¾â°îÿÓÿÃÿËÿÐÿËÿâÿçÿëñË¼ññ¼Ã»Ûñ¹ñë)
0204 . S. DC-BC dcbc-402:DCBC
0205 (MDP_known_event)
0206 C.
0207 C.
0208 . C. ***** ÿÐÿ¹•ï Daily±çíññë´øñ¹ñëDCBC•x²è *****
0209 . S. DC-BC dcbc-153:DCBC
0210 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0211 C.
0212 C.
0213 . C. ;ãLOSÿÁÿSÿÃÿ¹¼Ã»Û;ä
0214 C.
0215 . C. ***** LOS *****
0216 C.

*** OP Sequence for XRT ***

2024/04/04	11:28:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/04	11:28:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/04	11:28:58.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2024/04/04	11:29:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	00 05 0e b0 42			
2024/04/04	11:29:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/04/04	11:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/04/04	11:29:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/04/04	11:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/04/04	11:29:26.0	XRT_FLD_RESET_434_OG [0x1b2]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/04/04	11:31:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/04	11:31:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/04/04	11:31:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/04/04	11:31:56.0	XRT_QT_PROG_SET_418_OG [0x1a2]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2024/04/04	11:31:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2024/04/04	11:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/04/04	11:39:00.0	XRT_Custom_430_OG [0x1ae]						
2024/04/04	11:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/04/04	15:19:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/04	15:19:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/04/04	15:19:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/04/04	15:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/04/04	15:26:30.0	XRT_Custom_430_OG [0x1ae]						
2024/04/04	15:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/04/04	16:03:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/04	16:03:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/04	16:03:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2024/04/04	16:04:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	03 03 74 01 db			
2024/04/04	16:04:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/04/04	16:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/04/04	16:04:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/04/04	16:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/04/04	16:04:26.0	XRT_FLD_RESET_434_OG [0x1b2]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/04/04	16:06:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2024/04/04	16:06:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2024/04/04	16:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/04/04	16:52:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/04	16:52:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/04/04	16:52:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/04/04	16:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/04/04	17:15:30.0	XRT_Custom_430_OG [0x1ae]						
2024/04/04	17:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/04/04	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/04	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/04	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2024/04/04	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2024/04/04	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2024/04/04	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			

2024/04/04	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/04/04	18:02:58.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2024/04/04	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/04	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/04	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/04	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/04/04	18:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 03 74 01 db				
2024/04/04	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/04/04	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/04/04	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/04/04	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/04/04	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/04	18:12:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2024/04/04	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/04/04	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/04	18:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/04	18:28:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/04	18:28:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/04	18:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/04	18:52:00.0	XRT_Custom_430_OG [0x1ae]							
2024/04/04	18:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/04	20:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/04	20:05:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/04	20:05:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/04	20:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/04	20:28:30.0	XRT_Custom_430_OG [0x1ae]							
2024/04/04	20:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/04	21:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/04	21:42:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/04	21:42:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/04	21:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/04	22:05:30.0	XRT_Custom_430_OG [0x1ae]							
2024/04/04	22:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/04	23:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/04	23:19:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/04	23:19:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/04	23:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/04	23:41:00.0	XRT_Custom_430_OG [0x1ae]							
2024/04/04	23:42:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	00:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	00:57:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	00:57:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	01:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/05	01:06:00.0	XRT_Custom_430_OG [0x1ae]							
2024/04/05	01:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	02:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	02:22:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	02:22:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	02:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2024/04/05	02:39:30.0	XRT_Custom_430_OG [0x1ae]							
2024/04/05	02:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	03:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	03:55:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	03:55:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	03:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/05	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/04/05	04:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/04/05	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/04/05	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/04/05	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/04/05	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/04/05	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	04:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2024/04/05	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/04/05	04:16:00.5	XRT_Custom_430_OG [0x1ae]							
2024/04/05	04:17:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	05:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	05:24:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	05:24:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	05:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/05	05:53:30.0	XRT_Custom_430_OG [0x1ae]							
2024/04/05	05:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	06:02:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	06:02:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	06:02:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/04/05	06:02:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/04/05	06:02:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/04/05	06:02:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/04/05	06:02:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/04/05	06:05:28.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2024/04/05	06:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	06:12:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	06:12:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	06:12:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/04/05	06:12:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 03 74 01 db				
2024/04/05	06:12:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/04/05	06:12:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/04/05	06:12:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/04/05	06:12:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/04/05	06:12:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	06:15:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2024/04/05	06:15:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/04/05	06:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	07:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	07:05:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

2024/04/05	07:05:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	07:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/05	07:30:30.0	XRT_Custom_430_OG [0x1ae]							
2024/04/05	07:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	08:45:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	08:45:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	08:45:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	08:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/05	09:07:30.0	XRT_Custom_430_OG [0x1ae]							
2024/04/05	09:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	10:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	10:24:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	10:24:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	10:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/05	10:42:30.0	XRT_Custom_430_OG [0x1ae]							
2024/04/05	10:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	15:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	15:50:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	15:50:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	15:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/05	16:02:00.0	XRT_Custom_430_OG [0x1ae]							
2024/04/05	16:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	16:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	16:49:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	16:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/04/05	16:50:00.0	AOCS_Or-e-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/04/05	16:50:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/04/05	16:50:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/04/05	16:50:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/04/05	16:52:58.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2024/04/05	16:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	16:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/04/05	17:00:00.0	AOCS_Or-e-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 03 74 01 db				
2024/04/05	17:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/04/05	17:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/04/05	17:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/04/05	17:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/04/05	17:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	17:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2024/04/05	17:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2024/04/05	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	17:25:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	17:25:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	17:25:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	17:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/05	17:49:00.0	XRT_Custom_430_OG [0x1ae]							
2024/04/05	17:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

2024/04/05	19:02:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	19:02:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	19:02:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	19:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/05	19:25:30.0	XRT_Custom_430_OG [0x1ae]							
2024/04/05	19:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	20:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	20:39:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	20:39:04.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	20:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/05	21:02:00.0	XRT_Custom_430_OG [0x1ae]							
2024/04/05	21:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	22:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/04/05	23:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 fa 36 01 db				
2024/04/05	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/04/05	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/04/05	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/04/05	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/04/05	23:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	23:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2024/04/05	23:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/04/05	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/05	23:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/05	23:53:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/05	23:53:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/05	23:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/06	00:13:30.0	XRT_Custom_430_OG [0x1ae]							
2024/04/06	00:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/06	01:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/06	01:30:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/06	01:30:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/04/06	01:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/04/06	01:37:30.0	XRT_Custom_430_OG [0x1ae]							
2024/04/06	01:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/06	02:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/06	02:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/04/06	02:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/04/06	02:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 03 74 01 db				
2024/04/06	02:20:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/04/06	02:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/04/06	02:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/04/06	02:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/04/06	02:20:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/04/06	02:22:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2024/04/06	02:22:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/04/06	02:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/04/06	02:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							

2024/04/06	02:53:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2024/04/06	02:53:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/04/06	02:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/04/06	03:13:00.0	XRT_Custom_430_OG [0x1ae]							
2024/04/06	03:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/04/06	03:32:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/06	03:32:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/06	03:32:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2024/04/06	03:33:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2024/04/06	03:33:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/04/06	03:33:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/04/06	03:33:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/04/06	03:33:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/04/06	03:33:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/04/06	03:35:56.0	XRT_OT_PROG_SET_436_OG [0x1b4]	MDP_XRT_OT_PROG_SET	2	07-F0	c4 12			
2024/04/06	03:35:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2024/04/06	03:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/04/06	04:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/06	04:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/04/06	04:27:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/04/06	04:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/04/06	04:50:00.0	XRT_Custom_430_OG [0x1ae]							
2024/04/06	04:51:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/04/06	05:32:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/06	05:32:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/06	05:32:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2024/04/06	05:33:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2024/04/06	05:33:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2024/04/06	05:33:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2024/04/06	05:33:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/04/06	05:35:58.0	XRT_OT_PROG_SET_401_OG [0x191]	MDP_XRT_OT_PROG_SET	2	07-F0	c4 13			
2024/04/06	05:36:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/04/06	05:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/06	05:42:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/06	05:42:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2024/04/06	05:43:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 03 74 01 db			
2024/04/06	05:43:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/04/06	05:43:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/04/06	05:43:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/04/06	05:43:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/04/06	05:43:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/04/06	05:45:56.0	XRT_OT_PROG_SET_441_OG [0x1b9]	MDP_XRT_OT_PROG_SET	2	07-F0	c4 08			
2024/04/06	05:45:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2024/04/06	05:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/04/06	05:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/04/06	05:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/04/06	05:59:34.0	XRT_PREFLR_STRT_433_OG [0x1b1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			

2024/04/06	06:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2024/04/06	06:27:00.0	XRT_Custom_430_OG [0x1ae]	
2024/04/06	06:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2024/04/06	07:39:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1
2024/04/06	07:39:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1 07-F0 da
2024/04/06	07:39:34.0	XRT_PREFLR_STRT_433_OG [0x1b1] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2024/04/06	07:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2024/04/06	08:04:30.0	XRT_Custom_430_OG [0x1ae]	
2024/04/06	08:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2024/04/06	09:19:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1
2024/04/06	09:19:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1 07-F0 da
2024/04/06	09:19:34.0	XRT_PREFLR_STRT_433_OG [0x1b1] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2024/04/06	09:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2024/04/06	09:41:00.0	XRT_Custom_430_OG [0x1ae]	
2024/04/06	09:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2024/04/06	11:00:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1 07-F0 c1
2024/04/06	11:00:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1 07-F0 da
2024/04/06	11:00:04.0	XRT_PREFLR_STRT_433_OG [0x1b1] MDP_XRT_PREFLR_STRT	1 07-F0 e8
2024/04/06	11:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1 07-F0 e9
2024/04/06	11:13:30.0	XRT_Custom_430_OG [0x1ae]	
2024/04/06	11:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1 07-F0 c0
2024/04/06	11:33:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1 07-F0 c1
2024/04/06	11:34:00.0	AOCS_Or-point_Start_3_OG [0x099] AOCU_NM	5 02-76 00 00 00 00 00