XRT Timeline to be uploaded on 2024/04/11

Period: 2024/04/11 10:58:00 - 2024/04/16 11:04:00

* * * * * **Normal mode** * * * * *

XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/11 11:11:00 - 04/11 17:30:30	Track (451.0, 170.5) @ 04/11 11:08:00	# OP start + 10min + AR13628
04/12 00:21:30 - 04/12 03:40:54	Track (551.0, 163.2) @ 04/12 00:10:00	AR13628
04/12 05:54:00 - 04/12 11:09:54	Track (591.8, 159.6) @ 04/12 05:51:00	AR13628
04/13 06:12:30 - 04/13 11:40:54	Track (743.8, 142.2) @ 04/13 06:09:30	AR13628

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 05 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec												
Seqn= 47 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96 4-time(s) 180.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5795

Term	Pointing (x, y)	Comment
04/11 18:00:30 - 04/11 18:07:24	Fixed (0.0, 0.0)	synoptic, shifted -2.5 min
04/12 05:44:00 - 04/12 05:50:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted
04/12 17:45:30 - 04/12 17:52:24	Fixed (0.0, 0.0)	synoptic, shifted -17.5 min
04/13 06:02:30 - 04/13 06:09:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 19 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 83 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 41 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec												
med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

XOB #1D11: HOP480 - Filter-Ratio with thin-Be and Med-Be (short) with PFB, 384x384 at 1064 1048, with G-band (1ms/1ms VLS=CLS), 60 cad

Term	Pointing (x, y)	Comment
04/11 18:10:30 - 04/12 00:09:54	Track (-636.6, 348.8) @ 04/11 18:07:30	HOP480
04/12 17:55:30 - 04/13 00:24:54	Track (-472.8, 360.3) @ 04/12 17:52:30	HOP480

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 13 Inf.-time(s)												
Subr= 1 1-time(s) 10.0sec												
Seqn= 91 2-time(s) 5.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 240-time(s) 60.0sec												
Seqn= 65 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 64 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Subr= 3 240-time(s) 60.0sec												
Seqn= 65 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 64 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec

XOB #1D09: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 90min cad) + CME wat

Term	Pointing (x, y)	Comment
04/12 03:44:00 - 04/12 05:40:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted
04/13 04:08:30 - 04/13 05:59:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 18 Inf.-time(s)												
Subr= 1 1-time(s) 600.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 98 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 7-time(s) 600.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74 1-time(s) 2.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
04/12 11:21:30 - 04/12 17:42:24	Fixed (-21.0, 861.0)	HOP81 N-Pole

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
PROG= 16 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 16 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 30.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3 60-time(s) 60.0sec												
Seqn= 24 1-time(s) 30.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

XOB #1CD9: HOP393 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s c

Term	Pointing (x, y)	Comment

04/13 00:28:00 - 04/13 03:29:54	Fixed (-21.0, 57.0)							HOP393						
PROG= 11 Inf.-time(s)														
Subr= 1 1-time(s) 2.0sec														
Seqn= 30 1-time(s) 2.0sec														
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 52 1-time(s) 2.0sec														
Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec		
Subr= 2 30-time(s) 360.0sec														
Seqn= 97 2-time(s) 2.0sec														
Al-poly/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec		
Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval		

Flare mode

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment
04/11 11:11:00 - 04/11 17:30:30	Track (451.0, 170.5) @ 04/11 11:08:00	# OP start + 10min + AR13628
04/12 00:21:30 - 04/12 03:40:54	Track (551.0, 163.2) @ 04/12 00:10:00	AR13628
04/12 03:44:00 - 04/12 05:40:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted
04/12 05:54:00 - 04/12 11:09:54	Track (591.8, 159.6) @ 04/12 05:51:00	AR13628
04/12 11:21:30 - 04/12 17:42:24	Fixed (-21.0, 861.0)	HOP81 N-Pole
04/13 00:28:00 - 04/13 03:29:54	Fixed (-21.0, 57.0)	HOP393
04/13 04:08:30 - 04/13 05:59:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted
04/13 06:12:30 - 04/13 11:40:54	Track (743.8, 142.2) @ 04/13 06:09:30	AR13628

PROG= 14 30-time(s)														
Subr= 1 20-time(s) 2.0sec														
Seqn= 11 1-time(s) 2.0sec														
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 73 1-time(s) 10.0sec														
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2 1-time(s) 2.0sec														
Seqn= 10 1-time(s) 2.0sec														
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11 1-time(s) 2.0sec														
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 87 1-time(s) 2.0sec														
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval		

XOB #1D10: Flare - multifilter 5 sec cadence (Be/thin, Be/med), AEC 3, 384x384

Term	Pointing (x, y)	Comment
04/11 18:10:30 - 04/12 00:09:54	Track (-636.6, 348.8) @ 04/11 18:07:30	HOP480
04/12 17:55:30 - 04/13 00:24:54	Track (-472.8, 360.3) @ 04/12 17:52:30	HOP480

PROG= 15 1-time(s)														
Subr= 1 1-time(s) 2.0sec														
Seqn= 9 1-time(s) 2.0sec														
thin-Be/Open	med-Be/Open	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec		
Subr= 2 1-time(s) 2.0sec														
Seqn= 49 255-time(s) 5.0sec														
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval		

Active Region Search

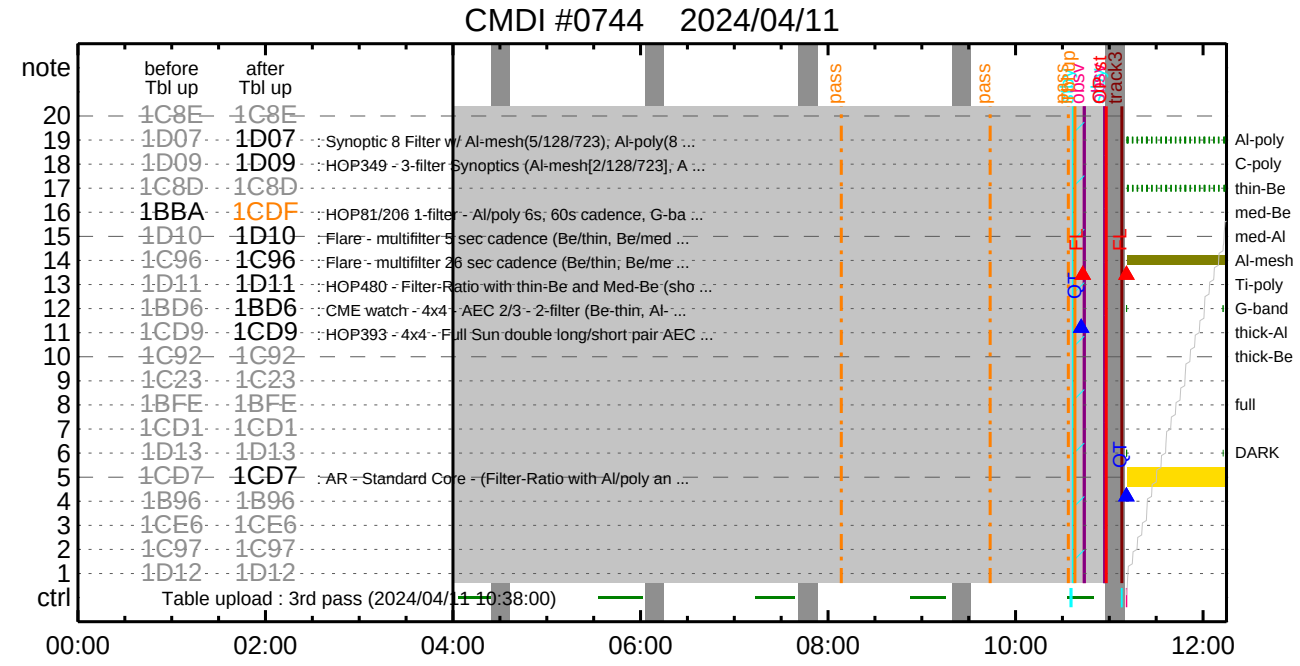
NOT USED

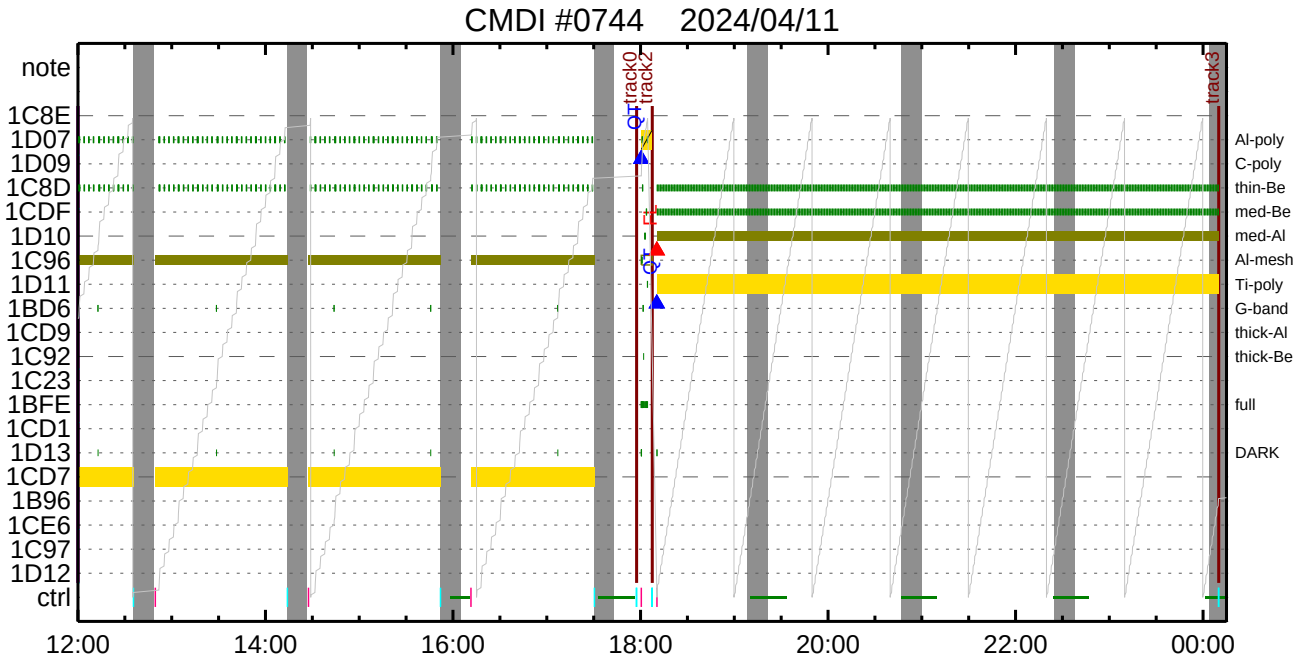
Flare Detection

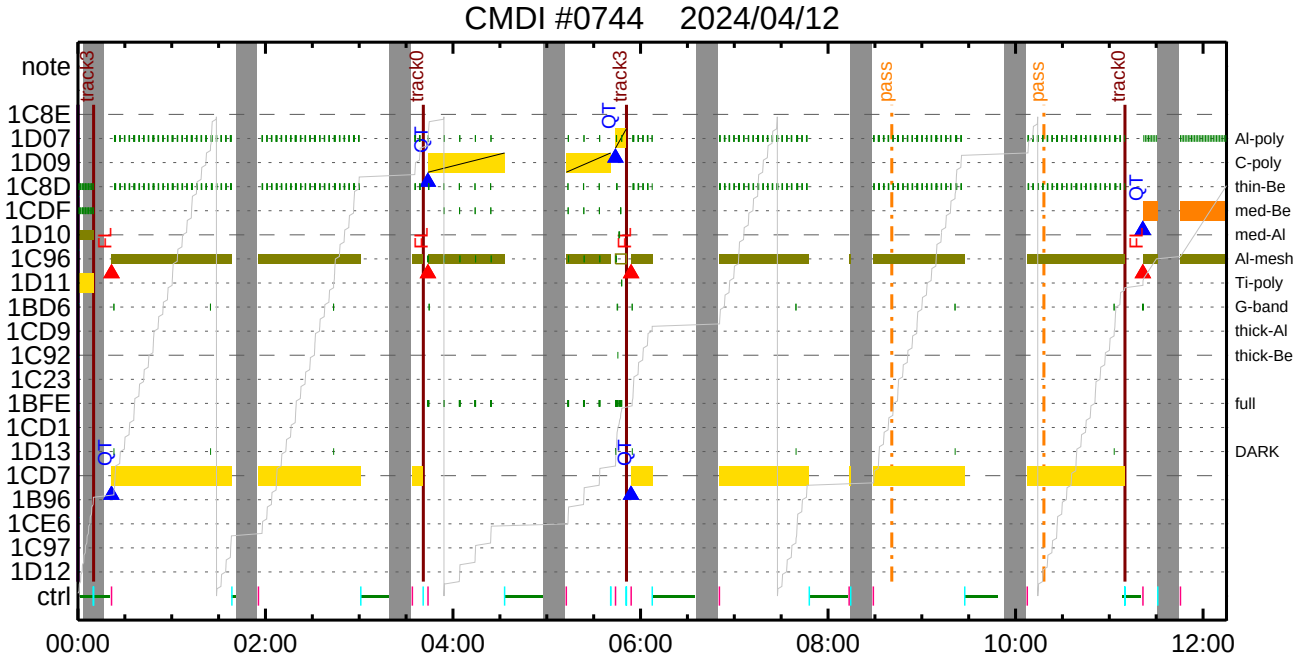
FLD Patrol

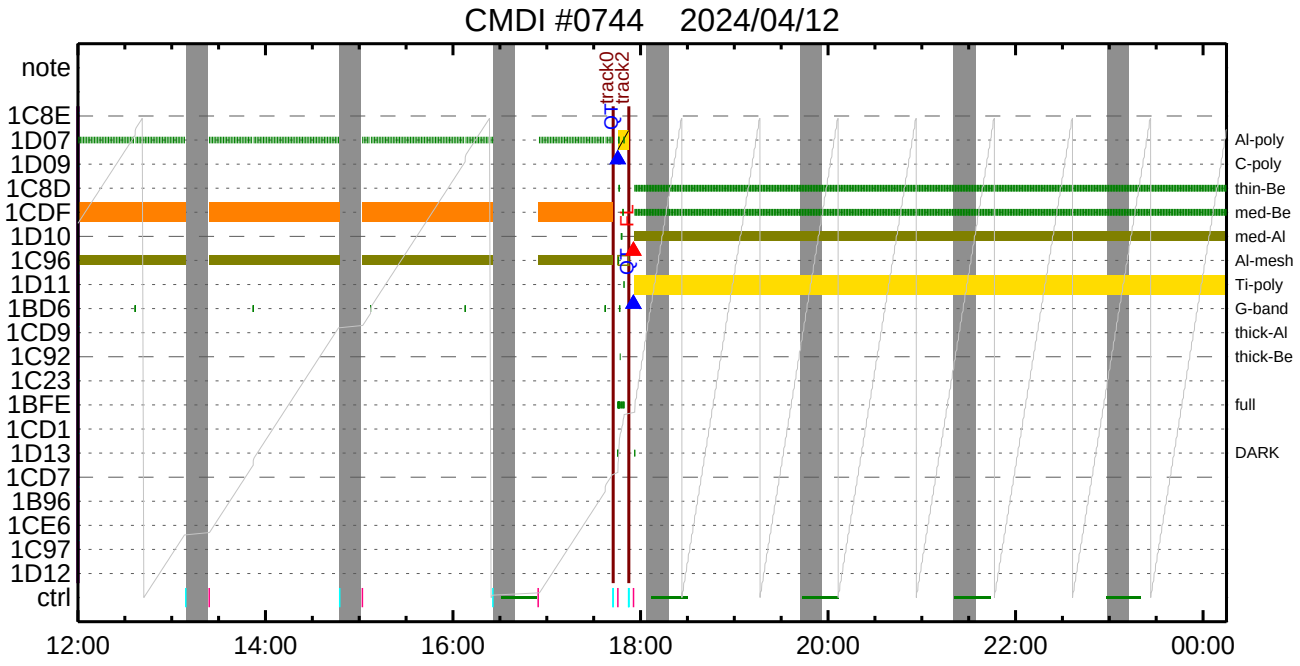
Term	Pointing (x, y)	Comment
04/11 10:39:00 - 04/11 17:57:48	cannot be identified	
04/11 18:07:48 - 04/12 05:41:18	Track (-636.6, 348.8) @ 04/11 18:07:30	HOP480
04/12 05:51:18 - 04/12 17:42:48	Track (591.8, 159.6) @ 04/12 05:51:00	AR13628
04/12 17:52:48 - 04/13 05:59:48	Track (-472.8, 360.3) @ 04/12 17:52:30	HOP480
04/13 06:09:48 - 04/16 11:04:00	Track (743.8, 142.2) @ 04/13 06:09:30	AR13628

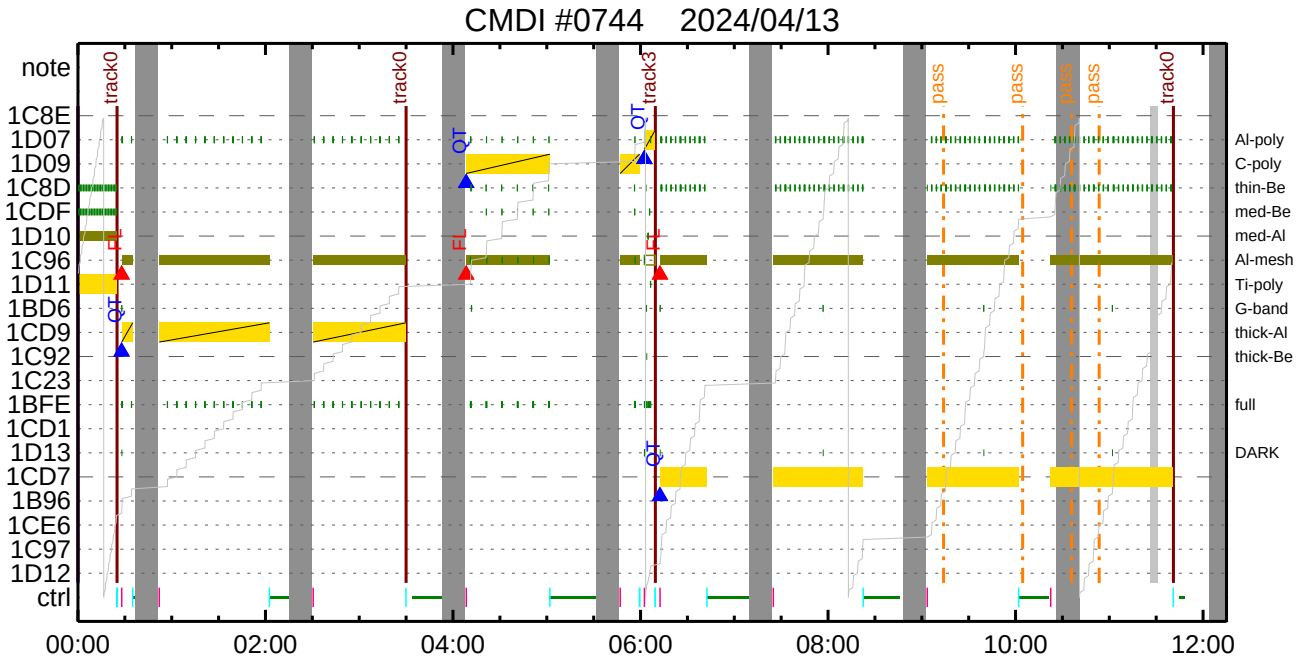
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50			30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval		

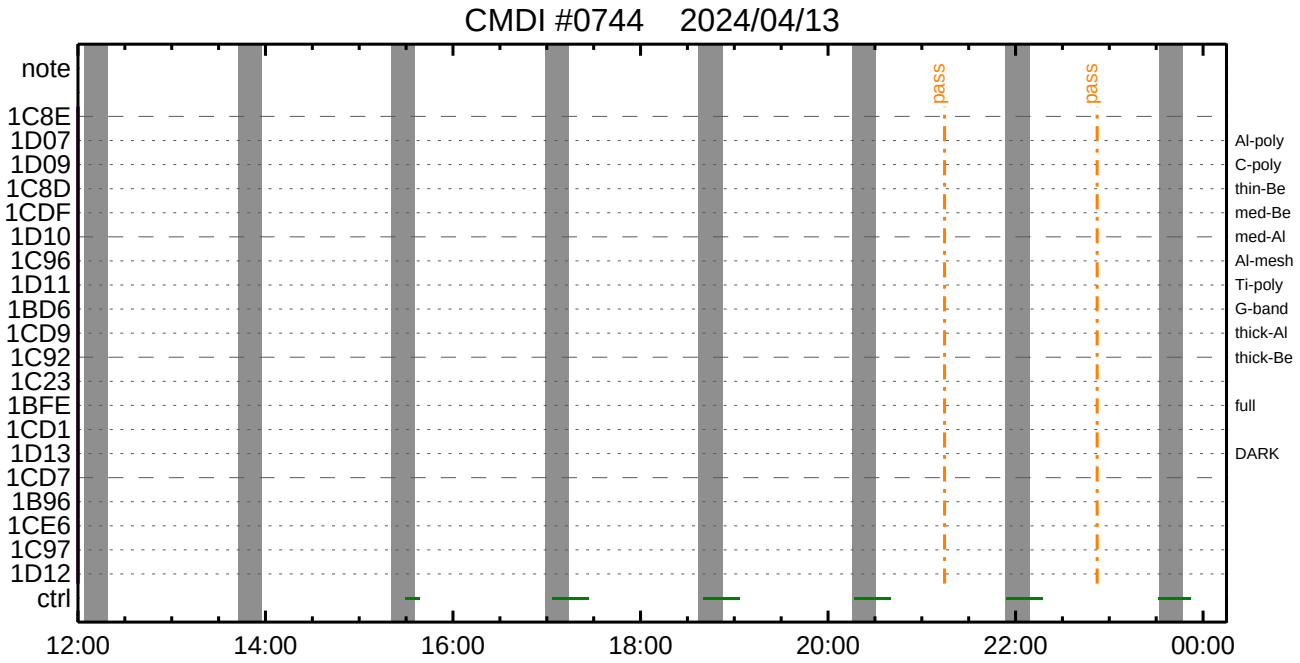

CMDI #0744 2024/04/11
note
before Tbl up
after Tbl up
1C8E
1D07
1D09
1C8D
1BBA
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
1CDF
1D07 : Synoptic 8 Filter w/ Al-mesh(5/128/723); Al-poly(8 ...
1D09 : HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], A ...
1BBA : HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-ba ...
1D10 : Flare - multifilter 5 sec cadence (Be/thin, Be/med ...
1C96 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
1D11 : HOP480 - Filter-Ratio with thin-Be and Med-Be (sho ...
1BD6 : CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al- ...
1CD9 : HOP393 - 4x4 - Full Sun double long/short pair AEC ...
1CD7 : AR - Standard Core - (Filter-Ratio with Al/poly an ...
Table upload : 3rd pass (2024/04/11 10:38:00)
pass
pass
pass
obsv
obsv
track3
FL
FL
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

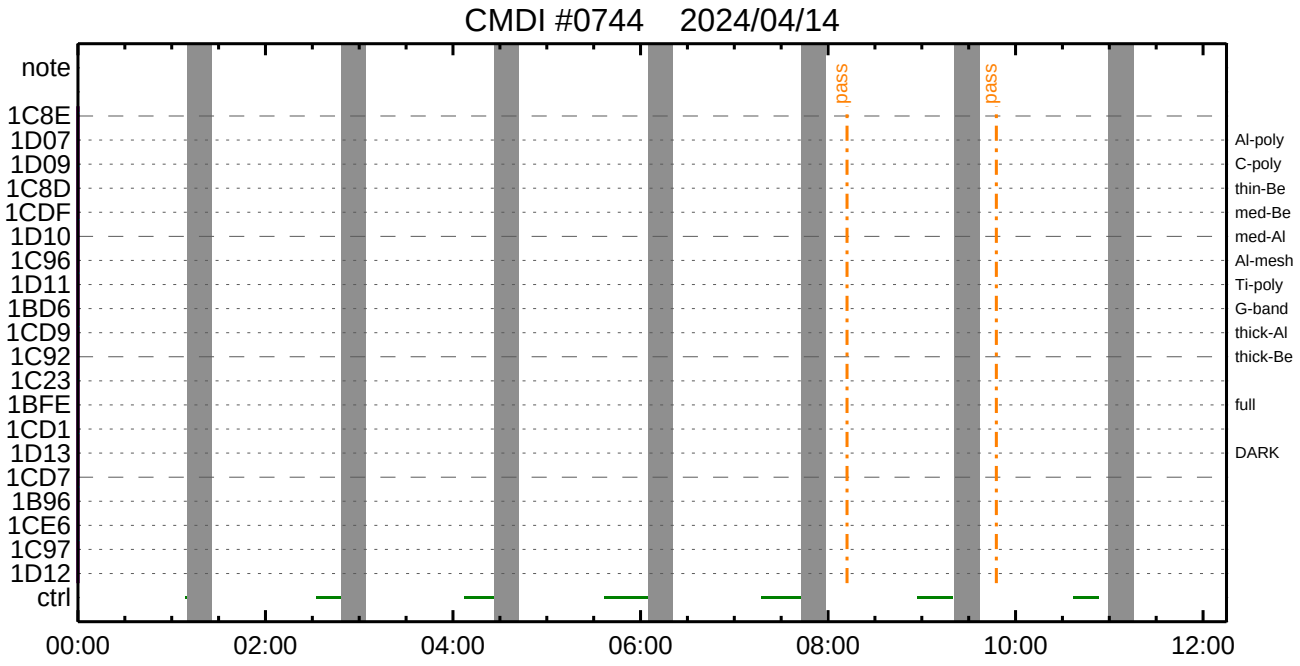

CMDI #0744 2024/04/11
note
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
track0
track2
track3
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

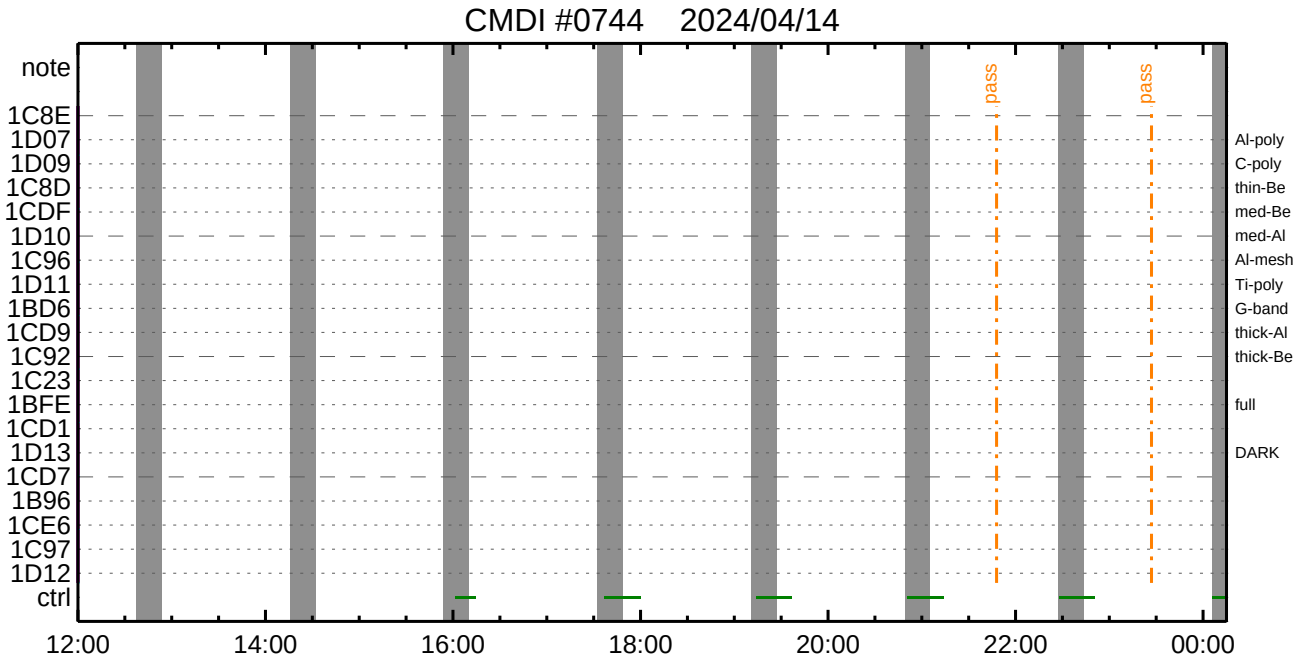

CMDI #0744 2024/04/12
note
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
track3
track0
track3
track0
pass
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

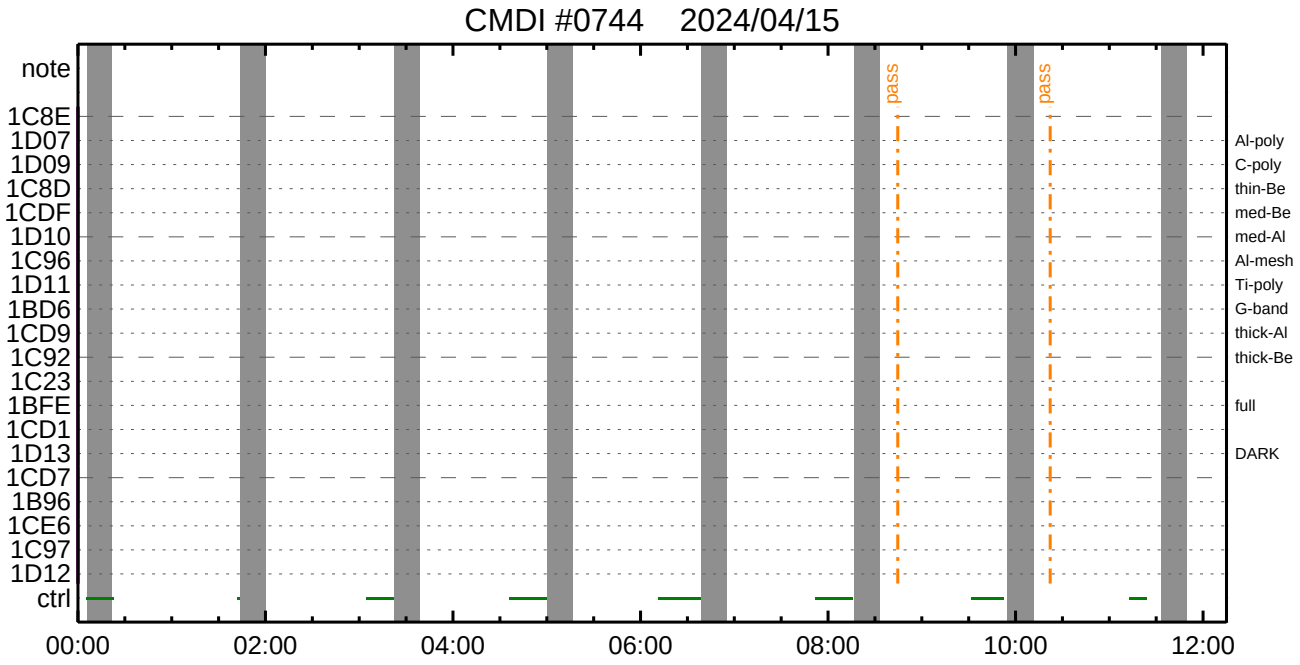

CMDI #0744 2024/04/12
note
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
track0
track2
QT
FL
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

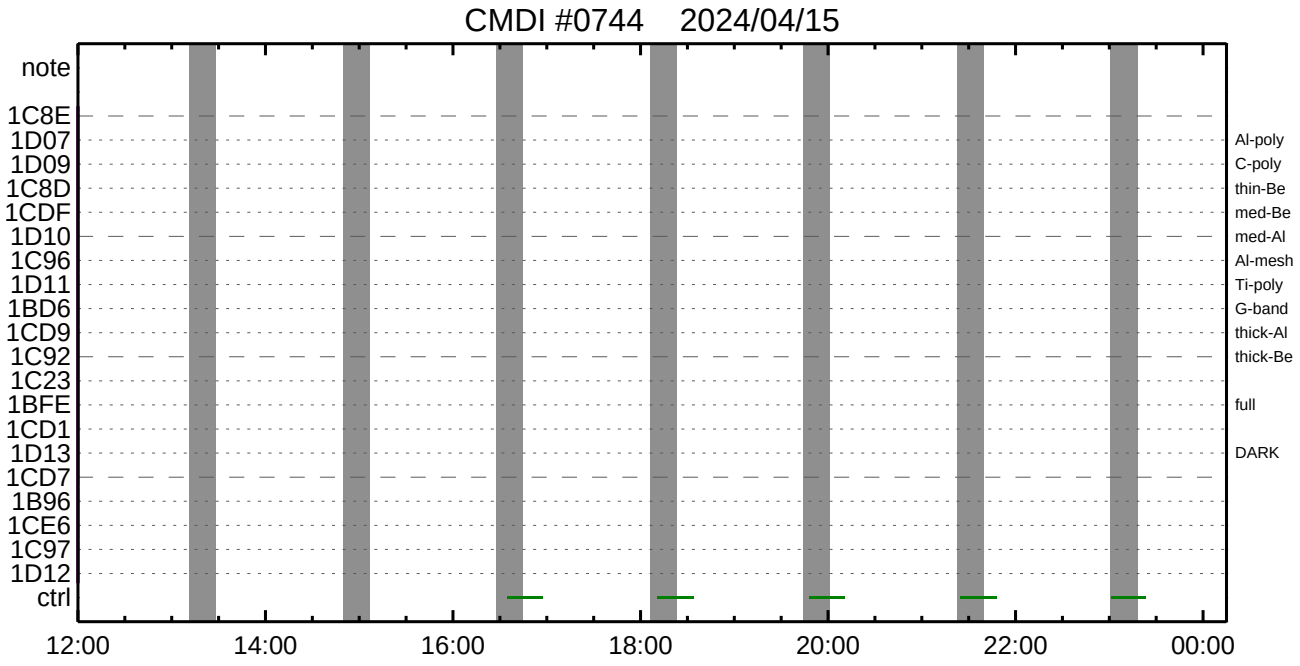

CMDI #0744 2024/04/13
note
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
track0
track0
track3
track0
pass
pass
pass
pass
QT
FL
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

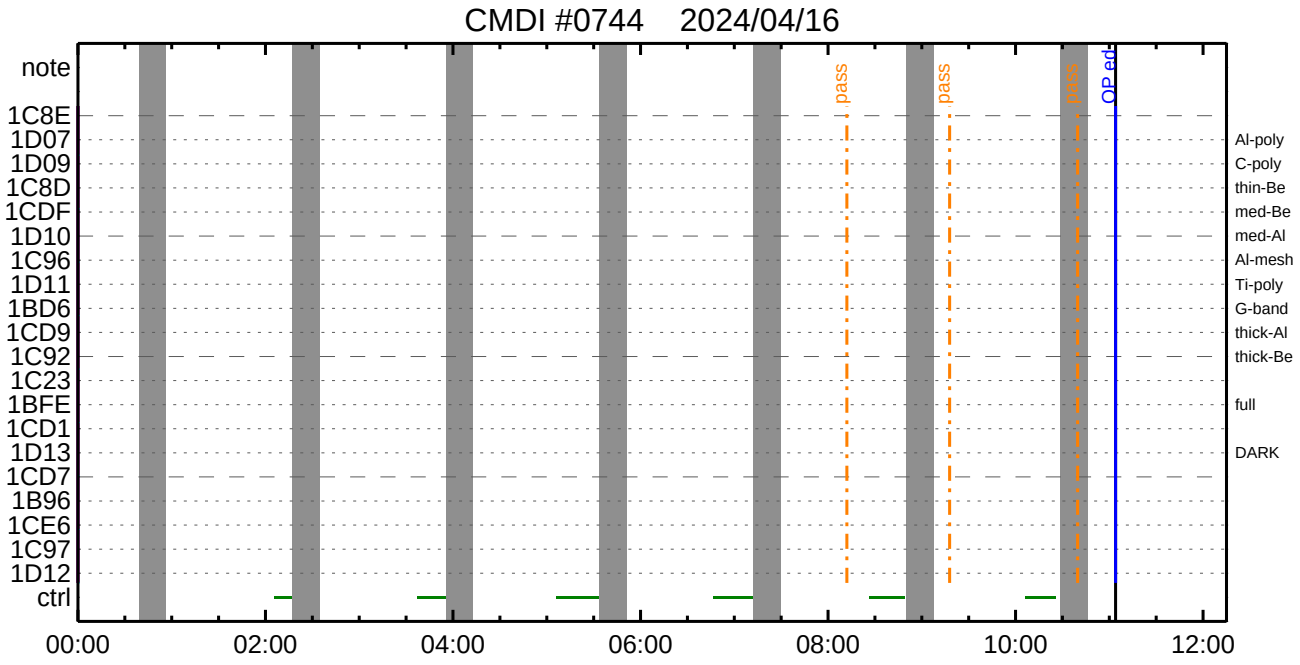

CMDI #0744 2024/04/13
note
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
pass
pass
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

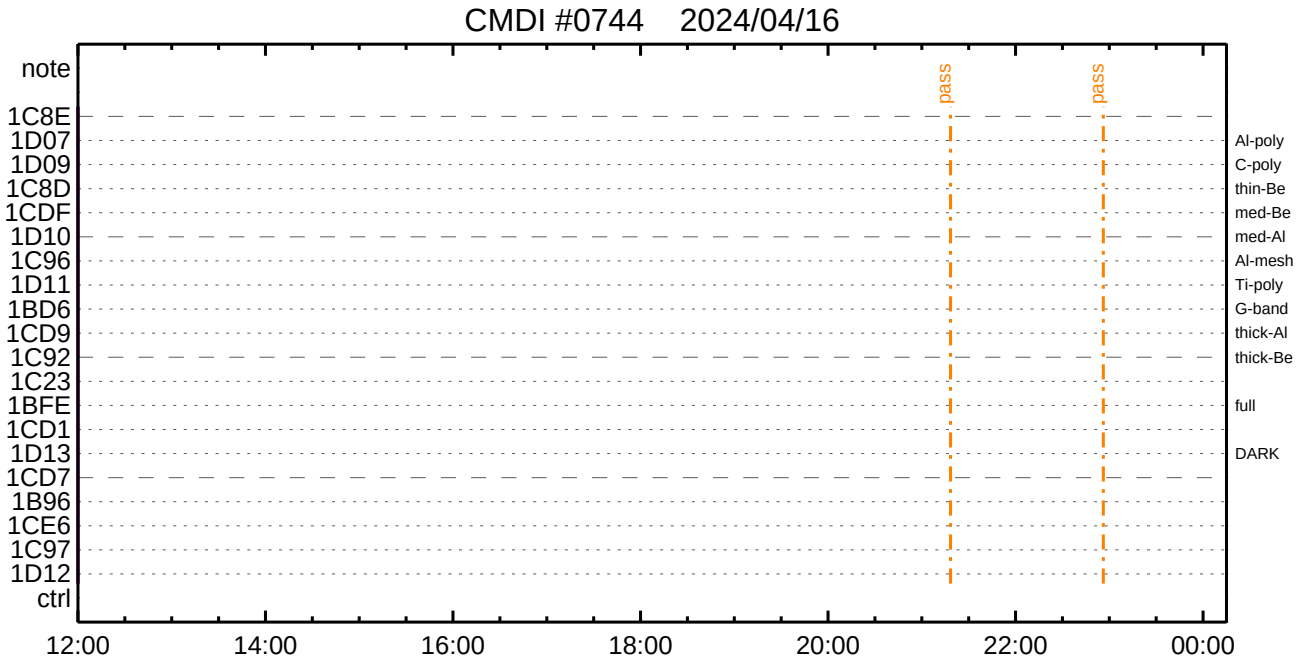

CMDI #0744 2024/04/14
note
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0744 2024/04/14
note
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0744 2024/04/15
note
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0744 2024/04/15
note
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0744 2024/04/16
note
pass
pass
pass
OP end
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0744 2024/04/16
note
pass
pass
1C8E
1D07
1D09
1C8D
1CDF
1D10
1C96
1D11
1BD6
1CD9
1C92
1C23
1BFE
1CD1
1D13
1CD7
1B96
1CE6
1C97
1D12
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-495 2024-04-11 11:21:50 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-495:OP
0104     ()
0105   . S. OG    og-495:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¥Ç¹Ô¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2024-04-11 10:53:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2024-04-11 10:53:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2024-04-11 10:53:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2024-04-11 10:57:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2024-04-11 10:57:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2024-04-11 10:57:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ************
0252     C.
0253     C.
0254     C.
0255     C. ************ XRT START ************
0256     C. Execute, after the success of OP upload.
0257   +. TI 2024-04-11 10:57:00.0
0258     DC 07-F0 MDP_XRT_MODE_STBY
0259     BC       (c3)
0260    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0261     C.
0262     C. ************ XRT END ************
0263     C.
0264    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266    . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271    . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277    . C. *********** LOS ***********
0278     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-496 2024-04-11 11:21:50 169 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_CHG_ENA
0131     BC       (20)
0132   . C. Verify EIS_MODE_CHG_FLG is ENA
0133   +. DC 07-FC EIS_MODE_MANU
0134     BC       (21 02)
0135   . C. Verify EIS in MANUAL mode
0136   . C. Estimated OBSTBL upload time is 14s
0137     C. *********************************
0138     C. EIS START OBSTBL LOAD
0139     C. *********************************
0140   . S. RAM   ram-820:EIS_OBSTBL
0141     ()
0142   +. DC 07-FC EIS_DUMP_OBSTBL
0143     BC       (07 07 07 00 00 70 00)
0144     C.
0145     C. Execute, after the success of OBSTBL upload.
0146     C. Set EIS TI-commands
0147   +. TI 2024-04-11 10:57:50.0
0148     DC 07-FC EIS_MODE_CHG_ENA
0149     BC       (20)
0150   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0151     C. *********************************
0152     C. EIS END OBSTBL LOAD
0153     C. *********************************
0154     C.
0155   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0156     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0157   . S. DC-BC dcbc-402:DCBC
0158     (MDP_known_event)
0159     C.
0160     C.
0161   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0162   . S. DC-BC dcbc-153:DCBC
0163     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164     C.
0165     C.
0166   . C. ¡ãLOS¥Á¥Ş¥Ã¥¯¼Â»Ü¡ä
0167     C.
0168   . C. *********** LOS ***********
0169     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-497 2024-04-11 11:21:51 100 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +. DC 07-F0 MDP_XRT_CTRL_MANU
0020     BC       (c1)
0021   +  DC 07-F0 MDP_XRT_MODE_STBY
0022     BC       (c3)
0023   . C. ----------- Success Verify ?              OK / NG____
0024     C.
0025     C. XRT Obs. Table Upload
0026   . S. RAM   ram-291:MDP_OBS_X
0027     ()
0028     C.
0029   +. DC 07-F0 MDP_DUMP_XRTTBL
0030     BC       (84 07 00 00 00 3a d4)
0031   . C. ----------- Comparison Check ?                        OK / ERR ____
0032     C.
0033     C.
0034   +. DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 01 b1 b1 04 04)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 02 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 03 b1 b1 08 08)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 04 b1 b1 06 06)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 05 85 83 06 06)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 06 80 80 08 08)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 07 80 80 20 20)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 08 85 83 06 06)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 09 80 80 20 08)
0052   +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 0a 80 80 08 20)
0054   +  DC 07-F0 MDP_XRT_ROI_SET
0055     BC       (cd 0f 80 80 06 06)
0056   +  DC 07-F0 MDP_XRT_ROI_SET
0057     BC       (cd 10 80 80 08 08)
0058   +  DC 07-F0 MDP_XRT_FLD_ENA
0059     BC       (d8)
0060   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061     BC       (c8)
0062   +  DC 07-F0 MDP_XRT_ARS_DIS
0063     BC       (d5)
0064   +  DC 07-F0 MDP_XRT_AEC_RESET
0065     BC       (d0)
0066   +  DC 07-F0 MDP_XRT_FLD_RESET
0067     BC       (da)
0068   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069     BC       (c4 0c)
0070   +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071     BC       (c5 0e)
0072   . C. ---------- Success Verify ?            OK / NG ____
0073     C.
0074     C.
0075   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0076     C.
0077   +. DC 07-F0 MDP_XRT_MODE_OBSV
0078     BC       (c2)
0079   +. TI 2024-04-11 10:57:02.0
0080     DC 07-F0 MDP_XRT_MODE_OBSV
0081     BC       (c2)
0082   . C. ---------- Success Verify ?            OK / NG ____
0083     C.
0084     C. ************ XRT END ***********
0085     C.
0086   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0087     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0088   . S. DC-BC dcbc-402:DCBC
0089     (MDP_known_event)
0090     C.
0091     C.
0092   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0093   . S. DC-BC dcbc-153:DCBC
0094     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095     C.
```

```
0096     C.
0097   . C. ¡ãLOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0098     C.
0099   . C. *********** LOS ***********
0100     C.
```

```
*** OP Sequence for XRT ***

2024/04/11  11:07:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/11  11:07:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/11  11:07:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/04/11  11:08:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 03 74 01 db
2024/04/11  11:08:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2024/04/11  11:08:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/04/11  11:08:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2024/04/11  11:08:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/11  11:08:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/11  11:10:56.0  XRT_QT_PROG_SET_428_OG [0x1ac]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2024/04/11  11:10:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/04/11  11:11:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/11  12:35:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/11  12:35:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/11  12:35:34.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/11  12:38:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/11  12:48:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/11  12:49:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/11  14:14:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/11  14:14:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/11  14:14:04.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/11  14:17:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/11  14:26:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/11  14:27:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/11  15:52:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/11  15:52:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/11  15:52:04.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/11  15:55:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/11  16:10:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/11  16:11:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/11  17:30:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/11  17:30:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/11  17:30:34.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/11  17:33:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/11  17:57:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/11  17:57:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/11  17:57:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/04/11  17:57:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2024/04/11  17:57:48.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/04/11  17:57:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/04/11  17:57:52.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/11  18:00:28.0  XRT_QT_PROG_SET_401_OG [0x191]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2024/04/11  18:00:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/11  18:07:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/11  18:07:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/11  18:07:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/04/11  18:07:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 02 03 74 01 db
```

```
2024/04/11  18:07:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2024/04/11  18:07:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/04/11  18:07:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2024/04/11  18:07:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/11  18:07:56.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/11  18:10:26.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2024/04/11  18:10:28.0  XRT_FL_PROG_SET_431_OG [0x1af]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0f
2024/04/11  18:10:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  00:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  00:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  00:09:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/04/12  00:10:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 03 74 01 db
2024/04/12  00:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2024/04/12  00:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/04/12  00:10:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2024/04/12  00:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/12  00:10:26.0  XRT_FLD_RESET_425_OG [0x1a9]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  00:21:26.0  XRT_QT_PROG_SET_428_OG [0x1ac]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2024/04/12  00:21:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/04/12  00:21:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  01:38:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  01:38:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  01:38:34.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/12  01:41:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/12  01:54:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/12  01:55:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  03:01:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  03:01:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  03:01:04.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/12  03:04:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/12  03:33:00.0  XRT_Custom_430_OG [0x1ae]
2024/04/12  03:34:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  03:40:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  03:40:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/04/12  03:41:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2024/04/12  03:41:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2024/04/12  03:41:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/04/12  03:41:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2024/04/12  03:41:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/12  03:41:24.0  XRT_FLD_RESET_438_OG [0x1b6]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  03:43:56.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2024/04/12  03:43:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/04/12  03:44:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  04:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  04:33:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  04:33:04.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/12  04:36:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/12  05:11:30.0  XRT_Custom_430_OG [0x1ae]
```

```
2024/04/12  05:12:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  05:40:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  05:40:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  05:40:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/04/12  05:41:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/04/12  05:41:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/04/12  05:41:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/12  05:43:58.0  XRT_QT_PROG_SET_401_OG [0x191]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2024/04/12  05:44:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  05:50:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  05:50:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  05:50:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/04/12  05:51:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 03 74 01 db
2024/04/12  05:51:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2024/04/12  05:51:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/04/12  05:51:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2024/04/12  05:51:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/12  05:51:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  05:53:56.0  XRT_QT_PROG_SET_428_OG [0x1ac]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2024/04/12  05:53:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/04/12  05:54:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  06:07:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  06:07:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  06:07:34.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/12  06:10:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/12  06:49:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/12  06:50:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  07:48:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  07:48:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  07:48:04.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/12  07:51:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/12  08:12:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/12  08:13:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  08:14:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  08:14:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  08:14:34.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/12  08:17:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/12  08:28:00.0  XRT_Custom_430_OG [0x1ae]
2024/04/12  08:29:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  09:27:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  09:27:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  09:27:34.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/12  09:30:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/12  10:06:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/12  10:07:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/12  11:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  11:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/12  11:09:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/04/12  11:10:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
```

```
                                        AOCU_NM               5 02-76 00 b3 75 01 db
2024/04/12  11:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA       1 07-F0 d8
2024/04/12  11:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA   1 07-F0 c8
2024/04/12  11:10:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET     1 07-F0 d0
2024/04/12  11:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS       1 07-F0 d5
2024/04/12  11:10:26.0  XRT_FLD_RESET_425_OG [0x1a9]
                                        MDP_XRT_FLD_RESET     1 07-F0 da
2024/04/12  11:21:26.0  XRT_QT_PROG_SET_407_OG [0x197]
                                        MDP_XRT_QT_PROG_SET   2 07-F0 c4 10
2024/04/12  11:21:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET   2 07-F0 c5 0e
2024/04/12  11:21:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO     1 07-F0 c0
2024/04/12  11:31:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/04/12  11:31:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET     1 07-F0 da
2024/04/12  11:31:04.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STRT   1 07-F0 e8
2024/04/12  11:34:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP   1 07-F0 e9
2024/04/12  11:44:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/12  11:45:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO     1 07-F0 c0
2024/04/12  13:09:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/04/12  13:09:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET     1 07-F0 da
2024/04/12  13:09:04.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STRT   1 07-F0 e8
2024/04/12  13:12:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP   1 07-F0 e9
2024/04/12  13:23:00.0  XRT_Custom_430_OG [0x1ae]
2024/04/12  13:24:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO     1 07-F0 c0
2024/04/12  14:47:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/04/12  14:47:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET     1 07-F0 da
2024/04/12  14:47:34.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STRT   1 07-F0 e8
2024/04/12  14:50:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP   1 07-F0 e9
2024/04/12  15:01:00.0  XRT_Custom_430_OG [0x1ae]
2024/04/12  15:02:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO     1 07-F0 c0
2024/04/12  16:25:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/04/12  16:25:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET     1 07-F0 da
2024/04/12  16:25:34.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                        MDP_XRT_PREFLR_STRT   1 07-F0 e8
2024/04/12  16:28:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP   1 07-F0 e9
2024/04/12  16:53:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/12  16:54:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO     1 07-F0 c0
2024/04/12  17:42:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/04/12  17:42:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/04/12  17:42:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2024/04/12  17:42:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM               5 02-76 00 00 00 00 00
2024/04/12  17:42:48.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS       1 07-F0 d9
2024/04/12  17:42:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2024/04/12  17:42:52.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS       1 07-F0 d5
2024/04/12  17:45:28.0  XRT_QT_PROG_SET_401_OG [0x191]
                                        MDP_XRT_QT_PROG_SET   2 07-F0 c4 13
2024/04/12  17:45:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO     1 07-F0 c0
2024/04/12  17:52:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/04/12  17:52:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/04/12  17:52:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION    4 07-F8 22 fe 97 00
2024/04/12  17:52:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM               5 02-76 02 03 74 01 db
2024/04/12  17:52:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA       1 07-F0 d8
2024/04/12  17:52:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA   1 07-F0 c8
2024/04/12  17:52:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET     1 07-F0 d0
```

```
2024/04/12  17:52:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/12  17:52:56.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/12  17:55:26.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2024/04/12  17:55:28.0  XRT_FL_PROG_SET_431_OG [0x1af]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0f
2024/04/12  17:55:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  00:24:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  00:24:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  00:24:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/04/13  00:25:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 fa f2 01 db
2024/04/13  00:25:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2024/04/13  00:25:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/04/13  00:25:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2024/04/13  00:25:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/13  00:25:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/13  00:27:56.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2024/04/13  00:27:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/04/13  00:28:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  00:35:01.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  00:35:03.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/13  00:35:05.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/13  00:38:15.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/13  00:51:00.0  XRT_Custom_430_OG [0x1ae]
2024/04/13  00:52:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  02:02:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  02:02:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/13  02:02:34.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/13  02:05:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/13  02:29:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/13  02:30:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  03:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  03:29:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/04/13  03:30:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2024/04/13  03:30:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2024/04/13  03:30:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/04/13  03:30:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2024/04/13  03:30:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/13  03:30:24.0  XRT_FLD_RESET_437_OG [0x1b5]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/13  04:08:26.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2024/04/13  04:08:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/04/13  04:08:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  05:02:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  05:02:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/13  05:02:04.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/13  05:05:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/13  05:46:00.0  XRT_Custom_430_OG [0x1ae]
2024/04/13  05:47:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  05:59:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  05:59:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
```

```
2024/04/13  05:59:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/04/13  05:59:48.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/04/13  05:59:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/04/13  05:59:52.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/13  06:02:28.0  XRT_QT_PROG_SET_401_OG [0x191]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2024/04/13  06:02:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  06:09:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  06:09:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  06:09:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/04/13  06:09:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 03 03 74 01 db
2024/04/13  06:09:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2024/04/13  06:09:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/04/13  06:09:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2024/04/13  06:09:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/04/13  06:09:56.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/13  06:12:26.0  XRT_QT_PROG_SET_428_OG [0x1ac]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2024/04/13  06:12:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/04/13  06:12:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  06:42:30.5  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  06:42:32.5  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/13  06:42:34.5  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/13  06:45:44.5  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/13  07:24:00.0  XRT_Custom_430_OG [0x1ae]
2024/04/13  07:25:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  08:22:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  08:22:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/13  08:22:34.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/13  08:25:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/13  09:02:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/13  09:03:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  10:02:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  10:02:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/04/13  10:02:04.0  XRT_PREFLR_STRT_433_OG [0x1b1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/04/13  10:05:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/04/13  10:21:30.0  XRT_Custom_430_OG [0x1ae]
2024/04/13  10:22:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/04/13  11:40:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/04/13  11:41:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
```