

XRT Timeline to be uploaded on 2024/05/02

Period: 2024/05/02 10:52:00 - 2024/05/07 11:30:00

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Normal mode

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XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/02 11:42:30 - 05/02 17:49:00	Track (-803.8, -311.7) ^{05/02 11:02:00}	# OP start + 10min + AR 13664 observation
05/03 06:19:30 - 05/03 17:59:54	Track (-708.7, -303.3) ^{05/03 06:16:30}	AR 13664 observation
05/04 06:31:00 - 05/04 10:29:00	Track (-556.7, -295.1) ^{05/04 06:28:00}	AR 13664 observation

PROG= 08 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 92	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2	4-time(s)	2.0sec
Seqn= 47	1-time(s)	2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 77	4-time(s)	300.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 95.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 95.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5792)

Term	Pointing (x, y)	Comment
05/02 18:26:00 - 05/02 18:32:54	Fixed (0.0, 0.0)	synoptic, shifted 23.0 min
05/03 06:09:30 - 05/03 06:16:24	Fixed (0.0, 0.0)	synoptic, shifted 6.5 min + HOP 349
05/03 18:03:00 - 05/03 18:09:54	Fixed (0.0, 0.0)	synoptic
05/04 06:21:00 - 05/04 06:27:54	Fixed (0.0, 0.0)	synoptic, shifted 18.0 min + HOP 349

PROG= 19 1-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 5	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 26	1-time(s)	2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 99	1-time(s)	2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 83	1-time(s)	2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 23	1-time(s)	4.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 41	1-time(s)	2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 17	1-time(s)	2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Al/Open	med-Al/Open close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 33	1-time(s)	2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 75	1-time(s)	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1CC2: HOP361 - High cadence (8s thin-Be only) 384x384 at 1064 1048

Term	Pointing (x, y)	Comment
05/02 18:36:00 - 05/02 22:44:00	Track (-769.6, -308.2) @ 05/02 18:33:00	AR 13664 observation
PROG= 04 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 22 250-time(s) 8.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

Term	Pointing (x, y)	Comment
XOB #1C39: CME watch - 4x4 - AEC 2 - Thin-Be - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 40s cad		
05/02 23:13:30 - 05/03 03:26:30	Track (201.7, 488.3) @ 05/02 23:00:00	HOP 473
05/03 23:03:00 - 05/04 03:59:30	Track (371.7, 482.0) @ 05/03 23:00:00	HOP 473
PROG= 15 Inf.-time(s)		
Subr= 1 50-time(s) 40.0sec		
Seqn= 20 1-time(s) 4.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

Term	Pointing (x, y)	Comment
XOB #1D09: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 90min cad) + CME watch		
05/03 04:03:00 - 05/03 04:54:30	Fixed (0.0, 0.0)	synoptic, shifted 6.5 min + HOP 349
05/04 04:39:00 - 05/04 05:29:30	Fixed (0.0, 0.0)	synoptic, shifted 18.0 min + HOP 349

Term	Pointing (x, y)	Comment
PROG= 18 Inf.-time(s)		
Subr= 1 1-time(s) 600.0sec		
Seqn= 55 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 98 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 79 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 7-time(s) 600.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 74 1-time(s) 2.0sec		
med-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 2.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 29 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter VLS	mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

Term	Pointing (x, y)	Comment
XOB #1CDE: HOP393/336 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 70s cad		
05/03 18:57:30 - 05/03 22:59:54	Track (-21.0, 25.6) @ 05/03 18:10:00	HOP 393

Term	Pointing (x, y)	Comment
PROG= 07 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 52 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Subr= 2 30-time(s) 720.0sec		
Seqn= 97 2-time(s) 2.0sec		
Al-poly/Open	med-Be/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	med-Be/Open close	Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-AI,thick-Be -384x384 + AI-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
05/02 11:42:30 - 05/02 17:49:00	Track (-803.8, -311.7) @ 05/02 11:02:00	# OP start + 10min + AR 13664 observation
05/02 18:36:00 - 05/02 22:44:00	Track (-769.6, -308.2) @ 05/02 18:33:00	AR 13664 observation
05/02 23:13:30 - 05/03 03:26:30	Track (201.7, 488.3) @ 05/02 23:00:00	HOP 473
05/03 04:03:00 - 05/03 04:54:30	Fixed (0.0, 0.0)	synoptic, shifted 6.5 min + HOP 349
05/03 06:19:30 - 05/03 17:59:54	Track (-708.7, -303.3) @ 05/03 06:16:30	AR 13664 observation
05/03 18:57:30 - 05/03 22:59:54	Track (-21.0, 25.6) @ 05/03 18:10:00	HOP 393
05/03 23:03:00 - 05/04 03:59:30	Track (371.7, 482.0) @ 05/03 23:00:00	HOP 473
05/04 04:39:00 - 05/04 05:29:30	Fixed (0.0, 0.0)	synoptic, shifted 18.0 min + HOP 349
05/04 06:31:00 - 05/04 10:29:00	Track (-556.7, -295.1) @ 05/04 06:28:00	AR 13664 observation

PROG= 14 30-time(s)

Subr= 1 20-time(s) 2.0sec														
├─ Seqn= 11 1-time(s) 2.0sec		Al-poly/Open	Al-poly/thick-AI	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
├─ Seqn= 73 1-time(s) 10.0sec		thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		med-Be/Open	Open/thick-AI	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec														
├─ Seqn= 10 1-time(s) 2.0sec		med-AI/Open	med-AI/thick-AI	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
├─ Seqn= 11 1-time(s) 2.0sec		Al-poly/Open	Al-poly/thick-AI	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
├─ Seqn= 87 1-time(s) 2.0sec		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

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Active Region Search

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NOT USED

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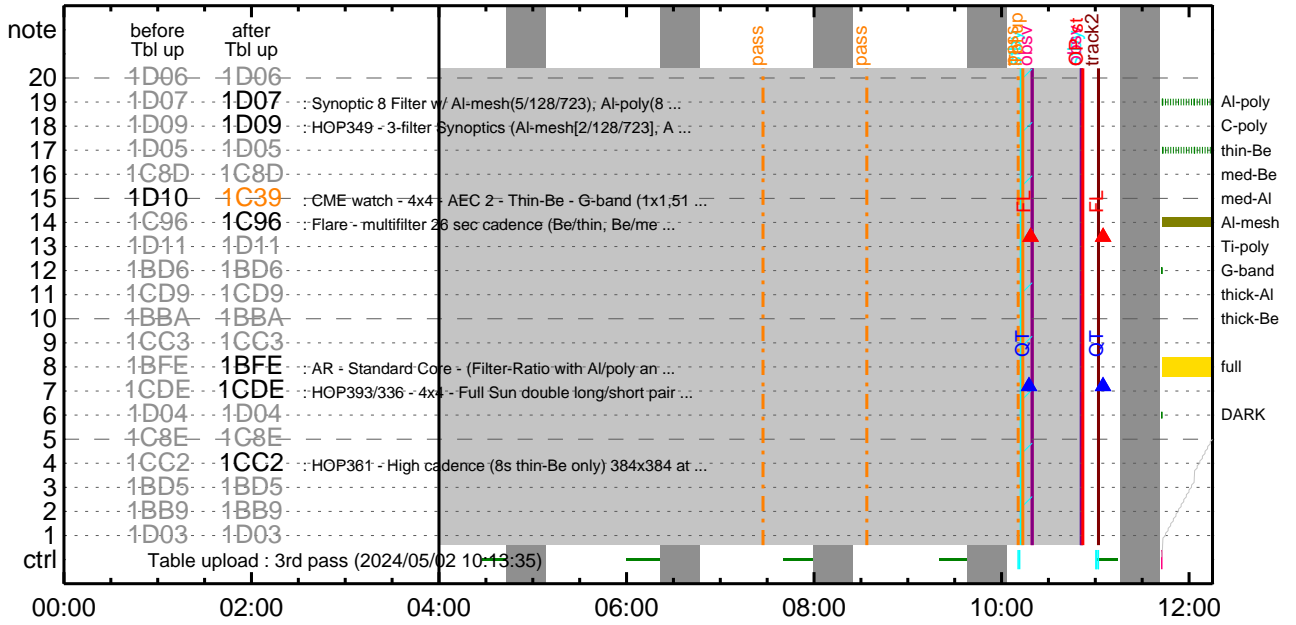
Flare Detection

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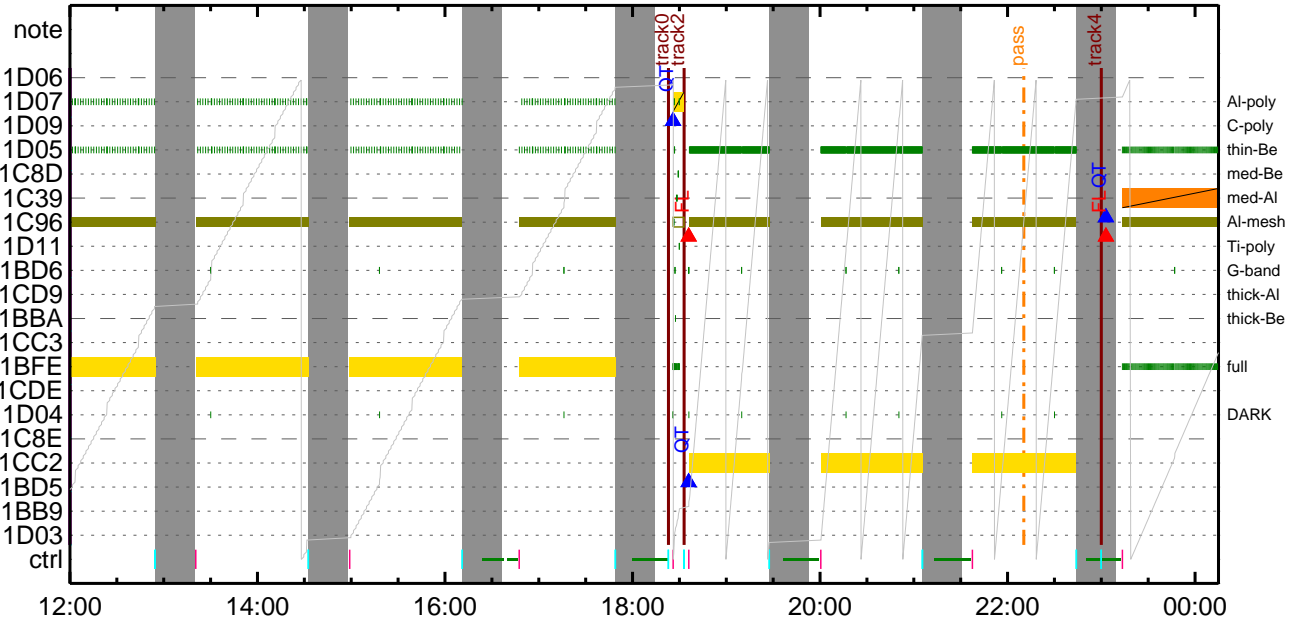
FLD Patrol

Term	Pointing (x, y)	Comment
05/02 10:14:35 - 05/02 18:23:18	cannot be identified	
05/02 18:33:18 - 05/03 06:06:48	Track (-769.6, -308.2) @ 05/02 18:33:00	AR 13664 observation
05/03 06:16:48 - 05/03 18:00:18	Track (-708.7, -303.3) @ 05/03 06:16:30	AR 13664 observation
05/03 18:10:18 - 05/04 06:18:18	Track (-21.0, 25.6) @ 05/03 18:10:00	HOP 393
05/04 06:28:18 - 05/07 11:30:00	Track (-556.7, -295.1) @ 05/04 06:28:00	AR 13664 observation
Al-poly/Open	Al-poly/Open close	Safe Norm 4ms Obs 8x8 Q=50 30sec

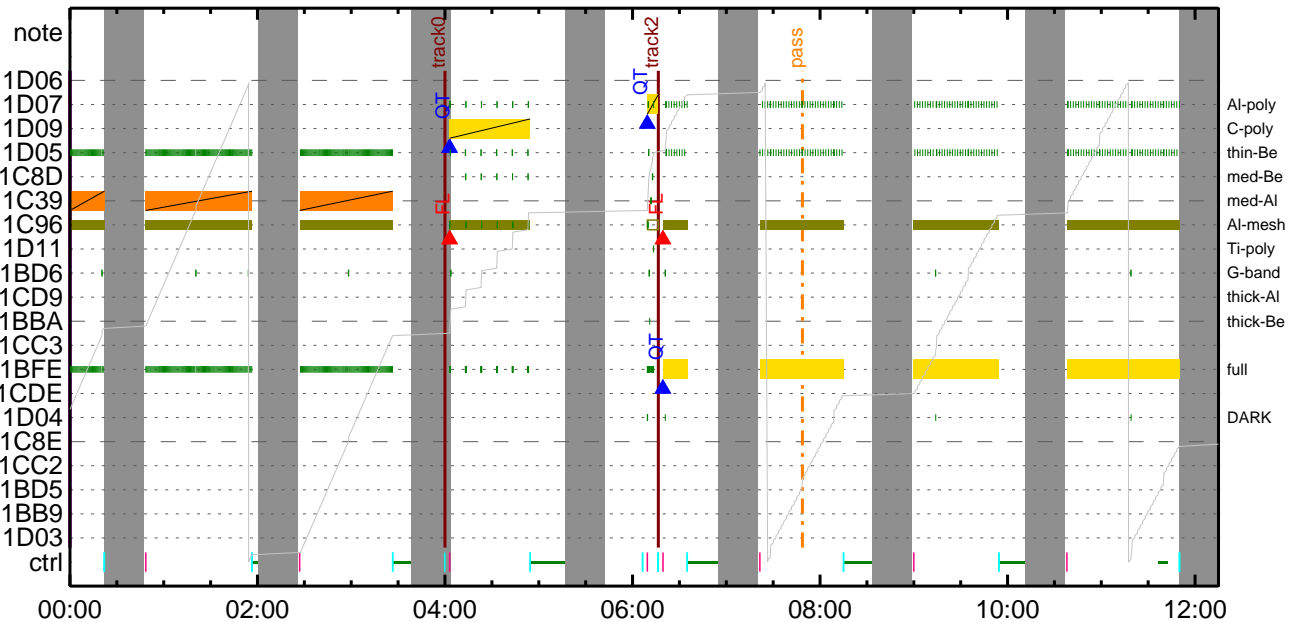
CMDI #0791 2024/05/02



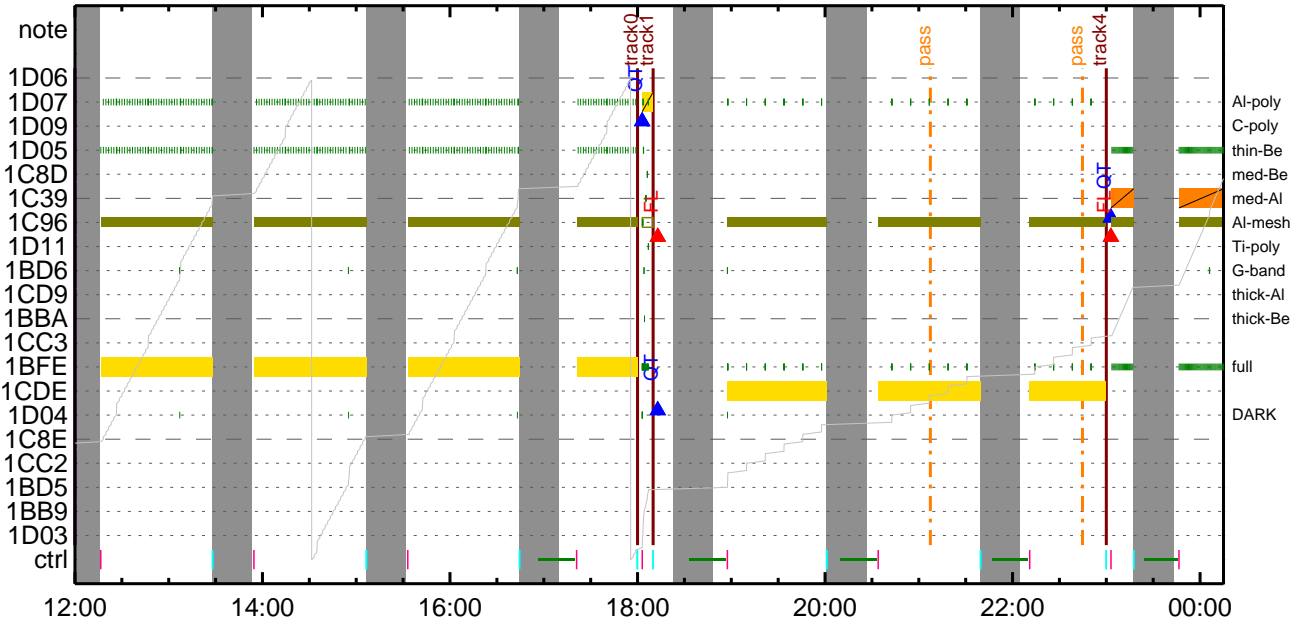
CMDI #0791 2024/05/02



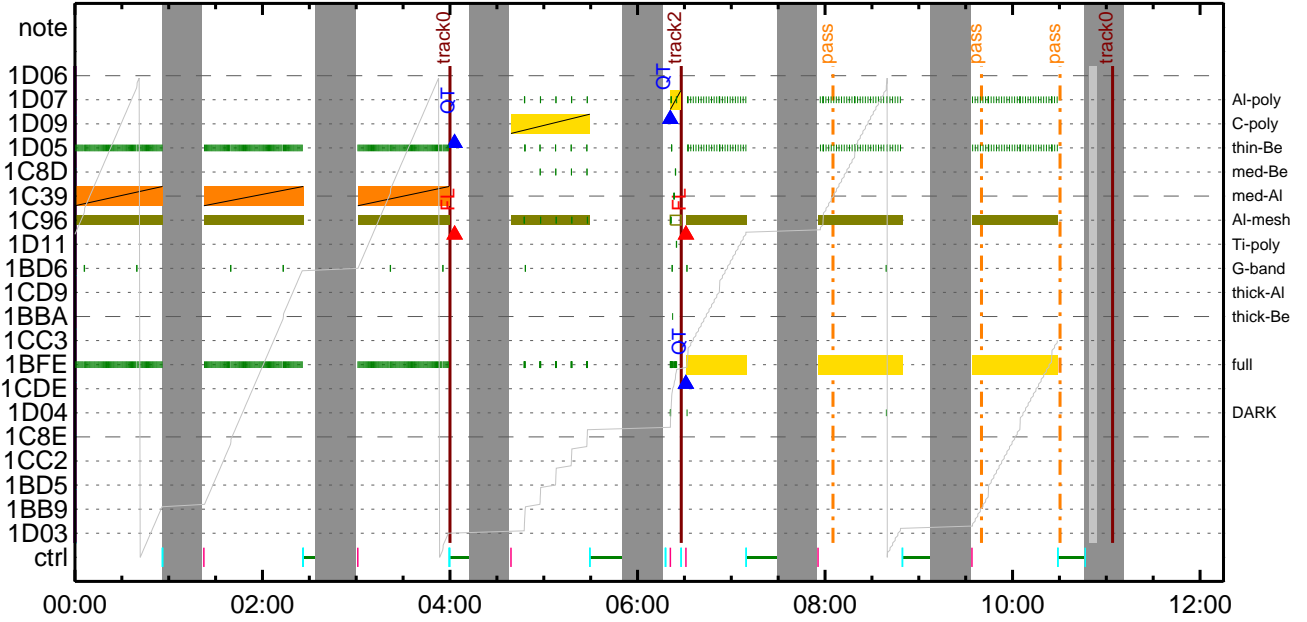
CMDI #0791 2024/05/03



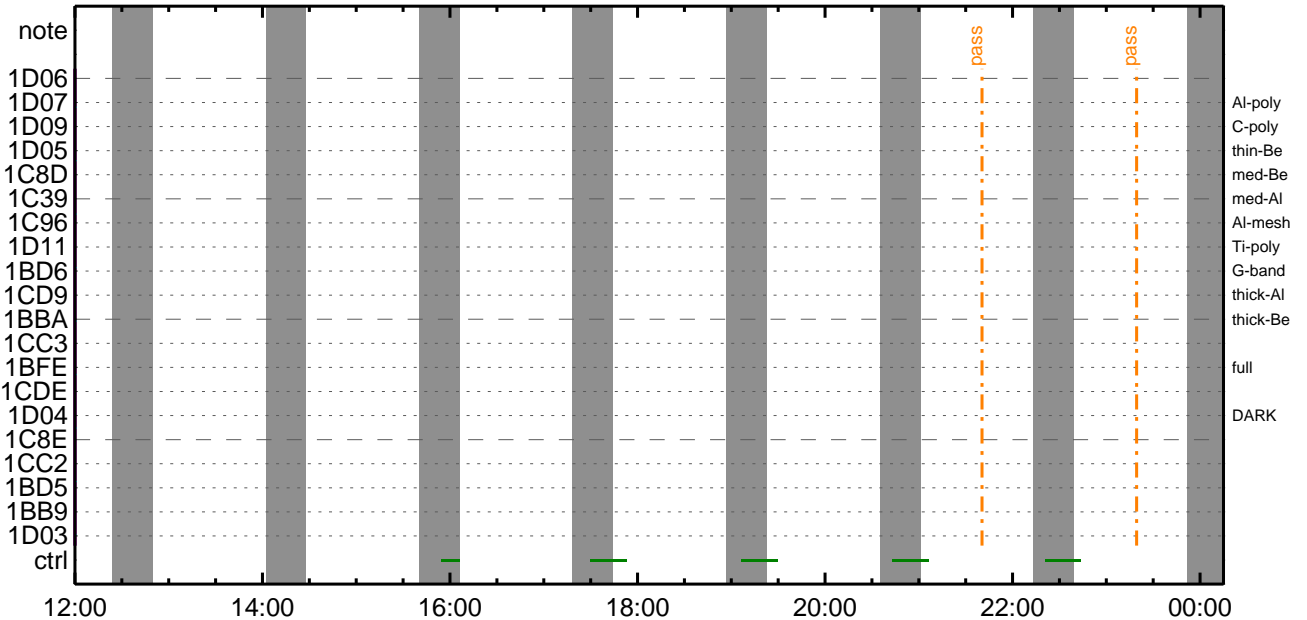
CMDI #0791 2024/05/03



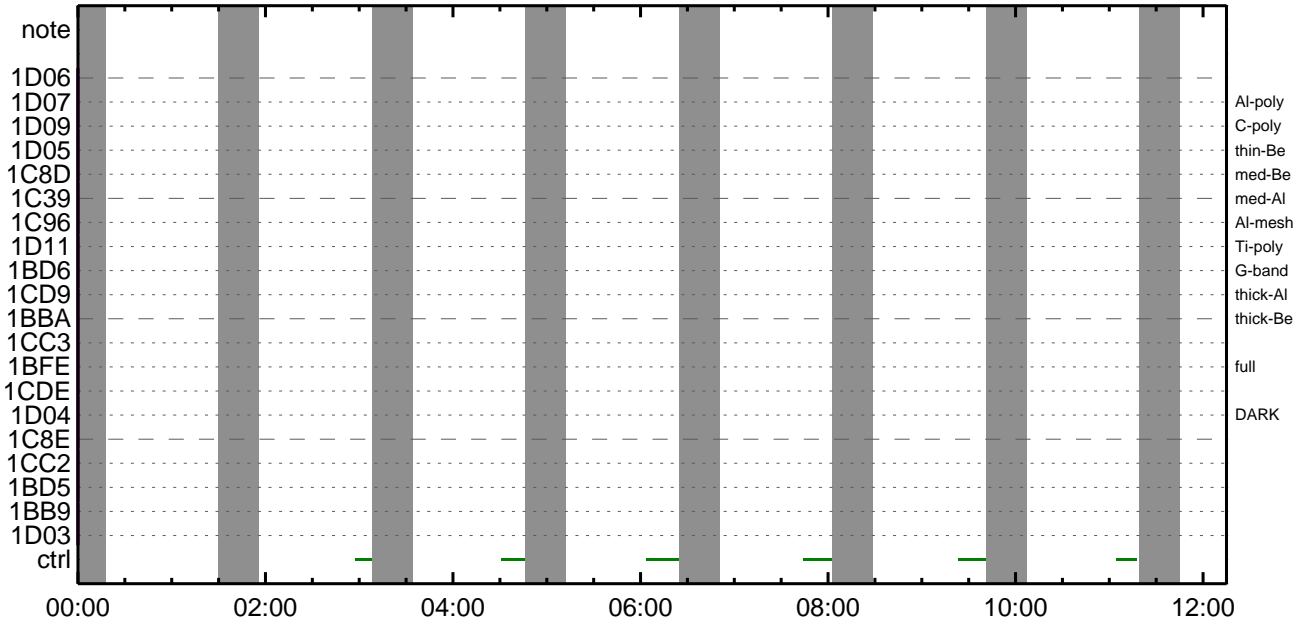
CMDI #0791 2024/05/04



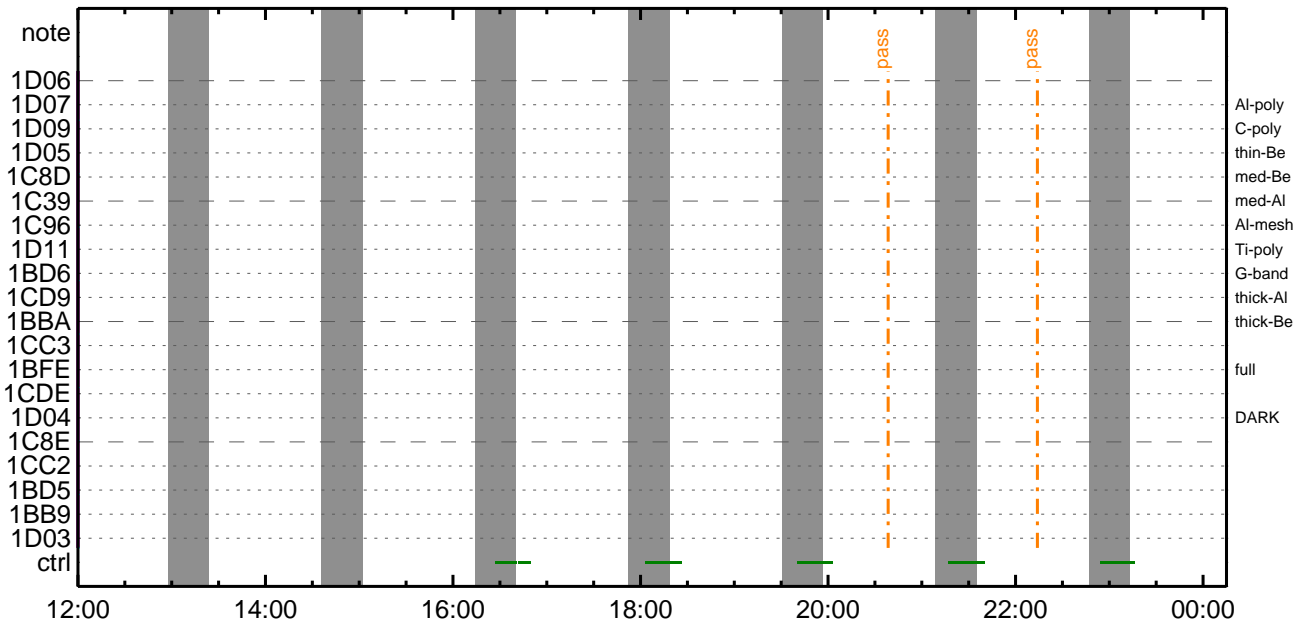
CMDI #0791 2024/05/04



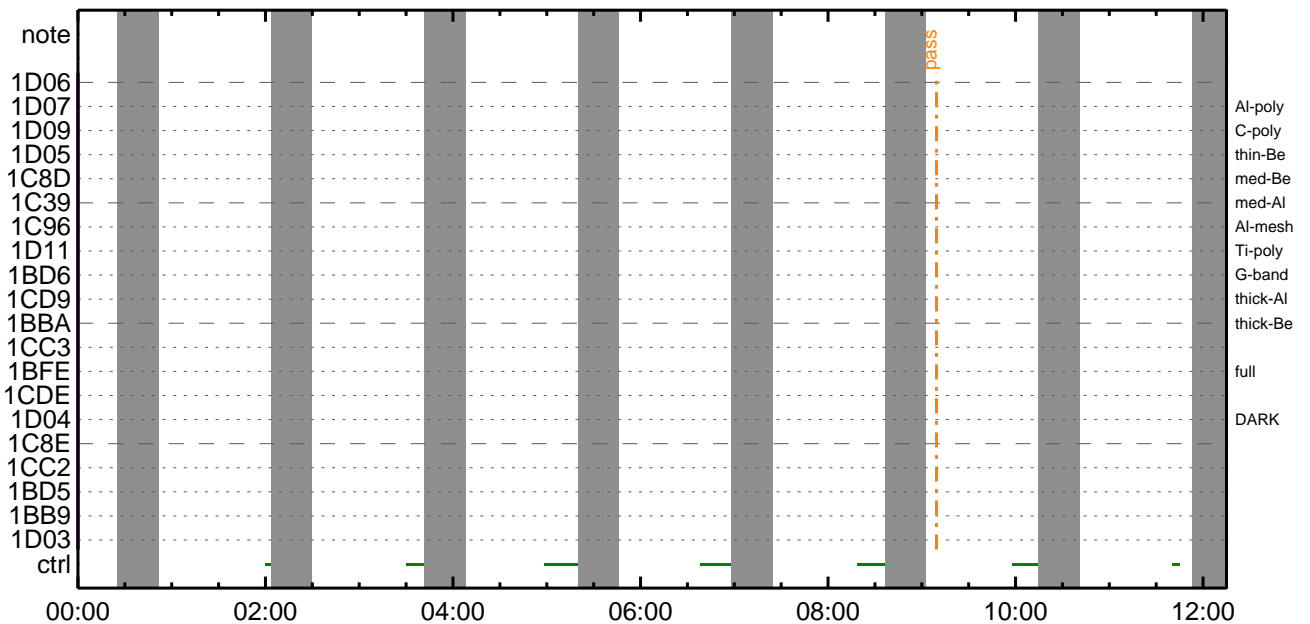
CMDI #0791 2024/05/05



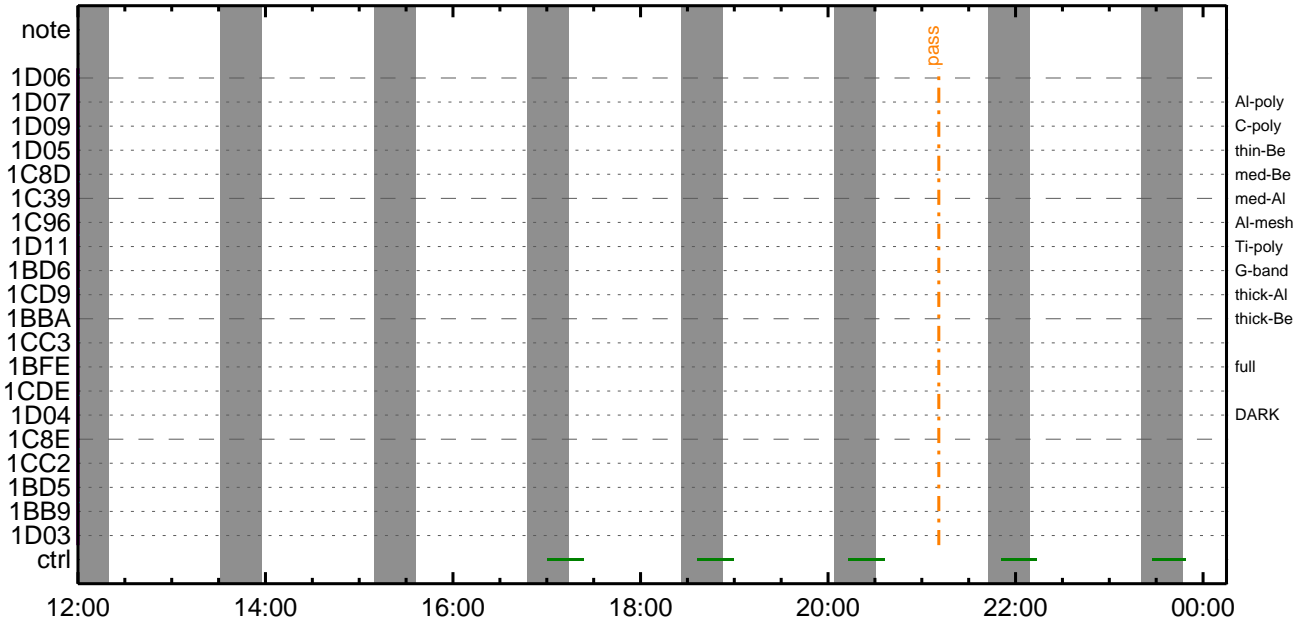
CMDI #0791 2024/05/05



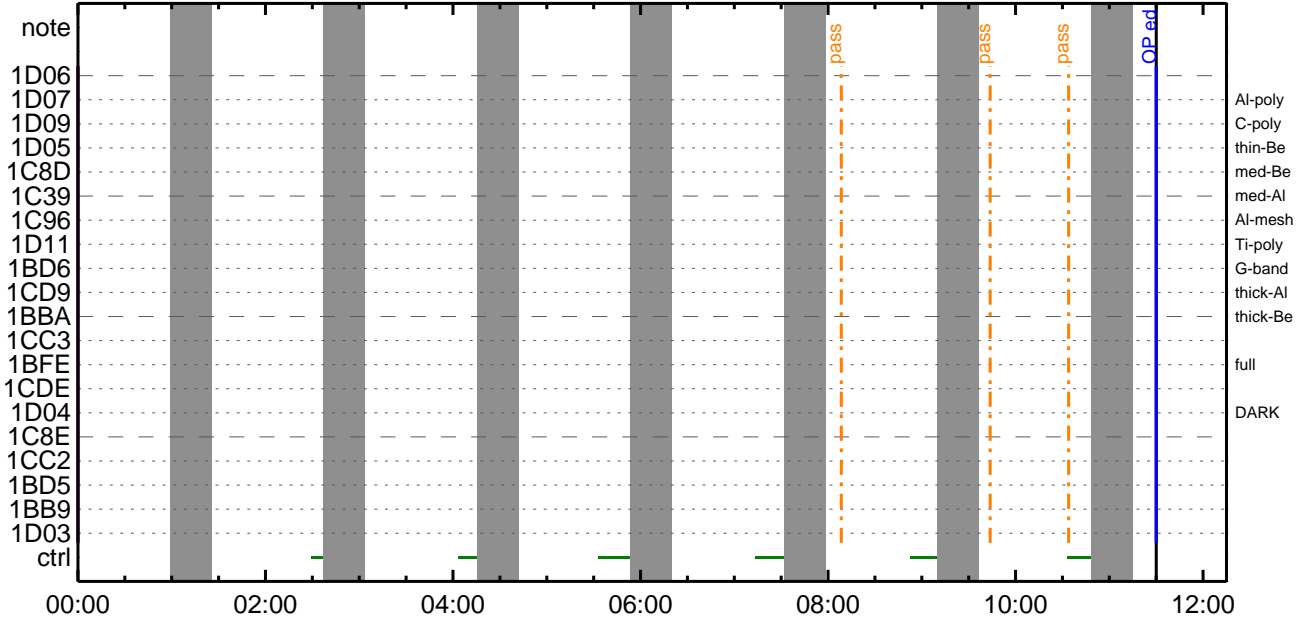
CMDI #0791 2024/05/06



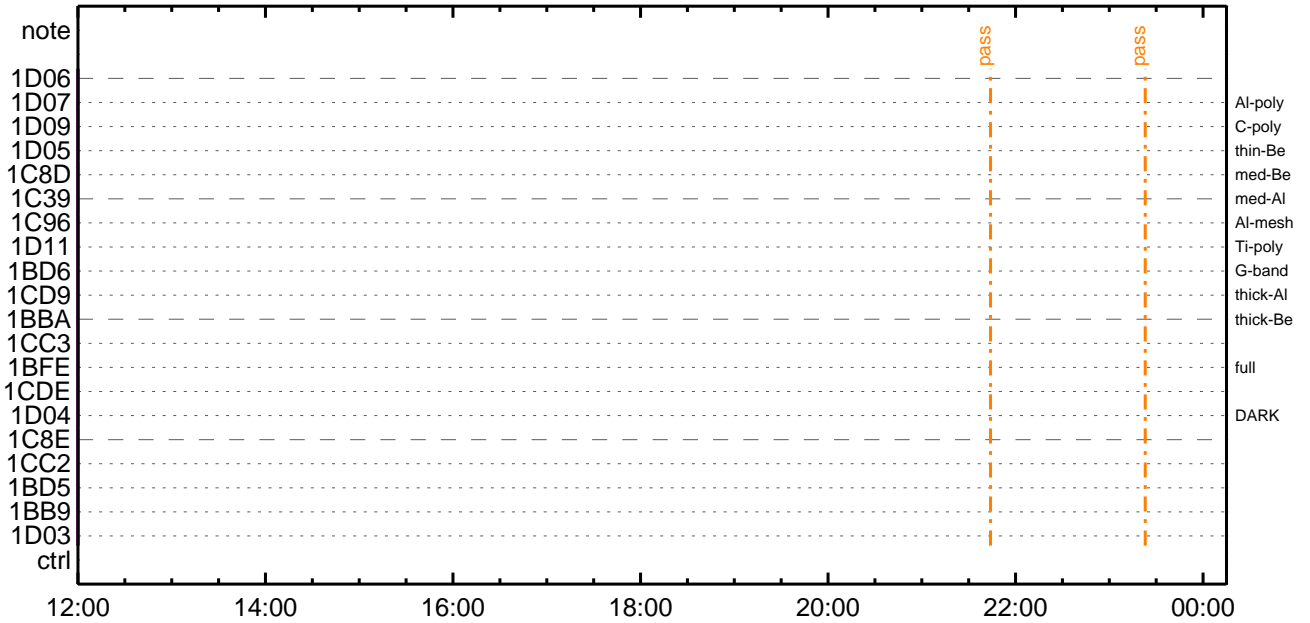
CMDI #0791 2024/05/06



CMDI #0791 2024/05/07



CMDI #0791 2024/05/07



(a) Spacecraft Operation Procedure (real-commands)

```
main-536 2024-05-02 12:50:34 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYSYÁY~¼Á»Ü;ä
0005 C.
0006 C. YÁYß;¼Y³YDÝóYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¼µÁµ•µ°Ë>Í×ÁÇµÍYçYÁY×Yí;¼YÉ;ËËËµ•íËË;ËËË¼°ÇÖµ•µ¿¼í¹ÇµÍ;ÇÁ®,ùn¹µËµµÇÁ÷¿®µ•µËµµµ³µË;Ë
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;YÁYóY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-536:OP
0020 ()
0021 . S. OG og-536:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍî°èYÁYóY×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C.          ¢¢[HK1_DMP_TOP_ADRS_1]          EQ          40
0029 C.          ¢¢[HK1_DMP_TOP_ADRS_0]          EQ          0
0030 C.          ¢¢[HK1_DMP_BLOCK_NUM]           EQ          127
0031 C.          ¢¢[HK1_DMP_REPEAT_NUM]         EQ          0
0032 C.          ¢¢[HK1_DMA_DMP_PIM]            EQ          DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C.          ¢¢[HK1_PKT_FORM_NO]            EQ          7
0036 C.          ¢¢[HK1_PKT_GEN_TIME]           EQ          0.25 s
0037 C.          ¢¢[HK1_S_TLM_BIT_RATE]         EQ          32k
0038 C.          ¢¢[HK1_X_TLM_BIT_RATE]         EQ          4M
0039 C.          ¢¢[HK1_DMP_CHK_FLG]            EQ          EXEC
0040 . C. YÁYóY×½ªí»µð³ÍÇS
0041 C.          ¢¢[HK1_DMP_CHK_FLG]            EQ          NON
0042 . C. RAM ID=NMOGµÍ¾Ë¹Ç•ë²ÍOKµð³ÍÇS
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C.          ¢¢[HK1_DMP_TOP_ADRS_1]          EQ          41
0048 C.          ¢¢[HK1_DMP_TOP_ADRS_0]          EQ          0
0049 C.          ¢¢[HK1_DMP_BLOCK_NUM]           EQ          127
0050 C.          ¢¢[HK1_DMP_REPEAT_NUM]         EQ          0
0051 C.          ¢¢[HK1_DMA_DMP_PIM]            EQ          DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C.          ¢¢[HK1_PKT_FORM_NO]            EQ          7
0055 C.          ¢¢[HK1_PKT_GEN_TIME]           EQ          0.25 s
0056 C.          ¢¢[HK1_S_TLM_BIT_RATE]         EQ          32k
0057 C.          ¢¢[HK1_X_TLM_BIT_RATE]         EQ          4M
0058 C.          ¢¢[HK1_DMP_CHK_FLG]            EQ          EXEC
0059 . C. YÁYóY×½ªªí»µð³ÍÇS
0060 C.          ¢¢[HK1_DMP_CHK_FLG]            EQ          NON
0061 . C. RAM ID=NMOGµÍ¾Ë¹Ç•ë²ÍOKµð³ÍÇS
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C.          ¢¢[HK1_DMP_TOP_ADRS_1]          EQ          42
0067 C.          ¢¢[HK1_DMP_TOP_ADRS_0]          EQ          0
0068 C.          ¢¢[HK1_DMP_BLOCK_NUM]           EQ          65
0069 C.          ¢¢[HK1_DMP_REPEAT_NUM]         EQ          0
0070 C.          ¢¢[HK1_DMA_DMP_PIM]            EQ          DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C.          ¢¢[HK1_PKT_FORM_NO]            EQ          7
0074 C.          ¢¢[HK1_PKT_GEN_TIME]           EQ          0.25 s
0075 C.          ¢¢[HK1_S_TLM_BIT_RATE]         EQ          32k
0076 C.          ¢¢[HK1_X_TLM_BIT_RATE]         EQ          4M
0077 C.          ¢¢[HK1_DMP_CHK_FLG]            EQ          EXEC
0078 . C. YÁYóY×½ªªí»µð³ÍÇS
0079 C.          ¢¢[HK1_DMP_CHK_FLG]            EQ          NON
0080 . C. RAM ID=NMOG, RAM ID=OPµÍ¾Ë¹Ç•ë²ÍOKµð³ÍÇS
0081 C.
0082 . C. ***** °Ë²¼µÍ¾Ë¹ÇÁ°µËË~µ°Á÷¿® (¾ãµ-YÁYóY×¼ë½ÇµðÁÓÃæµÇ¾ªµ~µË¾¼í¹ÇµÇµá) *****
0083 C. DHUYá;¼YÉ;Ë¼Y½, Yí;¼YÉ;ËµðÍáµ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C.          ¢¢[HK1_PKT_FORM_NO]            EQ          2
0087 C.          ¢¢[HK1_PKT_GEN_TIME]           EQ          0.5S
0088 C.          ¢¢[HK1_S_TLM_BIT_RATE]         EQ          32K
0089 C.          ¢¢[HK1_X_TLM_BIT_RATE]         EQ          4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADµ~Á÷¿®NGµÍ¾Ë¹Ç;ç°Ë²¼µÍTI-CMDÁ÷¿®µÍ¾Ë¹Ôµ•µËµµµ³µË;Ë
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0096 C.          01-03 DHU_OP_STOP
0097 C.
0098 . C. TI 2024-05-02 10:47:00.0
0099 +. TI 2024-05-02 10:47:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2024-05-02 10:47:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2024-05-02 10:47:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2024-05-02 10:51:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.          01-02 DHU_MODE_CHNG
0116 C.          [HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C.          [HK1_TI_CMD_NUM] EQ 4
0118 C.          [HK1_NEXT_EXEC_PIM] EQ DHU
0119 C.          [HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 . C. *****
0122 C. TI 01-23 DHU_DMA_DMP_PRM_SET
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.          [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.          [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.          [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.          [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.          [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.          [HK1_PKT_FORM_NO] EQ 7
0136 C.          [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.          [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.          [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.          [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 . C. 01-22 DHU_MODE_CHNG
0142 C.          [HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 . C. RAM ID=TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0145 C.
0146 . C. DHU 01-22 DHU_MODE_CHNG
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.          [HK1_PKT_FORM_NO] EQ 2
0150 C.          [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.          [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.          [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2024-05-02 10:51:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2024-05-02 10:51:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2024-05-02 10:51:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C.          [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2024-05-02 10:51:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C.          [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP 07-FC dcbc-402:DCBC *****
0192 C. (07-FC dcbc-402:DCBC)
0193 . S. DC-BC dcbc-402:DCBC

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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥Ð¥¹•Ï Daily±;ÍÑ«Ë'Ø«¹«èDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
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(a) Spacecraft Operation Procedure (real-commands)

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main-537 2024-05-02 12:50:34 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝÓYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëµ¿µÄµ•µ°Ë>Í×ÁÇµÍYÇYÁY×Yí;¼YÉ;ËËËµ•ííË;ËËË¼°ÇÖµ•µ¿¼í¹ÇµÍ; ÇÁ®, ùµ¹µËµDµÇÁ÷¿®µ•µËµµµ³µË; £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. Ç° ÒÈÀ, Í×ËÝµÄLOSµDµÇµÍ»p´Öµò¹íí, µ•; ÇÉÖÍ×µËXÁÓONµÍ¹ÖµËµÍµËµµµ³µË; £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYíYÁY~¾ÖÁÖµ¬°ÄÄËµ•µ¿µé; Ç°Ë²¼µÍ°ËÀ, ¼Ë½Çµò¼Á¹Öµ¹µË; £
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ËÀ,
0033 C. *****
0034 C. Ç° RESTART;ËPT1;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°Ë²¼µÍ¼Á¹Öµµ°; ÇDCBC-150µØ¿Ëµà; £
0035 C.
0036 . C. ;ãPT1°ËÀ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYËÀÚÁØ;ËÁ•Á°²óÈò;Ë, áµí°ËÀ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÀ, µ¬¼«°ËÀ»ßµ•µ¿, á; Ç°Ë²¼µò¼Á¹Öµ¹µË; £
0055 C. YÇYÓYÉYËÀÚÁØµàÁ•Á°²óÈòµ¬¼áµ¾¼í¹ÇµÍ´°í»µ¹µËµDµÇÁÖµÀ; £
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ËÀ,
0059 C. *****
0060 C. Ç° RESTART;ËPT2;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°Ë²¼µÍ¼Á¹Öµµ°; ÇDCBC-151µØ¿Ëµà; £
0061 C.
0062 . C. ;ãPT2°ËÀ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYËÀÚÁØ;ËÁ•Á°²óÈò;Ë, áµí°ËÀ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÀ, Áä»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÀ, Áä»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 19s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2024-05-02 10:51:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'uAÎI»ö¼YnEÄDn¹nëDCBC•x²è *****
0156 C. (%ã°iYÓYÄYÉYÐYËYÁYçYëE¼¼n¼Ä»Ün¹në)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** YÐY¹•I Daily±çÍÑE'Øn¹nëDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥Ä¥~¼Ä»Ü;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-538 2024-05-02 12:50:34 214 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝÓYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëµ¿µÄµ•µ°Ë>Í×ÁÇµÍYÇYÁY×Yí;¼YÉ;ËËÈµ•íÍË;ËËÈ¼°ÇÖµ•µ¿¼í¹ÇµÍ; ÇÁ®, ùµ¹µÈµDµÇÁ÷¿®µ•µËµµµ³µÈ; £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. Ç° ÒÈÀ, Í×ÈYµÄLOSµDµÇµÍ»p´Öµò¹ÍÍ, µ•; ÇÉÖÍ×µÈXÁÓONµÍ¹ÖµÈµÍµËµµµ³µÈ; £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYíYÁY~¾ÖÁÖµ¬°ÄÄÈµ•µ¿µé; Ç°È²¼µÍ°ÈÀ, ¼È½Çµò¼Á¹Öµ¹µÈ; £
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÈÀ,
0033 C. *****
0034 C. Ç° RESTART;ËPT1;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°È²¼µÍ¼Á¹Öµµ°; ÇDCBC-150µØ¿Èµà; £
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYËÁÚÁØ;ËÁ•Á°²óÈò;Ë, áµÍ°ÈÀ, °È³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, µ¬¼«°È°Äá»ßµ•µ¿, á; Ç°È²¼µò¼Á¹Öµ¹µÈ; £
0055 C. YÇYÓYÉYËÁÚÁØµÄÁ•Á°²óÈòµ¬¼áµ¾¼í¹ÇµÍ´°Í°Í»µ¹µÈµDµÇÁÖµÄ; £
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÈÀ,
0059 C. *****
0060 C. Ç° RESTART;ËPT2;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°È²¼µÍ¼Á¹Öµµ°; ÇDCBC-151µØ¿Èµà; £
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYËÁÚÁØ;ËÁ•Á°²óÈò;Ë, áµÍ°ÈÀ, °È³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Áá»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Áá»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-289:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2024-05-02 10:51:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 +. DC 07-F0 MDP_XRT_CTRL_MANU
0136 BC (c1)
0137 +. DC 07-F0 MDP_XRT_MODE_STBY
0138 BC (c3)
0139 . C. ----- Success Verify ? OK / NG____
0140 C.
0141 C. XRT Obs. Table Upload
0142 . S. RAM ram-291:MDP_OBS_X
0143 ( )
0144 C.
0145 +. DC 07-F0 MDP_DUMP_XRTTBL
0146 BC (84 07 00 00 00 3a d4)
0147 . C. ----- Comparison Check ? OK / ERR ____
0148 C.
0149 C.
0150 +. DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 01 b1 b1 04 04)
0152 +. DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 02 b1 b1 08 08)
0154 +. DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 03 b1 b1 08 08)
0156 +. DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 04 b1 b1 06 06)
0158 +. DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 +. DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 20)
0162 +. DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 08)
0164 +. DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 08 20)
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 80 80 08 08)
0168 +. DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0f 80 80 06 06)
0170 +. DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 10 80 80 08 08)
0172 +. DC 07-F0 MDP_XRT_FLD_ENA
0173 BC (d8)
0174 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0175 BC (c8)
0176 +. DC 07-F0 MDP_XRT_ARS_DIS
0177 BC (d5)
0178 +. DC 07-F0 MDP_XRT_AEC_RESET
0179 BC (d0)
0180 +. DC 07-F0 MDP_XRT_FLD_RESET
0181 BC (da)
0182 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0183 BC (c4 08)
0184 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0185 BC (c5 0e)
0186 . C. ----- Success Verify ? OK / NG ____
0187 C.
0188 C.
0189 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0190 C.
0191 +. DC 07-F0 MDP_XRT_MODE_OBSV
0192 BC (c2)
0193 +. TI 2024-05-02 10:51:02.0

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0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 . C. ----- Success Verify ? OK / NG ____
0197 C.
0198 C. ***** XRT END *****
0199 C.
0200 . C. ***** MDP 'ûÃÎ»ô¼Ý»ËÄÐ»¹»èDCBC•x²è *****
0201 C. (¾å°ï¥Ó¥Ã¥È¥Ð¥Ë¥Ä¥Ç¥è»Ë¼»¼Ã»Û¹»è)
0202 . S. DC-BC dcbc-402:DCBC
0203 (MDP_known_event)
0204 C.
0205 C.
0206 . C. ***** ¥Ð¥¹•Ï Daily±¿Î»Ë'Ø»¹»èDCBC•x²è *****
0207 . S. DC-BC dcbc-153:DCBC
0208 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209 C.
0210 C.
0211 . C. ;ãLOS¥Á¥S¥Ã¥~¼Ã»Û;ã
0212 C.
0213 . C. ***** LOS *****
0214 C.

2024/05/02	23:12:30.0	XRT_Custom_430_OG [0x1ae]			
2024/05/02	23:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/03	00:22:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	00:22:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	00:22:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/03	00:22:06.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/03	00:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/03	00:47:30.5	XRT_Custom_430_OG [0x1ae]			
2024/05/03	00:48:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/03	01:56:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	01:56:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	01:56:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/03	01:56:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/03	01:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/03	02:26:00.5	XRT_Custom_430_OG [0x1ae]			
2024/05/03	02:27:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/03	03:26:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	03:26:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	03:26:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/03	03:26:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/03	03:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/03	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/05/03	04:00:00.0	AOCS_Or-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2024/05/03	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2024/05/03	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/05/03	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2024/05/03	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2024/05/03	04:00:24.0	XRT_FLD_RESET_438_OG [0x1b6]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/03	04:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2024/05/03	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/05/03	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/03	04:54:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	04:54:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	04:54:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/03	04:54:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/03	04:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/03	06:06:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	06:06:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	06:06:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/05/03	06:06:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2024/05/03	06:06:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2024/05/03	06:06:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2024/05/03	06:09:28.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2024/05/03	06:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/03	06:16:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	06:16:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	06:16:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00

2024/05/03	06:16:30.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	02	03	74	01	db
2024/05/03	06:16:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0					d8
2024/05/03	06:16:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2024/05/03	06:16:52.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0					d0
2024/05/03	06:16:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0					d5
2024/05/03	06:16:56.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0					da
2024/05/03	06:19:26.0	XRT_QT_PROG_SET_441_OG [0x1b9] MDP_XRT_QT_PROG_SET	2	07-F0					c4 08
2024/05/03	06:19:28.0	XRT_FL_PROG_SET_439_OG [0x1b7] MDP_XRT_FL_PROG_SET	2	07-F0					c5 0e
2024/05/03	06:19:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0					c0
2024/05/03	06:35:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	06:35:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	06:35:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0					da
2024/05/03	06:35:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0					e8
2024/05/03	06:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0					e9
2024/05/03	07:20:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/03	07:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0					c0
2024/05/03	08:15:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	08:15:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	08:15:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0					da
2024/05/03	08:15:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0					e8
2024/05/03	08:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0					e9
2024/05/03	08:59:00.5	XRT_Custom_430_OG [0x1ae]							
2024/05/03	09:00:00.5	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0					c0
2024/05/03	09:54:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	09:54:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	09:54:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0					da
2024/05/03	09:54:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0					e8
2024/05/03	09:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0					e9
2024/05/03	10:37:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/03	10:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0					c0
2024/05/03	11:50:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	11:50:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	11:50:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0					da
2024/05/03	11:50:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0					e8
2024/05/03	11:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0					e9
2024/05/03	12:15:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/03	12:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0					c0
2024/05/03	13:28:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	13:28:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	13:28:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0					da
2024/05/03	13:28:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0					e8
2024/05/03	13:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0					e9
2024/05/03	13:53:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/03	13:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0					c0
2024/05/03	15:06:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	15:06:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0					c1
2024/05/03	15:06:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0					da
2024/05/03	15:06:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0					e8
2024/05/03	15:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0					e9

2024/05/03	15:32:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/03	15:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/05/03	16:44:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	16:44:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	16:44:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/05/03	16:44:36.0	XRT_PREFLR_STRT_403_OG [0x193]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/05/03	16:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/05/03	17:20:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/03	17:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/05/03	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2024/05/03	18:00:00.0	AOCS_Or-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2024/05/03	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2024/05/03	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2024/05/03	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/05/03	18:02:58.0	XRT_QT_PROG_SET_401_OG [0x191]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2024/05/03	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/05/03	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2024/05/03	18:10:00.0	AOCS_Or-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2024/05/03	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/05/03	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/05/03	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/05/03	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/05/03	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/05/03	18:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07			
2024/05/03	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2024/05/03	18:56:30.5	XRT_Custom_430_OG [0x1ae]						
2024/05/03	18:57:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/05/03	20:01:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	20:01:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	20:01:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/05/03	20:01:06.0	XRT_PREFLR_STRT_403_OG [0x193]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/05/03	20:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/05/03	20:33:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/03	20:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/05/03	21:39:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	21:39:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	21:39:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/05/03	21:39:36.0	XRT_PREFLR_STRT_403_OG [0x193]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/05/03	21:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/05/03	22:10:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/03	22:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/05/03	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/05/03	22:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2024/05/03	23:00:00.0	AOCS_Or-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	04 03 74 01 db			

2024/05/03	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2024/05/03	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/05/03	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2024/05/03	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2024/05/03	23:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/03	23:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2024/05/03	23:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/05/03	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/03	23:17:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	23:17:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/03	23:17:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/03	23:17:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/03	23:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/03	23:45:30.0	XRT_Custom_430_OG [0x1ae]			
2024/05/03	23:46:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/04	00:56:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/04	00:56:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/04	00:56:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/04	00:56:06.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/04	00:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/04	01:21:30.0	XRT_Custom_430_OG [0x1ae]			
2024/05/04	01:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/04	02:26:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/04	02:26:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/04	02:26:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/04	02:26:06.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/04	02:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/04	03:00:00.0	XRT_Custom_430_OG [0x1ae]			
2024/05/04	03:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/04	03:59:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/04	03:59:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/04	03:59:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/04	03:59:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/04	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/04	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/05/04	04:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2024/05/04	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2024/05/04	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/05/04	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2024/05/04	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2024/05/04	04:00:24.0	XRT_FLD_RESET_438_OG [0x1b6]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/04	04:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/04	04:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2024/05/04	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/05/04	04:38:00.0	XRT_Custom_430_OG [0x1ae]			
2024/05/04	04:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/04	05:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/04	05:29:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/04	05:29:34.0	XRT_FLD_RESET_415_OG [0x19f]			

2024/05/04	05:29:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/05/04	05:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/04	06:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	06:17:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	06:17:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/05/04	06:18:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2024/05/04	06:18:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2024/05/04	06:18:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/05/04	06:20:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2024/05/04	06:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/05/04	06:27:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	06:27:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	06:27:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2024/05/04	06:28:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 03 74 01 db		
2024/05/04	06:28:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/05/04	06:28:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/05/04	06:28:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/05/04	06:28:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/05/04	06:28:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/05/04	06:30:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2024/05/04	06:30:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e		
2024/05/04	06:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/05/04	07:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	07:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	07:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/05/04	07:09:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/05/04	07:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/04	07:54:30.0	XRT_Custom_430_OG [0x1ae]						
2024/05/04	07:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/05/04	08:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	08:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	08:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/05/04	08:49:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/05/04	08:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/04	09:33:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/04	09:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/05/04	10:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	10:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	10:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/05/04	10:29:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/05/04	10:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/04	10:46:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/04	11:04:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		