

XRT Timeline to be uploaded on 2024/05/07

Period: 2024/05/07 11:30:00 - 2024/05/11 11:15:00

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Normal mode

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XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/07 11:43:00 - 05/07 18:44:54	Track (654.6, 382.6) @ 05/07 11:40:00	# OP start + 10min. AR 16633 Major Flare Watch.
05/08 05:54:30 - 05/08 17:39:54	Track (735.9, 374.2) @ 05/08 05:51:30	# AR cont.
05/09 02:34:30 - 05/09 05:59:24	Track (800.0, 364.6) @ 05/09 02:00:00	# AR cont.

PROG= 02 Inf-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 92	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2	5-time(s)	2.0sec
Seqn= 47	1-time(s)	2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 96	4-time(s)	180.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D08: Synoptic for HOP448 w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/

Term	Pointing (x, y)	Comment
05/07 18:48:00 - 05/07 18:57:00	Fixed (0.0, 0.0)	synoptic, shifted + stray light measurement
05/08 05:44:30 - 05/08 05:51:24	Fixed (0.0, 0.0)	synoptic, shifted -18.5 min
05/08 17:43:00 - 05/08 17:52:30	Fixed (0.0, 0.0)	synoptic, shifted
05/09 06:02:30 - 05/09 06:09:24	Fixed (0.0, 0.0)	synoptic, shifted -0.5 min

PROG= 01 1-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 5	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 55	1-time(s)	2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 98	1-time(s)	2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 76	1-time(s)	2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 23	1-time(s)	4.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 41	1-time(s)	2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 18	1-time(s)	2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Al/Open	med-Al/Open close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Al/Open	med-Al/Open close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 86	1-time(s)	2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D14: Stray light study 2024-1; Al-mesh and Al-poly, 2x2 full FOV(1min-cad) and 1x1 384 on AR(10sec-cad)

Term	Pointing (x, y)	Comment
05/07 18:59:30 - 05/07 19:09:54	Fixed (910.0, 0.0)	# XRT stray light measurement at west limb.
05/08 17:55:00 - 05/08 18:06:54	Fixed (-950.0, 0.0)	# XRT stray light measurement at east limb

PROG= 10 1-time(s)													
└─ Subr= 2 16-time(s) 2.0sec													
└─ Seqn= 40 1-time(s) 10.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.83s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 9-time(s) 2.0sec													
└─ Seqn= 31 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 40 3-time(s) 10.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.83s	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with												
	Term	Pointing (x, y)	Comment									
	05/07 19:34:30 - 05/08 05:41:24	Track (690.7, 379.2)	④ 05/07 19:10:00 # AR cont.									
	05/08 18:31:30 - 05/08 22:50:00	Track (778.3, 368.4)	④ 05/08 18:07:00 # AR cont.									
	05/09 06:12:30 - 05/09 11:25:30	Track (809.7, 362.6)	④ 05/09 06:09:30 # AR cont.									

PROG= 08 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Subr= 2 4-time(s) 2.0sec													
└─ Seqn= 47 1-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 77 4-time(s) 300.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C10: CME watch - 4x4 - AEC 2 - Al-poly - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 60s cad												
	Term	Pointing (x, y)	Comment									
	05/08 23:21:00 - 05/09 01:59:54	Track (681.7, -68.2)	④ 05/08 23:00:00 # HOP 473									

PROG= 13 Inf.-time(s)													
└─ Subr= 1 60-time(s) 60.0sec													
└─ Seqn= 39 1-time(s) 4.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 30 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * **Flare mode** * * * * *

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G												
	Term	Pointing (x, y)	Comment									
	05/07 11:43:00 - 05/07 18:44:54	Track (654.6, 382.6)	④ 05/07 11:40:00 # OP start + 10min. AR 16633 Major Flare Watch.									
	05/07 19:34:30 - 05/08 05:41:24	Track (690.7, 379.2)	④ 05/07 19:10:00 # AR cont.									
	05/08 05:54:30 - 05/08 17:39:54	Track (735.9, 374.2)	④ 05/08 05:51:30 # AR cont.									
	05/08 18:31:30 - 05/08 22:50:00	Track (778.3, 368.4)	④ 05/08 18:07:00 # AR cont.									
	05/08 23:21:00 - 05/09 01:59:54	Track (681.7, -68.2)	④ 05/08 23:00:00 # HOP 473									
	05/09 02:34:30 - 05/09 05:59:24	Track (800.0, 364.6)	④ 05/09 02:00:00 # AR cont.									
	05/09 06:12:30 - 05/09 11:25:30	Track (809.7, 362.6)	④ 05/09 06:09:30 # AR cont.									

PROG= 14 30-time(s)													
└─ Subr= 1 20-time(s) 2.0sec													
└─ Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 73 1-time(s) 10.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 11 1-time(s) 2.0sec													

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87			1-time(s)			2.0sec						
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

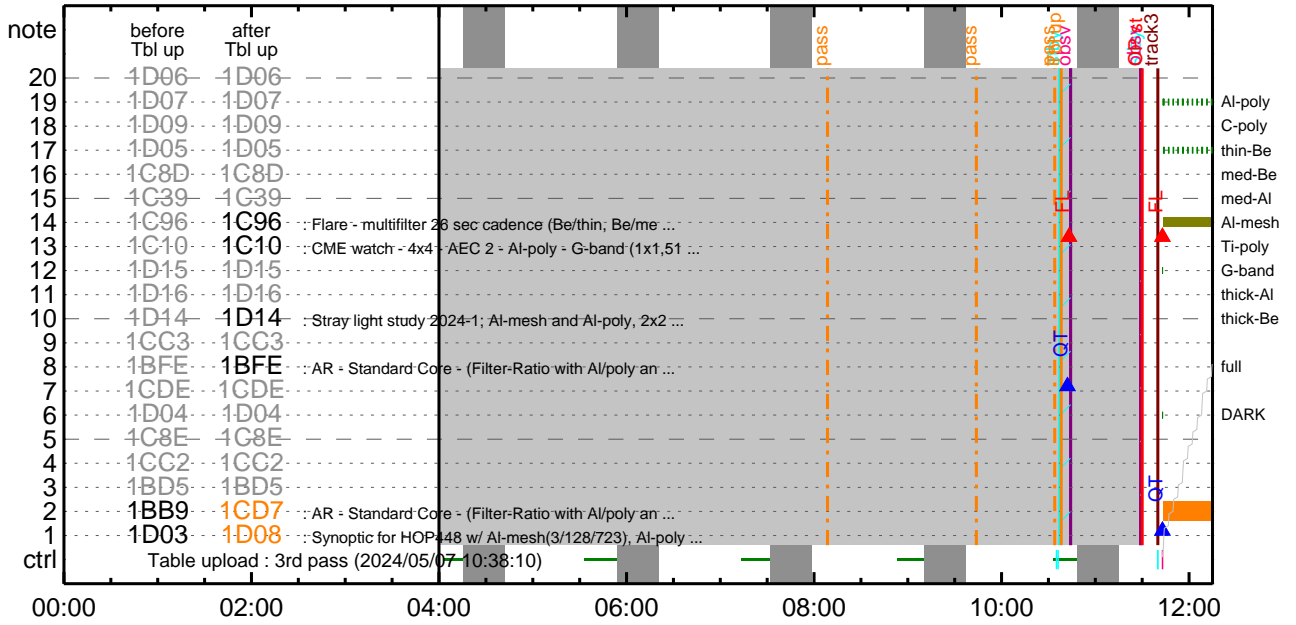
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Flare Detection

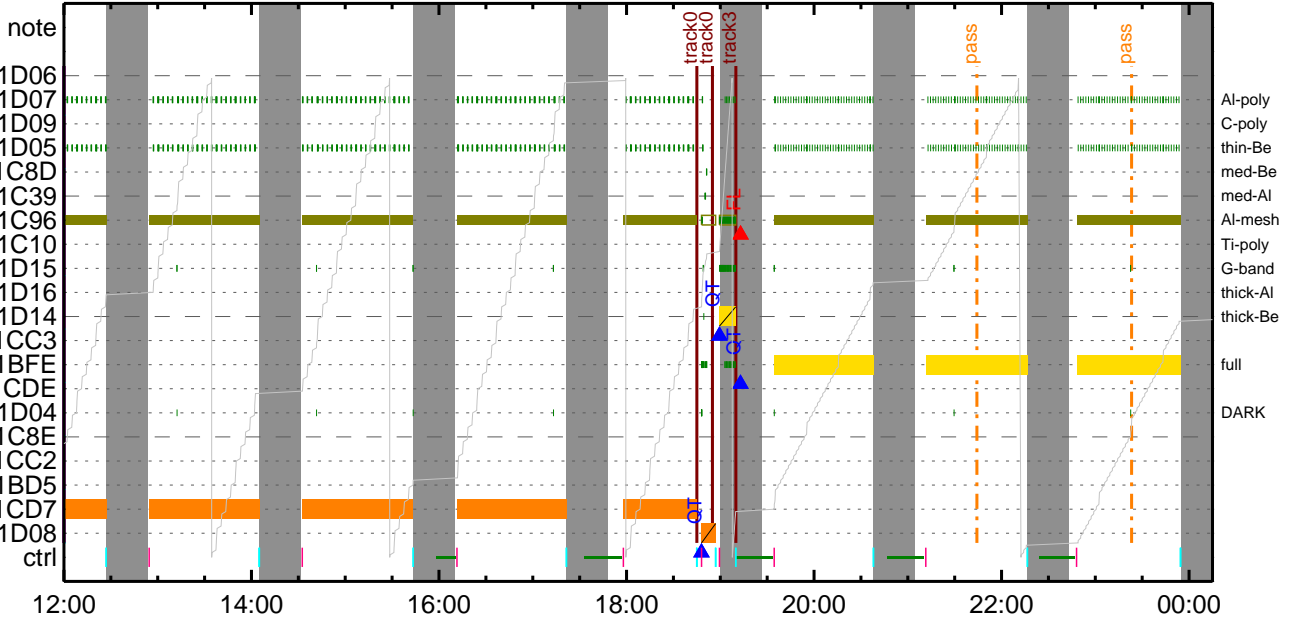
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FLD Patrol												
Term		Pointing (x, y)					Comment					
05/07 10:39:10 - 05/07 18:45:18		cannot be identified										
05/07 19:10:18 - 05/08 05:41:48		Track (690.7, 379.2) @ 05/07 19:10:00					# AR cont.					
05/08 05:51:48 - 05/08 17:40:18		Track (735.9, 374.2) @ 05/08 05:51:30					# AR cont.					
05/08 18:07:18 - 05/09 05:59:48		Track (778.3, 368.4) @ 05/08 18:07:00					# AR cont.					
05/09 06:09:48 - 05/11 11:15:00		Track (809.7, 362.6) @ 05/09 06:09:30					# AR cont.					
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

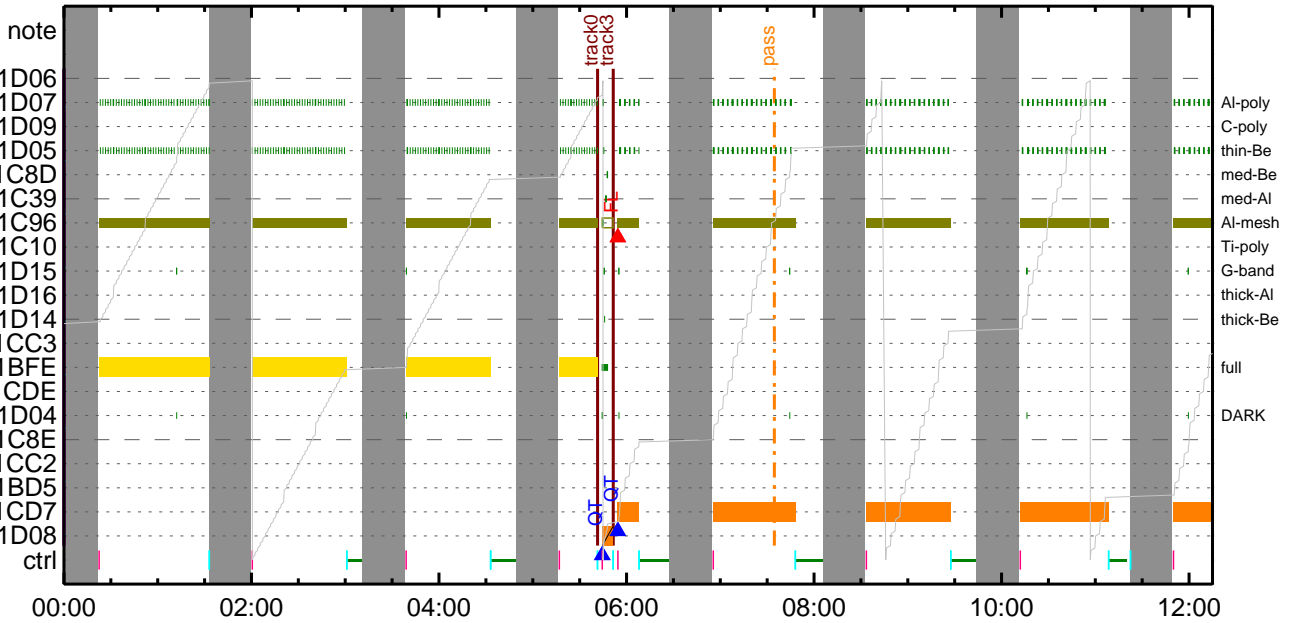
CMDI #0801 2024/05/07



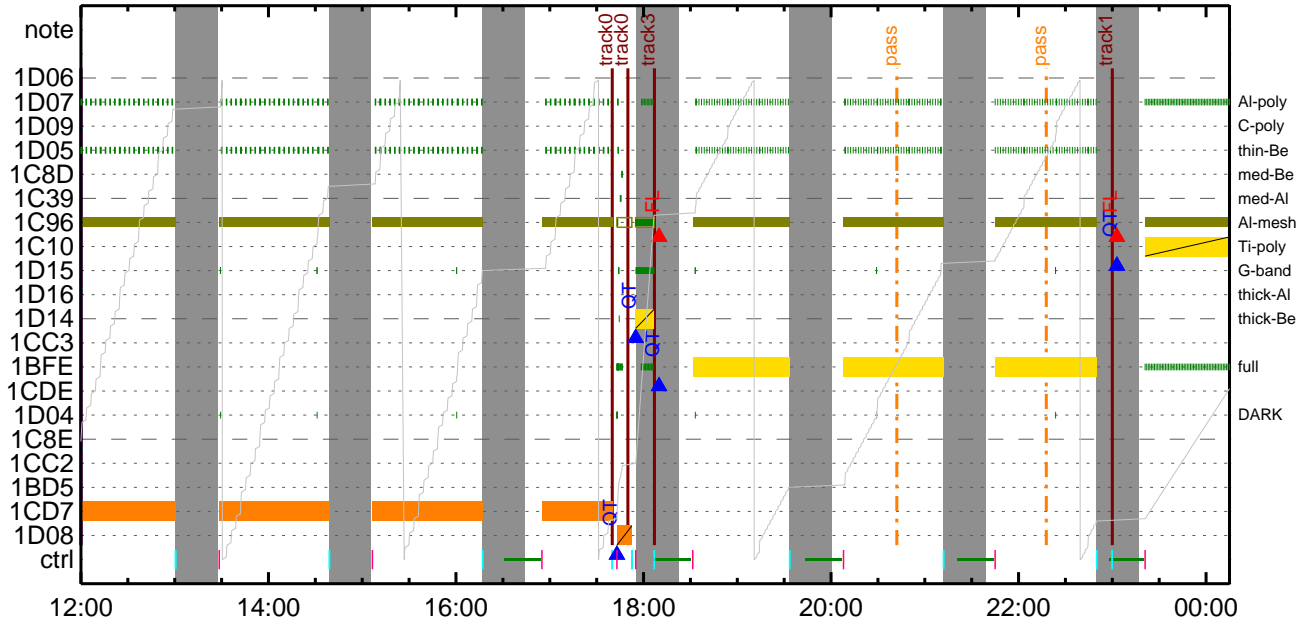
CMDI #0801 2024/05/07



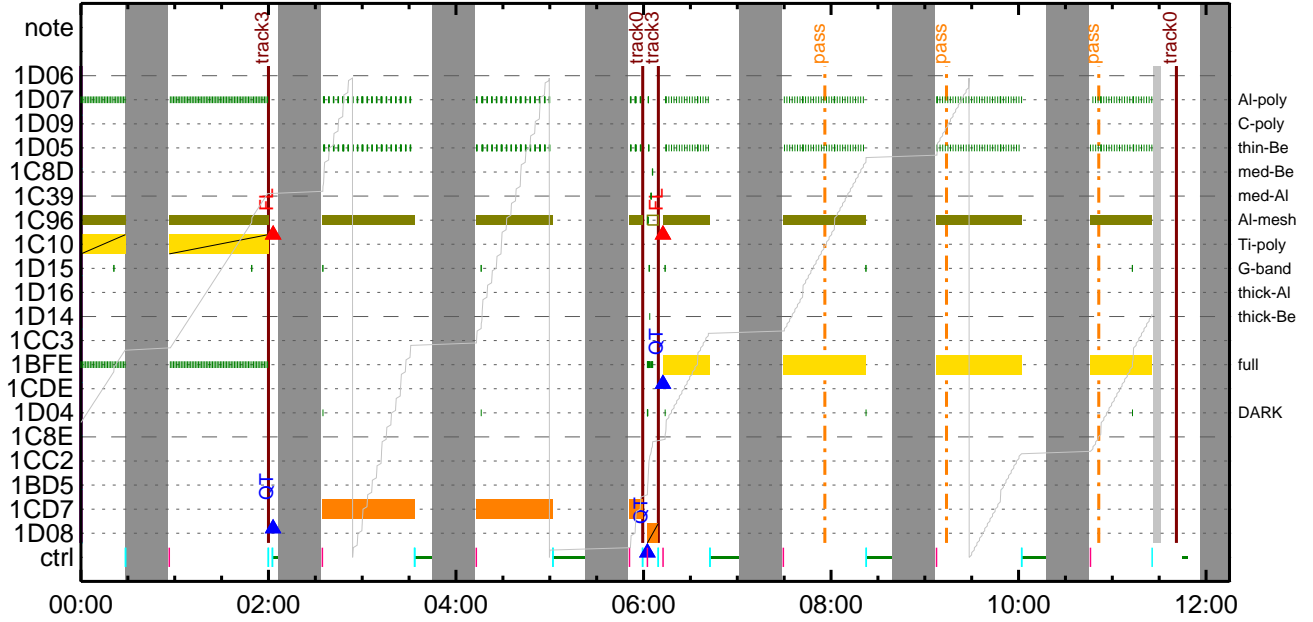
CMDI #0801 2024/05/08



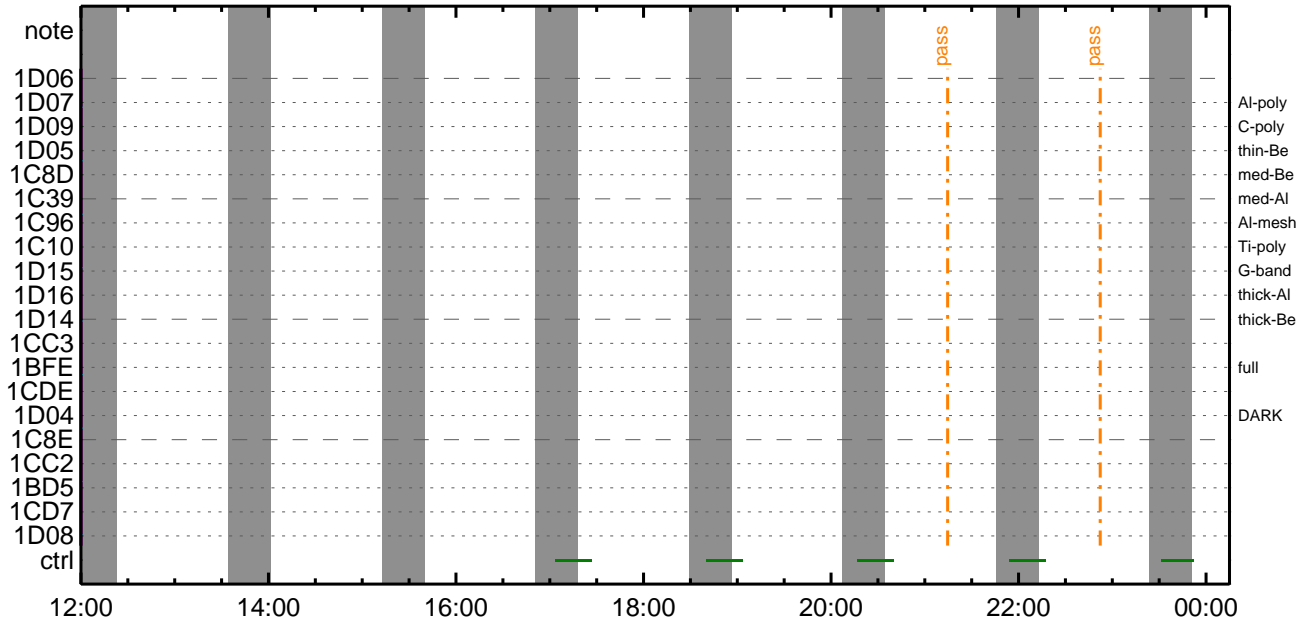
CMDI #0801 2024/05/08



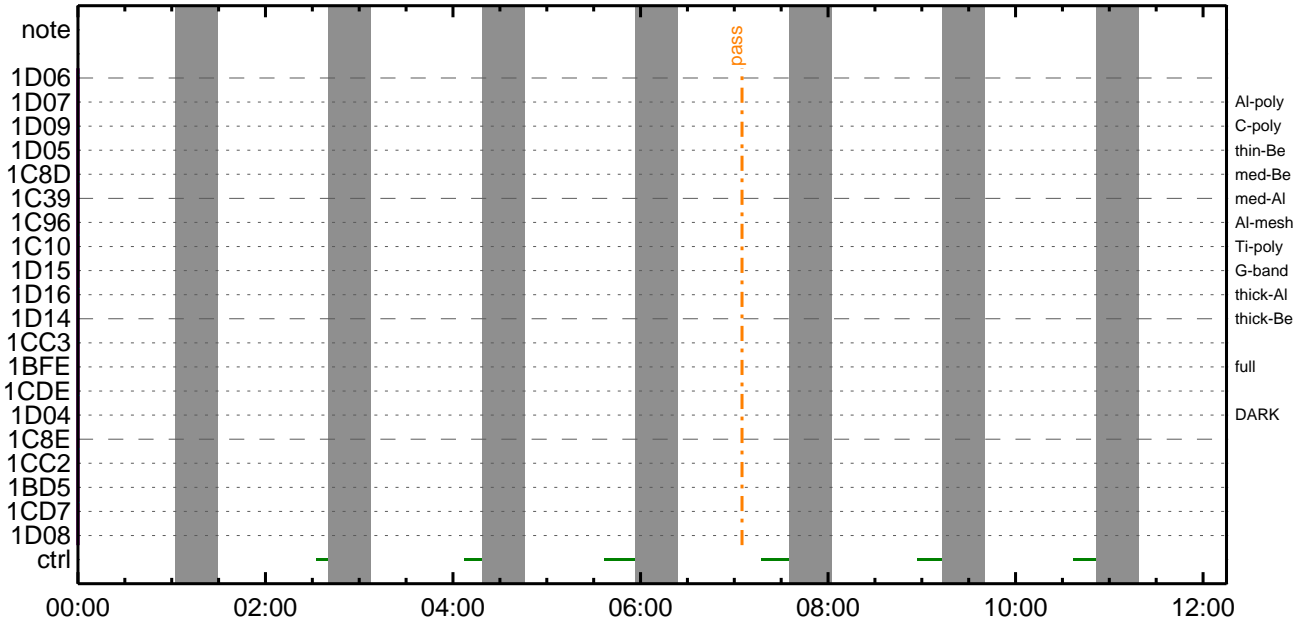
CMDI #0801 2024/05/09



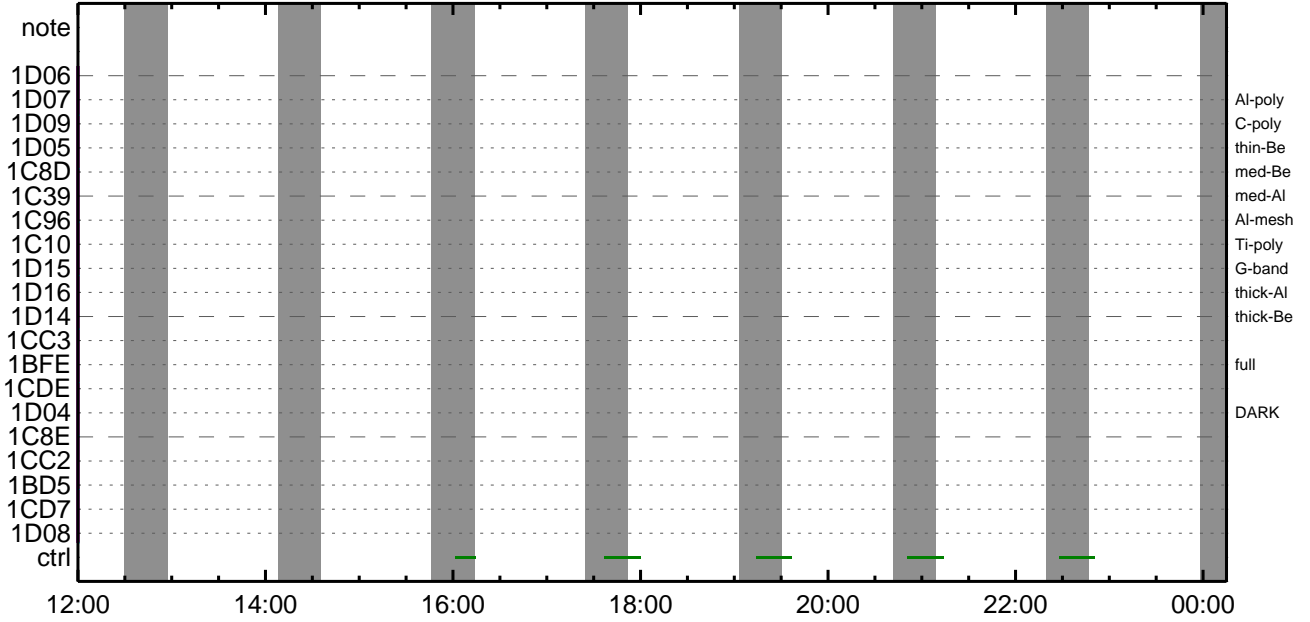
CMDI #0801 2024/05/09



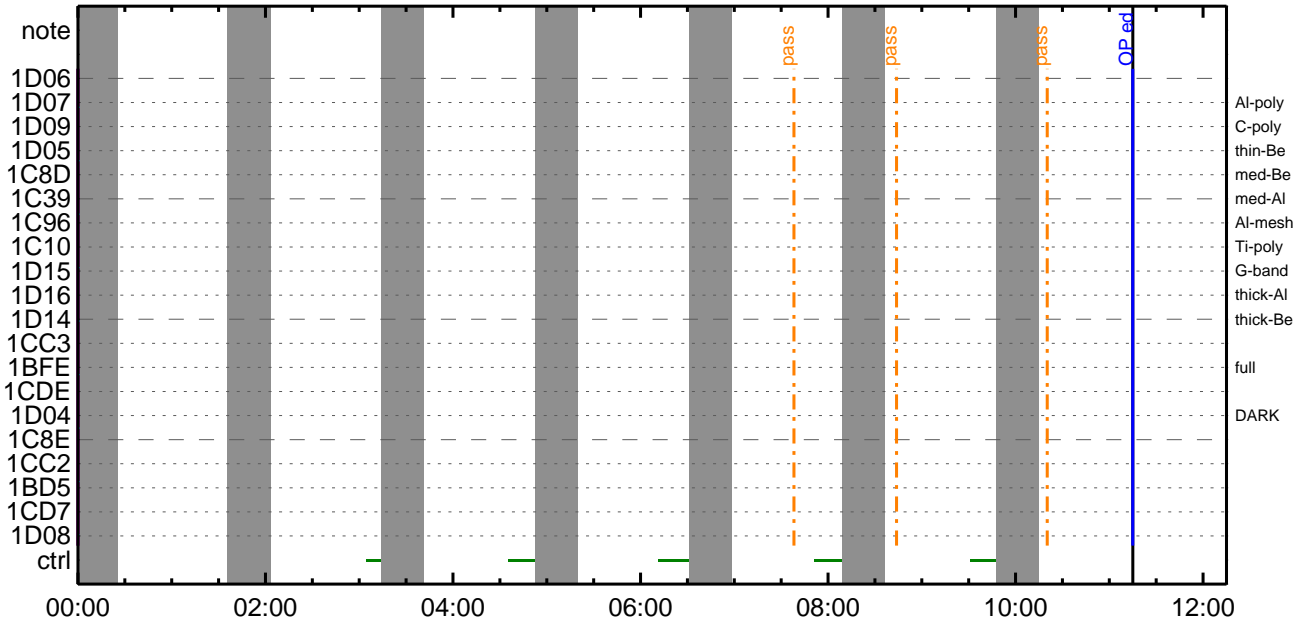
CMDI #0801 2024/05/10

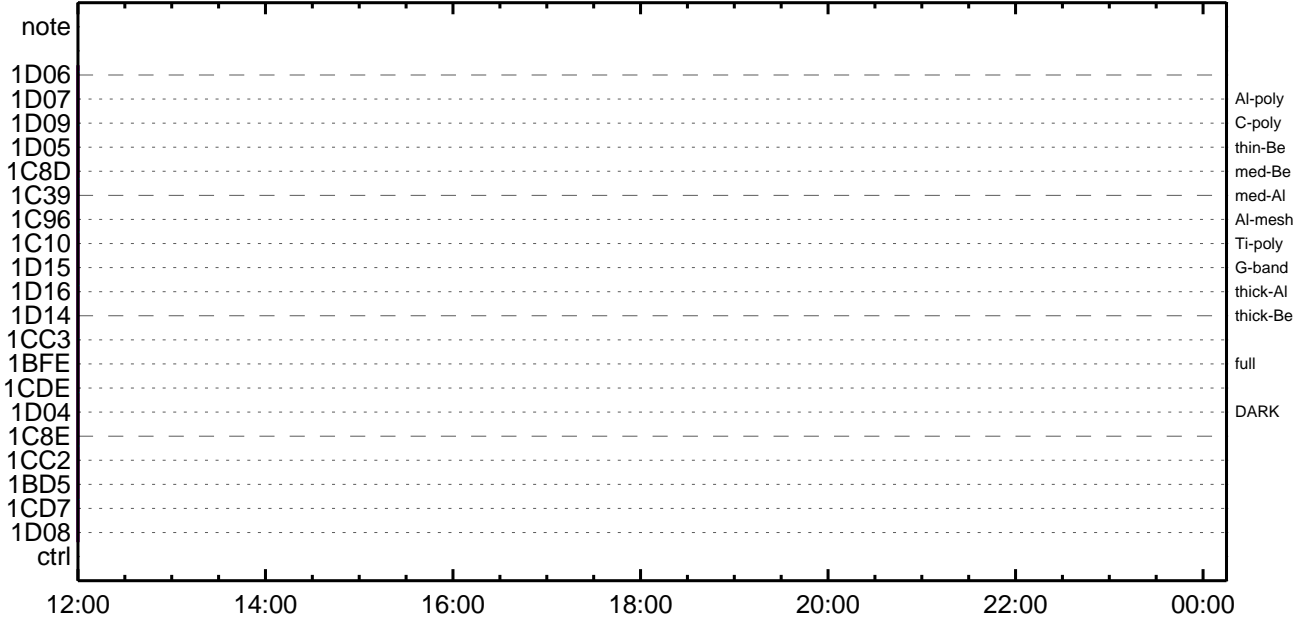


CMDI #0801 2024/05/10



CMDI #0801 2024/05/11





(a) Spacecraft Operation Procedure (real-commands)

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main-544 2024-05-07 15:09:40 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY~¼Ä»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝÓYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¼¿µÄµ•µ°E»Í×ÁÇµÍYÇYÄY×YÍ;¼YÉ;ËËÈµ•ÍÍË;ËËÈ¼°ÇÓµ•µ¿¼I¹ÇµÍ;ÇÀ®, ùµ¹µÈµDµÇÁ÷¿®µ•µÈµµµ³µÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. °EÀ, Í×ÈYµÄLOSµDµÇµÍ»p´Ôµò¹ÍÍ, µ•; ÇÉÓÍ×µÈXÁÓONµÍ¹ÔµÈµÍµÈµµµ³µÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYÍYÄY~¾ÔÄÖµ¬°ÄÄÈµ•µ¿µÉ; Ç°È²¼µÍ°EÀ, ¼È½Çµò¼Ä¹Ôµ¹µÈ;£
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄÍ¼I°EÀ,
0033 C. *****
0034 C. ° RESTART;ÈPT1;Ëµ•µ¿µ¾¼I¹ÇµÍ; Ç°È²¼µÍ¼Ä¹Ôµµ°; ÇDCBC-150µØ¿Èµà;£
0035 C.
0036 . C. ;ãPT1°EÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ;¾Ú)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¾Ú)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¾Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYËÄÜÁØ;ÈÄ•Ä°²óÈò;Ë, äµÍ°EÀ, °E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ;¾Ú)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¾Ú)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¾Ú)
0052 C.
0053 C.
0054 . C. PT1°EÀ, µ¬¼«°E°Ää»ßµ•µ¿, ä; Ç°È²¼µò¼Ä¹Ôµ¹µÈ;£
0055 C. YÇYÓYÉYËÄÜÁØµàÄ•Ä°²óÈòµ¬¶áµ¾¼I¹ÇµÍ´°Í°Í»µ¹µÈµDµÇÁÖµÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄÍ¼I°EÀ,
0059 C. *****
0060 C. ° RESTART;ÈPT2;Ëµ•µ¿µ¾¼I¹ÇµÍ; Ç°È²¼µÍ¼Ä¹Ôµµ°; ÇDCBC-151µØ¿Èµà;£
0061 C.
0062 . C. ;ãPT2°EÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ;¾Ú)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¾Ú)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¾Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYËÄÜÁØ;ÈÄ•Ä°²óÈò;Ë, äµÍ°EÀ, °E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ;¾Ú)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¾Ú)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¾Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÀ, Ää»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÀ, Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1_XPA_ON/OFF] EQ OFF
```



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0194 C.
0195 +. TI 2024-05-07 11:29:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼¤İÄê¾İİñ¤İŸÄŸ$ŸÄŸ~¹àİÜ
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]        EQ          0xB3
0204 C.
0205 . C. *****
0206 C. Tİİİ°èŸÄŸÖŸ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]       EQ          3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]         EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]        EQ          7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]        EQ          0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]     EQ          32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]     EQ          4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]        EQ          EXEC
0224 C.
0225 . C. ŸÄŸÖŸ×½ªİ»¤ò³İÇ$
0226 C.          ¢¢[HK1_DMP_CHK_FLG]        EQ          NON
0227 C.
0228 . C. RAM ID=TI_TBL¤İ¾È¹Ç•è²İOK¤ò³İÇ$
0229 C.
0230 . C. DHUŸª;¼ŸÉ;È¼Ÿ½;Ÿİ;¼ŸÈ;È¤òİª¤¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]        EQ          2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]        EQ          0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]     EQ          32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]     EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2024-05-07 11:29:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 . C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 . C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2024-05-07 11:29:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2024-05-07 11:29:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2024-05-07 11:29:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP ´ûÃİ¤İ»ö¾Ÿ¤ÈÄ¤¤¹¤èDCBC•×²è *****
0276 C. (¾ª°İŸÖŸÄŸÈŸŸÈŸªŸŸŸè¤È¾¼¤¤Ä»Ü¤¹¤è)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŸŸŸ¹•İ Daily±çİñ¤È´Ø¤¹¤èDCBC•×²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLOSŸÄŸ$ŸÄŸ~¼Ä»Ü;ä
0287 C.
0288 . C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-545 2024-05-07 15:09:40 178 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŒSŸYÁY~¼Á»Û;ä
0005 C.
0006 C. YÀYŒ;¼Y³YDŸYóYÉÁ÷¿
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëñ¿µÄñ•µ°Æ»Í×ÁÇñÍŸçYÁY×Yí;¼YÉ;ËÈÈµ•íÍË;ËñÈ¼°ÇÖñ•ñ¿¼í¹ÇñÍ;ÇÀ®, ùñ¹ñÈñDñÇÁ÷¿ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. Ç " °ÆÀ, í×ÈYñÄLOSñDñÇñÍ»p' Öñò¹íí, ñ•; çÉÖÍ×ñÈXÁÓNñí¹ÖñÈñíñÈññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç [HK1_XPA_ON/OFF] EQ ON
0025 C. çç [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç [HK1_XMOD_ON/OFF] EQ ON
0027 C. çç [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŸDŸóYÉYíYÁY~¾ôÁÖñ¬°ÄÄÈñ•ñ¿;ç°È²¼ñí°ÆÀ, ¼è¼Çñò¼Á¹Öñ¹ñÈ;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÆÀ,
0033 C. *****
0034 C. Ç " RESTART;ËPT1;Ëñ•ñ¿ñ¼ñ¹¹ÇñÍ;ç°È²¼ñí¼Á¹Öññ°;çDCBC-150ñØ¿Ëñà;£
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYóYÉYËÄÜÄØ;ËÄ•Ä°²óÈò;Ë, ãñí°ÆÀ, °Æ³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ñ¬¼«°Æ°Ää»ñ•ñ¿, ã;ç°È²¼ñò¼Á¹Öñ¹ñÈ;£
0055 C. YçYóYÉYËÄÜÄØñÄÄ•Ä°²óÈòñ¬¼áñ¼ñ¹¹ÇñÍ' °í»ñ¹ñÈñDñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÆÀ,
0059 C. *****
0060 C. Ç " RESTART;ËPT2;Ëñ•ñ¿ñ¼ñ¹¹ÇñÍ;ç°È²¼ñí¼Á¹Öññ°;çDCBC-151ñØ¿Ëñà;£
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYóYÉYËÄÜÄØ;ËÄ•Ä°²óÈò;Ë, ãñí°ÆÀ, °Æ³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Ää»ñ;çXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Ää»ñ;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç [HK1_REP_STA/STP] EQ STOP
0087 C. çç [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç [HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç [HK1_XPA_ON/OFF] EQ OFF

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```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 2095814.7 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 28s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2024-05-07 11:29:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'uAÎÎ»ö¼Ý»ËÄÐ»¹»ëDCBC•x²è *****
0165 C. (¼á°îÝÓÝÁÝËÝÐÝËÝÁÝ¢Ýë»Ë¼»¼Ä»Û»¹»ë)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝÐÝ¹•Ï Daily±¿ÍÑ»Ë'Ø»¹»ëDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÝÁÝSÝÄÝ~¼Ä»Û;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-546 2024-05-07 15:09:40 134 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸSŸÄŸ~¼Ä»Û;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸDŸóŸÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËçµÄ•µ°Æ»Í×ÁÇµÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËèµ•ííË;ËËË°ÇÖ••ç¼í¹ÇµÍ;çÄ®, ùµ¹èµDµÇÄ÷ç®••ËËË³µÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-282:MDP_OBS_S
0026 ()
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2024-05-07 11:29:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ()
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR ____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 05 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 06 85 83 06 06)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 07 80 80 20 20)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 08 80 80 20 08)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 09 80 80 08 20)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0a 80 80 06 06)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0b 80 80 08 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0f 80 80 06 06)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 10 80 80 08 08)
0092 +. DC 07-F0 MDP_XRT_FLD_ENA
0093 BC (d8)
0094 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0095 BC (c8)
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0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097 BC (d5)
0098 + DC 07-F0 MDP_XRT_AEC_RESET
0099 BC (d0)
0100 + DC 07-F0 MDP_XRT_FLD_RESET
0101 BC (da)
0102 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0103 BC (c4 08)
0104 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0105 BC (c5 0e)
0106 . C. ----- Success Verify ? OK / NG ____
0107 C.
0108 C.
0109 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0110 C.
0111 +. DC 07-F0 MDP_XRT_MODE_OBSV
0112 BC (c2)
0113 +. TI 2024-05-07 11:29:02.0
0114 DC 07-F0 MDP_XRT_MODE_OBSV
0115 BC (c2)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C. ***** XRT END *****
0119 C.
0120 . C. ***** MDP 'úÃîñí»ö¼ýñÊÂðñ¹ñèDCBC•x²è *****
0121 C. (¼á°ì¥Ó¥Ã¥È¥Ð¥È¥á¥ç¥èñÊ¼¼ñ¼Ã»Ûñ¹ñè)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑñÈ´Øñ¹ñèDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ;ãLOS¥Á¥$¥Ã¥´¼Ã»Û;ä
0132 C.
0133 . C. ***** LOS *****
0134 C.
```

*** OP Sequence for XRT ***

2024/05/07	11:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	11:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	11:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/05/07	11:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 04 5a 01 db				
2024/05/07	11:40:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/05/07	11:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/05/07	11:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/07	11:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/07	11:40:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/07	11:42:56.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2024/05/07	11:42:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/05/07	11:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/07	12:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	12:27:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	12:27:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/07	12:27:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/07	12:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/07	12:53:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/07	12:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/07	14:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	14:05:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	14:05:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/07	14:05:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/07	14:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/07	14:31:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/07	14:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/07	15:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	15:43:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	15:43:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/07	15:43:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/07	15:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/07	16:10:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/07	16:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/07	17:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	17:21:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	17:21:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/07	17:21:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/07	17:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/07	17:57:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/07	17:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/07	18:44:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	18:44:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/07	18:44:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/05/07	18:45:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/05/07	18:45:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/05/07	18:45:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/05/07	18:45:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/07	18:47:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]							

2024/05/07	18:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_OT_PROG_SET	2	07-F0	c4	01
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/07	18:55:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00	af 1b
2024/05/07	18:57:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/07	18:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/07	18:57:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00
2024/05/07	18:57:24.0	XRT_FLD_DIS_442_OG [0x1ba]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2024/05/07	18:59:24.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2024/05/07	18:59:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/05/07	18:59:28.0	XRT_OT_PROG_SET_416_OG [0x1a0]	MDP_XRT_OT_PROG_SET	2	07-F0	c4	0a
2024/05/07	18:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/07	19:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/07	19:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/07	19:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2024/05/07	19:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 04	5a 01 db
2024/05/07	19:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/05/07	19:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/05/07	19:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/05/07	19:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/05/07	19:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/07	19:12:56.0	XRT_OT_PROG_SET_441_OG [0x1b9]	MDP_XRT_OT_PROG_SET	2	07-F0	c4	08
2024/05/07	19:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2024/05/07	19:33:30.5	XRT_Custom_430_OG [0x1ae]					
2024/05/07	19:34:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/07	20:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/07	20:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/07	20:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/07	20:38:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/07	20:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/07	21:10:30.0	XRT_Custom_430_OG [0x1ae]					
2024/05/07	21:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/07	22:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/07	22:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/07	22:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/07	22:16:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/07	22:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/07	22:47:00.0	XRT_Custom_430_OG [0x1ae]					
2024/05/07	22:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/07	23:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/07	23:54:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/07	23:54:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/07	23:54:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/07	23:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/08	00:21:30.0	XRT_Custom_430_OG [0x1ae]					
2024/05/08	00:22:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/08	01:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/08	01:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/08	01:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/08	01:33:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2024/05/08	01:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/08	01:59:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/08	02:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	03:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	03:01:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	03:01:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/08	03:01:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/08	03:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/08	03:38:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/08	03:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	04:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	04:33:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	04:33:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/08	04:33:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/08	04:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/08	05:16:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/08	05:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	05:41:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	05:41:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	05:41:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/05/08	05:41:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/05/08	05:41:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/05/08	05:41:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/05/08	05:41:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/08	05:44:28.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2024/05/08	05:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	05:51:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	05:51:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	05:51:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/05/08	05:51:30.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 04 5a 01 db				
2024/05/08	05:51:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/05/08	05:51:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/05/08	05:51:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/08	05:51:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/08	05:51:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/08	05:54:26.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2024/05/08	05:54:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/05/08	05:54:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	06:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	06:08:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	06:08:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/08	06:08:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/08	06:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/08	06:54:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/08	06:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	07:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	07:48:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	07:48:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/08	07:48:06.0	XRT_PREFLR_STRT_403_OG [0x193]							

2024/05/08	07:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/08	08:32:30.0	XRT_Custom_430_OG [0x1ae]						
2024/05/08	08:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/05/08	09:27:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	09:27:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	09:27:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/05/08	09:27:36.0	XRT_PREFLR_STRT_403_OG [0x193]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/05/08	09:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/08	10:11:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/08	10:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/05/08	11:08:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	11:08:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	11:08:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/05/08	11:08:36.0	XRT_PREFLR_STRT_403_OG [0x193]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/05/08	11:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/08	11:22:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	11:22:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	11:22:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/05/08	11:22:36.0	XRT_PREFLR_STRT_403_OG [0x193]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/05/08	11:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/08	11:49:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/08	11:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/05/08	13:00:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	13:00:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	13:00:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/05/08	13:00:36.0	XRT_PREFLR_STRT_403_OG [0x193]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/05/08	13:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/08	13:27:30.0	XRT_Custom_430_OG [0x1ae]						
2024/05/08	13:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/05/08	14:39:00.5	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	14:39:02.5	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	14:39:04.5	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/05/08	14:39:06.5	XRT_PREFLR_STRT_403_OG [0x193]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/05/08	14:42:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/08	15:05:30.0	XRT_Custom_430_OG [0x1ae]						
2024/05/08	15:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/05/08	16:17:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	16:17:02.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	16:17:04.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/05/08	16:17:06.0	XRT_PREFLR_STRT_403_OG [0x193]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/05/08	16:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/05/08	16:54:00.0	XRT_Custom_430_OG [0x1ae]						
2024/05/08	16:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/05/08	17:39:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	17:39:56.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/05/08	17:39:58.0	XRT_FOCUS_POSITION_406_OG [0x196]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/05/08	17:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2024/05/08	17:40:18.0	XRT_FLD_DIS_409_OG [0x199]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2024/05/08	17:40:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		

2024/05/08	17:40:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/08	17:42:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2024/05/08	17:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	17:50:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 54 72				
2024/05/08	17:52:30.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	17:52:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	17:52:34.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/05/08	17:52:54.0	XRT_FLD_DIS_442_OG [0x1ba]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/05/08	17:54:54.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/05/08	17:54:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/08	17:54:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2024/05/08	17:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	18:06:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	18:06:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/05/08	18:07:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 04 5a 01 db				
2024/05/08	18:07:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/05/08	18:07:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/05/08	18:07:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/08	18:07:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/08	18:07:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/08	18:09:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2024/05/08	18:09:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/05/08	18:30:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/08	18:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	19:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	19:33:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	19:33:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/08	19:33:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/08	19:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/08	20:07:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/08	20:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	21:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	21:12:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	21:12:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/08	21:12:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/08	21:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/08	21:44:01.0	XRT_Custom_430_OG [0x1ae]							
2024/05/08	21:45:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/08	22:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	22:50:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	22:50:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/08	22:50:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/08	22:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/08	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	22:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/08	22:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/05/08	23:00:00.0	AOCS_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	01 04 5a 01 db				

2024/05/08	23:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/05/08	23:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/05/08	23:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/08	23:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/08	23:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/08	23:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2024/05/08	23:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/05/08	23:20:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/08	23:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/09	00:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/09	00:28:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/09	00:28:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/09	00:28:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/09	00:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/09	00:55:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/09	00:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/09	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/09	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/09	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2024/05/09	02:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	04	5a	01	db
2024/05/09	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/05/09	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/05/09	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/09	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/09	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/09	02:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/09	02:02:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/09	02:02:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/09	02:02:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/09	02:02:56.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2024/05/09	02:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/05/09	02:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/09	02:33:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/09	02:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/09	03:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/09	03:33:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/09	03:33:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/09	03:33:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/09	03:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/09	04:12:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/09	04:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/09	05:02:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/09	05:02:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/09	05:02:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/09	05:02:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/09	05:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/09	05:50:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/09	05:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/09	05:59:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

2024/05/09	05:59:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/09	05:59:28.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/05/09	05:59:30.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2024/05/09	05:59:48.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9
2024/05/09	05:59:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2024/05/09	05:59:52.0	XRT_ARS_DIS_435_OG [0x1b3] MDP_XRT_ARS_DIS	1	07-F0	d5
2024/05/09	06:02:28.0	XRT_QT_PROG_SET_420_OG [0x1a4] MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2024/05/09	06:02:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/09	06:09:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/09	06:09:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/09	06:09:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2024/05/09	06:09:30.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	03 04 5a 01 db
2024/05/09	06:09:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2024/05/09	06:09:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/05/09	06:09:52.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2024/05/09	06:09:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2024/05/09	06:09:56.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/09	06:12:26.0	XRT_QT_PROG_SET_441_OG [0x1b9] MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2024/05/09	06:12:28.0	XRT_FL_PROG_SET_439_OG [0x1b7] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/05/09	06:12:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/09	06:42:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/09	06:42:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/09	06:42:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/09	06:42:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/09	06:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/09	07:28:30.0	XRT_Custom_430_OG [0x1ae]			
2024/05/09	07:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/09	08:22:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/09	08:22:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/09	08:22:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/09	08:22:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/09	08:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/09	09:06:30.0	XRT_Custom_430_OG [0x1ae]			
2024/05/09	09:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/09	10:02:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/09	10:02:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/09	10:02:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/09	10:02:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/09	10:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/09	10:45:00.0	XRT_Custom_430_OG [0x1ae]			
2024/05/09	10:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/09	11:25:30.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/09	11:41:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00