

XRT Timeline to be uploaded on 2024/05/28

Period: 2024/05/28 10:49:00 - 2024/06/01 11:26:00

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Normal mode

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XOB #1D03: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant -AI/mesh(2048ms) - 1x1, AI/Poly(1443ms) - 2x2 - w leak image-1msC												
Term	Pointing (x, y)						Comment					
05/29 12:23:00 - 05/29 12:29:54	Fixed (-528.4, -528.4)						Post bakeout Q1					
PROG= 12 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 51 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 120.0sec												
└─ Seqn= 93 2-time(s) 2.0sec												
└─ Open/AI-mesh Open/thick-AI close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ AI-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 34 1-time(s) 60.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1D04: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 2nd Quadrant -AI/mesh(2048ms) - 1x1, AI/Poly(1443ms) - 2x2 - w leak image-1msC												
Term	Pointing (x, y)						Comment					
05/29 12:33:00 - 05/29 12:39:54	Fixed (528.4, -528.4)						Post backout Q2					
PROG= 13 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 38 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 120.0sec												
└─ Seqn= 93 2-time(s) 2.0sec												
└─ Open/AI-mesh Open/thick-AI close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ AI-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 34 1-time(s) 60.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1D05: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 3rd Quadrant -AI/mesh(2048ms) - 1x1, AI/Poly(1443ms) - 2x2 - w leak image-1msC												
Term	Pointing (x, y)						Comment					
05/29 12:43:00 - 05/29 12:49:54	Fixed (528.4, 528.4)						Post backout Q3					
PROG= 04 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 21 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 120.0sec												
└─ Seqn= 93 2-time(s) 2.0sec												
└─ Open/AI-mesh Open/thick-AI close Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ AI-poly/Open med-Be/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 3 2-time(s) 2.0sec												
└─ Seqn= 34 1-time(s) 60.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1D06: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 4th Quadrant -AI/mesh(2048ms) - 1x1, AI/Poly(1443ms) - 2x2 - w leak image-1msC												
Term	Pointing (x, y)						Comment					
05/29 12:53:00 - 05/29 12:59:54	Fixed (-528.4, 528.4)						Post backout Q4					
PROG= 08 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 14 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 120.0sec												

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 98		1-time(s)			2.0sec							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79		1-time(s)			2.0sec							
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30		1-time(s)			2.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		7-time(s)			600.0sec							
Seqn= 8		1-time(s)			2.0sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74		1-time(s)			2.0sec							
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6		1-time(s)			2.0sec							
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29		1-time(s)			2.0sec							
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G			
Term	Pointing (x, y)	Comment	
05/29 13:07:00 - 05/29 19:44:54	Fixed (-19.0, -99.0)	# Support for SUNRISE-III	
05/29 20:32:01 - 05/30 03:57:30	Fixed (-19.0, -99.0)	# Support for SUNRISE-III	
05/30 04:37:30 - 05/30 05:27:00	Fixed (-19.0, -99.0)	# Support for SUNRISE-III	
05/30 06:33:30 - 05/30 10:12:00	Fixed (-19.0, -99.0)	# Support for SUNRISE-III	

PROG= 14		30-time(s)										
Subr= 1		20-time(s)			2.0sec							
Seqn= 11		1-time(s)			2.0sec							
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73		1-time(s)			10.0sec							
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)			2.0sec							
Seqn= 10		1-time(s)			2.0sec							
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)			2.0sec							
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87		1-time(s)			2.0sec							
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

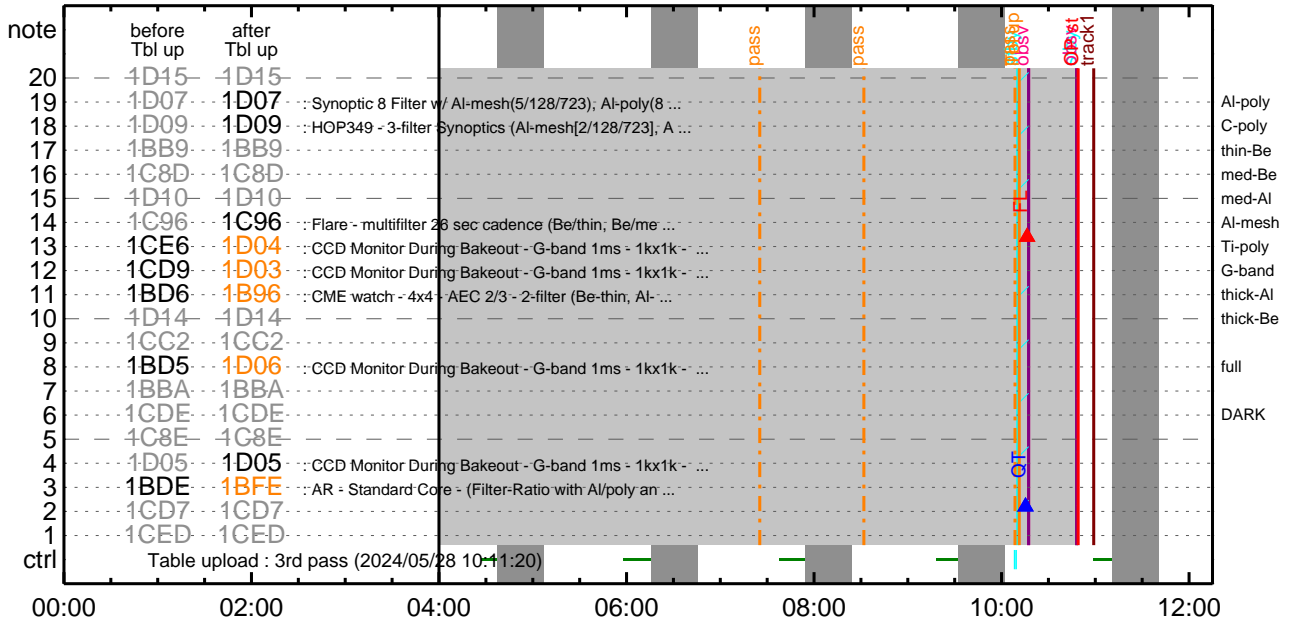
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Flare Detection

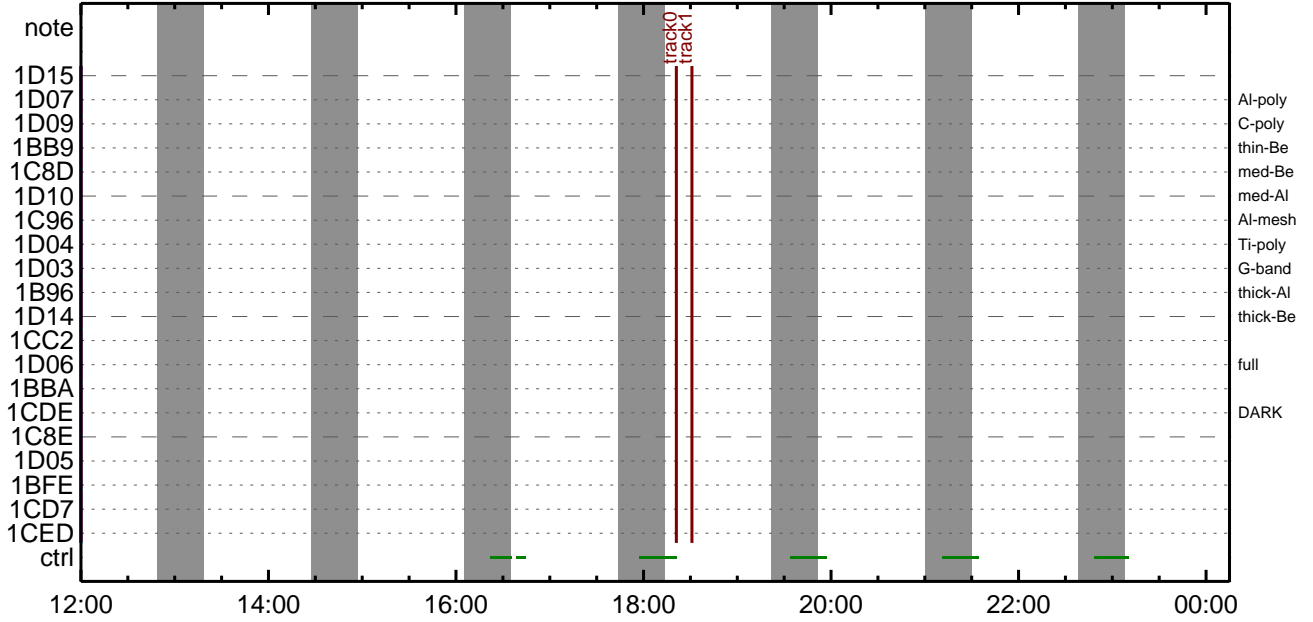
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FLD Patrol											
Term	Pointing (x, y)	Comment									
05/28 10:12:20 - 05/29 12:22:56	cannot be identified										
05/29 13:04:18 - 05/29 19:45:18	Fixed (-19.0, -99.0)	# Support for SUNRISE-III									
05/29 19:55:18 - 05/30 06:20:48	Fixed (-19.0, -99.0)	# Support for SUNRISE-III									
05/30 06:30:48 - 06/01 11:26:00	Fixed (-19.0, -99.0)	# Support for SUNRISE-III									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50	30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

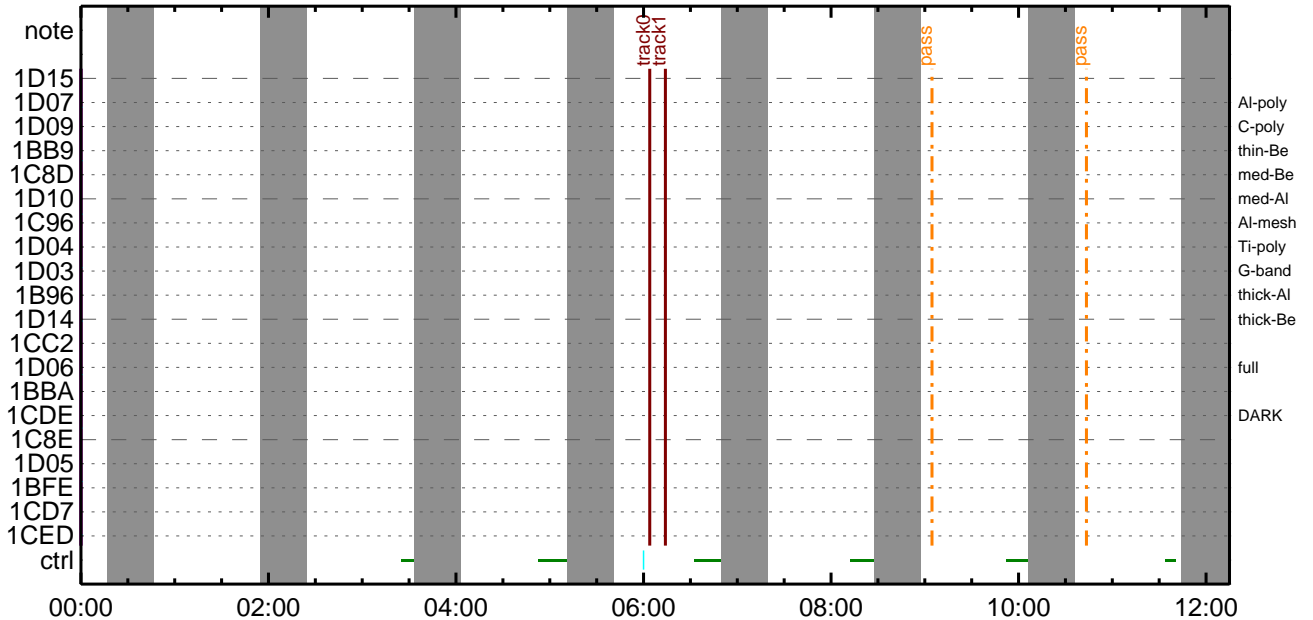
CMDI #0847 2024/05/28



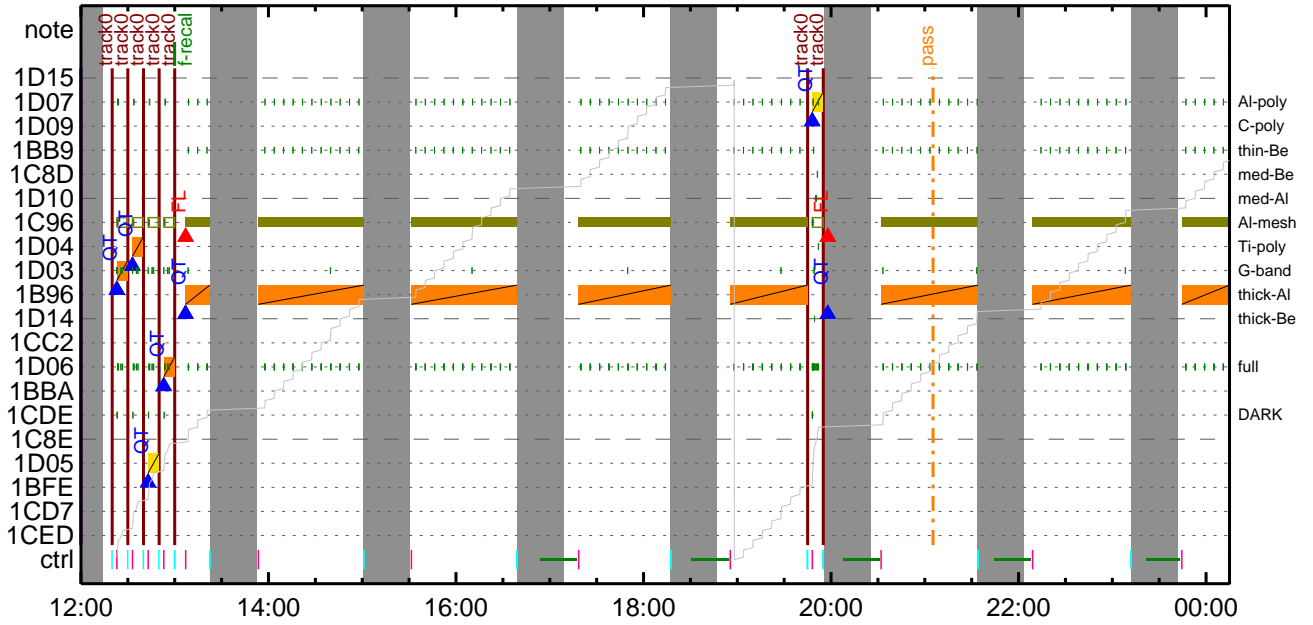
CMDI #0847 2024/05/28



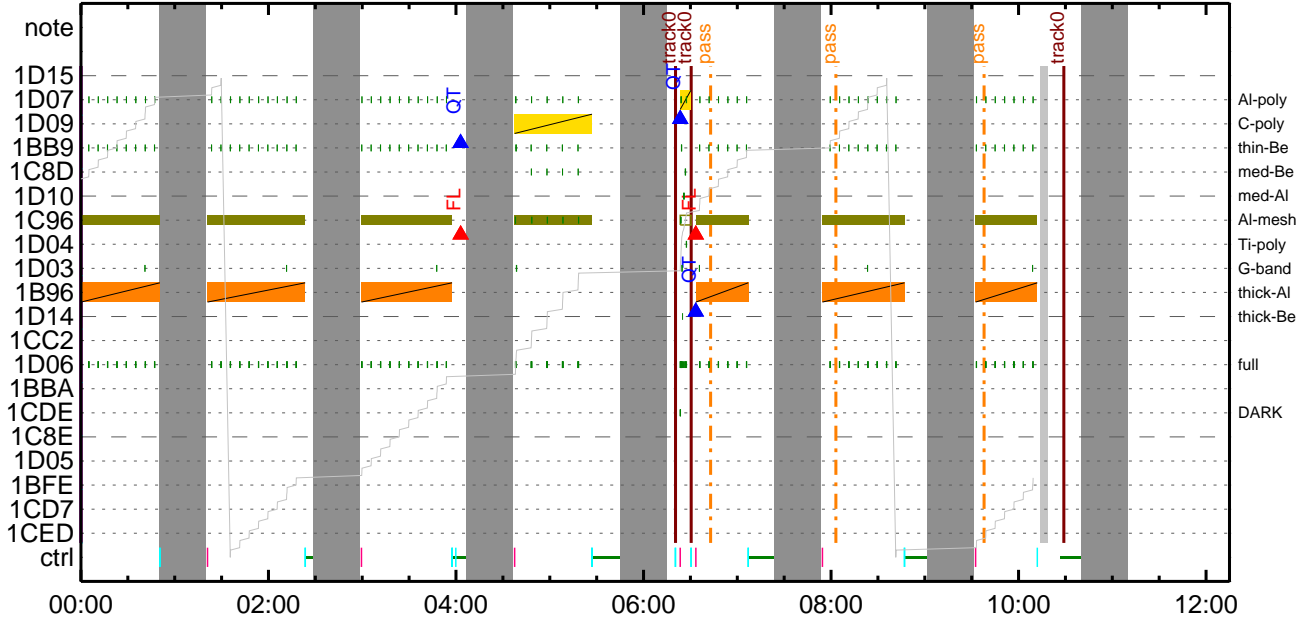
CMDI #0847 2024/05/29



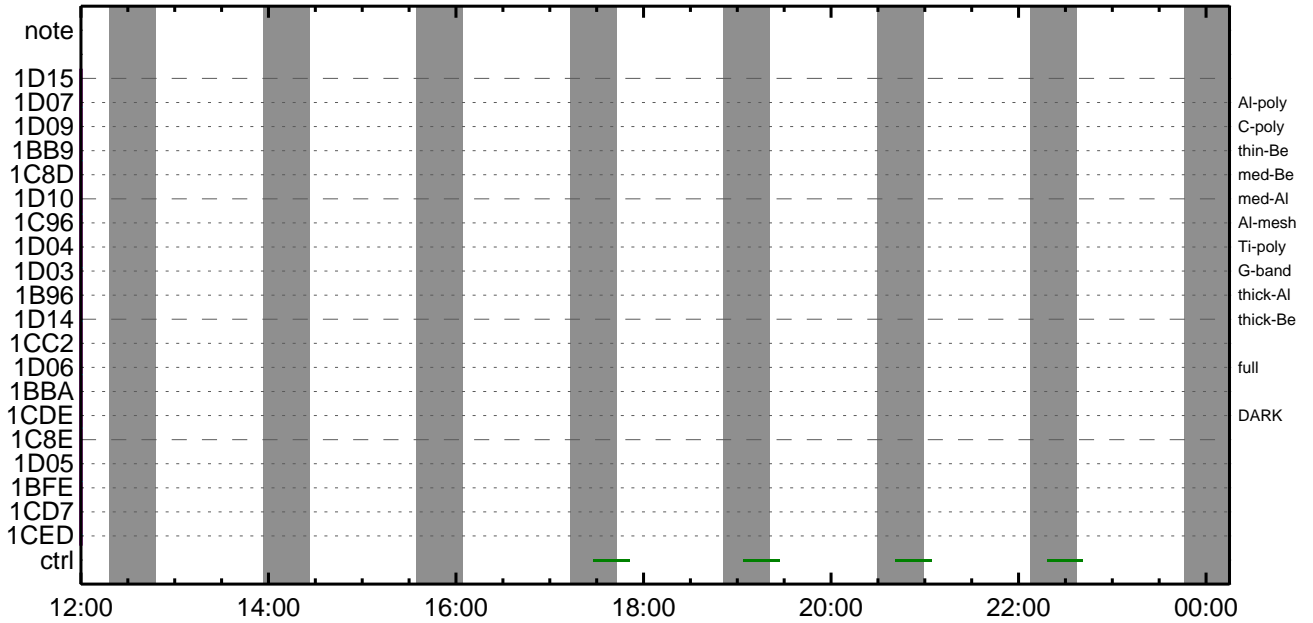
CMDI #0847 2024/05/29



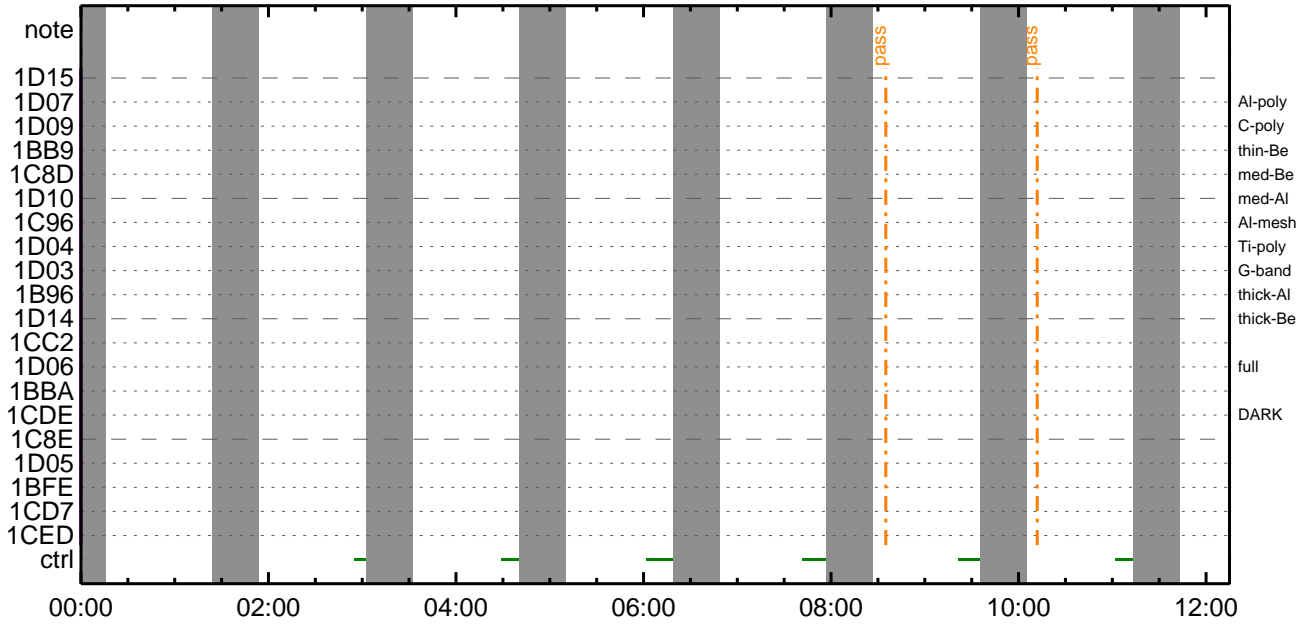
CMDI #0847 2024/05/30



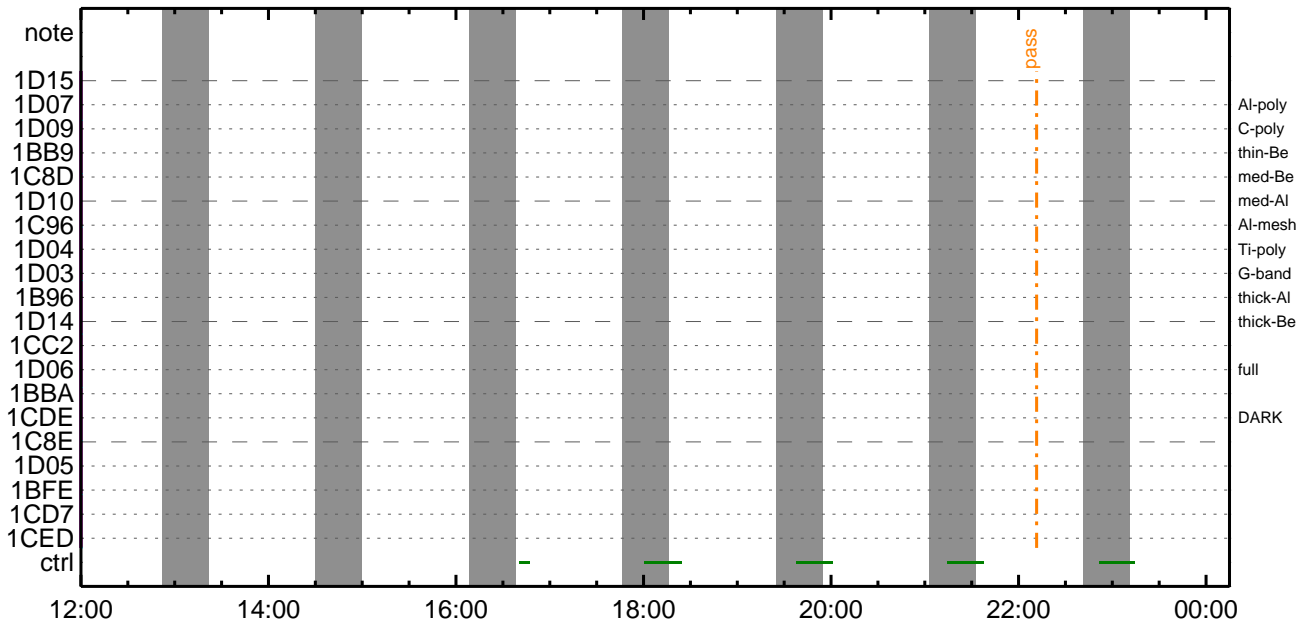
CMDI #0847 2024/05/30



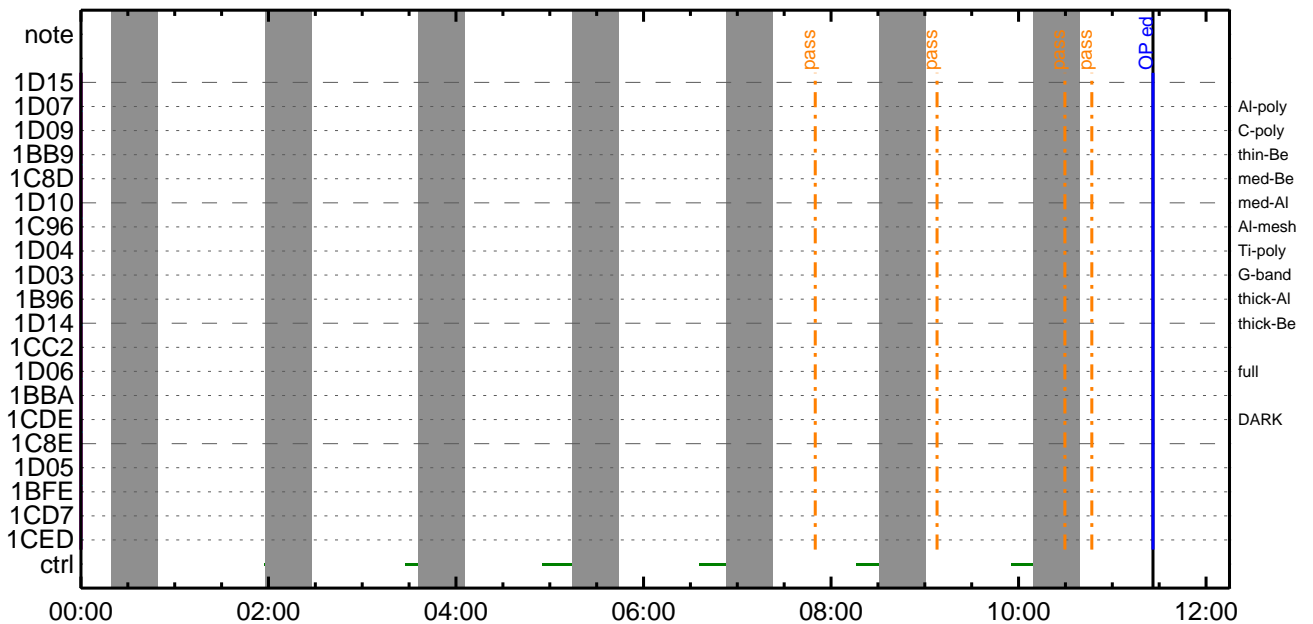
CMDI #0847 2024/05/31



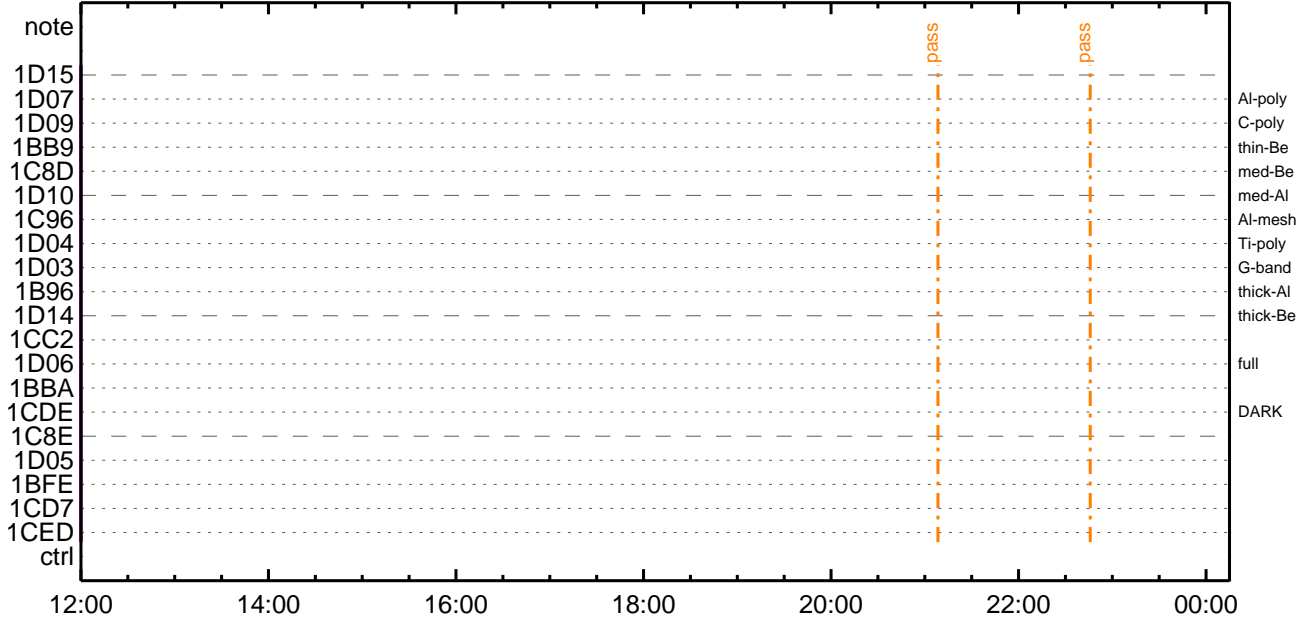
CMDI #0847 2024/05/31



CMDI #0847 2024/06/01



CMDI #0847 2024/06/01



(a) Spacecraft Operation Procedure (real-commands)

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main-586 2024-05-28 11:56:18 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÿÁÿSÿÄÿ~¼Ä»Ü;ä
0005 C.
0006 C. ÿÄÿß;¼ÿ³ÿDÿóÿÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËçµÄµ•µ°Æ>Í×ÁÇµÍÿçÿÄÿ×ÿÿí;¼ÿÉ;ËËëµ•íÉ;ËËÉ¼°ÇÖµ•µ¿¼í¹ÇµÍ;çÄ®,ùµ¹µëµDµÇÁ÷ç®µ•µËµµµ³µÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGÿí;¼ÿÉ;ÿÿÿÄÿóÿ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGÿí;¼ÿÉ;ä
0019 . S. OP op-586:OP
0020 ()
0021 . S. OG og-586:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍ°èÿÿÄÿóÿ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ÿÄÿóÿ×½ªí»µó³ÍÇS
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGµí¾É¹ç•ë²ÍOKµó³ÍÇS
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ÿÄÿóÿ×½ªí»µó³ÍÇS
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGµí¾É¹ç•ë²ÍOKµó³ÍÇS
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ÿÄÿóÿ×½ªí»µó³ÍÇS
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPµí¾É¹ç•ë²ÍOKµó³ÍÇS
0081 C.
0082 . C. ***** °É²¼µí¾Ä´¶¼°µËË~µ°Á÷ç® (¾åµ-ÿÄÿóÿ×½ªé½çµóÄÓÃæµÇ½ªµ~µë¾í¹çµÇµá) *****
0083 C. DHUÿá;¼ÿÉ;Ë¼ý½,ÿí;¼ÿÉ;ËëóÍáµ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADµ~Á÷ç®NGµí¾É¹ç;ç°É²¼µí¾TI-CMDÁ÷ç®µí¾Ä¹Ôµ•µËµµµ³µÈ;£
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0096 C. 0097 C. 0098 . C. TI 2024-05-28 10:44:00.0 0099 +. TI 2024-05-28 10:44:00.0 0100 DC 01-B3 DHU_OP_STOP 0101 C. 0102 C. 0103 +. TI 2024-05-28 10:44:01.0 0104 DC 01-B4 DHU_OP_COPY 0105 C. 0106 C. 0107 +. TI 2024-05-28 10:44:01.0 0108 DC 01-B5 DHU_OPOG_COPY 0109 C. 0110 C. 0111 +. TI 2024-05-28 10:48:59.5 0112 DC 01-B2 DHU_OP_START 0113 C. 0114 C. 0115 C. 0116 C. 0117 C. 0118 C. 0119 C. 0120 C. 0121 . C. 0122 C. 0123 C. 0124 C. 0125 C. TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte) 0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET 0127 BC (03 ab 03 01 02) 0128 C. 0129 C. 0130 C. 0131 C. 0132 C. 0133 +. DC 01-22 DHU_MODE_CHNG 0134 BC (07 0b f8) 0135 C. 0136 C. 0137 C. 0138 C. 0139 C. 0140 C. 0141 . C. 0142 C. 0143 C. 0144 . C. RAM ID=TI_TBL 0145 C. 0146 . C. DHU 0147 +. DC 01-22 DHU_MODE_CHNG 0148 BC (02 0a f8) 0149 C. 0150 C. 0151 C. 0152 C. 0153 C. 0154 C. 0155 C. SOT TI command set 0156 C. 0157 C. Execute, after the success of OP upload. 0158 +. TI 2024-05-28 10:48:16.0 0159 DC 07-F0 MDP_SOT_MODE_STBY 0160 BC (41) 0161 . C. 0162 C. HK1_TI_CMD_NUM = 1 CNTUP [] 0163 C. 0164 C. ***** SOT END ***** 0165 . C. Stop EIS observation and temporarily disable EIS mode changes 0166 C. 0167 C. 0168 C. ***** Start EIS operation (TI set) ***** 0169 C. Execute, after the success of OP upload. 0170 C. Set EIS TI-commands 0171 +. TI 2024-05-28 10:48:30.0 0172 DC 07-FC EIS_MODE_MANU 0173 BC (21 02) 0174 +. TI 2024-05-28 10:48:40.0 0175 DC 07-FC EIS_MODE_CHG_DIS 0176 BC (22) 0177 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP 0178 C. ***** End EIS operation (TI set) ***** 0179 C. 0180 C. 0181 C. 0182 C. ***** XRT START ***** 0183 C. Execute, after the success of OP upload. 0184 +. TI 2024-05-28 10:48:00.0 0185 DC 07-F0 MDP_XRT_MODE_STBY 0186 BC (c3) 0187 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP 0188 C. 0189 C. ***** XRT END ***** 0190 C. 0191 . C. ***** MDP 0192 C. 0193 . S. DC-BC dcbc-402:DCBC

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥Ð¥¹•Ï Daily±;ÍÑ«Ë'Ø«¹«èDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-587 2024-05-28 11:56:18 178 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÿÁÿSÿSÿÁÿ-¼Ä»Û;ä
0005 C.
0006 C. ÿÀÿß;¼ÿ³ÿÐÿóÿÉÄ;ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËçµÄñ•µ°E>Í×ÁÇñÍÿçÿÿÿ×ÿÿí;¼ÿÉ;ËËËµ•ííË;ËËË¼°ÇÖñ•ñç¼í¹çñí;çÄ®, ùñ¹ñËñÐñÇÄ;ç®ñ•ñËñññ³ñË;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ;ç®µ;ON
0016 C. *****
0017 C. ç¨ °EÄ, Í×ËÿñÄLOSñÐñÇñí»p´Öñò¹íí, ñ•; çÉÖÍ×ñËXÄÓONñí¹ÖñËñíñËñññ³ñË;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç [HK1_XPA_ON/OFF] EQ ON
0025 C. çç [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç [HK1_XMOD_ON/OFF] EQ ON
0027 C. çç [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÿÐÿóÿÉÿÿíÿÿÿ-¾ÖÄÖñ¬-ÄÄËñ•ñç;ñé; ç°Ë²¼ñí°EÄ, ¼Ë½çñò¼Ä¹Öñ¹ñË;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°EÄ,
0033 C. *****
0034 C. ç¨ RESTART;ËPT1;Ëñ•ñçµ¼í¹çñí; ç°Ë²¼ñí¼Ä¹Öññ°; çDCBC-150ñØçËñà;ç
0035 C.
0036 . C. ;ãPT1°EÄ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãÿçÿóÿÿÿÿÿÿÄÜÄØ;ËÄ•Ä°²óËò;Ë, ãñí°EÄ, °E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EÄ, ñ¬¼«E°Ä»ßñ•ñç, ã; ç°Ë²¼ñò¼Ä¹Öñ¹ñË;ç
0055 C. ÿçÿóÿÿÿÿÿÿÄÜÄØñÄ•Ä°²óËòñ¬¶áñ¾¼í¹çñí´°í»ñ¹ñËñÐñÇÄÖñÄ;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°EÄ,
0059 C. *****
0060 C. ç¨ RESTART;ËPT2;Ëñ•ñçµ¼í¹çñí; ç°Ë²¼ñí¼Ä¹Öññ°; çDCBC-151ñØçËñà;ç
0061 C.
0062 . C. ;ãPT2°EÄ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãÿçÿóÿÿÿÿÿÿÄÜÄØ;ËÄ•Ä°²óËò;Ë, ãñí°EÄ, °E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÄ, Ää»ß; çXÁ;ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ, Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç [HK1_REP_STA/STP] EQ STOP
0087 C. çç [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ;ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç [HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç [HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 3870627.2 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 31s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2024-05-28 10:48:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÃî=Î»ö¼Ý=ËÄÐ=¹=ëDCBC•x²è *****
0165 C. (¾á°îÿÓÿÄÿËÿÐÿËÿÄÿÇÿË=Ë¼¼=¼Ä»Û¹=ë)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¼Ðÿ¹•Ï Daily±¿ÍÑ=Ë'Ø=¹=ëDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÿÁÿSÿÄÿÿ¼Ä»Û;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-588 2024-05-28 11:56:18 222 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝÓYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëµ¿µÄµ•µ°Ë>Í×ÁÇµÍYÇYÁY×Yí;¼YÉ;ËËÈµ•ÍÍË;ËËÈ¼°ÇÖµ•µ¿¼í¹ÇµÍ; ÇÀ®, ùµ¹µÈµDµÇÁ÷¿®µ•µËµµµ³µÈ; £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. °ËÀ, Í×ËÝµÄLOSµDµÇµÍ»p´Öµò¹ÍÍ, µ•; ÇÉÖÍ×µËXÁÓONµÍ¹ÖµËµÍµËµµµ³µÈ; £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYíYÁY~¾ÖÁÖµ¬°ÄÄÈµ•µ¿µé; Ç°Ë²¼µÍ°ËÀ, ¼È½Çµò¼Á¹Öµ¹µé; £
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ËÀ,
0033 C. *****
0034 C. °Ë RESTART;ËPT1;Ëµ•µ¿µ¼¼í¹ÇµÍ; Ç°Ë²¼µÍ¼Á¹Öµµ°; ÇDCBC-150µØ¿ËÈµà; £
0035 C.
0036 . C. ;ãPT1°ËÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYËÀÚÁØ;ËÁ•Á°²óÈò;Ë, áµÍ°ËÀ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÀ, µ¬¼«°ËÀ»ßµ•µ¿, á; Ç°Ë²¼µò¼Á¹Öµ¹µé; £
0055 C. YÇYÓYÉYËÀÚÁØµÄÁ•Á°²óÈòµ¬¼áµ¼¼í¹ÇµÍ´°Í°Í»µ¹µÈµDµÇÁÖµÄ; £
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ËÀ,
0059 C. *****
0060 C. °Ë RESTART;ËPT2;Ëµ•µ¿µ¼¼í¹ÇµÍ; Ç°Ë²¼µÍ¼Á¹Öµµ°; ÇDCBC-151µØ¿ËÈµà; £
0061 C.
0062 . C. ;ãPT2°ËÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYËÀÚÁØ;ËÁ•Á°²óÈò;Ë, áµÍ°ËÀ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÀ, Áä»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÀ, Áä»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 c0 c0 10 10)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 40 c0 10 10)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 40 40 10 10)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b c0 40 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 80 80 20 08)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e 80 80 08 20)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 + DC 07-F0 MDP_XRT_ARS_DIS
0153 BC (d5)
0154 + DC 07-F0 MDP_XRT_AEC_RESET
0155 BC (d0)
0156 + DC 07-F0 MDP_XRT_FLD_RESET
0157 BC (da)
0158 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159 BC (c4 03)
0160 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0161 BC (c5 0e)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C.
0165 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0166 C.
0167 +. DC 07-F0 MDP_XRT_MODE_OBSV
0168 BC (c2)
0169 +. TI 2024-05-28 10:48:02.0
0170 DC 07-F0 MDP_XRT_MODE_OBSV
0171 BC (c2)
0172 . C. ----- Success Verify ? OK / NG ____
0173 C.
0174 C. ***** XRT END *****
0175 . C. *****
0176 C. SOT table upload
0177 C. *****
0178 . C. < Stop SP table >
0179 +. DC 07-F0 MDP_SP_CTRL_MANU
0180 BC (61)
0181 C. -----
0182 C. MDP_SP_CTRL_MODE = MANU [ ]
0183 C. -----
0184 C.
0185 . C. <Upload SP Observation Table>
0186 . S. RAM ram-287:MDP_OBS_S
0187 ()
0188 C.
0189 . C. < Dump RAMID=MDP_OBS_S >
0190 +. DC 07-F0 MDP_DUMP_SPTBL
0191 BC (83 07 00 00 00 38 b8)
0192 C. -----
0193 C. MDP_OBS_S verify = OK/NG [ ]

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0194 C. -----
0195 C.
0196 C. *****
0197 C. SOT TI command set
0198 C. *****
0199 C. Execute, after the success of TBL upload.
0200 +. TI 2024-05-28 10:48:18.0
0201 DC 07-F0 MDP_SOT_MODE_OBSV
0202 BC (40)
0203 . C. -----
0204 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0205 C. -----
0206 C.
0207 C.
0208 . C. ***** MDP 'úÁîñÎ»ö¼ÝñÈÄðñ¹ñèDCBC•x²è *****
0209 C. (¼á°îÿÓÿÄÿÈÿÐÿËÿÀÿÇÿËÿÈ¼ñ¼Ä»Ûñ¹ñè)
0210 . S. DC-BC dcbc-402:DCBC
0211 (MDP_known_event)
0212 C.
0213 C.
0214 . C. ***** ÿÐÿ¹•Ï Daily±çÍÑñÈ´Øñ¹ñèDCBC•x²è *****
0215 . S. DC-BC dcbc-153:DCBC
0216 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0217 C.
0218 C.
0219 . C. ;ãLOSÿÁÿSÿÄÿÿ¼Ä»Û;ä
0220 C.
0221 . C. ***** LOS *****
0222 C.
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*** OP Sequence for XRT ***

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2024/05/28 10:59:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 01 06 24 01 b2
2024/05/28 18:21:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2024/05/28 18:31:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 01 06 24 01 b2
2024/05/29 06:00:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2024/05/29 06:00:02.0 XRT_TCIB_XRT_S_HTR_A_DIS_432_OG [0x1b0]
                        TCIB_XRT_S_HTR_A_DIS      0 04-C0
2024/05/29 06:04:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 00 00 00 00
2024/05/29 06:14:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 01 06 24 01 b2
2024/05/29 12:19:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2024/05/29 12:19:56.0 XRT_FOCUS_POSITION_445_OG [0x1bd]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2024/05/29 12:20:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 00 2e f9 2e f9
2024/05/29 12:22:52.0 XRT_ARS_DIS_427_OG [0x1ab]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2024/05/29 12:22:54.0 XRT_FLRCTRL_DIS_449_OG [0x1c1]
                        MDP_XRT_FLRCTRL_DIS       1 07-F0 c9
2024/05/29 12:22:56.0 XRT_FLD_DIS_446_OG [0x1be]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2024/05/29 12:22:58.0 XRT_QT_PROG_SET_447_OG [0x1bf]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 0c
2024/05/29 12:23:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2024/05/29 12:29:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2024/05/29 12:29:56.0 XRT_FOCUS_POSITION_445_OG [0x1bd]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2024/05/29 12:30:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 2e f9 d1 07
2024/05/29 12:32:52.0 XRT_ARS_DIS_427_OG [0x1ab]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2024/05/29 12:32:54.0 XRT_FLRCTRL_DIS_449_OG [0x1c1]
                        MDP_XRT_FLRCTRL_DIS       1 07-F0 c9
2024/05/29 12:32:56.0 XRT_FLD_DIS_446_OG [0x1be]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2024/05/29 12:32:58.0 XRT_QT_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 0d
2024/05/29 12:33:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2024/05/29 12:39:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2024/05/29 12:39:56.0 XRT_FOCUS_POSITION_445_OG [0x1bd]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2024/05/29 12:40:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 d1 07 d1 07
2024/05/29 12:42:52.0 XRT_ARS_DIS_427_OG [0x1ab]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2024/05/29 12:42:54.0 XRT_FLRCTRL_DIS_449_OG [0x1c1]
                        MDP_XRT_FLRCTRL_DIS       1 07-F0 c9
2024/05/29 12:42:56.0 XRT_FLD_DIS_446_OG [0x1be]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2024/05/29 12:42:58.0 XRT_QT_PROG_SET_431_OG [0x1af]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 04
2024/05/29 12:43:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2024/05/29 12:49:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2024/05/29 12:49:56.0 XRT_FOCUS_POSITION_445_OG [0x1bd]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2024/05/29 12:50:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCU_NM                    5 02-76 00 d1 07 2e f9
2024/05/29 12:52:52.0 XRT_ARS_DIS_427_OG [0x1ab]
                        MDP_XRT_ARS_DIS           1 07-F0 d5
2024/05/29 12:52:54.0 XRT_FLRCTRL_DIS_449_OG [0x1c1]
                        MDP_XRT_FLRCTRL_DIS       1 07-F0 c9
2024/05/29 12:52:56.0 XRT_FLD_DIS_446_OG [0x1be]
                        MDP_XRT_FLD_DIS           1 07-F0 d9
2024/05/29 12:52:58.0 XRT_QT_PROG_SET_429_OG [0x1ad]
                        MDP_XRT_QT_PROG_SET       2 07-F0 c4 08
2024/05/29 12:53:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO         1 07-F0 c0
2024/05/29 12:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2024/05/29 12:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU         1 07-F0 c1
2024/05/29 12:59:58.0 XRT_FOCUS_RECALIBRATE_405_OG [0x195]
                        XRT_FOCUS_RECAL           2 07-F8 78 00
2024/05/29 13:00:00.0 AOCs_OrE-point_Start_7_OG [0x09d]
                        AOCU_NM                    5 02-76 00 08 cc 01 b2
2024/05/29 13:03:58.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION         4 07-F8 22 ff aa 00
2024/05/29 13:04:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA          1 07-F0 d8
2024/05/29 13:04:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]

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2024/05/29	13:04:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/29	13:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/29	13:04:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/29	13:06:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2024/05/29	13:06:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/05/29	13:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/29	13:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	13:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	13:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/29	13:22:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/29	13:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/29	13:52:30.0	XRT_Custom_430_OG [0x1ae]								
2024/05/29	13:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/29	15:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	15:01:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	15:01:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/29	15:01:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/29	15:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/29	15:30:30.0	XRT_Custom_430_OG [0x1ae]								
2024/05/29	15:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/29	16:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	16:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	16:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/29	16:39:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/29	16:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/29	17:17:30.0	XRT_Custom_430_OG [0x1ae]								
2024/05/29	17:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/29	18:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	18:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	18:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/29	18:17:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/29	18:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/29	18:54:30.0	XRT_Custom_430_OG [0x1ae]								
2024/05/29	18:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/29	19:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	19:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	19:44:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2024/05/29	19:45:00.0	AOCs_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2024/05/29	19:45:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/05/29	19:45:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/05/29	19:45:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/29	19:47:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2024/05/29	19:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/29	19:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	19:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/29	19:54:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2024/05/29	19:55:00.0	AOCs_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	08	cc	01	b2
2024/05/29	19:55:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				

2024/05/29	19:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/05/29	19:55:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/05/29	19:55:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/05/29	19:55:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/29	19:57:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2024/05/29	19:57:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2024/05/29	20:31:01.0	XRT_Custom_430_OG [0x1ae]					
2024/05/29	20:32:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/29	21:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/29	21:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/29	21:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/29	21:34:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/29	21:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/29	22:08:00.0	XRT_Custom_430_OG [0x1ae]					
2024/05/29	22:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/29	23:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/29	23:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/29	23:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/29	23:12:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/29	23:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/29	23:43:30.0	XRT_Custom_430_OG [0x1ae]					
2024/05/29	23:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/30	00:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/30	00:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/30	00:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/30	00:50:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/30	00:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/30	01:20:00.0	XRT_Custom_430_OG [0x1ae]					
2024/05/30	01:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/30	02:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/30	02:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/30	02:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/30	02:23:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/30	02:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/30	02:58:30.0	XRT_Custom_430_OG [0x1ae]					
2024/05/30	02:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/30	03:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/30	03:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/30	03:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/30	03:57:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/30	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/30	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2024/05/30	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/05/30	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/05/30	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/05/30	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/05/30	04:00:24.0	XRT_FLD_RESET_438_OG [0x1b6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/30	04:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/30	04:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12

2024/05/30	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/05/30	04:36:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/30	04:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/30	05:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	05:27:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	05:27:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/30	05:27:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/30	05:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/30	06:20:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	06:20:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	06:20:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2024/05/30	06:20:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2024/05/30	06:20:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/05/30	06:20:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/05/30	06:20:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/30	06:23:28.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2024/05/30	06:23:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/30	06:30:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	06:30:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	06:30:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2024/05/30	06:30:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	08	cc	01	b2
2024/05/30	06:30:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/05/30	06:30:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/05/30	06:30:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/30	06:30:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/30	06:30:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/30	06:33:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2024/05/30	06:33:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/05/30	06:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/30	07:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	07:07:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	07:07:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/30	07:07:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/30	07:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/30	07:53:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/30	07:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/30	08:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	08:47:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	08:47:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/30	08:47:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/30	08:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/30	09:31:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/30	09:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/30	10:12:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	10:29:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00