

XRT Timeline to be uploaded on 2024/05/30

Period: 2024/05/30 10:19:00 - 2024/06/04 11:29:00

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Normal mode

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XOB #1CC3: HOP361 - High cadence (10s thin-Be only) 256x256 at 1064 1048												
Term		Pointing (x, y)					Comment					
05/30 11:11:00 - 05/30 18:05:54		Track (-682.1, -381.7) ^{05/30 10:29:00}					# OP start + 10min. AR 13697 observations.					
PROG= 05 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 12 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 256x256 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 256x256 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 256x256 (1064, 1048) Q=98 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 28 250-time(s) 10.0sec												
└─ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 256x256 (1064, 1048) Q=95 3 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5792)												
Term		Pointing (x, y)					Comment					
05/30 18:09:00 - 05/30 18:15:54		Fixed (0.0, 0.0)					synoptic, shifted 6.0 min					
05/31 05:38:00 - 05/31 05:44:54		Fixed (0.0, 0.0)					synoptic, shifted -25.0 min					
05/31 18:27:30 - 05/31 18:34:24		Fixed (0.0, 0.0)					synoptic, shifted 24.5 min					
06/01 05:56:00 - 06/01 06:02:54		Fixed (0.0, 0.0)					# Synoptic					

PROG= 19 1-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└─ Seqn= 26 1-time(s) 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 99 1-time(s) 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 83 1-time(s) 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 23 1-time(s) 4.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 41 1-time(s) 2.0sec												
└─ Open/thick-Be Open/thick-Be close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Seqn= 17 1-time(s) 2.0sec												
└─ med-Al/Open med-Al/thick-Al close Safe Norm 250ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ med-Al/Open med-Al/thick-Al close Safe Norm 8.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ med-Al/Open med-Al/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 33 1-time(s) 2.0sec												
└─ med-Be/Open Open/thick-Al close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ med-Be/Open med-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└─ Seqn= 75 1-time(s) 2.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└─ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with												
Term		Pointing (x, y)					Comment					
05/30 18:19:00 - 05/31 05:34:54		Track (-636.4, -381.5) ^{05/30 18:16:00}					# AR obs.					

PROG= 03 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
└─ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
└─ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└─ Subr= 2 4-time(s) 2.0sec												
└─ Seqn= 47 1-time(s) 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec												
└─ Al-poly/Open thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 77 4-time(s) 300.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CC2: HOP361 - High cadence (8s thin-Be only) 384x384 at 1064 1048

Term	Pointing (x, y)	Comment
05/31 05:48:00 - 05/31 09:21:30	Track (-562.8, -381.4) @ 05/31 05:45:00	# AR obs.
PROG= 09 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 92 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 22 250-time(s) 8.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1BD6: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 900s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
05/31 10:06:30 - 05/31 17:46:30	Fixed (-17.0, -99.0)	# SUNRISE
05/31 18:37:30 - 06/01 01:57:30	Fixed (-17.0, -99.0)	# SUNRISE
06/01 06:06:00 - 06/01 11:19:30	Fixed (-17.0, -99.0)	# SUNRISE
PROG= 16 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 8-time(s) 900.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open	close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D09: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 90min cad) + CME watch

Term	Pointing (x, y)	Comment
06/01 02:29:00 - 06/01 04:55:30	Fixed (-17.0, -99.0)	# SUNRISE
PROG= 18 Inf.-time(s)		
Subr= 1 1-time(s) 600.0sec		
Seqn= 55 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 98 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open	close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 79 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open	close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 7-time(s) 600.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 74 1-time(s) 2.0sec		
med-Be/Open	med-Be/Open	close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
med-Be/Open	med-Be/Open	close Safe Norm 2.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open	close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 29 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec

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Flare mode

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XOB #1D10: Flare - multifilter 5 sec cadence (Be/thin, Be/med), AEC 3, 384x384											
Term	Pointing (x, y)							Comment			
05/30 11:11:00 - 05/30 18:05:54	Track (-682.1, -381.7) @ 05/30 10:29:00							# OP start + 10min. AR 13697 observations.			
05/31 05:48:00 - 05/31 09:21:30	Track (-562.8, -381.4) @ 05/31 05:45:00							# AR obs.			
PROG= 15 1-time(s)											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 9 1-time(s) 2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 49 255-time(s) 5.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GB											
Term	Pointing (x, y)							Comment			
05/30 18:19:00 - 05/31 05:34:54	Track (-636.4, -381.5) @ 05/30 18:16:00							# AR obs.			
05/31 10:06:30 - 05/31 17:46:30	Fixed (-17.0, -99.0)							# SUNRISE			
05/31 18:37:30 - 06/01 01:57:30	Fixed (-17.0, -99.0)							# SUNRISE			
06/01 02:29:00 - 06/01 04:55:30	Fixed (-17.0, -99.0)							# SUNRISE			
06/01 06:06:00 - 06/01 11:19:30	Fixed (-17.0, -99.0)							# SUNRISE			
PROG= 14 30-time(s)											
└─ Subr= 1 20-time(s) 2.0sec											
└─ Seqn= 11 1-time(s) 2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0 2.0sec
└─ Seqn= 73 1-time(s) 10.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 10 1-time(s) 2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0 2.0sec
└─ Seqn= 11 1-time(s) 2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0 2.0sec
└─ Seqn= 87 1-time(s) 2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0 2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0 2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

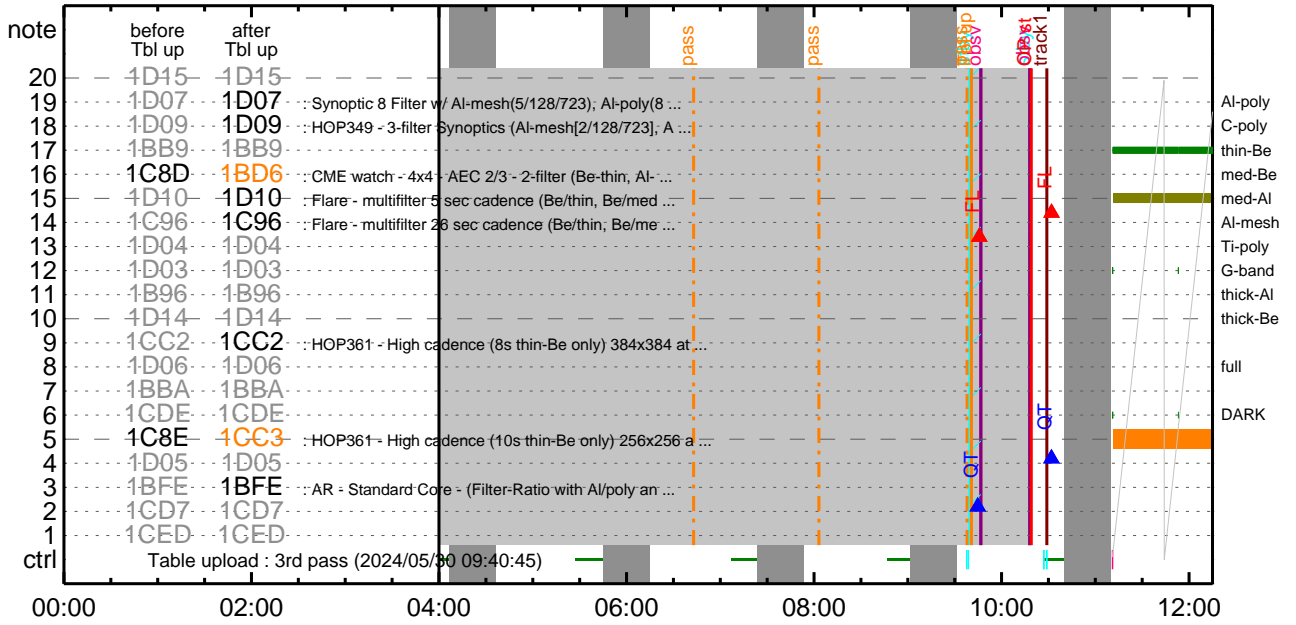
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Flare Detection

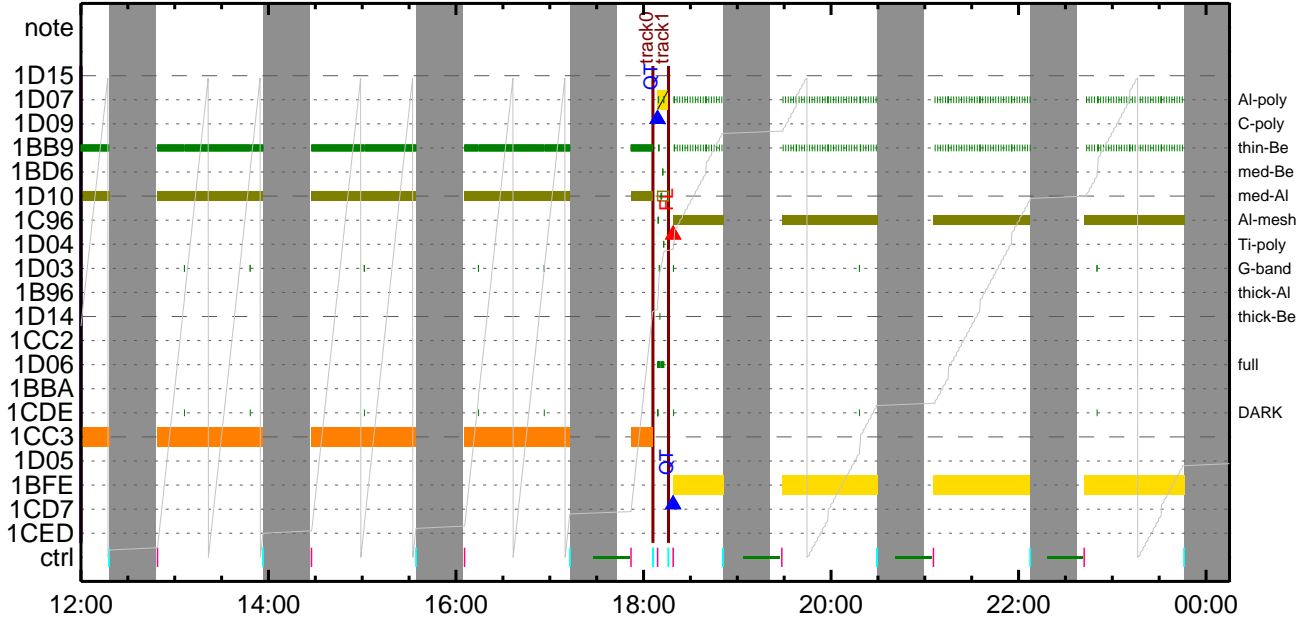
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FLD Patrol											
Term	Pointing (x, y)							Comment			
05/30 09:41:45 - 05/30 18:06:18	cannot be identified										
05/30 18:16:18 - 05/31 05:35:18	Track (-636.4, -381.5) @ 05/30 18:16:00							# AR obs.			
05/31 05:45:18 - 05/31 18:24:48	Track (-562.8, -381.4) @ 05/31 05:45:00							# AR obs.			
05/31 18:34:48 - 06/01 05:53:18	Fixed (-17.0, -99.0)							# SUNRISE			
06/01 06:03:18 - 06/04 11:29:00	Fixed (-17.0, -99.0)							# SUNRISE			
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

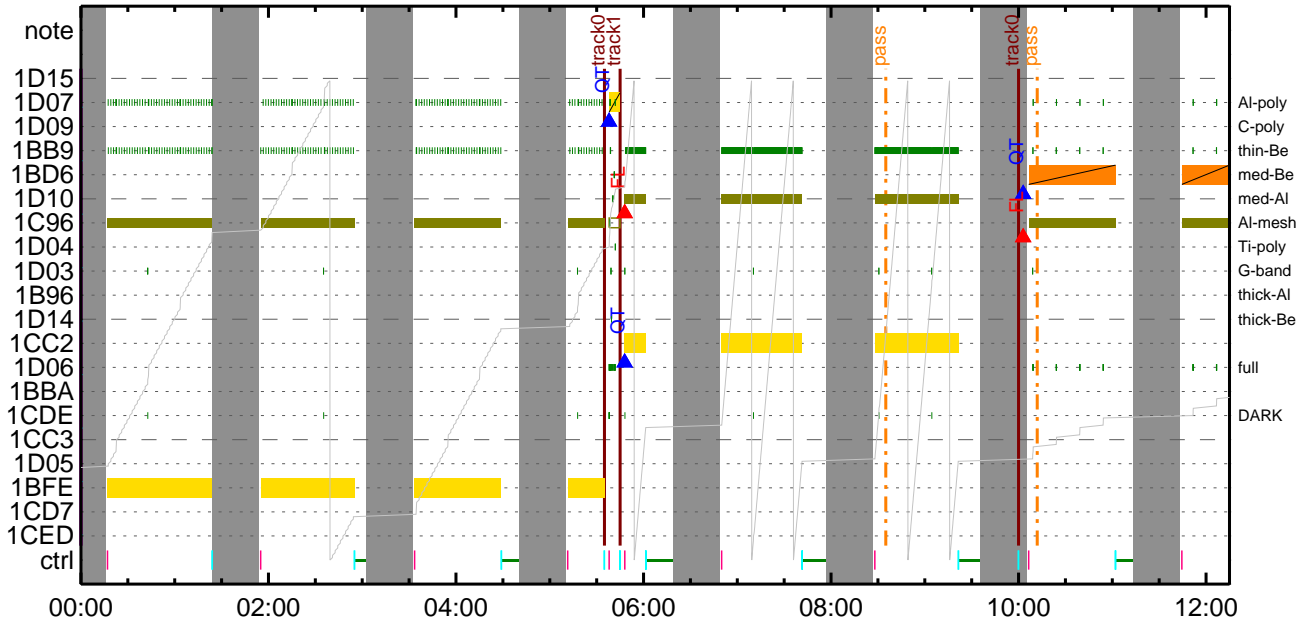
CMDI #0852 2024/05/30



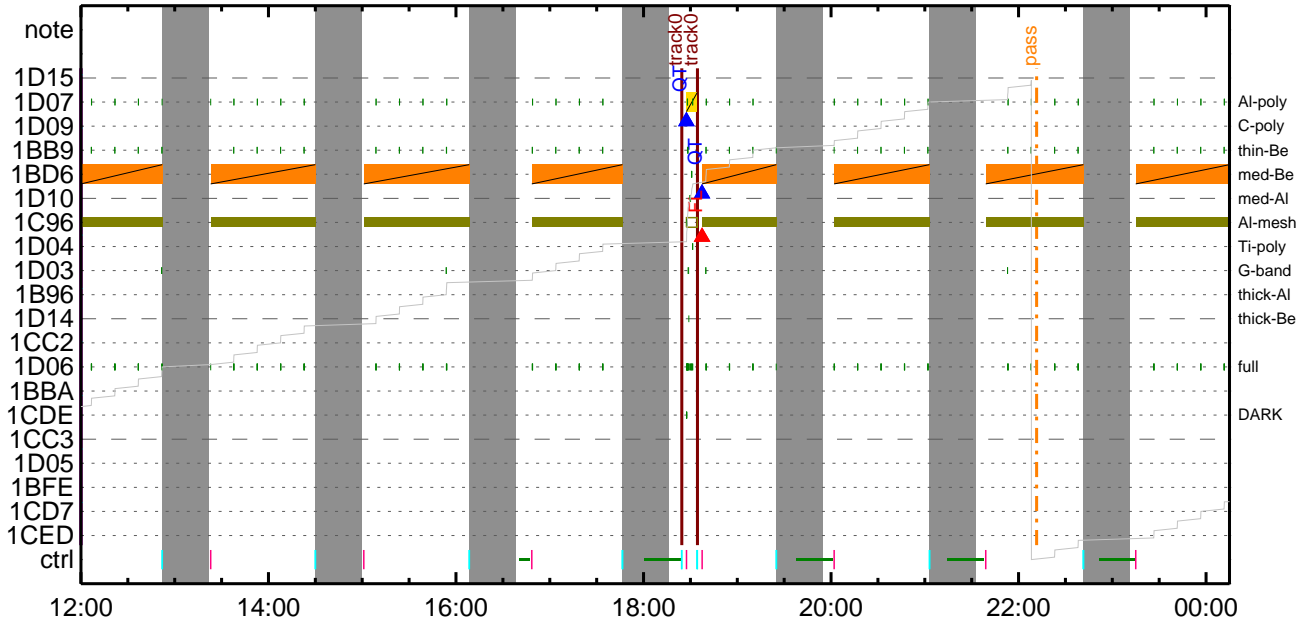
CMDI #0852 2024/05/30



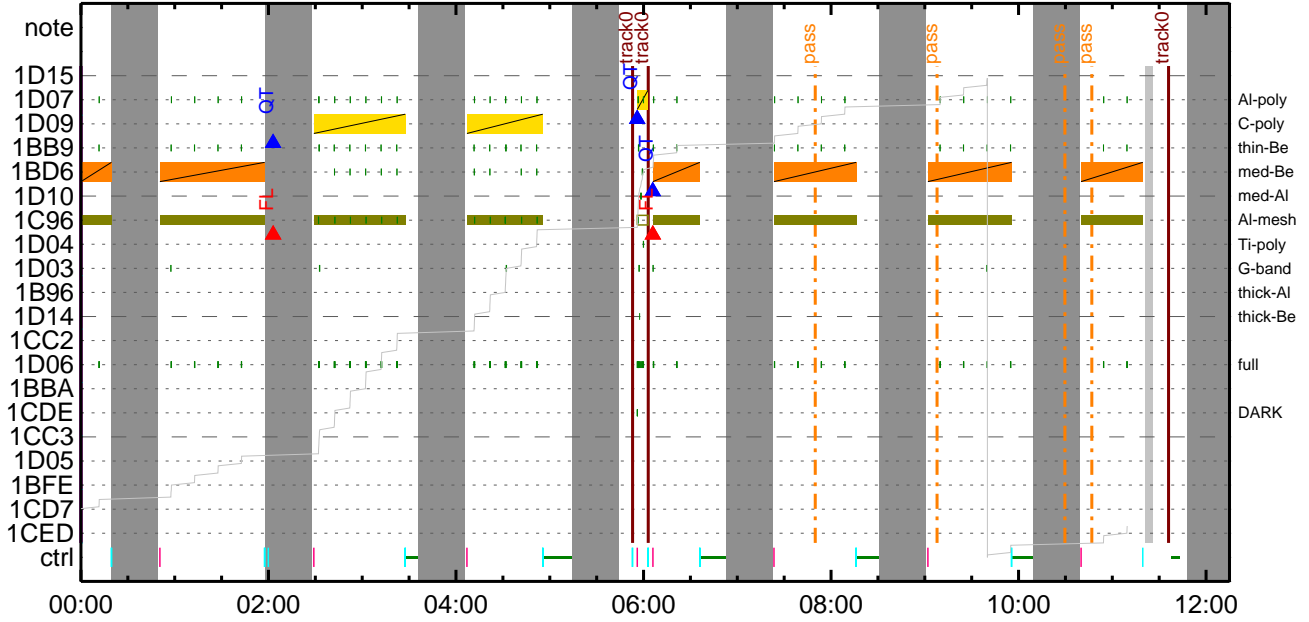
CMDI #0852 2024/05/31



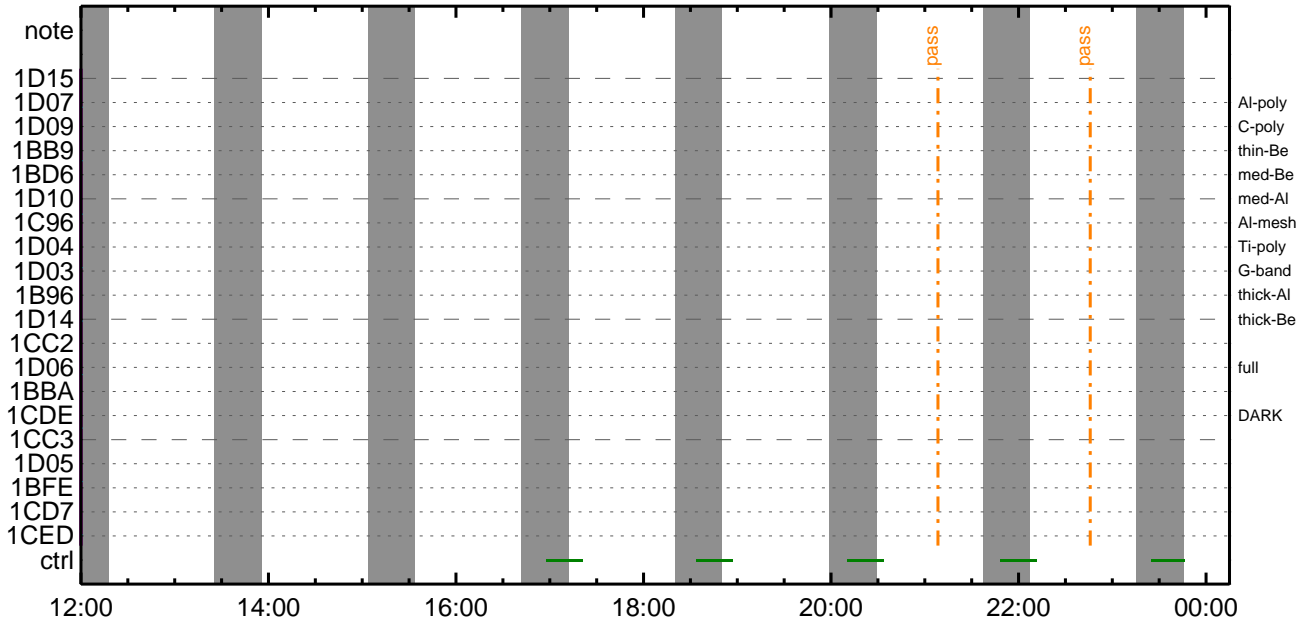
CMDI #0852 2024/05/31



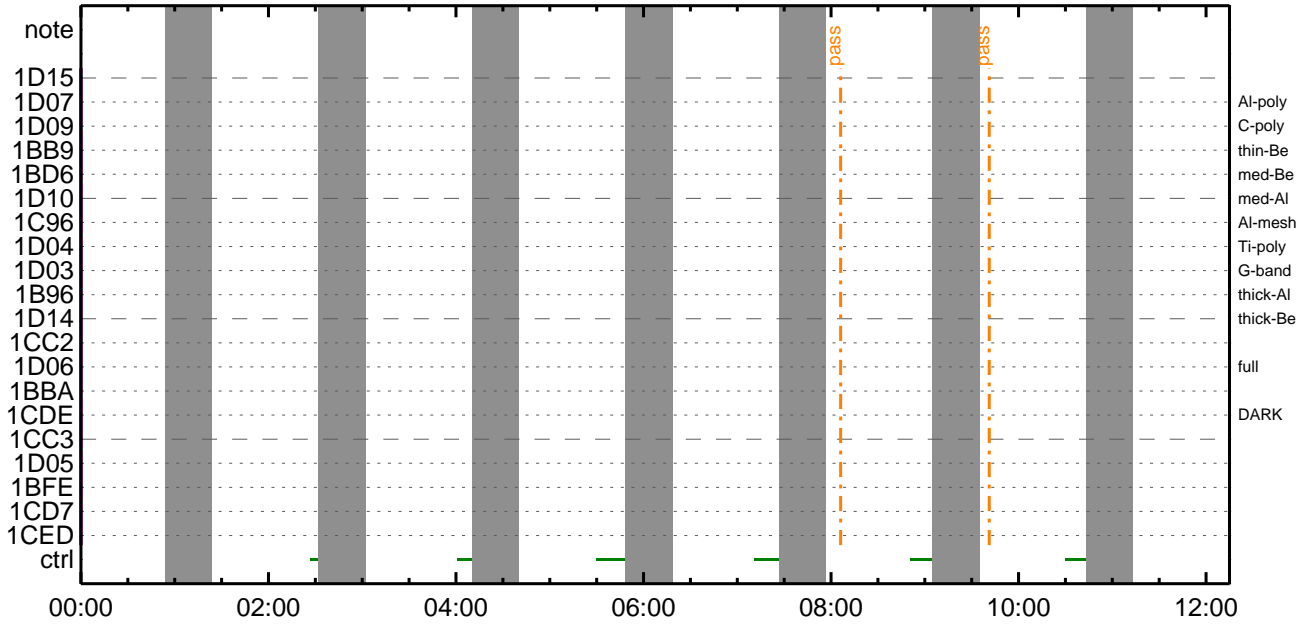
CMDI #0852 2024/06/01



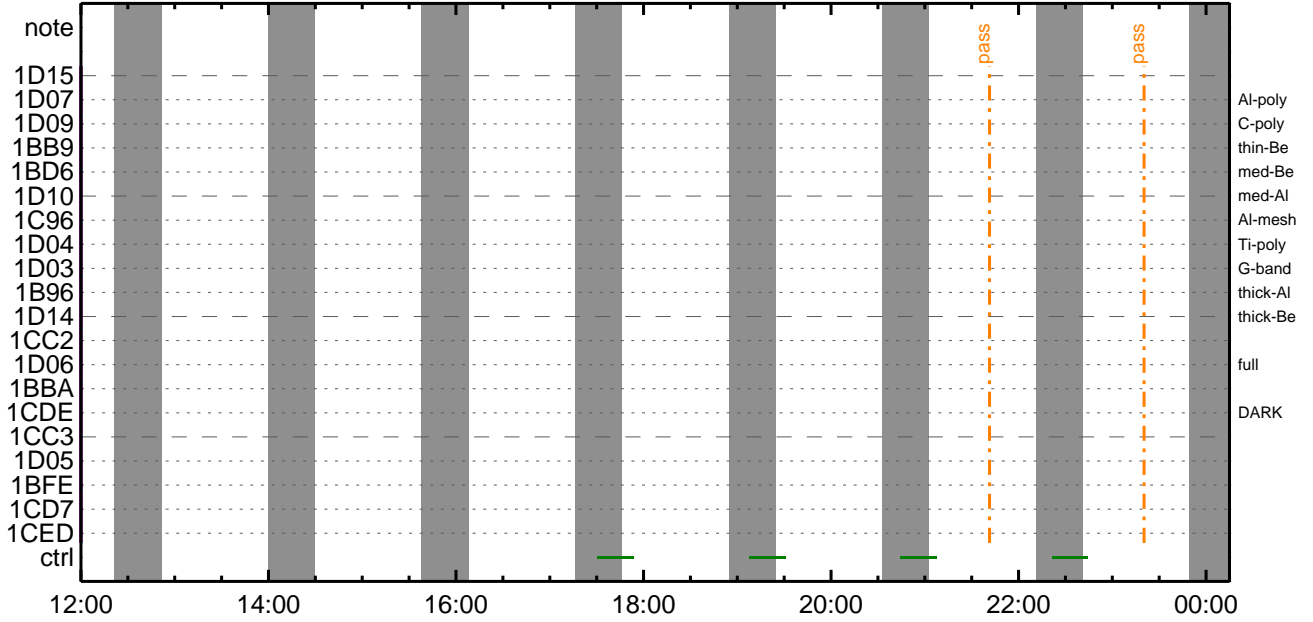
CMDI #0852 2024/06/01



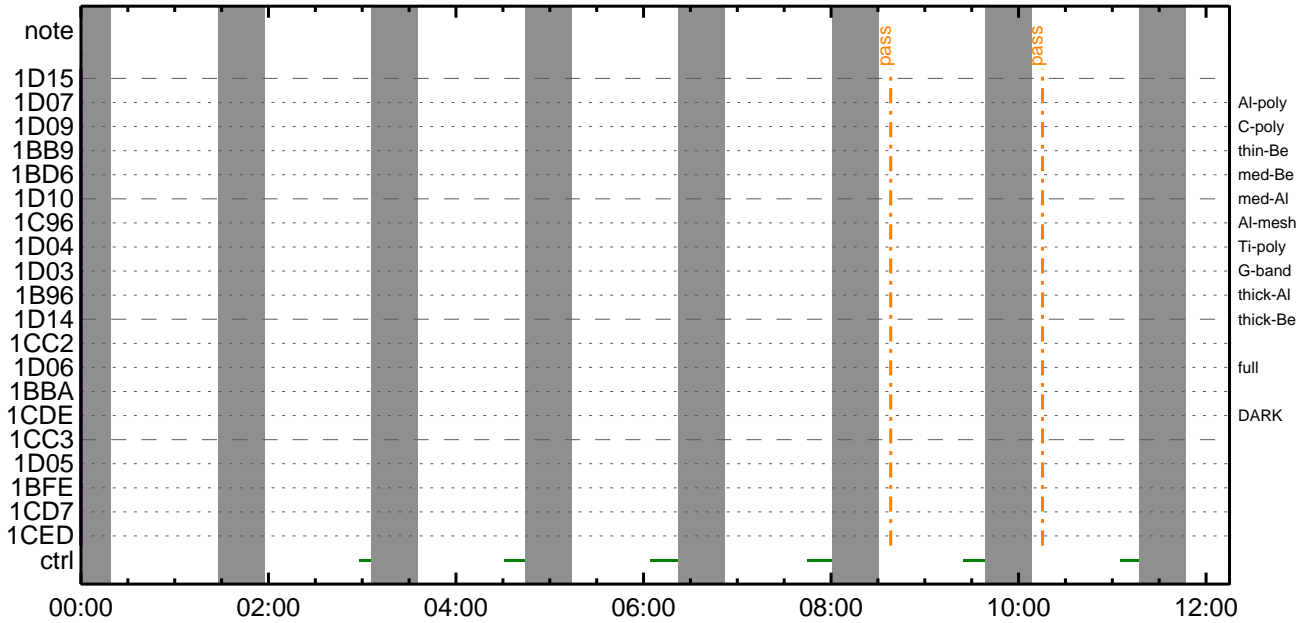
CMDI #0852 2024/06/02



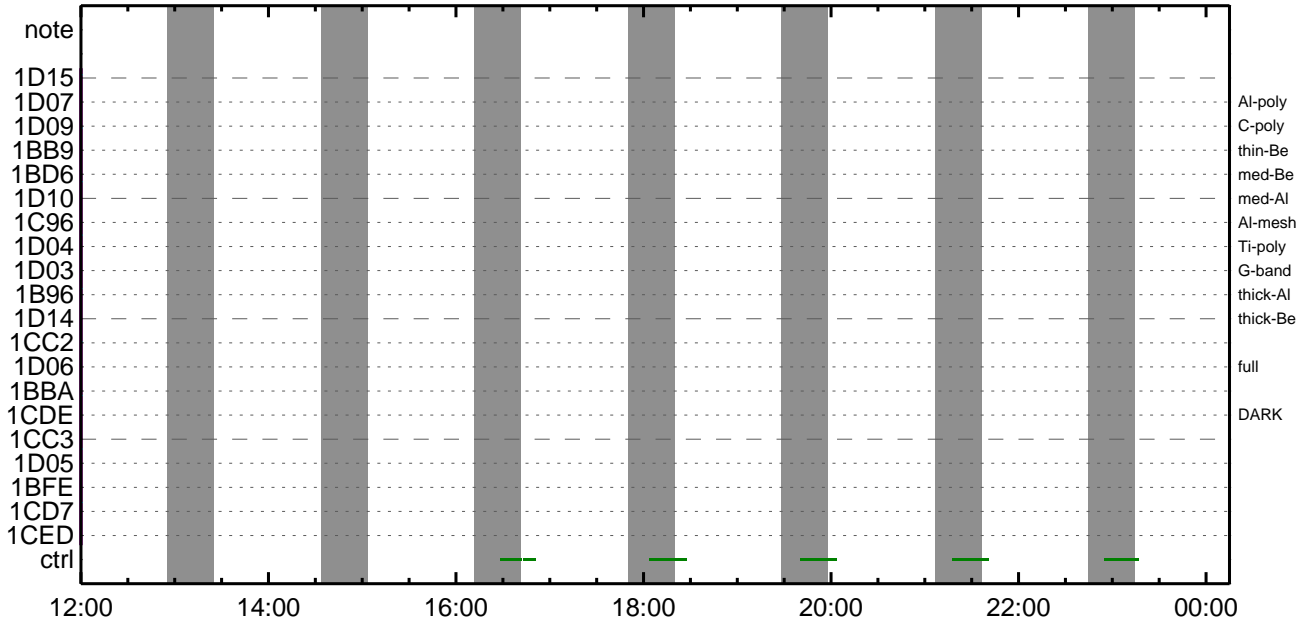
CMDI #0852 2024/06/02



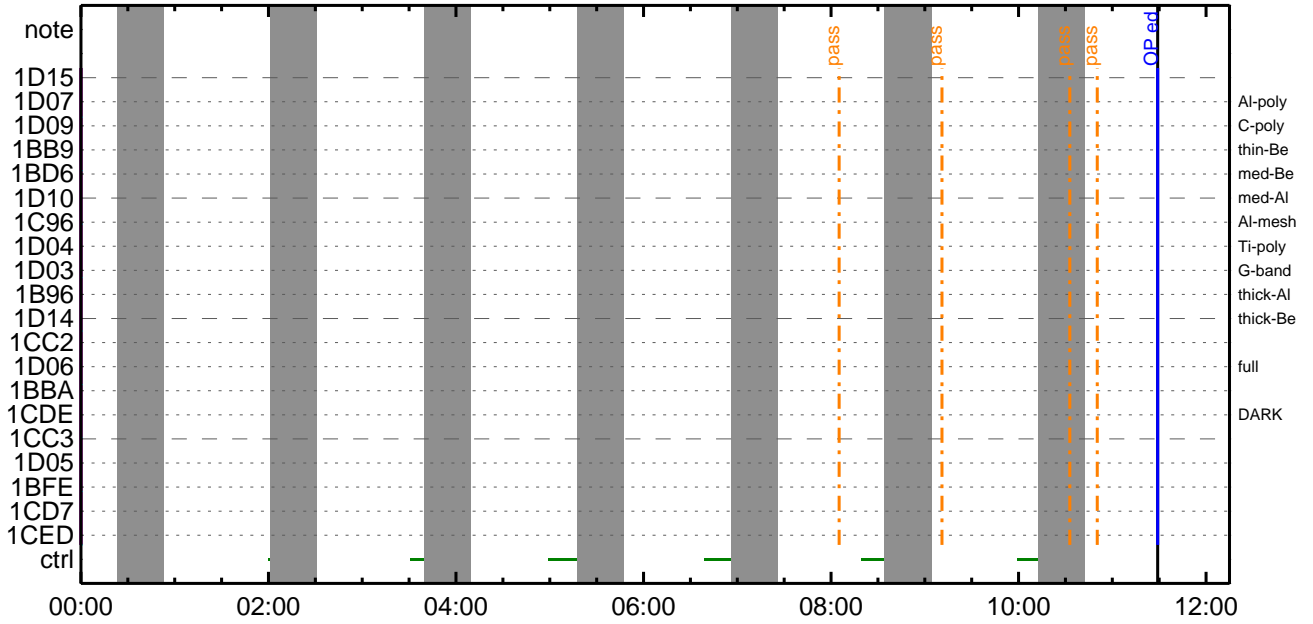
CMDI #0852 2024/06/03



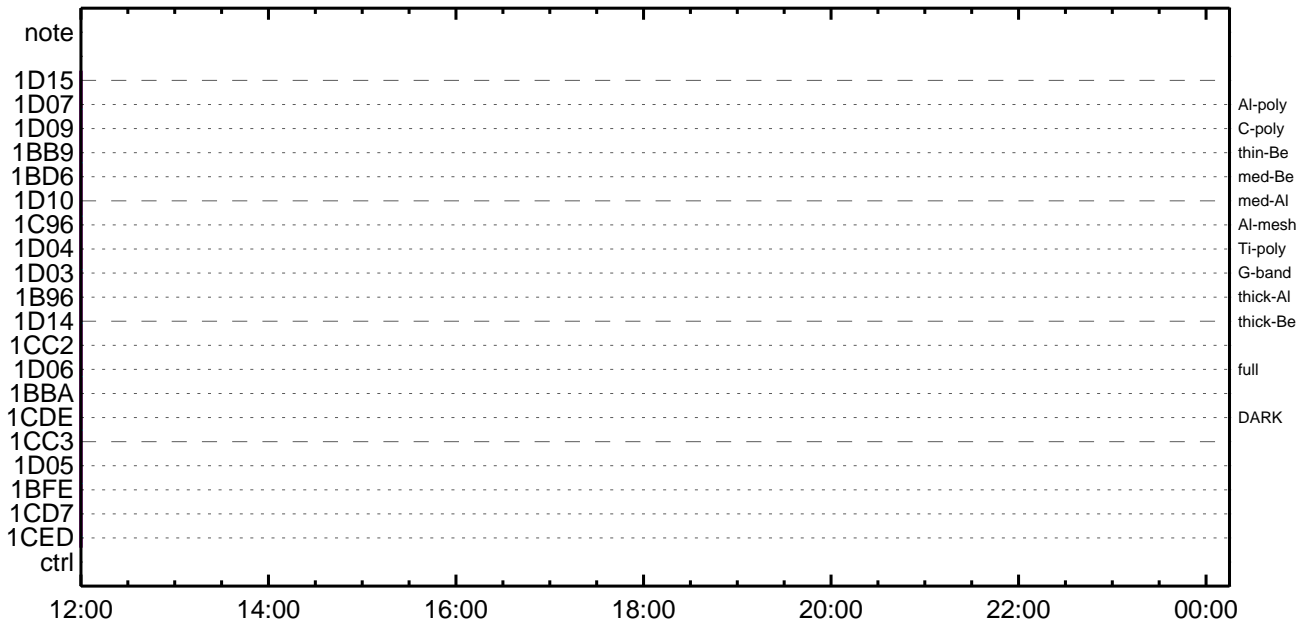
CMDI #0852 2024/06/03



CMDI #0852 2024/06/04



CMDI #0852 2024/06/04



(a) Spacecraft Operation Procedure (real-commands)

main-591 2024-05-30 12:43:31 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÿÁÿ\$ÿÄÿ~¼Ä»Ü;ä
0005 C.
0006 C. ÿÄÿß;¼ÿ³ÿÐÿóÿÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËñçµÄñ•µ°Æ»Í×ÁÇñÍÿçÿÿÄÿ×ÿÿí;¼ÿÉ;ËËËµ•íÍË;ËñË¼°ÇÓñ•ñç¼í¹ÇñÍ;çÄ®, ùñ¹ñËñÐñÇÄ÷ç®ñ•ñËñññ³ñË;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGÿí;¼ÿÉ;|ÿÄÿóÿ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGÿí;¼ÿÉ;ä
0019 . S. OP op-591:OP
0020 ()
0021 . S. OG og-591:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPí°èÿÿÄÿóÿ×;ä
0025 C. NMOG(0x200000-0x207FFF;\$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç [HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç [HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç [HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç [HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç [HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç [HK1_PKT_FORM_NO] EQ 7
0036 C. çç [HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç [HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç [HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç [HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ÿÄÿóÿ×½ªî»ð³îÇ\$
0041 C. çç [HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñî¼Ë¹ç•ë²îOKñð³îÇ\$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;\$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç [HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç [HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç [HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç [HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç [HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç [HK1_PKT_FORM_NO] EQ 7
0055 C. çç [HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç [HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç [HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç [HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ÿÄÿóÿ×½ªî»ð³îÇ\$
0060 C. çç [HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñî¼Ë¹ç•ë²îOKñð³îÇ\$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;\$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç [HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç [HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç [HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç [HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç [HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç [HK1_PKT_FORM_NO] EQ 7
0074 C. çç [HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç [HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç [HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç [HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ÿÄÿóÿ×½ªî»ð³îÇ\$
0079 C. çç [HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPñî¼Ë¹ç•ë²îOKñð³îÇ\$
0081 C.
0082 . C. ***** °Ë²¼ñî¼Ë¹ç•ë²îOKñð³îÇ\$ *****
0083 C. DHUÿä;¼ÿÉ;Ëÿý½, ÿí;¼ÿÉ;ËñðÍáñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç [HK1_PKT_FORM_NO] EQ 2
0087 C. çç [HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç [HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç [HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;\$ OPOG UPLOADñ~Ä÷ç®ññ¼Ë¹ç;ç°Ë²¼ñî¼Ë¹ç•ë²îOKñð³îÇ\$ *****


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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 29s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2024-05-30 10:18:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'uAÎI»ö¼YnEÄDn¹nëDCBC•x²è *****
0156 C. (%ã°I¥ÓYÄYÉYÐYËYÄYÇYÈnÈ¼n¼Ä»Ün¹në)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•Ï Daily±çÍÑnÈ'Øn¹nëDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥Ä¥~¼Ä»Ü;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-593 2024-05-30 12:43:31 184 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÿÁÿSÿÃÿ~¼Ä»Û;ã
0005 C.
0006 C. ÿÀÿß;¼ÿ³ÿPÿóÿËÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëç¾Ä#•µ°E»Í×ÁÇ#Íÿçÿÿ×ÿÿí;¼ÿË;ËËËµ•ííË;ËËË¼°ÇÖ#•#ç¼i¹Ç#Í; ÇÄ®, ù#¹#È#P#ÇÄ÷ç®#•#È#P##È; É
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷ç®µ;ON
0016 C. *****
0017 C. ç " °EÄ, í×ËÿäL#OS#P#Ç#Í»P´Ö#ò¹íí, #•; çÉÖÍ×#ÈXÄÓ#N#í¹Ö#È#í#È#P##È; É
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç [HK1_XPA_ON/OFF] EQ ON
0025 C. çç [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç [HK1_XMOD_ON/OFF] EQ ON
0027 C. çç [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÿDÿóÿËÿÿíÿÿÿ~¾óÄÖ#¬°ÄÄÈ#•#ç;#é; ç°È²¼#î°EÄ, ¼é½Ç#ò¼Ä¹Ö#¹#È; É
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°EÄ,
0033 C. *****
0034 C. ç " RESTART; ÈPT1; È#•#ç¾¼i¹Ç#Í; ç°È²¼#ííÄ¹Ö#P#; çDCBC-150#ØçÈ#à; É
0035 C.
0036 . C. ;ãPT1°EÄ, ³«»Û;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãÿçÿóÿËÿÿËÀÚÄØ; ÈÄ•Ä°²óÈò; È, å#í°EÄ, °E³«; ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EÄ, #¬¼«°E°Ä»ß#•#ç; å; ç°È²¼#ò¼Ä¹Ö#¹#È; É
0055 C. ÿçÿóÿËÿÿËÀÚÄØ#ÄÄ•Ä°²óÈò#¬¾å#¾¼i¹Ç#Í´°Í»#¹#È#P#ÇÄÖ#Ä; É
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°EÄ,
0059 C. *****
0060 C. ç " RESTART; ÈPT2; È#•#ç¾¼i¹Ç#Í; ç°È²¼#ííÄ¹Ö#P#; çDCBC-151#ØçÈ#à; É
0061 C.
0062 . C. ;ãPT2°EÄ, ³«»Û;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãÿçÿóÿËÿÿËÀÚÄØ; ÈÄ•Ä°²óÈò; È, å#í°EÄ, °E³«; ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÄ, Äã»ß; çXÁ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ, Äã»ß; ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç [HK1_REP_STA/STP] EQ STOP
0087 C. çç [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷ç®µ;OFF; ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç [HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç [HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 04 04)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 80 80 08 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_AEC_RESET
0149 BC (d0)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 03)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 0e)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2024-05-30 10:18:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 C.
0170 . C. ***** MDP 'úÃî»Ï»ö¼Ý»ËÄ»¹»ëDCBC•x²è *****
0171 C. (%ã°î¥Ö¥Ã¥Ë¥Ð¥Ë¥Á¥ç¥ë»Ë¼»¼»Ä»Û»¹»é)
0172 . S. DC-BC dcbc-402:DCBC
0173 (MDP_known_event)
0174 C.
0175 C.
0176 . C. ***** ¥Ð¥¹•Ï Daily±çÍÑ»Ë'Ø»¹»ëDCBC•x²è *****
0177 . S. DC-BC dcbc-153:DCBC
0178 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0179 C.
0180 C.
0181 . C. ;ãLOS¥Á¥S¥Ã¥~¼»Ä»Û;ã
0182 C.
0183 . C. ***** LOS *****
0184 C.

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*** OP Sequence for XRT ***

2024/05/30	10:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	10:27:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	10:27:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/30	10:27:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/30	10:28:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	10:28:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	10:28:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/05/30	10:29:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 06 24 01 81				
2024/05/30	10:29:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/05/30	10:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/05/30	10:29:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/30	10:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/30	10:29:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/30	10:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/30	10:31:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2024/05/30	10:31:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f				
2024/05/30	11:10:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/30	11:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/30	12:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	12:18:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	12:18:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/30	12:18:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/30	12:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/30	12:48:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/30	12:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/30	13:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	13:56:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	13:56:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/30	13:56:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/30	13:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/30	14:26:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/30	14:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/30	15:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	15:34:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	15:34:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/30	15:34:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/30	15:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/30	16:04:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/30	16:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/30	17:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	17:13:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	17:13:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/30	17:13:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/30	17:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/30	17:51:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/30	17:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/30	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/30	18:05:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

2024/05/30	18:05:58.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/05/30	18:06:00.0	AOCS_Ore-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2024/05/30	18:06:18.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9
2024/05/30	18:06:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2024/05/30	18:06:22.0	XRT_ARS_DIS_435_OG [0x1b3] MDP_XRT_ARS_DIS	1	07-F0	d5
2024/05/30	18:08:58.0	XRT_QT_PROG_SET_401_OG [0x191] MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2024/05/30	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/30	18:15:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/30	18:15:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/30	18:15:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2024/05/30	18:16:00.0	AOCS_Ore-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01 06 24 01 81
2024/05/30	18:16:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2024/05/30	18:16:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/05/30	18:16:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2024/05/30	18:16:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2024/05/30	18:16:26.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/30	18:18:56.0	XRT_QT_PROG_SET_436_OG [0x1b4] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2024/05/30	18:18:58.0	XRT_FL_PROG_SET_439_OG [0x1b7] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/05/30	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/30	18:51:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/30	18:51:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/30	18:51:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/30	18:51:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/30	18:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/30	19:27:30.0	XRT_Custom_430_OG [0x1ae]			
2024/05/30	19:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/30	20:29:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/30	20:29:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/30	20:29:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/30	20:29:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/30	20:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/30	21:04:30.0	XRT_Custom_430_OG [0x1ae]			
2024/05/30	21:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/30	22:07:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/30	22:07:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/30	22:07:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/30	22:07:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/30	22:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/30	22:41:00.0	XRT_Custom_430_OG [0x1ae]			
2024/05/30	22:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/30	23:46:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/30	23:46:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/30	23:46:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/05/30	23:46:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/05/30	23:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/05/31	00:16:00.0	XRT_Custom_430_OG [0x1ae]			
2024/05/31	00:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/05/31	01:24:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/05/31	01:24:02.0	XRT_CTRL_MANU_402_OG [0x192]			

2024/05/31	01:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	01:24:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/31	01:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/31	01:54:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/31	01:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/31	02:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	02:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	02:55:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	02:55:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/31	02:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/31	03:32:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/31	03:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/31	04:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	04:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	04:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	04:29:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/31	04:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/31	05:10:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/31	05:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/31	05:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	05:34:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	05:34:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	05:35:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2024/05/31	05:35:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00	
2024/05/31	05:35:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2024/05/31	05:35:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2024/05/31	05:37:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/05/31	05:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2024/05/31	05:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/31	05:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	05:44:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	05:45:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2024/05/31	05:45:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 06 24 01 81	
2024/05/31	05:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/05/31	05:45:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/05/31	05:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/05/31	05:45:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/05/31	05:47:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/31	05:47:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2024/05/31	05:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0f	
2024/05/31	06:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/31	06:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	06:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/05/31	06:01:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/05/31	06:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/05/31	06:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/05/31	06:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/05/31	07:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2024/05/31	07:41:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	07:41:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	07:41:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/31	07:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/31	08:27:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/31	08:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/31	09:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	09:21:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	09:21:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	09:21:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/31	09:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/31	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	09:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/05/31	10:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 08 cc 01 81				
2024/05/31	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/05/31	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/05/31	10:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/31	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/31	10:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	10:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2024/05/31	10:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/05/31	10:05:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/31	10:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/31	11:02:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	11:02:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	11:02:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	11:02:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/31	11:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/31	11:43:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/31	11:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/31	12:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	12:52:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	12:52:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	12:52:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/31	12:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/31	13:22:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/31	13:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/31	14:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	14:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	14:30:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	14:30:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/31	14:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/31	15:00:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/31	15:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/31	16:08:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	16:08:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	16:08:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	16:08:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

2024/05/31	16:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/31	16:47:30.0	XRT_Custom_430_OG [0x1ae]							
2024/05/31	16:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/31	17:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	17:46:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	17:46:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	17:46:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/31	17:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/31	18:24:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	18:24:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	18:24:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/05/31	18:24:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/05/31	18:24:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/05/31	18:24:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/05/31	18:24:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/31	18:27:28.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2024/05/31	18:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/31	18:34:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	18:34:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	18:34:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/05/31	18:34:30.5	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 08 cc 01 81				
2024/05/31	18:34:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/05/31	18:34:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/05/31	18:34:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/05/31	18:34:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/05/31	18:34:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	18:37:26.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2024/05/31	18:37:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/05/31	18:37:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/31	19:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	19:25:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	19:25:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	19:25:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/31	19:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/31	20:01:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/31	20:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/31	21:03:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	21:03:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	21:03:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	21:03:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/31	21:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/05/31	21:38:00.0	XRT_Custom_430_OG [0x1ae]							
2024/05/31	21:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/05/31	22:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	22:41:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/05/31	22:41:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/05/31	22:41:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/05/31	22:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

2024/05/31	23:14:00.0	XRT_Custom_430_OG [0x1ae]			
2024/05/31	23:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/01	00:19:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	00:19:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	00:19:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/01	00:19:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/01	00:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/01	00:49:30.0	XRT_Custom_430_OG [0x1ae]			
2024/06/01	00:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/01	01:57:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	01:57:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	01:57:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/01	01:57:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/01	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	01:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/06/01	02:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2024/06/01	02:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/06/01	02:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2024/06/01	02:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2024/06/01	02:00:24.0	XRT_FLD_RESET_438_OG [0x1b6]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/01	02:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/01	02:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2024/06/01	02:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/06/01	02:28:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/01	02:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/01	03:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	03:27:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	03:27:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/01	03:27:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/01	03:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/01	04:06:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/01	04:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/01	04:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	04:55:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	04:55:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/01	04:55:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/01	04:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/01	05:52:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	05:52:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	05:52:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/06/01	05:53:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2024/06/01	05:53:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2024/06/01	05:53:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2024/06/01	05:53:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2024/06/01	05:55:58.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2024/06/01	05:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/01	06:02:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	06:02:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/01	06:02:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			

2024/06/01	06:03:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
			AOCU_NM	5	02-76	00	08	cc	01 81
2024/06/01	06:03:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/06/01	06:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/06/01	06:03:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/06/01	06:03:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/06/01	06:03:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/06/01	06:05:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10		
2024/06/01	06:05:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e		
2024/06/01	06:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/06/01	06:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/01	06:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/01	06:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/06/01	06:36:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/06/01	06:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/06/01	07:22:30.0	XRT_Custom_430_OG [0x1ae]							
2024/06/01	07:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/06/01	08:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/01	08:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/01	08:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/06/01	08:16:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/06/01	08:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/06/01	09:01:00.0	XRT_Custom_430_OG [0x1ae]							
2024/06/01	09:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/06/01	09:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/01	09:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/01	09:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/06/01	09:55:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/06/01	09:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/06/01	10:39:00.0	XRT_Custom_430_OG [0x1ae]							
2024/06/01	10:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/06/01	11:19:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/01	11:36:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00 00